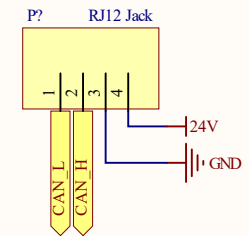
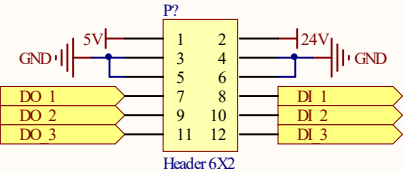


# 1 X CAN



Pin connection diagram for the WZS5010 module. The module is shown with pins 1 through 12. Pin 1 is GND, Pin 2 is GND, Pin 3 is MOSI, Pin 4 is SCLK, Pin 5 is CS, Pin 6 is SCNN, Pin 7 is INTN, Pin 8 is GND, Pin 9 is 3.3V, Pin 10 is 3.3V, Pin 11 is NC, Pin 12 is RSTN. External components include a 100nF/6.3V capacitor connected between VMCU and GND, and a 10k resistor connected between VMCU and GND. The module is labeled WZS5010.

|       |                                           |                      |  |
|-------|-------------------------------------------|----------------------|--|
| Title |                                           | <b>COMMUNICATION</b> |  |
| Size  | Number                                    | Revision             |  |
| A4    | <b>16.01.2023</b>                         |                      |  |
| Date: | 23.01.2023                                | Sheet of             |  |
| File: | D:\Projects\Personel\Communication.SchDoc | Drawn By:            |  |



|       |                                           |  |                  |  |
|-------|-------------------------------------------|--|------------------|--|
| Title |                                           |  | <b>CONNECTOR</b> |  |
| Size  | Number                                    |  | Revision         |  |
| A4    | <b>12.01.2023</b>                         |  |                  |  |
| Date: | 23.01.2023                                |  | Sheet of         |  |
| File: | D:\Projects\Personel\...\Connector.SchDoc |  | Drawn By:        |  |

Module\_Esp32  
Module\_Esp32.SchDoc



Communication  
Communication.SchDoc



Input\_Analog  
Input\_Analog.SchDoc



Output\_Analog  
Output\_Analog.SchDoc



Connector  
Connector.SchDoc

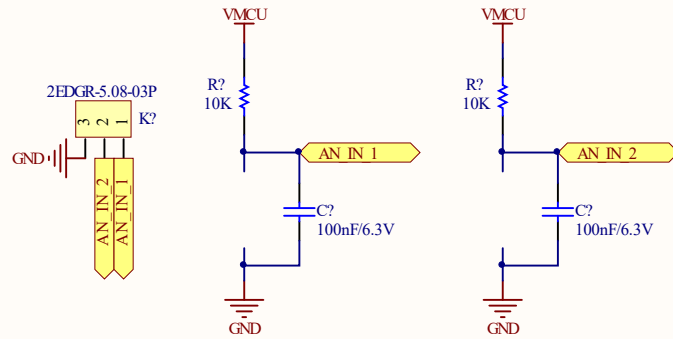


Peripheral  
Peripheral.SchDoc

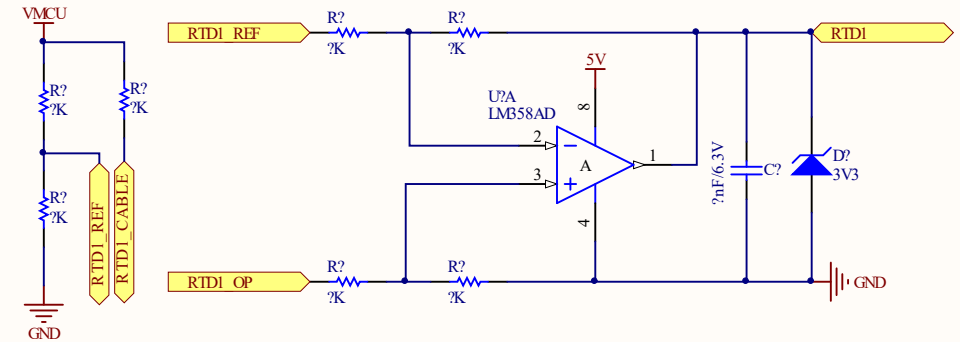


|       |  |                                                  |                  |           |  |
|-------|--|--------------------------------------------------|------------------|-----------|--|
| Title |  |                                                  | ESP32_DEVBOARD_T |           |  |
| Size  |  | Number                                           |                  | Revision  |  |
| A4    |  | 12.01.2023                                       |                  | R1.0      |  |
| Date: |  | 23.01.2023                                       |                  | Sheet of  |  |
| File: |  | D:\Projects\Personel\...\ESP32_DevBoard_T.SchDoc |                  | Drawn By: |  |

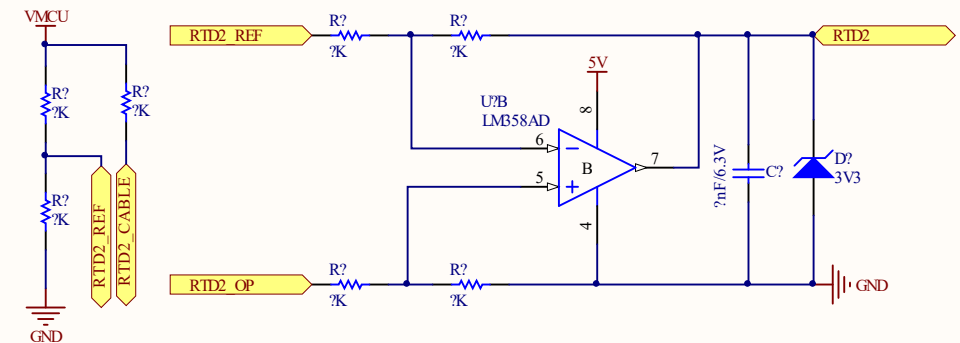
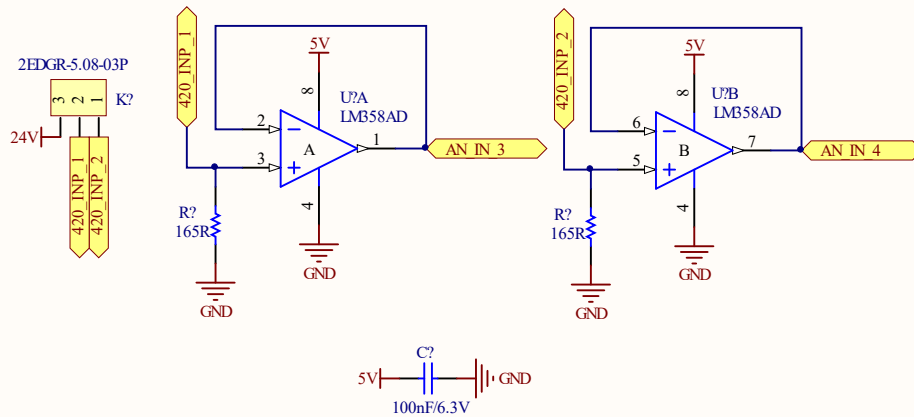
## 2 X AI (NTC)



## 2 X AI (RTD)

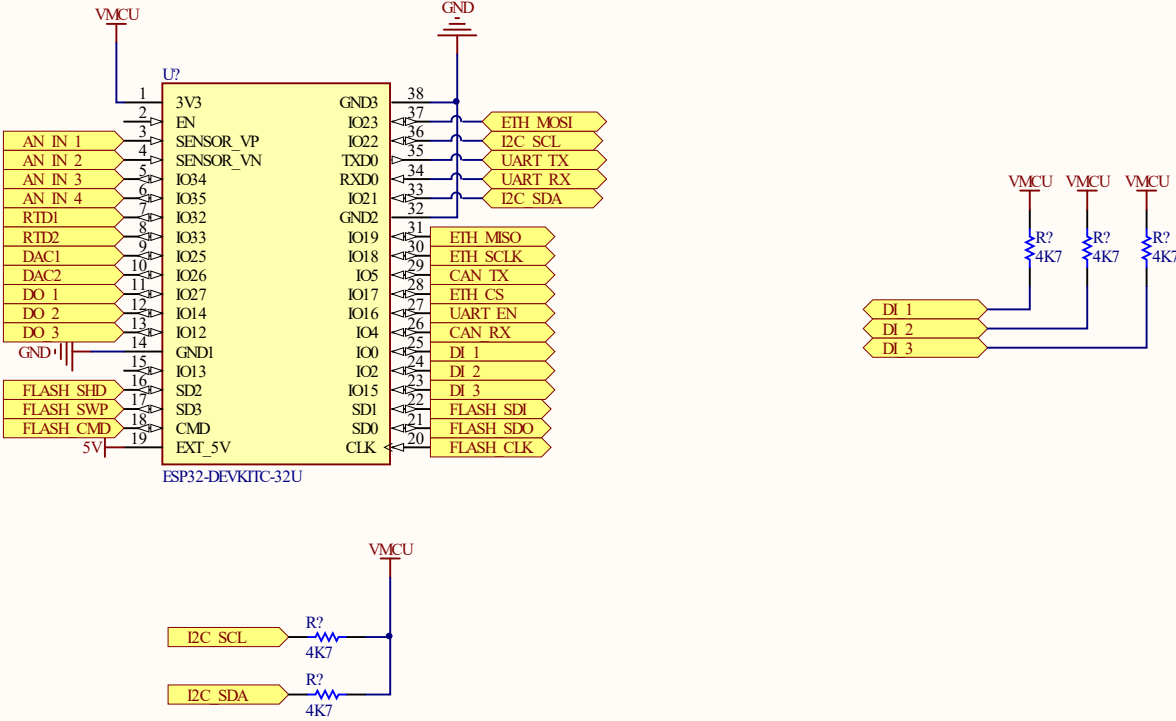


## 2 X AI (4-20MA)



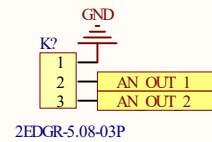
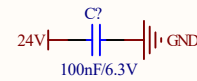
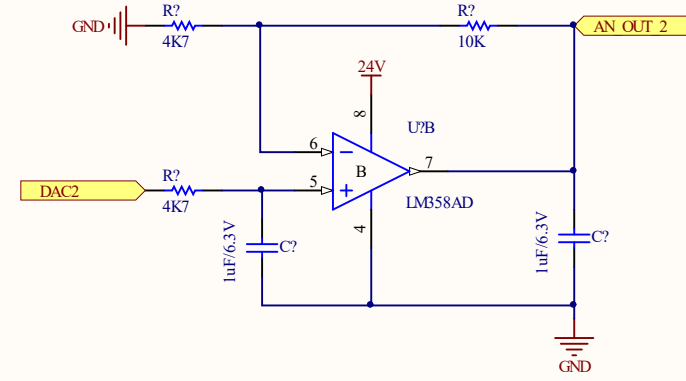
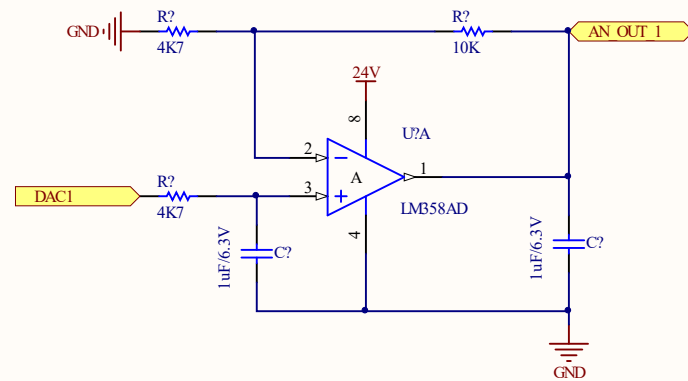
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|--------------|----------------------------------------------|-----------|
| Title        |                                              |           |
| INPUT_ANALOG |                                              |           |
| Size         | Number                                       | Revision  |
| A4           | 12.01.2023                                   |           |
| Date:        | 23.01.2023                                   | Sheet of  |
| File:        | D:\Projects\Personel\...\Input_Analog.SchDoc | Drawn By: |

# 1 X MODULE



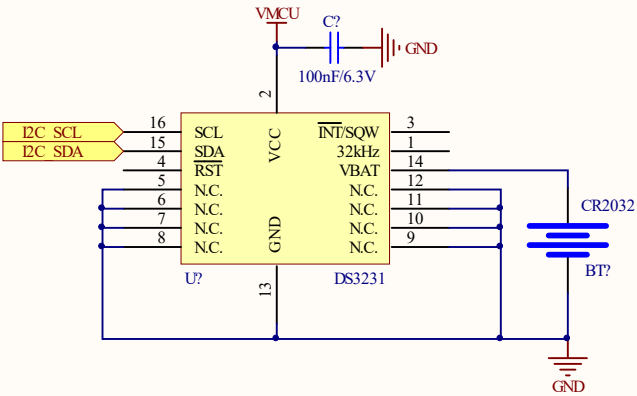
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| Title |                                              |            | MODULE_ESP32 |          |
| Size  | Number                                       | 16.01.2023 |              | Revision |
| A4    |                                              |            |              |          |
| Date: | 23.01.2023                                   | Sheet of   |              |          |
| File: | D:\Projects\Personel\...\Module_Esp32.SchDoc | Drawn By:  |              |          |

# 2 X A0 (0-10V)



|       |                                               |  |               |  |
|-------|-----------------------------------------------|--|---------------|--|
| Title |                                               |  | OUTPUT_ANALOG |  |
| Size  | Number                                        |  | Revision      |  |
| A4    | 14.01.2023                                    |  |               |  |
| Date: | 23.01.2023                                    |  | Sheet of      |  |
| File: | D:\Projects\Personel\...\Output_Analog.SchDoc |  | Drawn By:     |  |

# 1 X RTC



|       |                                            |           |            |
|-------|--------------------------------------------|-----------|------------|
| Title |                                            |           | PERIPHERAL |
| Size  | Number                                     | Revision  |            |
| A4    | 12.01.2023                                 |           |            |
| Date: | 23.01.2023                                 | Sheet     | of         |
| File: | D:\Projects\Personel\...\Peripheral.SchDoc | Drawn By: |            |