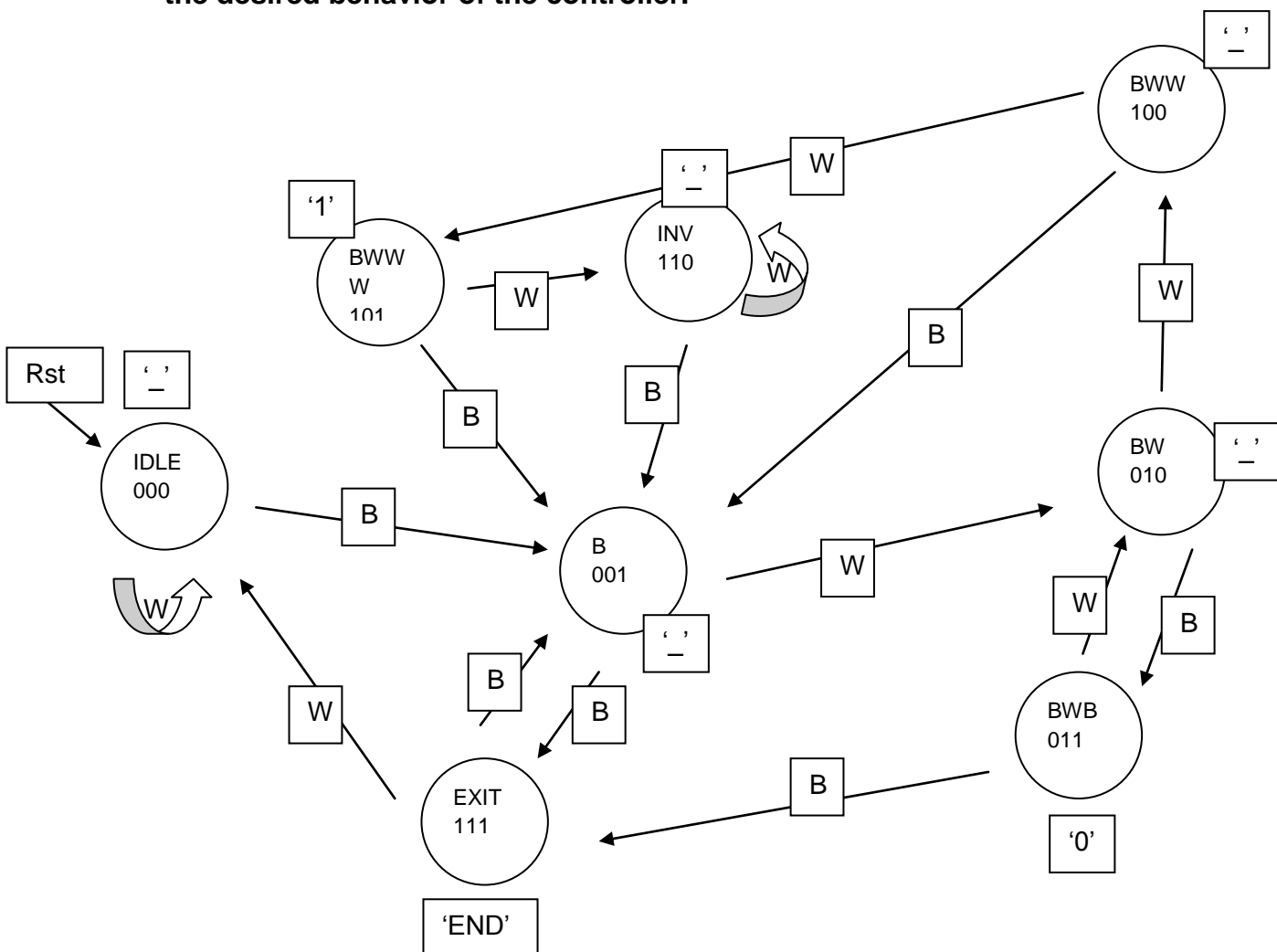


Session: 1

(For tables please use insert table feature of Microsoft Word)

Step 1: Capture the FSM: Create and draw the finite state machine that describes the desired behavior of the controller.



```
11: END
```

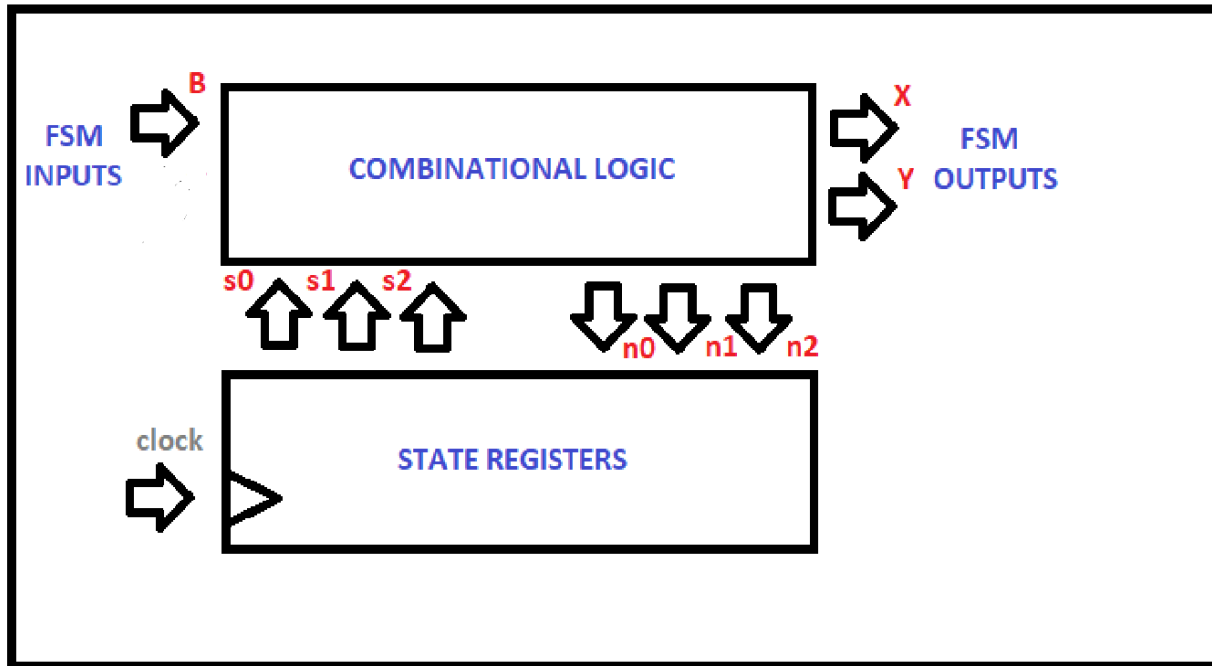
Student Names: Metin Dumandağ, Fatih İver

Student IDs: 2015400048, 2016400264

Group ID: 16

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Step 2: Create the architecture: Create and draw standard architecture by a using state register of the appropriate width and combinational logic. Refer to book or lecture slides. Use the same convention.



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Step 3: Encode the states: Assign a unique binary number to each state. Each binary number representing a state is known as an encoding. Any encoding will do as long as each state has a unique encoding. (The content of the following table is an example. Rename the states according to the ones you specified in the first section. Also use any encoding you want.)

STATE NAME	ENCODING
IDLE	000
B	001
BW	010
BWB	011
BWW	100
BWWW	101
INV	110
EXIT	111

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Step 4: Create the state table: Create a truth table for the combinational logic such that the logic will generate the correct FSM outputs and next state signals. Ordering the inputs with state bits first makes this truth table describe the state behavior, so the table is a state table. (Update the table according to the number of state variables that you have used.)

CURRENT STATE	INPUTS	NEXT STATE	OUTPUTS
000	0	000	00
000	1	001	00
001	0	010	00
001	1	111	00
010	0	100	00
010	1	011	00
011	0	010	01
011	1	111	01
100	0	101	00
100	1	001	00
101	0	110	10
101	1	001	10
110	0	110	00
110	1	001	00
111	0	000	11
111	1	001	11

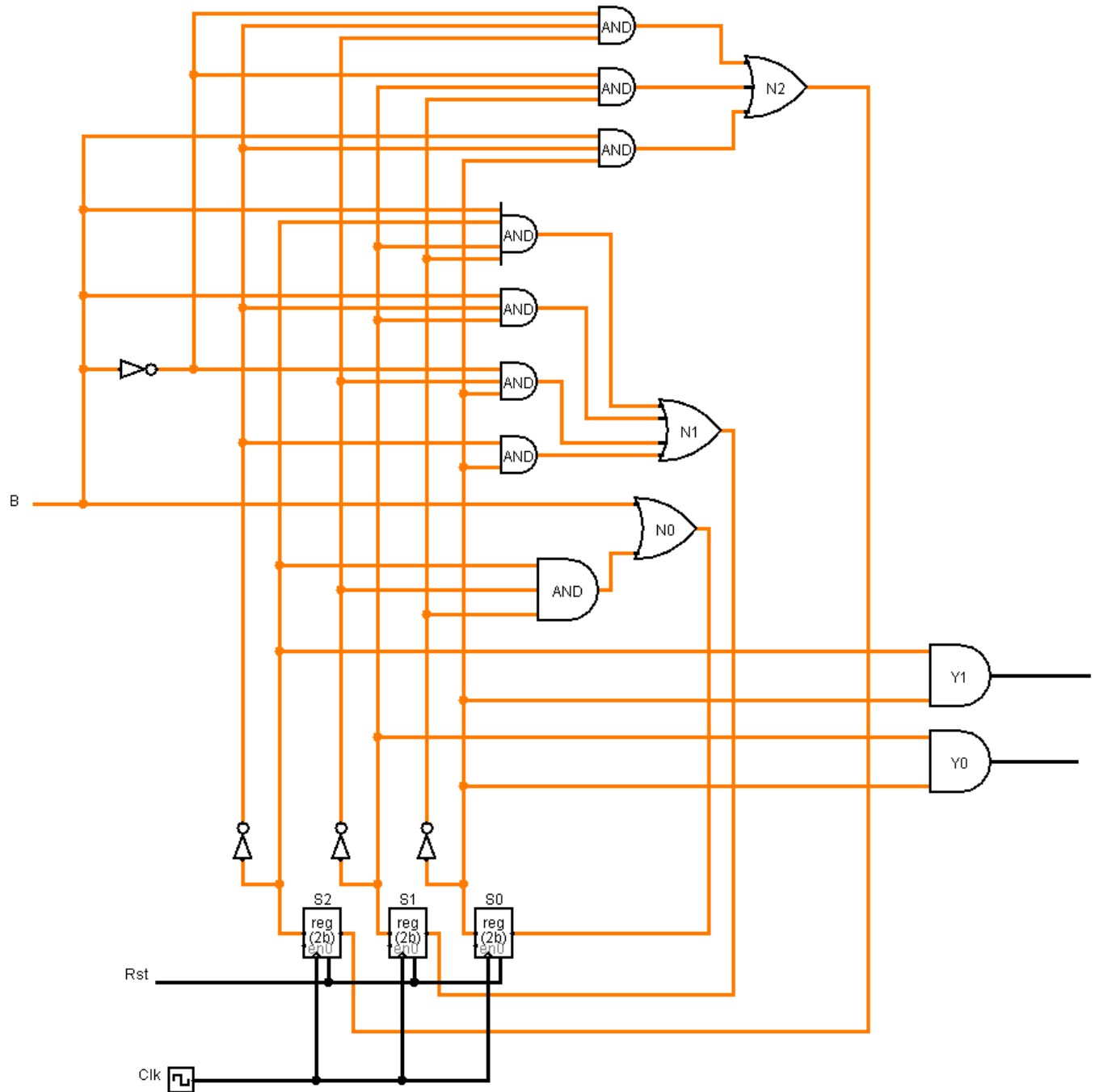
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Step 5: Draw the combinational logic: Implement the combinational logic using any method (You do not need to draw the inside circuit of multiplexers or decoders if you are using any. You can show those as blocks).



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