# P-Cloth: Interactive Complex Cloth Simulation on Multi-GPU Systems using Dynamic Matrix Assembly and Pipelined Implicit Integrators Supplemetary Material

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## 1 ALGORITHMS FOR TIME INTEGRATION

Some algorithms used for parallel time integration is described in the following pseudo-code.

Algorithm 1 Work Queue Generation Algorithm used for Pipelined SpMV

- 1: sw: index of the switch
- 2: offset: offset for sub-vector index
- 3: lvl: level in the binary tree, 0 for leaf nodes
- 4: GetGPURange:
- 5: Returns the indices of the GPU that the given switch interconnects. For a leaf switch, returns exact 2 GPU indices.
- 6: GetChildSwitches:
- 7: Return the indices of 2 child switches of a given switch.
- 9: // Call GenerateWorkQueues(0, 0, log<sub>2</sub>n) for the overall 10: // work queues.
- 11: **procedure** GenerateWorkQueues(sw, offset, lvl)
- // Recursively generate optimal work queues for
- // its children. 14:
- if lvl != 0 then 15:
- sw0,  $sw1 \leftarrow GetChildSwitches(sw)$ 16:

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```
GenerateWorkQueues(sw0, offset, lvl - 1)
            GenerateWorkQueues(sw1, offset, lvl - 1)
18:
       else
19:
20:
            // Each leaf switch interconnects exact 2 GPUs.
            G0, G1 \leftarrow \text{GetGPURange}(sw)
            node0 \leftarrow (G1, G0 + offset)
            node1 \leftarrow (G0, G1 + offset)
23:
            Push node0 to Q(G0)
24:
            Push node1 to Q(G1)
25:
        end if
26:
27:
        // step (2):
       // Transfer minimized amount of data between
29:
       // its children.
30:
        range0 \leftarrow GetGPURange(sw0)
31:
        range1 \leftarrow GetGPURange(sw1)
32:
33:
        for G0, G1 \in range0, range1 do
            node0 \leftarrow (G1, G0 + offset)
34:
            node1 \leftarrow (G0, G1 + offset)
35:
            Push node0 to Q(G0)
36:
            Push node1 to Q(G1)
37:
        end for
38:
39:
       // step (3):
       // Delegate rest of the work queue generation
       // tasks to its children.
42:
       offset0 \leftarrow offset + 2^{lvl}
43:
        offset1 \leftarrow offset - 2^{lvl}
44:
        // An offset is applied so that the work queue is
45:
        // generated for the delegated sub-vectors.
        GenerateWorkQueues(sw0, offset0, lvl - 1)
        GenerateWorkQueues(sw1, offset1, lvl - 1)
49: end procedure
```

## Algorithm 2 Sparse Matrix Filling Algorithm

```
1: // Index Table Allocating:
2: for each GPU_i do in parallel
       IndexTable_i \leftarrow AllocateIndexTable(i)
  end for
6: // Index Filling:
```

```
7: for each GPU_i do in parallel
       for Element \in Assembly Elements do
 9:
            rowIdx \leftarrow GetElementRowIdx(Element)
10:
           colIdx \leftarrow GetElementColIdx(Element)
           Push colIdx to IndexTable<sub>i</sub>[rowIdx] // atomic operator
11:
12:
       end for
13: end for
14
15: // Index Compacting:
16: for each GPU_i do in parallel
       for row \in IndexTable_i do
17:
            RemoveDuplication(row)
18:
        end for
19:
20: end for
21:
   // Value Table Allocating:
23: for each GPU_i do in parallel
        ValueTable_i \leftarrow AllocateValueTable(i)
24
25: end for
26
   // Value Filling:
27:
28: for each GPU_i do in parallel
       for Element ∈ AssemblyElements do
29
            entry \leftarrow FindElementEntry(Element)
30:
            value \leftarrow GetElementValue(Element)
31:
            ValueTable<sub>i</sub>[entry] += value // atomic operator
32:
       end for
33:
34: end for
```

### 2 STITCHING ALGORITHM

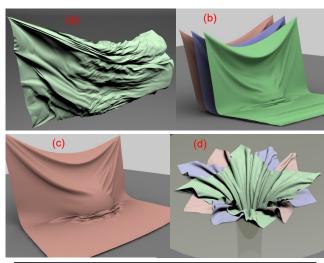
The stitching algorithm is described in the following pseudo-code.

## Algorithm 3 Stitching Algorithm

```
1: Input: Stitching node pairs NP.
2: Output: Stitched and refined cloth mesh.
3:
4: // Stitching cloth pieces with linking constraints
5: // for all the NP.
6: StitchingCoarsePieces(NP)
7:
8: // Merge the pieces together by merging
9: // all the node pairs NP
10: MergePieces(NP)
11:
12: // Refine the merged cloth mesh to higher resolution
13: // by subdividing.
14: RefinePieces()
```

## 3 ADDITIONAL BENCHMARKS

We use some complex cloth simulation benchmarks for regular/irregular-shaped cloth simulation (Fig. 1 and Fig. 2).



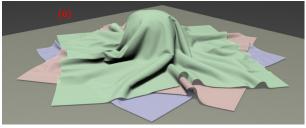


Fig. 1. **Benchmarks:** We use different multi-layered cloth simulation benchmarks ((a)Flag, (b) Sphere, (c) Sphere-1M, (d) Funnel, and (e) Twisting) for evaluation. The mesh complexity varies between  $0.5-1.65\mathrm{M}$  triangles. P-Cloth can perform cloth simulation at 2-5 fps on the 4-GPU workstation. The memory overhead on each GPU is between 4-8 GB.



Fig. 2. Multi-layer Garment Benchmarks: We used these benchmarks: (a) Miku with  $1.33\mathrm{M}$  triangles, (b) Zoey with  $569\mathrm{K}$  triangles, (c) Andy with  $538 \mathrm{K}$  triangles, and (d) Kimono with  $1 \mathrm{M}$  triangles, for multi-layer garment simulation.