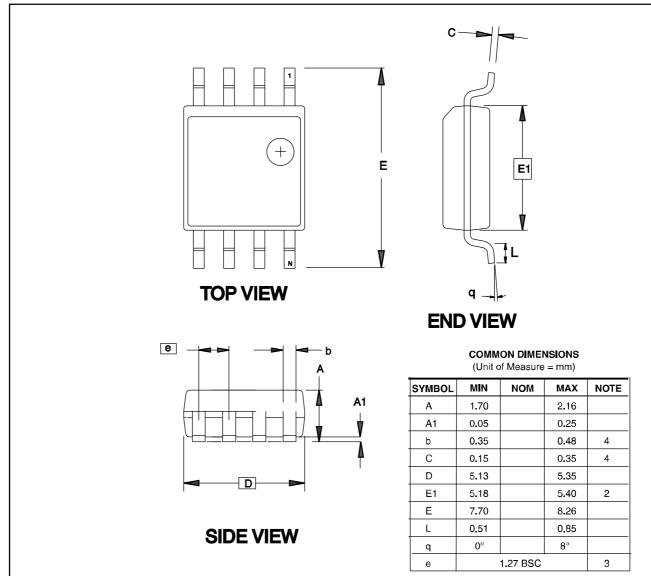
28. Packaging Information

28.1 8S2 - 8-lead EIAJ SOIC



- Notes: 1. This drawing is for general information only; refer to EIAJ Drawing EDR-7320 for additional information.
 - 2. Mismatch of the upper and lower dies and resin burrs aren't included.
 - 3. Determines the true geometric position.
 - 4. Values b,C apply to plated terminal. The standard thickness of the plating layer shall measure between 0.007 to .021 mm.

4/15/08

Package Drawing Contact: contact@adestotech.com 8S2, 8-lead, 0.208" Body, Plastic Small Outline Package (EIAJ) STN 8S2 F	adesto™	TITLE	GPC	DRAWING NO.	REV.	l
	Package Drawing Contact:	* * * * * * * * * * * * * * * * * * * *	STN	8S2	F	

