











	1		3	IC?A
A			120 121 122 123 124 137	PAO/TIM2_CH1/TIM2_ETR/TIM5_CH1/TIM8_ETR/USART2_CTS/UART4_TX/ETH_MIL_CRS/ADC123_IN0/WKUP/EVENTOUT PA1/TIM2_CH2/TIM5_CH2/USART2_RTS/UART4_RX/ETH_RMIL_REF_CLK/ETH_MIL_RX_CLK/ADC123_IN1/EVENTOUT PA2/TIM5_CH3/TIM5_CH3/TIM5_CH1/USART2_TX/ETH_MDIO/ADC123_IN2/EVENTOUT PA3/TIM5_CH3/TIM5_CH4/TIM6_CH2/USART2_RX/OTG_HS_ULPI_DO/ETH_MIL_COL/ADC123_IN3/EVENTOUT PA4/SPI1_NSS/SPI3_NSS/I2S3_WS/USART2_CK/OTG_HS_SOF/DCML_HSYNC/LCD_VSYNC/ADC12_IN4/DAC_OUT1/EVENTOUT PA5/TIM2_CH1/TIM6_CH1/TIM6_CH1N/SPI1_SCK/OTG_HS_ULPI_CK/ADC12_IN5/DAC_OUT2/EVENTOUT PA6/TIM1_BKIN/TIM3_CH2/ITM8_CH1N/SPI1_MISO/TIM13_CH1/DCML_PIXCLK/LCD_G2/ADC12_IN6/EVENTOUT PA7/TIM1_CH1N/TIM3_CH2/IN5_CH2/TIM8_CH1N/SPI1_MOSI/TIM4_CH1/ETH_MIL_RX_DV/ETH_RMIL_CRS_DV/ADC12_IN7/EVENTOUT PA9/TIM1_CH2/12C3_SMBA/USART1_CK/OTG_FS_SOF/EVENTOUT PA10/TIM1_CH3/USART1_RX/OTG_FS_ID/DCM_D1/EVENTOUT PA11/TIM1_CH3/USART1_RX/OTG_FS_ID/DCM_D1/EVENTOUT PA12/TIM1_CH3/USART1_RX/OTG_FS_ID/DCM_D1/EVENTOUT PA12/TIM1_SCSWIDI/CVENTOUT PA13/JTMS_SWIDI/CVENTOUT PA14/JTCK_SWCLK/EVENTOUT PA14/JTCK_SWCLK/EVENTOUT PA14/JTCK_SWCLK/EVENTOUT PA15/JTDI/TIM2_CH1/TIM2_ETR/SPI1_NSS/SPI3_NSS/I2S3_WS/EVENTOUT LQFP176
П				STM32F429IXTX
В			56 57 58 161 162 163 164 165 167 168 79 80 92 93 94	PB0/TIM1_CH2N/TIM3_CH3/TIM8_CH2N/LCD_R3/OTG_HS_ULPI_D1/ETH_MIL_RXD2/ADC12_IN8/EVENTOUT PB1/TIM1_CH3N/TIM3_CH4/TIM8_CH3N/LCD_R6/OTG_HS_ULPI_D2/ETH_MIL_RXD3/ADC12_IN9/EVENTOUT PB2/B0071/EVENTOUT PB3//TID0/TRACESW0/TIM2_CH2/SPI1_SCK/SPI3_SCK/L2S3_CK/EVENTOUT PB4/NJTRST/TIM3_CH1/SPI1_MISO/SPI3_MISO/I2S3ext_SD/EVENTOUT PB4/NJTRST/TIM3_CH2/I2C1_SMBA/SPI1_MOSI/SPI3_MOSI/I2S3_SD/CAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/FMC_SDCKE1/DCMI_D10/EVENTOUT PB6/TIM4_CH1/I2C1_SCL/USART1_TX/CAN2_TX/FMC_SDNE1/DCMI_D5/EVENTOUT PB7/TIM4_CH2/I2C1_SDA/USART1_RX/FMC_NL/DCMI_USYNC_EVENTOUT PB8/TIM4_CH3/TIM10_CH1/I2C1_SCL/CAN1_RX/ETH_MII_TXD3/SDIO_D4/DCMI_D6/LCD_B6/EVENTOUT PB97/TIM4_CH4/TIM11_CH1/I2C1_SDA/SPI2_NSS/I2S2_WS/CAN1_TX/SDIO_D5/DCMI_D7/LCD_B7/EVENTOUT PB10/TIM2_CH3/I2C2_SDA/USART3_RX/OTG_HS_ULPI_D4/ETH_MIL_TX_EN/ETH_ENRIL_TX_EN/LCD_G5/EVENTOUT PB11/TIM2_CH4/I2C2_SDA/USART3_RX/OTG_HS_ULPI_D4/ETH_MIL_TX_EN/ETH_ENRIL_TX_EN/LCD_G5/EVENTOUT PB12/TIM1_BKIN/I2C2_SMBA/SPI2_NSS/I2S2_WS/USART3_CK/CAN2_RX/OTG_HS_ULPI_D5/ETH_MII_TXD0/ETH_RMIL_TXD0/OTG_HS_ID/EVENTOUT PB13/TIM1_CH3/N/SPI2_SCK/USART3_CTS/CAN2_TX/OTG_HS_ULPI_D6/ETH_MII_TXD1/ETH_RMIL_TXD1/OTG_HS_VBUS/EVENTOUT PB13/TIM1_CH3/N/SPI2_MISO/I2S2ext_SD/USART3_TS/TIM12_CH4/OTG_HS_DN/EVENTOUT PB15/RTC_REFIN/TIM1_CH3N/TIM8_CH3N/SPI2_MOSI/I2S2_SD/TIM12_CH2/OTG_HS_DP/EVENTOUT LQFP176 STM32F429IXTX
С			32 33 34 35 54 55 115 116 117 118 139 140 141	PCO/OTG_HS_ULPL_STP/FMC_SDNWE/ADC123_IN10/EVENTOUT PC1/ETH_MDC/ADC123_IN11/EVENTOUT PC2/SPI2_MISO/IZS2ext_SD/OTG_HS_ULPL_DIR/ETH_MIL_TXD2/FMC_SDNEO/ADC123_IN12/EVENTOUT PC3/SPI2_MOSI/I2S2_SD/OTG_HS_ULPL_DIX/ETH_MIL_TXD2/FMC_SDCKEO/ADC123_IN13/EVENTOUT PC4/ETH_MIL_RXD0/ETH_RMIL_RXD0/ADC12_IN14/EVENTOUT PC6/ETH_MIL_RX_D1/ETH_MIL_RXD0/ADC12_IN14/EVENTOUT PC6/TIM3_CH1/TIM8_CH1/I2S2_MCK/USART6_TX/SDIO_D6/DCMI_D0/LCD_HSYNC/EVENTOUT PC7/TIM3_CH2/TIM8_CH3/USART6_CK/SDIO_D0/DCMI_D2/EVENTOUT PC8/TIM3_CH3/TIM8_CH3/USART6_CK/SDIO_D0/DCMI_D2/EVENTOUT PC9/MC02/TIM3_CH4/TIM8_CH4/I2C3_SDA/I2S_CKIN/SDIO_D1/DCMI_D3/EVENTOUT PC10/SPI3_SCK/I2S3_CK/USART3_TX/UART4_TX/SDIO_D2/DCMI_D8/LCD_R2/EVENTOUT
Н				STM32F429IXTX
D		2	7	Sheet: /uacs-stm32-pa-c/ File: uacs-2-bot-stm32-pa-c.sch



