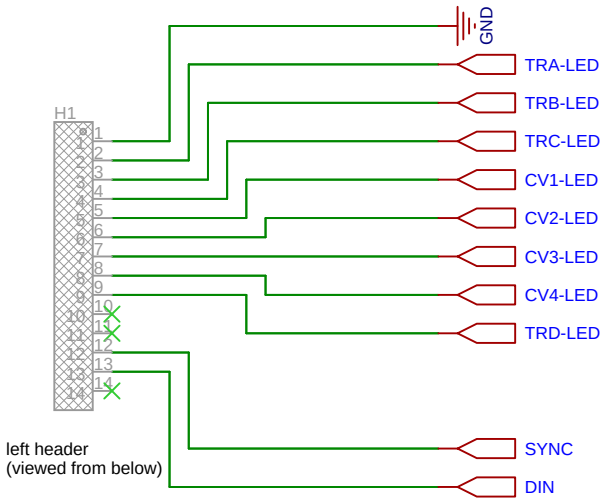

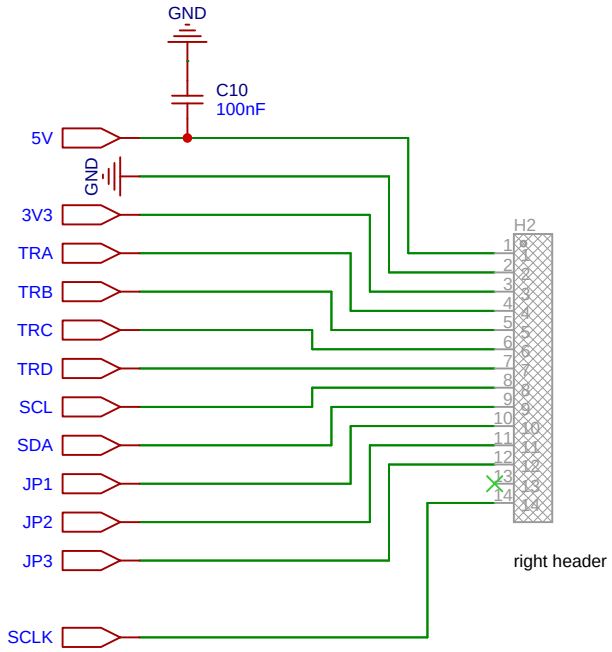

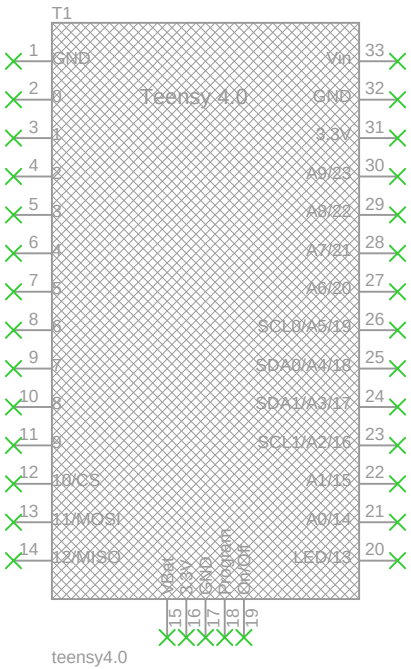

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Reviewed	EasyEDA Pro		
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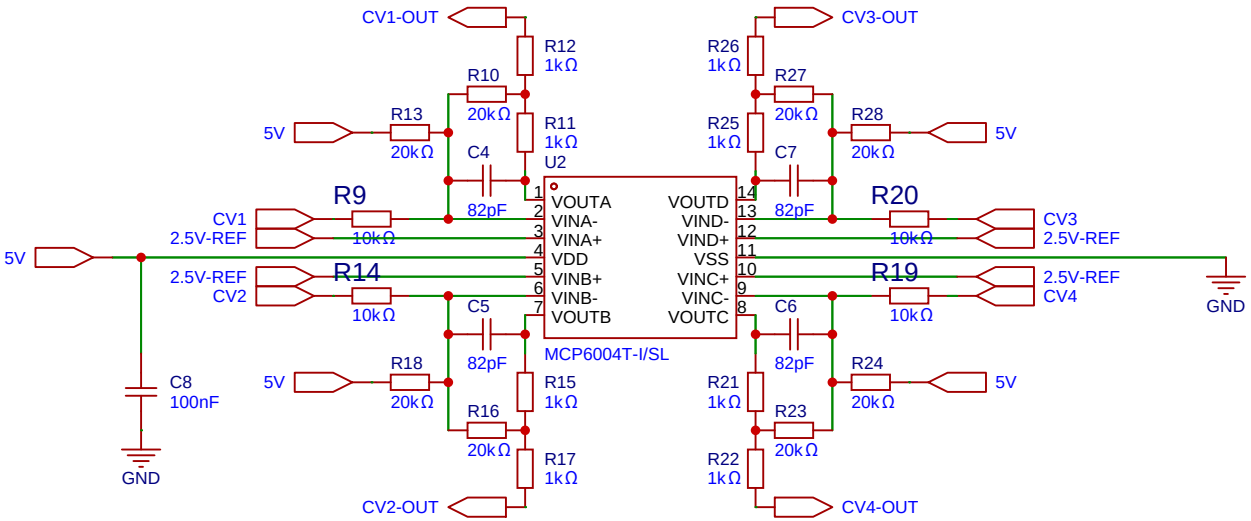
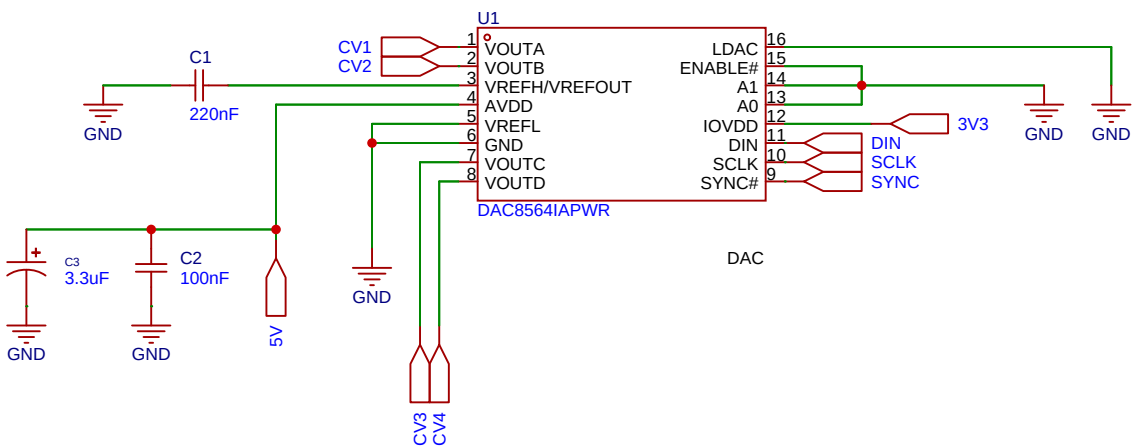
		PAGE 1 OF 4	
EasyEDA	V0.1	A4	EasyEDA.com

cv op-amps



3V3 from teensy powers DAC



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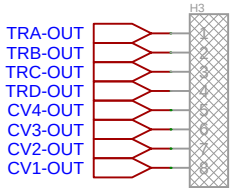
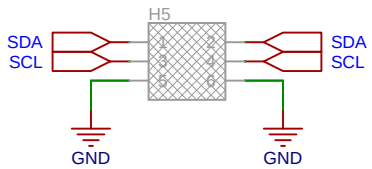
EasyEDA	V0.1	A4	EasyEDA.com

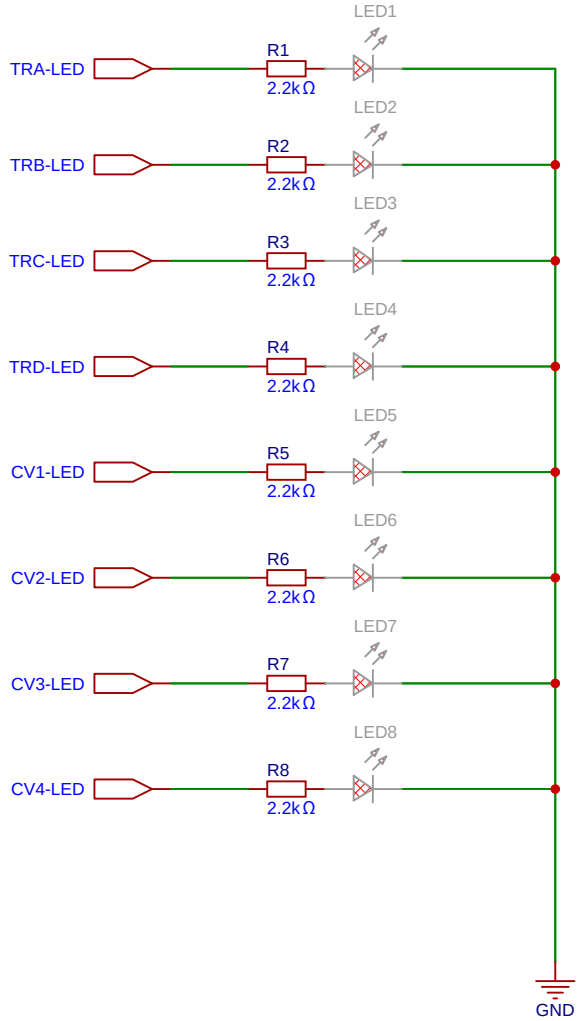
output headers



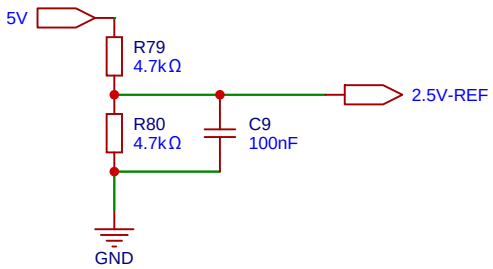
8/2: changed order of CV outputs to match the wacky DAC output order

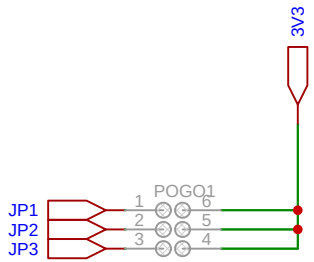
i2c header





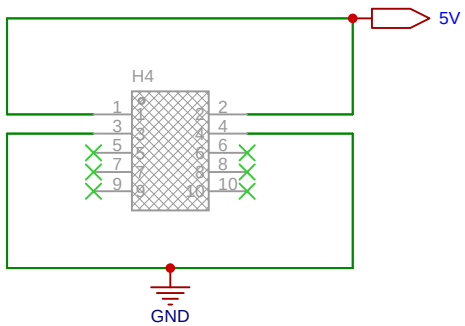
reference for cv op-amps





address jumpers

bus connection



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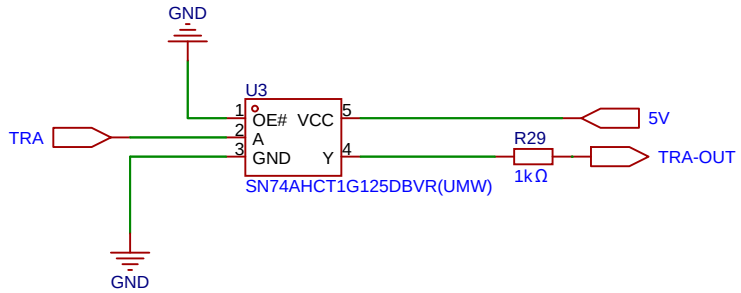

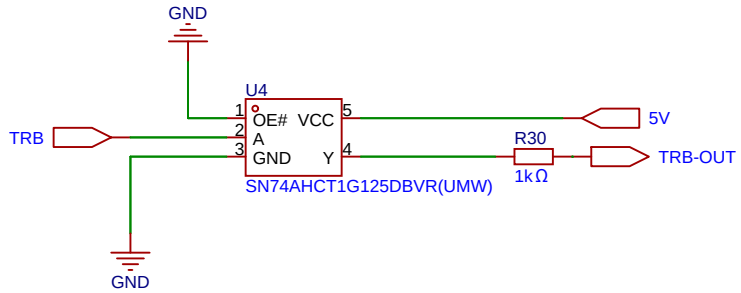

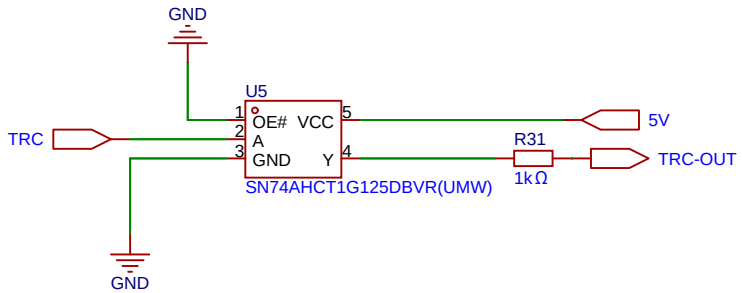

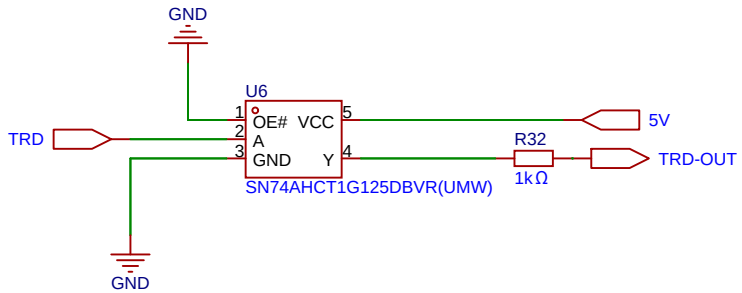

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Reviewed	EasyEDA Pro		
		VER / SIZE / PAGE 4 OF 4	
EasyEDA	V0.1 / A4	EasyEDA.com	