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ID: _____ ASSIGNMENT # 1 (EE-1005) Assigned Number: _____

DIGITAL LOGIC & DESIGN (EE-1005) ASSIGNMENT #3

ID: 241-6516

NAME: M Afay Ahseen

SECTION: Ai - C

Read the Instructions Carefully

❖ NOTE: Use Assigned number & Your Name

❖ (Same as Assignment 1)

❖ Your assigned number is given to you in excel sheet provided with assignment

❖ FOR EXAMPLE: Assigned Number if your assigned number is 0821

	Assign Digit 0	Assign Digit 1	Assign Digit 2	Assign Digit 3
Short for Assigned Digit	A0	A1	A2	A3
Write Assigned Number Digit By Digit	7 8	8 2	2 6	1 0

FOR EXAMPLE: Name is HAMAZADAUD

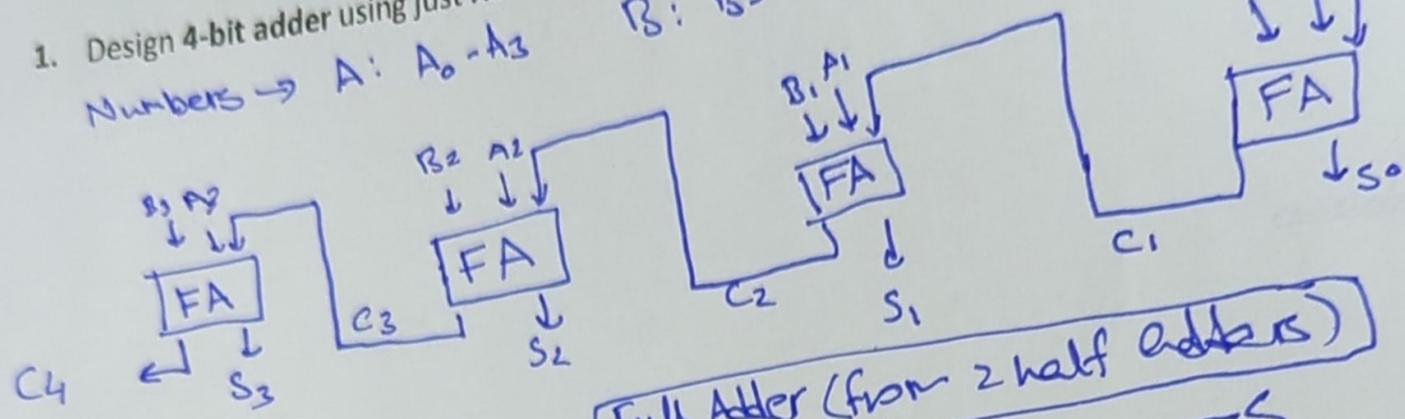
- ❖ Use your name instead of HAMZA
- ❖ If your name starts with MUHAMMAD kindly use your second name
- ❖ Convert repeated character to small letter or to other symbols to make them unique (see example for A, a & @)

SIXTH CHARACTER OF YOUR NAME	FIFTH CHARACTER OF YOUR NAME	FOURTH CHARACTER OF YOUR NAME	THIRD CHARACTER OF YOUR NAME	SECOND CHARACTER OF YOUR NAME	FIRST CHARACTER OF YOUR NAME	ZERO CHARACTER OF YOUR NAME	
Short for CHARACTER	c0	c1	c2	c3	c4	c5	c6
YOUR NAME CHARACTER BY CHARACTER	\$	H	@	Q	a	F	A

ID:

[CLO 3: Design combinational and sequential logic circuits]

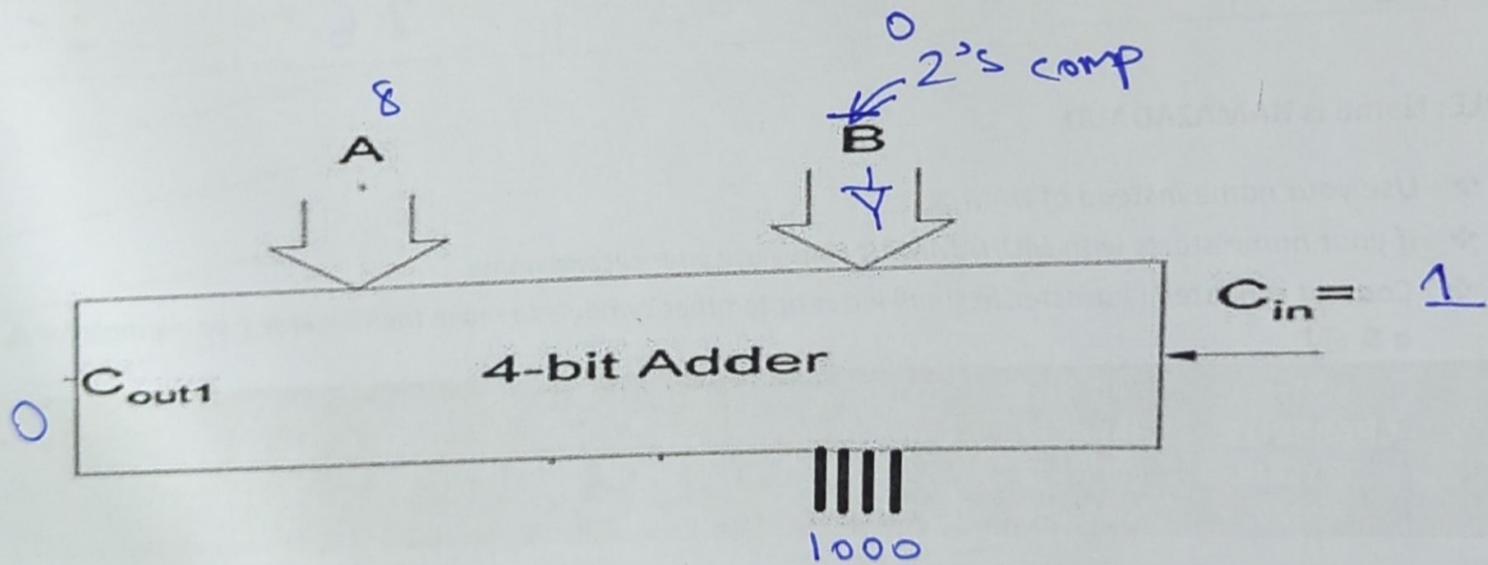
1. Design 4-bit adder using just Half adders? Only circuit is required?
 Numbers \rightarrow A: $A_0 - A_3$ B: $B_0 - B_3$



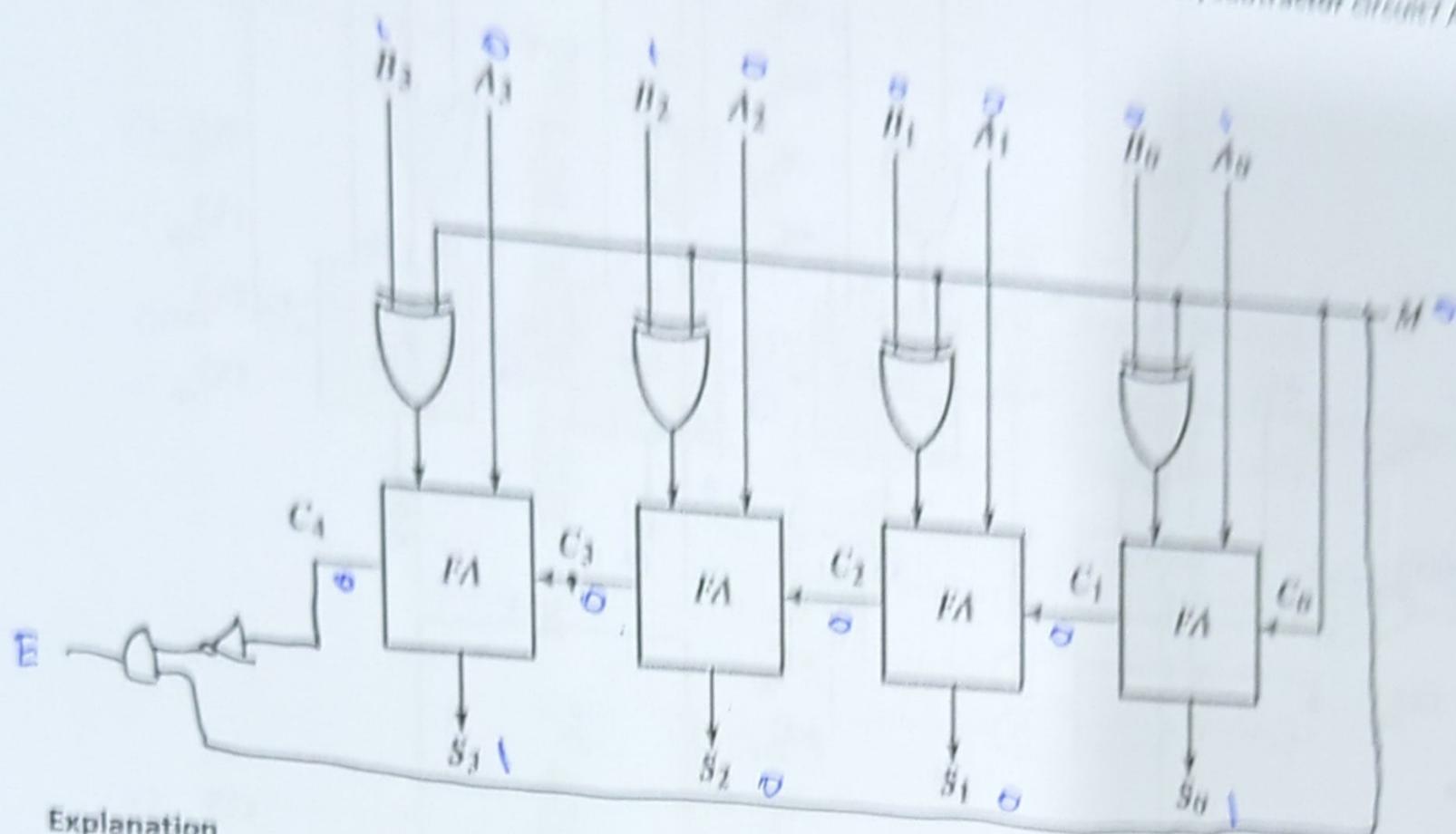
Final Ans } $C_4 S_3 S_2 S_1 S_0$

2. Convert following 4-bit adder to 4-bit subtractor circuit? So it could perform operation $A-B$? Use the circuit you have designed to subtract binary $(A_0 - A_3) - B$

HINT: could use 2's complement



3. Perform unsigned Addition $A+B$ where $A=1$ and $B=(A0)_2$ using following is 8-adder/subtractor circuit. Also write short explanation when $A+B$



Explanation

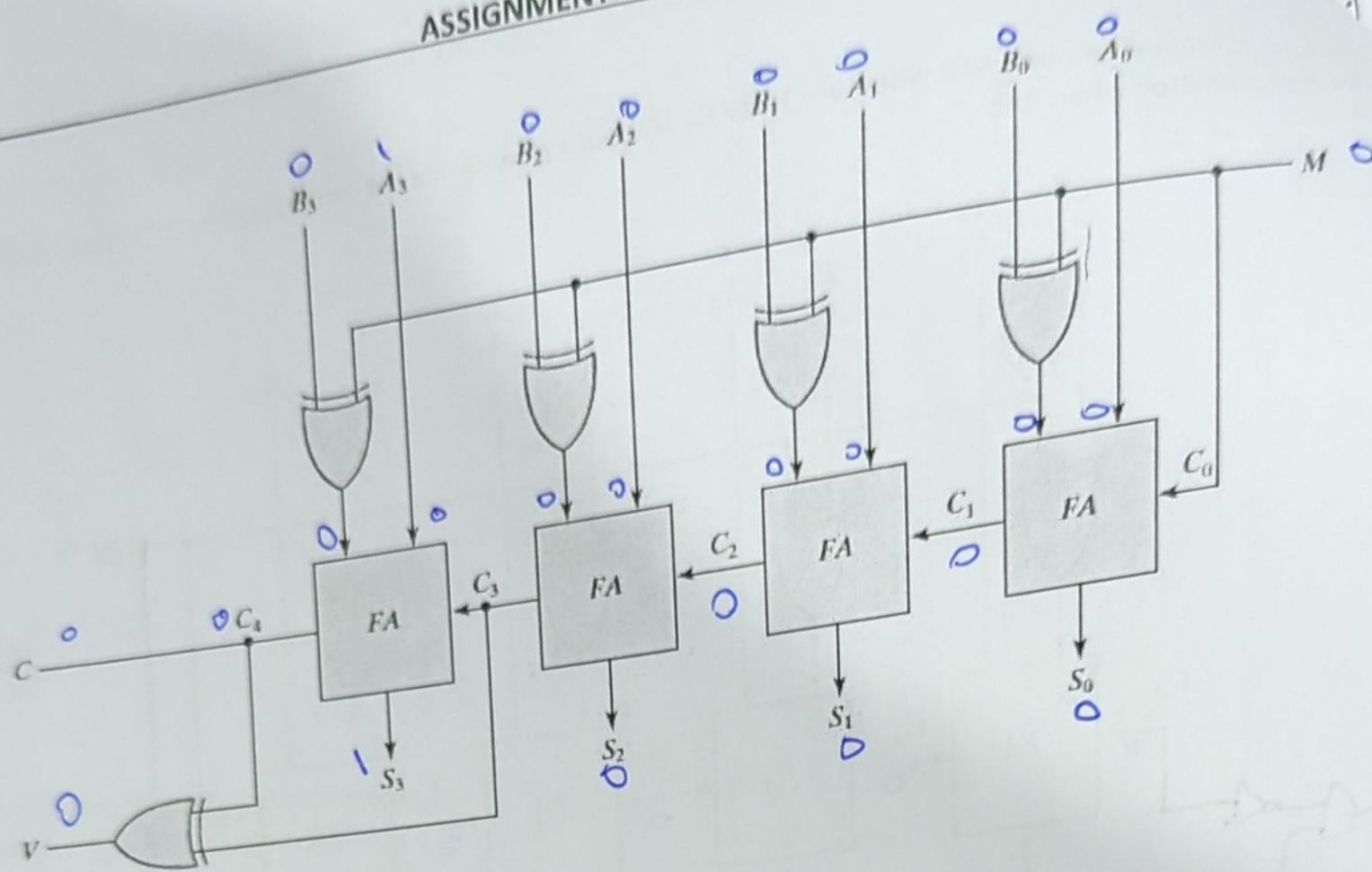
The circuit at the end (output E) shows if the result is in 2's comp form or not. If $A+B$ (addition), use 0 as no carry & the result is in 2's comp. form.

6. Perform signed Addition $A+B$ where $A=2$'s complement of $(A0)_2$ and $B=2$'s complement of $(A3)_2$ using following is 4-adder/subtractor circuit? Also write short explanation on working of overflow flag(v)

NOTE: If $A0 = 9$ which cannot be represented using 4-bit signed representation than consider it equal to 8, by taking 2's complement of $A0$ and $A1$ you are considering it as negative number

$$\begin{array}{l}
 A \rightarrow 8 \rightarrow 1000 \xrightarrow{\text{2's comp}} 0111 \backslash 000 \\
 B \rightarrow 0 \rightarrow 0000 \xrightarrow{\text{2's comp}} \backslash 0000
 \end{array}$$

ID:



Explanation

Overflow flag is associated with signed numbers. It represents signed overflow. The flag turns out either when there's a carry in or a carry out, but not both.

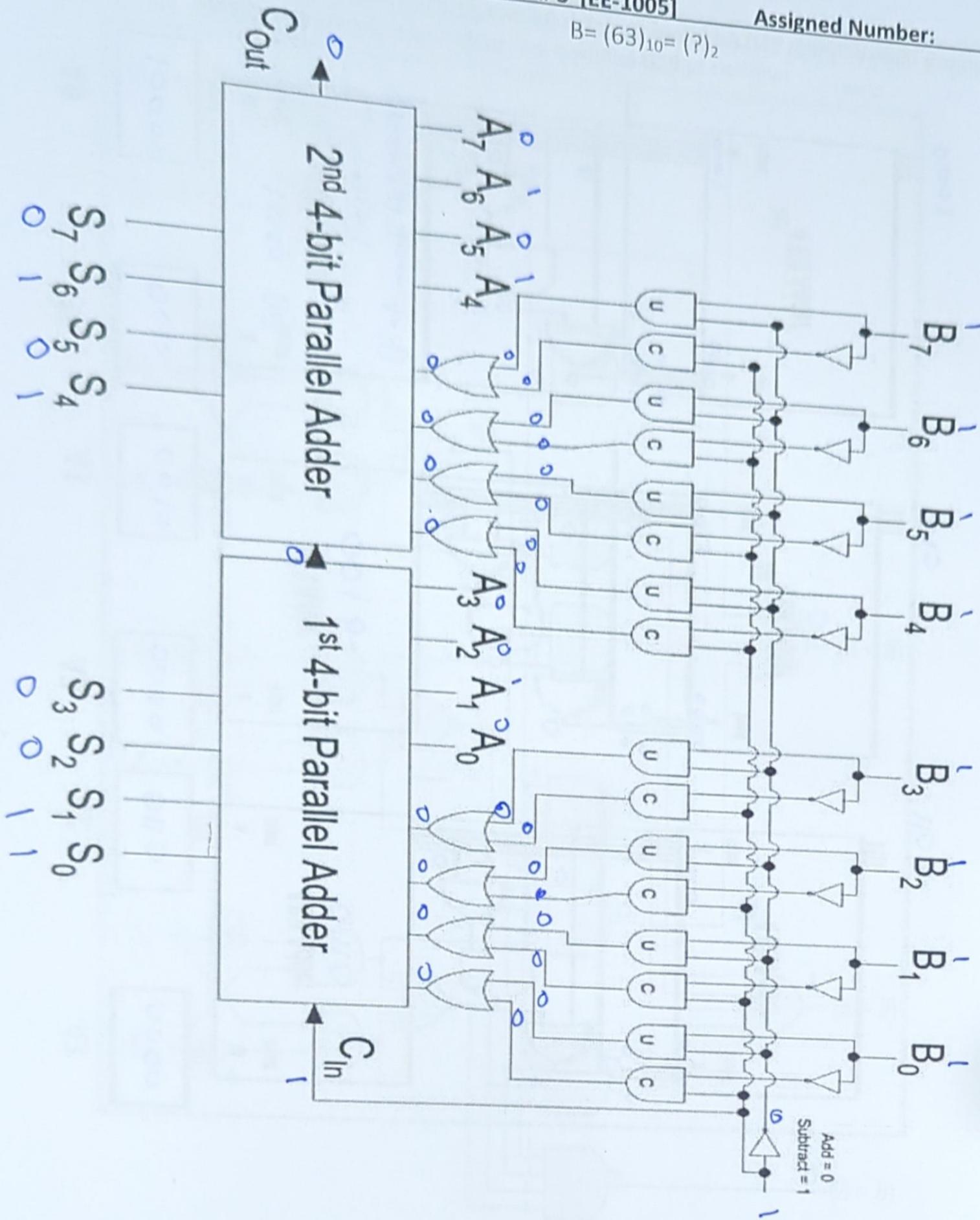
7. Following is an 8-bit unsigned adder/subtractor circuit. Apply following numbers to the circuit and perform A-B
 NOTE: where A = Binary of the first two digits of the assigned number

$$A \rightarrow (82)_{10} \rightarrow (1010010)_2$$

$$B \rightarrow (63)_{10} \rightarrow (11111)_2$$

$$A = (A_0 A_1)_{10} = (?)_2$$

$$B = (63)_{10} = (?)_2$$

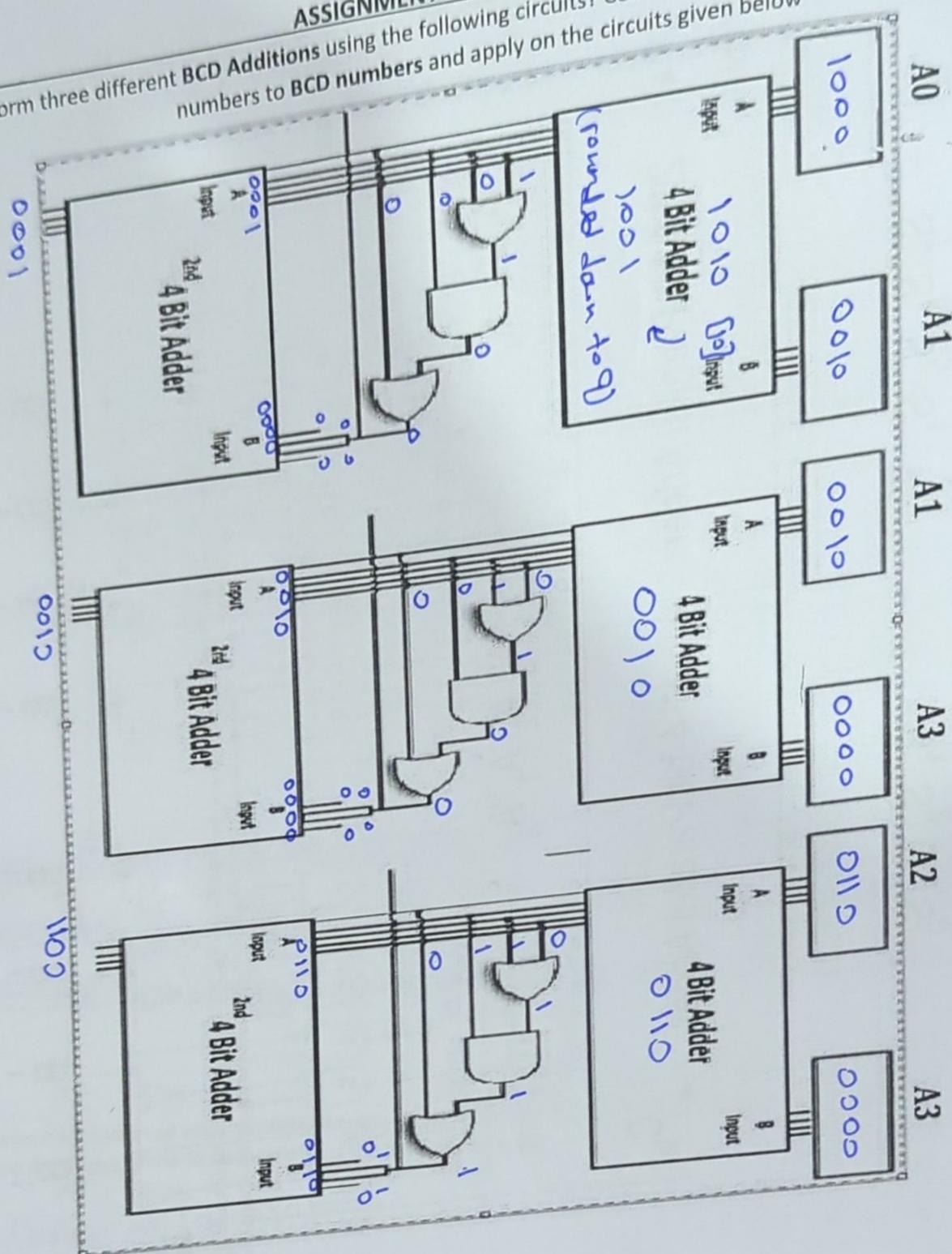


ASSIGNMENT # 1 [EE-1005]

Assigned Number:

- ID: 8. Perform three different BCD Additions using the following circuits? Convert A0, A1, A2, A3 from your assigned numbers to BCD numbers and apply on the circuits given below

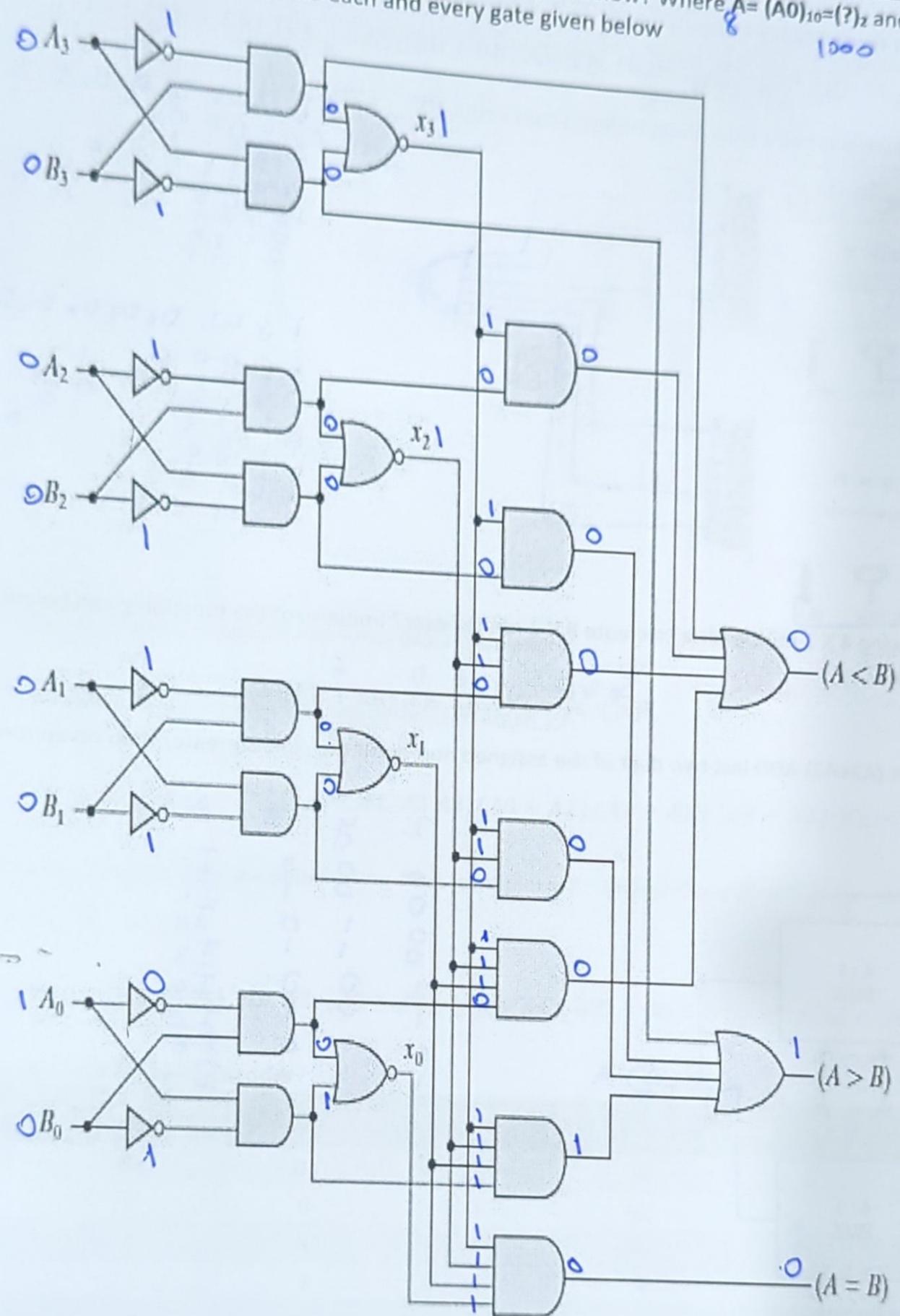
9. p



9. Process given values of A and B on a comparator circuit give below? Where $A = (A_0)_{10} = (?)_2$ and $B = (A_3)_{10} = (?)_2$
- NOTE: You are supposed to process each and every gate given below

ASSIGNMENT # 3 [EE-1005]

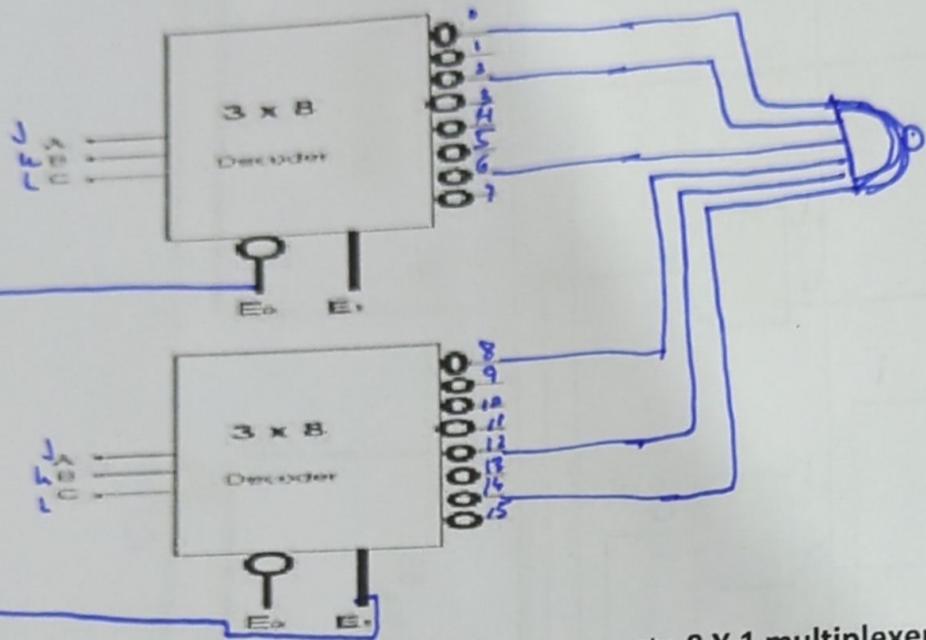
Assigned Number:

8
1000 0 0000

ID:

10. Implement the following function using two 3×8 Decoder. Also write expressions for output $O_0 - O_7$
 $(i, j, k, l) = \sum(2, A_0, A_1, A_2, A_3, (A_0 + A_2), (A_1 + A_3), (A_2 + A_3), 12, 14)$

NOTE: If any Sum is greater than 15 considered sum equal 10

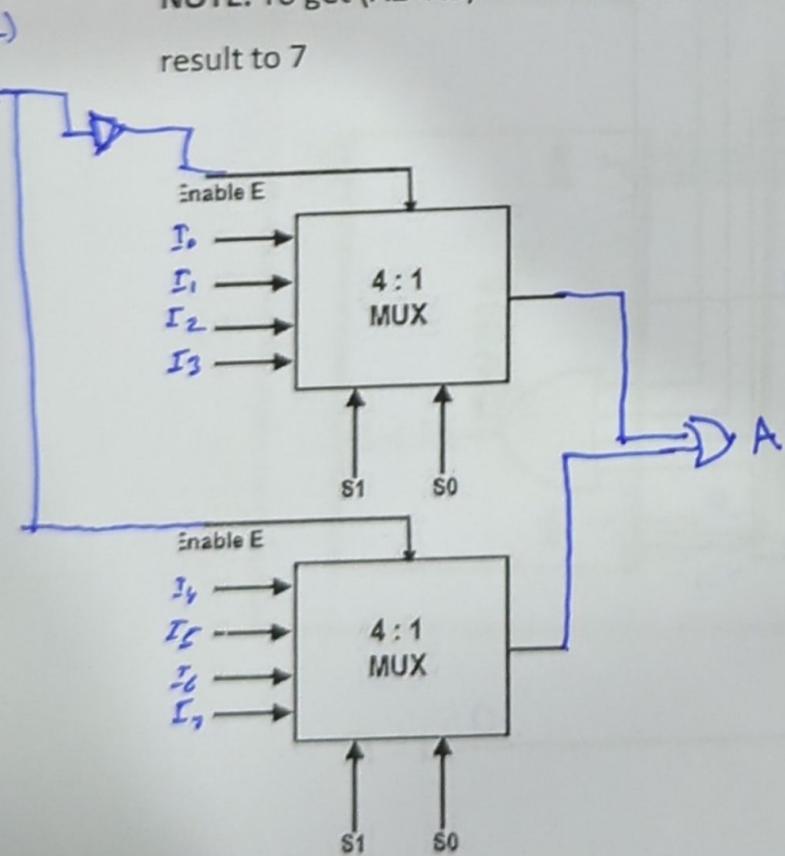


i	j	k	l	D ₀	D ₁	D ₂	D ₃	D ₄	D ₅	D ₆	D ₇
0	0	0	0	0	0	0	0	0	0	0	0
0	0	0	1	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0	0	0
0	0	1	1	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	0	0
0	1	0	1	0	0	0	0	0	0	0	0
0	1	1	0	0	0	0	0	0	0	0	0
0	1	1	1	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0	0	0	0
1	0	1	0	0	0	0	0	0	0	0	0
1	0	1	1	0	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0	0	0	0	0
1	1	0	1	0	0	0	0	0	0	0	0
1	1	1	0	0	0	0	0	0	0	0	0
1	1	1	1	0	0	0	0	0	0	0	0

11. Use following 4×1 multiplexer to create 8×1 multiplexer? Implement the function given below using 8×1 multiplexer?

$$F(x, y, z) = \sum(0, A_2, A_3, (A_2 + A_3), 6)$$

NOTE: To get $(A_2 + A_3)$ ADD last two digit of the assigned number if the sum is greater than seven force sum equal result to 7



Z	Y	X	A
0	0	0	I ₀
0	1	0	I ₁
1	0	0	I ₂
1	1	0	I ₃
0	0	1	I ₄
0	1	1	I ₅
1	0	1	I ₆
1	1	1	I ₇

12. Implement the function given below using 4×1 multiplexer?

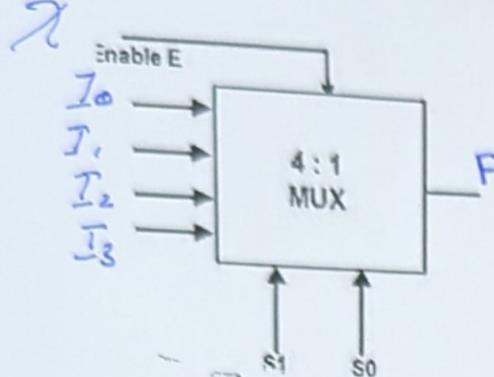
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ASSIGNMENT # 3 [EE-1005]

Assigned Number:

$$F(x, y, z) = \sum(0, A_2, A_3, (A_2 + A_3), 6, 0, 6)$$

NOTE: To get $(A_2 + A_3)$ ADD last two digit of the assigned number if the sum is greater than seven force sum equal result to 7



X	Y	Z	F
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	0

Implementation Table

	D ₀	D ₁	D ₂	D ₃
A	0	1	2	3
A	4	5	6	7
A	0	A	0	0

13. Implement the function given below using 8x1 multiplexer?

NOTE: If any Sum is greater than 15 considered sum equal 10

$$F(i, j, k, l) = \sum(2, A_0, A_1, A_2, A_3, (A_0 + A_2), (A_1 + A_3), (A_2 + A_3), 12, 14)$$

14. Implement the function given below using 16x1 multiplexer? NOTE: If any Sum is greater than 31 considered sum equal 31

$$F(a, b, c, d, e) = \sum(3, A_0, A_1, A_2, A_3, (A_0 + A_2), (A_0 + A_1), (A_0 + A_2 + A_3), (A_1 + A_2 + A_3), 17, 23, 27, 30)$$

15. Design 4X2 Low Priority Encoder?

IN				OUTPUT			Valid
D ₀	D ₁	D ₂	D ₃	A	B		
0	0	0	0	X	X	0	0
1	X	X	X	0	0	1	1
0	1	X	X	0	1	1	1
0	0	1	X	1	0	1	1
0	0	0	1	1	1	1	1

ID: SIMPLIFICATION AND EXPRESSIONS

For A:		3	2	1	0
D ₃	D ₂	00	01	11	10
00	X	1	1	1	
01		0	0	0	0
11		0	0	0	0
10		0	0	0	0

$$A = \overline{D_0} \overline{D_1}$$

For B:		3	2	1	0
D ₃	D ₂	00	01	11	10
00	X	1	0	0	
01	1	1	1	1	
11	0	0	0	0	0
10	0	0	0	0	0

$$B = \overline{D_0} \overline{D_2} + \overline{D_0} D_1$$

16. Design an 8 X 3 Low Priority Encoder? Priority Encoder you are designing A₂ and A₃ has the most priority respectively than the other input. Arrange other inputs in ascending order

TABLE

		INPUTS						OUTPUTS				VALID
A ₂ =4	A ₃ =6	0	1	2	3	5	7	0 ₀	0 ₁	0 ₂		VALID
1	X	X	X	X	X	X	X	1	0	0	1	
0	1	X	X	X	X	X	X	1	1	0	1	
0	0	1	X	X	X	X	X	0	0	0	1	
0	0	0	1	X	X	X	X	0	0	1	1	
0	0	0	0	1	X	X	X	0	1	0	1	
0	0	0	0	0	1	X	X	0	1	1	1	
0	0	0	0	0	0	1	X	1	0	1	1	
0	0	0	0	0	0	0	1	1	1	1	1	

SIMPLIFICATION AND EXPRESSION

$$O_0 = A_2 + \overline{A_2} A_3 + \overline{A_2} A_3 \overline{D_0} \overline{D_1} \overline{D_2} \overline{D_3} \overline{D_5} + \overline{A_2} \overline{A_3} \overline{D_0} \overline{D_1} \overline{D_2} \overline{D_3} \overline{D_5} + \overline{A_2} \overline{A_3} \overline{D_0} \overline{D_1} \overline{D_2} \overline{D_3} \overline{D_7}$$

$$O_1 = \cancel{A_2} \cancel{A_3} \overline{A_2} A_3 + \overline{A_2} \overline{A_3} \overline{D_0} \overline{D_1} D_2 + \overline{A_2} \overline{A_3} \overline{D_0} \overline{D_1} D_3 + \overline{A_2} \overline{A_3} \overline{D_0} \overline{D_2} \overline{D_3} \overline{D_7}$$

$$O_2 = \overline{A_2} \overline{A_3} \overline{D_0} D_1 + \overline{A_2} \overline{A_3} \overline{D_0} \overline{D_1} \overline{D_2} D_3 + \overline{A_2} \overline{A_3} \overline{D_0} \overline{D_1} \overline{D_2} \overline{D_3} D_5 + \overline{A_2} \overline{A_3} \overline{D_0} \overline{D_1} \overline{D_2} \overline{D_3} D_7$$

17. Design a single bit comparator from the following table? Write expression for $A=B$, $A>B$? Design circuit from expression using logic gates?

Input		Output		
A	B	$A < B$	$A > B$	$A = B$
0	0	0	0	1
0	1	1	0	0
1	0	0	1	0
1	1	0	0	1

Find Expressions:

$$A > B = A \bar{B}$$

$$A < B = \bar{A} B$$

$$A = B = \bar{A} \bar{B} + A B$$

18. Write expression for 6-bit comparator? $x_i = A_i B_i + \bar{A}_i \bar{B}_i$

$$A > B = A_5 \bar{B}_5 + x_5 A_4 \bar{B}_4 + x_5 x_4 A_3 \bar{B}_3 + x_5 x_4 x_3 A_2 \bar{B}_2 + x_5 x_4 x_3 x_2 A_1 \bar{B}_1 + x_5 x_4 x_3 x_2 x_1 A_0 \bar{B}_0$$

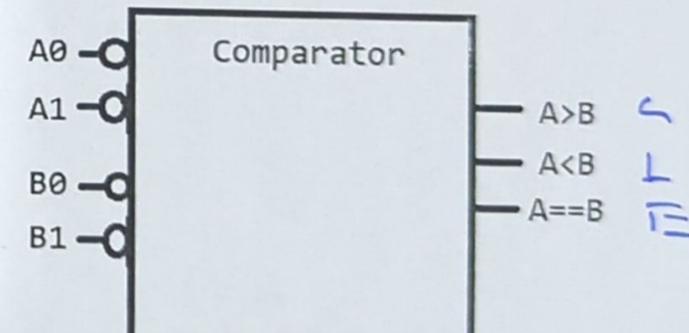
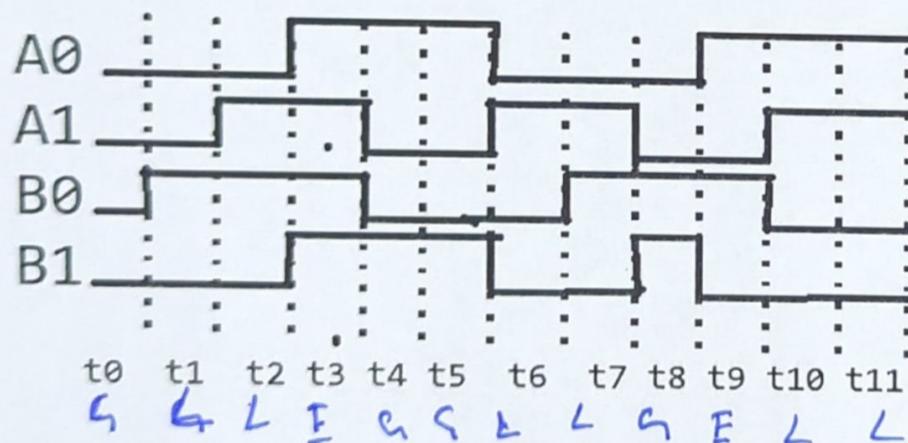
$$A < B = \bar{A}_5 B_5 + x_5 \bar{A}_4 B_4 + x_5 x_4 \bar{A}_3 B_3 + x_5 x_4 x_3 \bar{A}_2 B_2 + x_5 x_4 x_3 x_2 \bar{A}_1 B_1 + x_5 x_4 x_3 x_2 x_1 \bar{A}_0 B_0$$

$$A = B = x_5 x_4 x_3 x_2 x_1 x_0$$

19. Given the waveforms and circuit of a comparator. Draw the output waveform for each output. Note that the circuit is active low input.

$$A = A_1 A_0$$

$$B = B_1 B_0$$

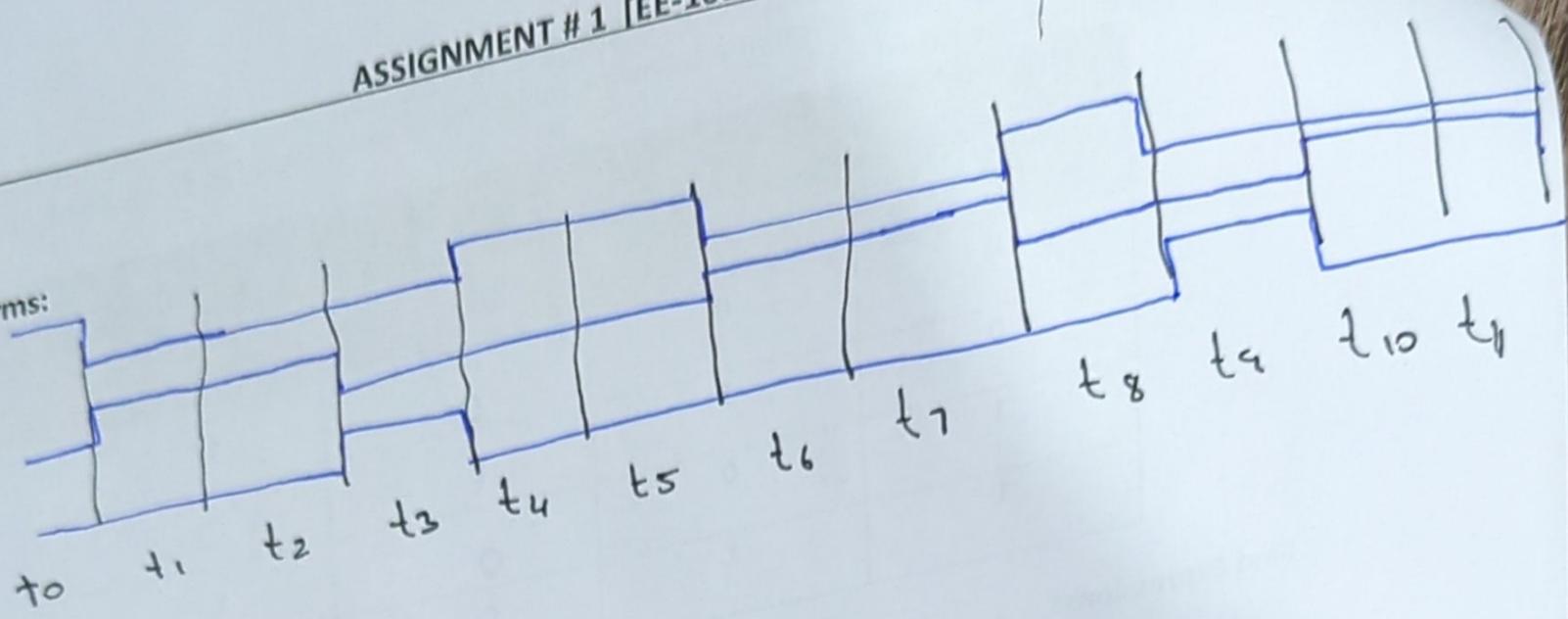


$A > B$ & $A < B$ (A & L)

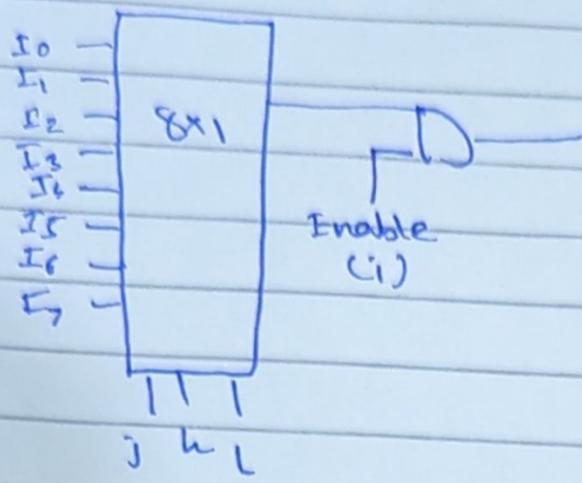
swap due to active low inputs

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Waveforms:

 $A > B =$ $A < B =$ $A == B =$ 

$$3) f(\text{lijst, hsl}) = \{2, 8, 2, 6, 0, 14, 6, 9, 12, 14\}$$



14) 16×1 Mux

$$F(a, b, c, d, e) \in \{3, 8, 2, 6, 0, 10, 16, 8, 17, 23, 27, 30\}$$

