# CSE 2421 HW5 Assembler Execution Homework 22 points

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Start in class, due midnight the next class day. You may work in groups, but each person must turn in their own.

Given the partial machine state on the next page, walk through each instruction and determine what changes when each instruction is executed.

If a register changes:

* List the name of the register that changed
* Give the new value it has
* Change the value in the register state table (top of next page)

If a memory location changes:

* list the address in hex
* Give the new value
* Give the size in bytes of the new value
* Change the value at that location in the memory table (bottom of next page)

Numbers are decimal unless marked with a leading 0x to denote hex. You do not need to indicate changes to the condition codes such as zero and overflow in this homework. When done, answer the 3 questions on this side of the paper.

For example…

If the given machine state is:

%rax = 1

%rsp = 0xC0B0A090

You would add the text in italics in the comment

pushq %rax # \_\_\_*memory 0xC0B0A088 = 1 (8bytes), %rsp = 0xC0B0A088*

…end of example.

**Answer these after doing the questions on the other side**

Q1: What will happen if ret is the next instruction? Why?

Ends the function and continues execution at the point function was called by popping return address from stack

Q2: Does this code initialize an array of 2 longs to 0 and 1?

No

Q3: If not, what single fix makes this code initialize an array of two longs to 0 and 1?

movq %rdi, (%rax, %rdi, ~~8~~ 1)

For this homework, start with this partial machine state: (Keep old values by crossing them out and put new values in after the crossed out old value). Use the ~~strikethrough~~ font effect to cross out old values as shown for rax (current value is 1, old value was 256). **This table is graded**

|  |  |
| --- | --- |
| Register | Value |
| rax | ~~256~~ ~~1~~ 0xC0B0A088 |
| rdi | ~~0~~ 1 |
| rsp | ~~0xC0B0A090 0xC0B0A088 0xC0B0A078~~ 0xC0B0A090 |
| rbp | ~~0xC0B0A0A0 0xC0B0A088~~ 0xC0B0A0A0 |

Assume the instruction “**call hw5**” was the last instruction that executed prior to this. This tells you something important about memory – go make that change to the memory table below. Execute each line of the following code and tell what changed:

**hw5:**

pushq %rbp # decrement rsp by 8 and %rbp is pushed to stack; 0xC0B0A088 = 0xC0B0A0A0

movq %rsp, %rbp # copy stack pointer value to %rbp

leaq (%rsp), %rax # copies address of %rsp into %rax

subq $16, %rsp # subtract 16 from %rsp

movq %rdi, (%rax, %rdi, 8) # move value of %rdi into (%rax + %rdi \* 8); 0xC0B0A088 = 0

addq $1, %rdi # increment %rdi by 1

movq %rdi, (%rax, %rdi, 8) # move value of %rdi into (%rax + %rdi \* 8); 0xC0B0A090 = 1

leave # restore %rbp from stack and adjust %rsp

This table is a scratch pad where you keep track of what is in memory. **It will be graded.** The addresses are on 8 byte boundaries. If you know the meaning of a location but not its value, write that meaning (such as “return address”). If you know both, tag the numeric value with the meaning, such as “old rbp.” Use the ~~strikethrough~~ font effect on the old value if you change any value

|  |  |
| --- | --- |
| Address | Value |
| 0xC0B0A0A0 | (old rbp=0xC0B0A0A0) |
| 0xC0B0A098 |  |
| 0xC0B0A090 | 1 |
| 0xC0B0A088 | ~~(old rbp=0xC0B0A0A0)~~ 0 |
| 0xC0B0A080 |  |
| 0xC0B0A078 |  |
| 0xC0B0A070 |  |