Computer Architecture Lab 3 Clock—HCS12

im Studiengang Technische Informatik der Fakultät Informationstechnik

Wintersemester 2022

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January 20, 2023

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1 Readme

1.1 General

The lab was worked on by **Nikita Tetzlaff** and **Fabian Zaiser**. It was examined by Profs Keller and Zimmermann on the 17.01.2023. The problems which occurred on the hardware were fixed with some inspiration by Prof Keller and the program is now working as described in the requirements chapter. The program was tested on 18.01.2023 and was now running on the hardware in the lab as it was intended.

1.2 Work distribution

> Nikita Tetzlaff:

- sampleDCF77() function
- buttonACtion() function
- setClock() function
- flowcharts
- global variables list

> Fabian Zaiser:

- processDCF77() function
- DecodeTransmission() function
- checkParity() function
- checkButton() function
- tick10ms() function modified
- ifdefs for automatic toggling between simulation and on hardware testing so no code has to be modified when switching between simulation and testing on hardware
- essembled the documentation in LaTeX

2 Functional requirements

2.1 Software requirements

- The first line of the Board shall display the time in the format hours: minutes: seconds
- The second line shall display the date in the format day .month .year
- The clock shall continue working as in the lab before when no radio transmission is received or it is failed
- As in the previous lab the B.0 LED shall toggle every second
- The B.1 LED shall be turned on when the signal of the DCF77 is low and shall be turned off when the Signal is high
- The LED on B.3 shall light up when the time and date has been decoded. It is turned off if the transmission fails.
- the DCF77 signal shall be polled every 10ms
- the Weekday MON(day), TUE(sday), ... shall be displayed infront of the date.
- pressing the H3 button shall toggle between European Time and US eastern timezone (European time - 6hrs)
- The displayed timezone shall be indicated by either "DE" or "US" on the LCD

3 User Interface of the program

3.1 LCD Display

• 1st line: timezone and time display

• 2nd line: weekday and date display

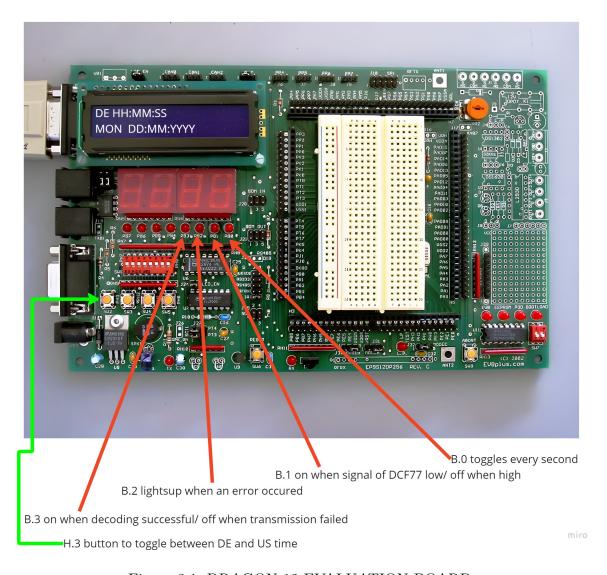


Figure 3.1: DRAGON-12 EVALUATION BOARD

3.2 LED display

- LED0: toggles once per second
- LED1: on when signal is low and off when signal is high
- LED2: is on when transmission of the signal has failed
- LED3: lights up when decoding was successful

3.3 Operating buttons

• SW2 (H.3): pressing H.3 toggles between the timezones

4 Module overview

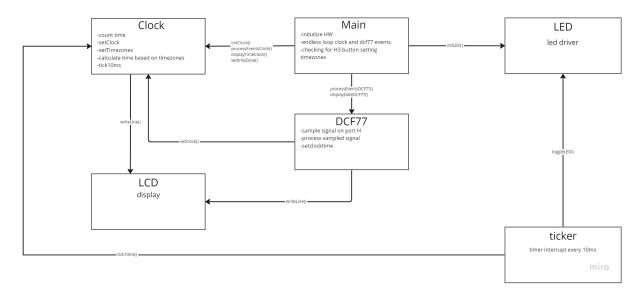


Figure 4.1: overview over all modules

5 Data dictionary

5.1 List of all global variables

Module where variable is declared	Variable name	C like datatype	Purpose					
			Variable holding the					
clock	clockEvent	CLOCKEVENT	last clock event					
	hours	static char	Hours (as numbers)					
	m inutes	static char	Minutes (as numbers)					
	seconds	static char	Seconds (as numbers)					
	uptime	static int	software timer					
	2 C. (1) (1) (1)	A CONTRACTOR OF THE CONTRACTOR	ticks used to calculate					
	ticks	static int	elapsed time					
	timezone_curr	static int	current timezone flag					
			flag if timezone has					
			been changed since					
	timezone Changed	extern int	last setClock					
			Variable holding the					
dcf77	dcf77Event	DCF77EVENT	last DCF77event					
			decoded year based on					
	dcf77Year	int	DCF77-signal					
		1-1/2	decoded month based					
	dcf77Month	int	on DCF77-signal					
	A CONTRACT OF STREET	11 111	decoded Weekday					
	dcf77Weekday	int	based on DCF77-signal					
			decoded day based on					
	dcf77Day	int	DCF77-signal					
			decoded hour based					
	dcf77Hour	int	on DCF77-signal					
			decoded minute based					
	dcf77Minute	int	on DCF77-signal					
			Array holding the					
	11111111111111111		shorthand symbols for					
	weekdayArray	static char*	weekdays					
	1 1 - 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		array storing the bits					
			of the last signals					
			(needed to eleminate					
	lastSignals	static int [10]	spikes in signal)					
			Blocks processing of					
			event while					
	state Machine Enabled	int	dcf77Event is invalid					
			array to store the					
			transmissioned signal					
	1111111 - 1111	210.000	and used to check					
	transm ission	int [59]	parity					
	A CONTRACTOR OF THE CONTRACTOR	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	index to walk over					
	transm Index	int	array transmission					
			Blocks processing of					
			event while					
	state Machine Enabled	int	dcf77Event is invalid					

Figure 5.1: list of global variables

5.2 Hardware resources

Module	HCS12 or Dragon 12 HW resource	Purpose						
Clock	CPU Port K	Display: Line 1; display time						
	LCD Display	Line 2: display date						
LED	Port B	Various status signals						
[clock][DCF77]	LEDs							
Clock	Port H.3	Toggle european time						
[buttons]	Buttons SW2	and US east time						
Ticker	Enhanced Capture Timer	10 ms ticker						
[clock]	Channel 4							

Figure 5.2: list of hardware resources $\frac{1}{2}$

6 Flowcharts of all modules

6.1 main.c

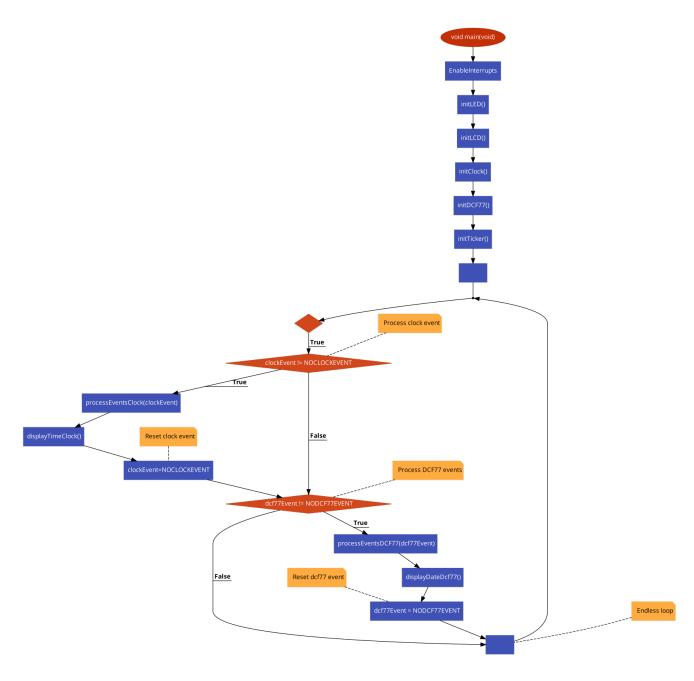


Figure 6.1: main() flowchart

6.2 clock

6.2.1 tick10ms

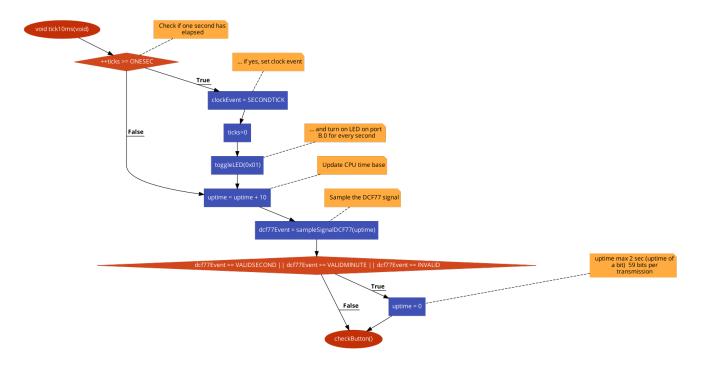


Figure 6.2: tick10ms flowchart

6.2.2 processEventsClock



Figure 6.3: processEventsClock flowchart

6.2.3 setClock

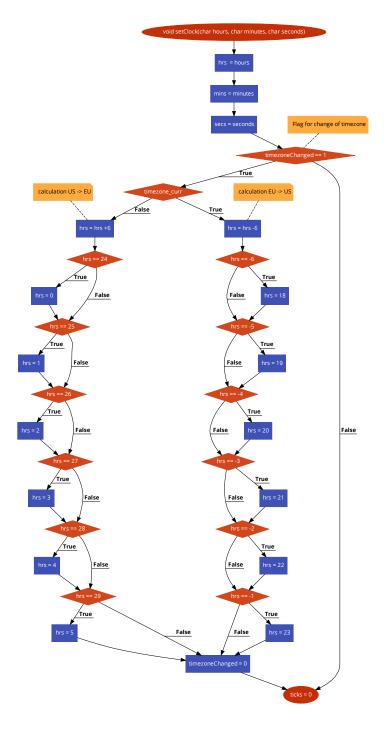


Figure 6.4: setClock flowchart

6.2.4 displayTimeClock

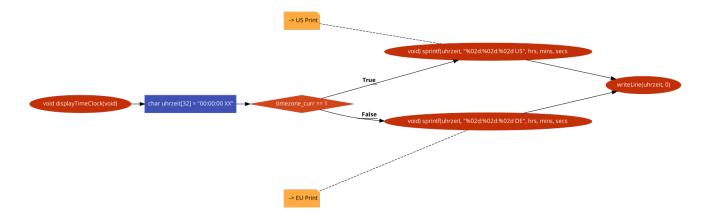


Figure 6.5: displayTimeClock flowchart

6.2.5 settimeZone

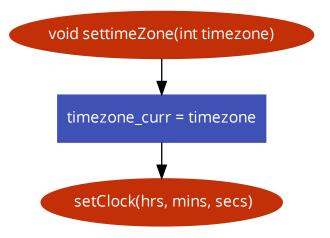


Figure 6.6: settimeZone flowchart

6.3 button

6.3.1 checkButton

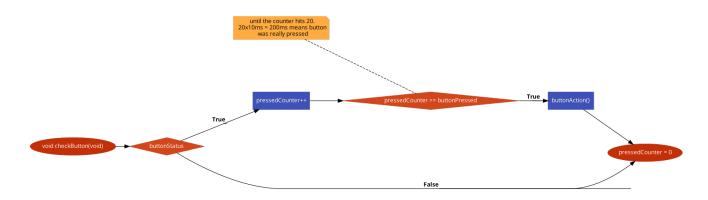


Figure 6.7: checkButton flowchart

6.3.2 buttonAction

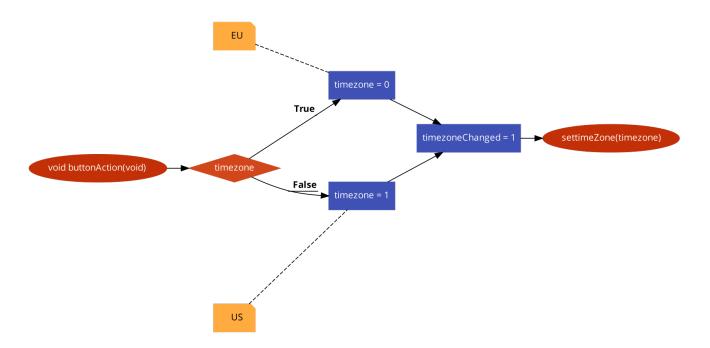
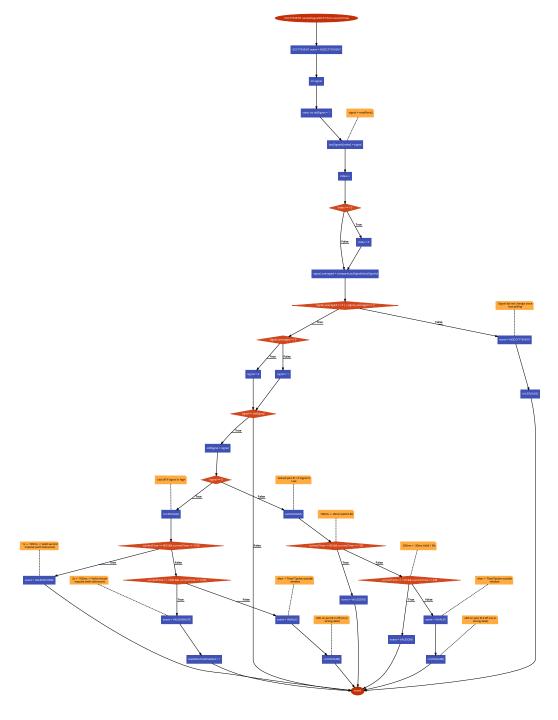


Figure 6.8: buttonAction flowchart

6.4 DCF77

6.4.1 sampleSignalDCF77



 ${\bf Figure~6.9:~sample Signal DCF77~flow chart}$

6.4.2 compareLastSignals

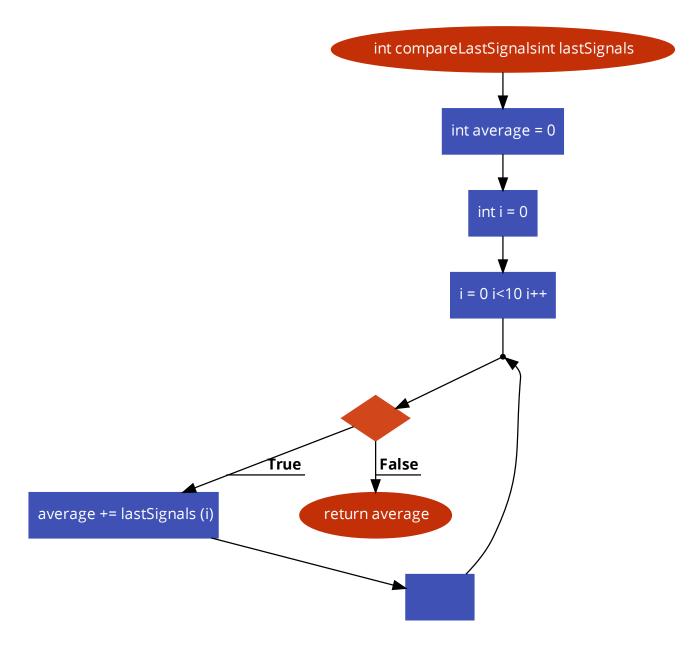
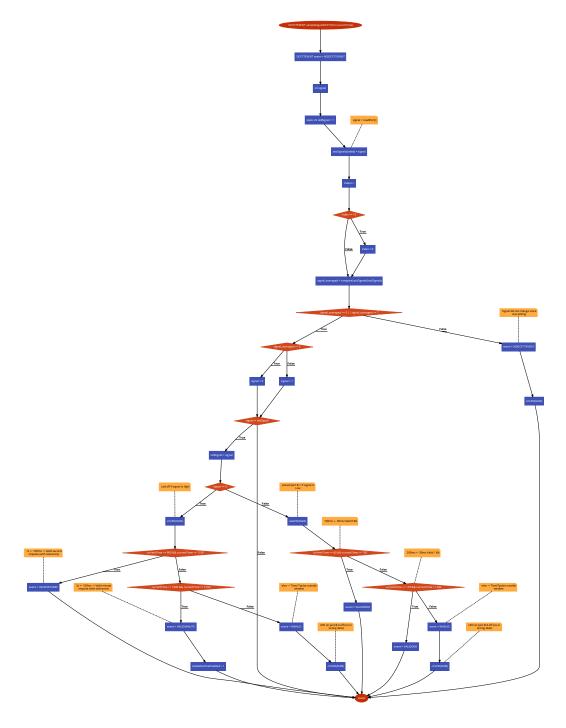


Figure 6.10: compareLastSignals flowchart

6.4.3 processEventsDCF77



 ${\bf Figure~6.11:~processEventsDCF77~flowchart}$

6.4.4 DecodeTransmission

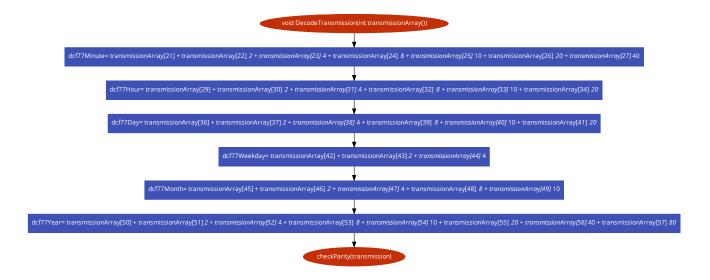


Figure 6.12: DecodeTransmission flowchart

6.4.5 checkParity

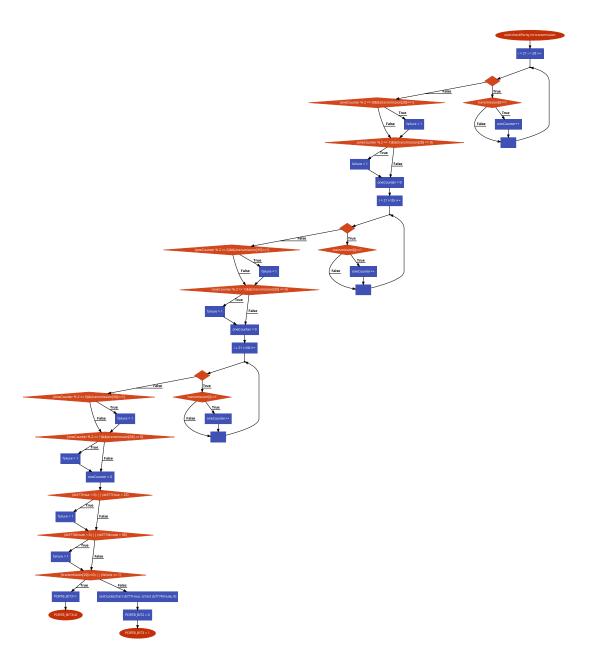


Figure 6.13: checkParity flowchart