A4.1.5   B, BL   DDI 01001

31 28	27	26	25	24	23
cond	1	0	1	L	

B{L}{<cond>}   <target_address>

```
if ConditionPassed(cond) then
    if L == 1 then
        LR = address of the instruction after the branch instruction
    PC = PC + (SignExtend_30(signed_immed_24) << 2)
```