




EFR32 Mighty Gecko Radio Board
2.4 GHz / 868 MHz 10 dBm, VDCDC to PAVDD

Board Function	Page
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RF, Antenna & Power	2
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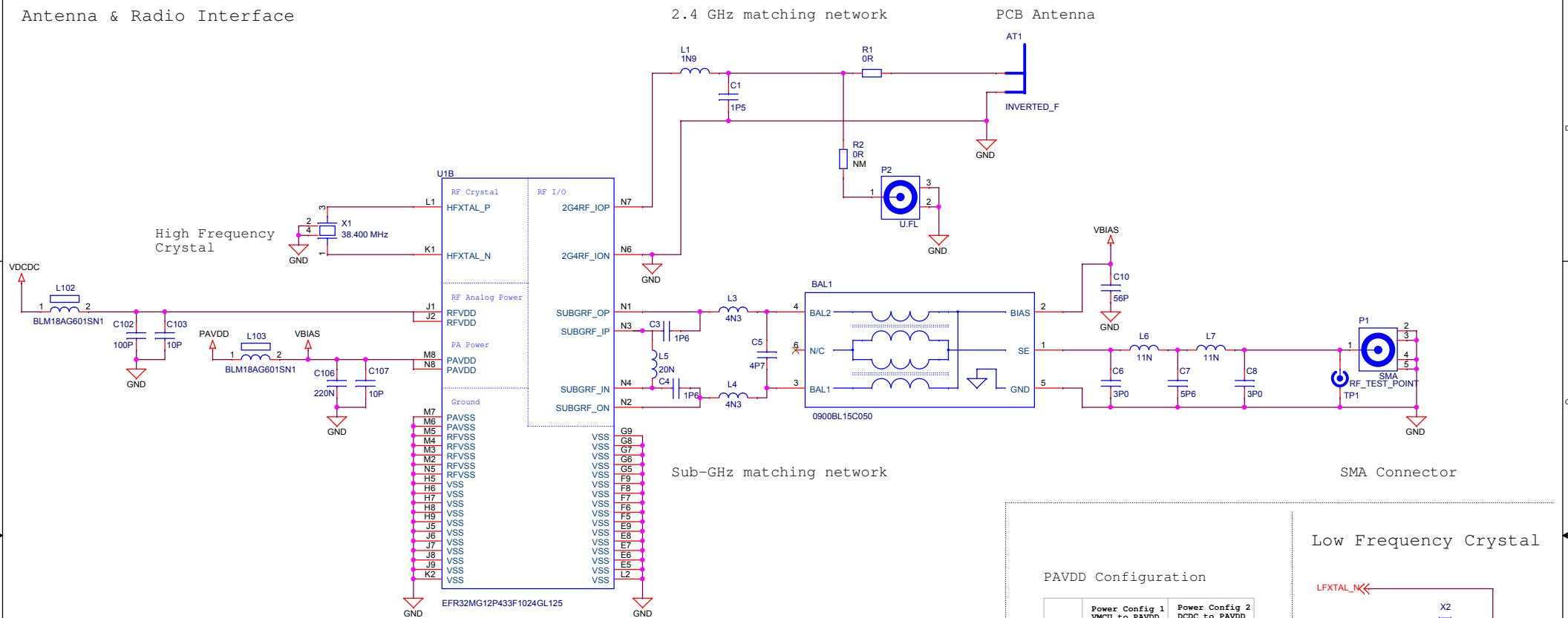
Revision History	
Rev.	Description
A00	Initial version.
A01	Updated EFR32 chip revision and 32 kHz crystal part number.
A02	Fixing EFR32 pin names; Updated PCB revision.

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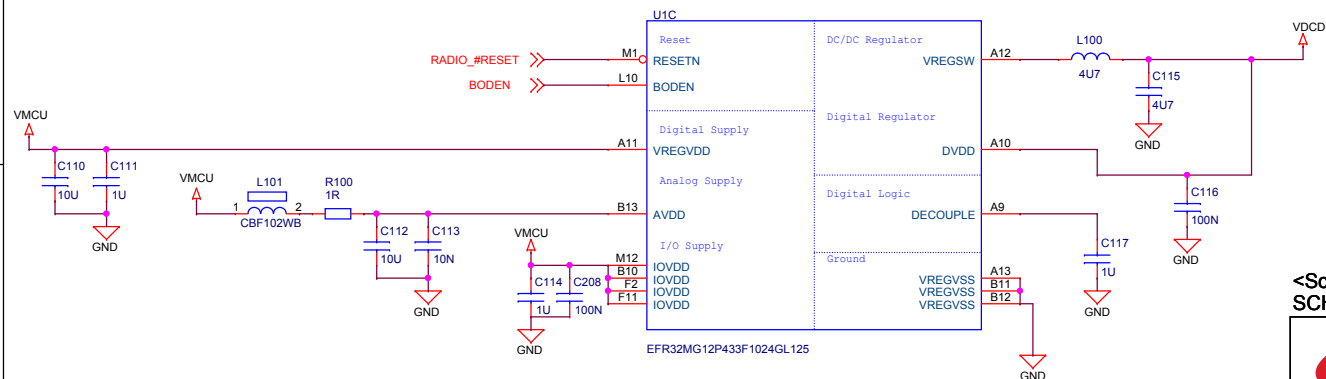
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Size A3	BOM Doc No: <Cage Code>	Document number BRD4163A	Revision A02
Design Created Date: Monday, April 02, 2012		Sheet Created Date Monday, April 02, 2012	Sheet Modified Date Friday, January 20, 2017
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Antenna & Radio Interface

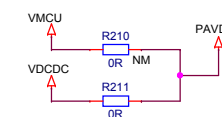


Power & Decoupling

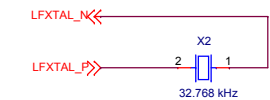


PAVDD Configuration

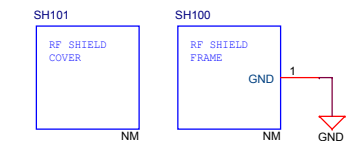
	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



Low Frequency Crystal



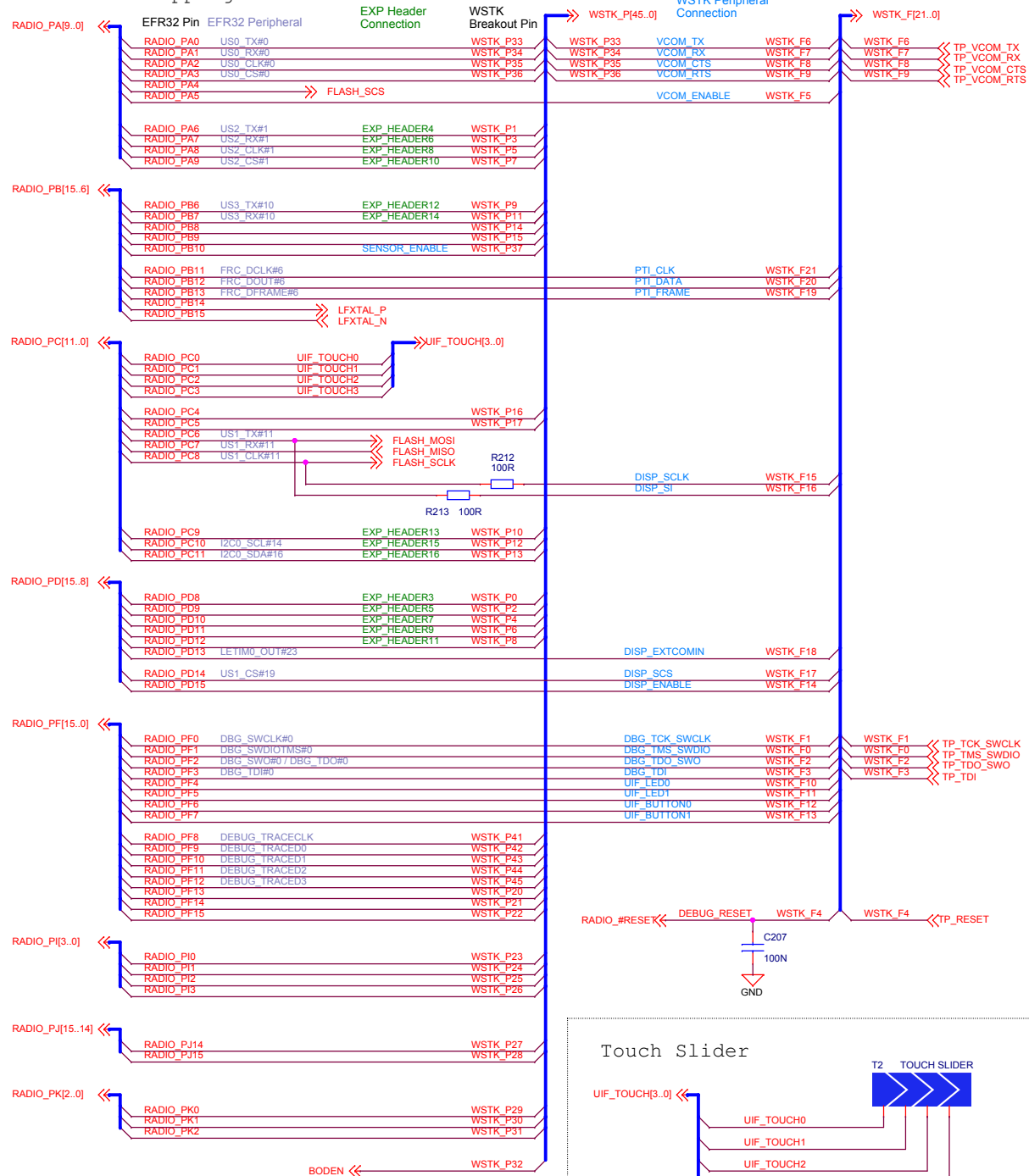
RF Shielding



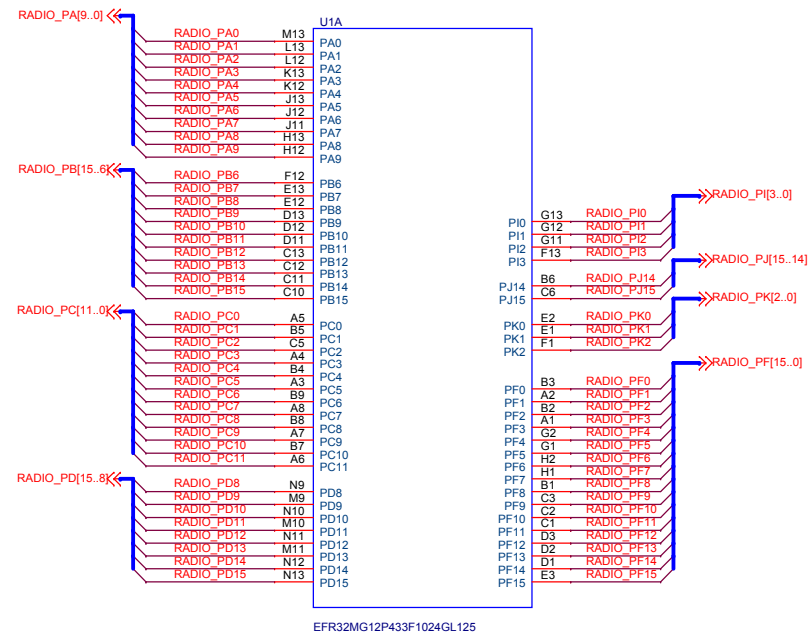
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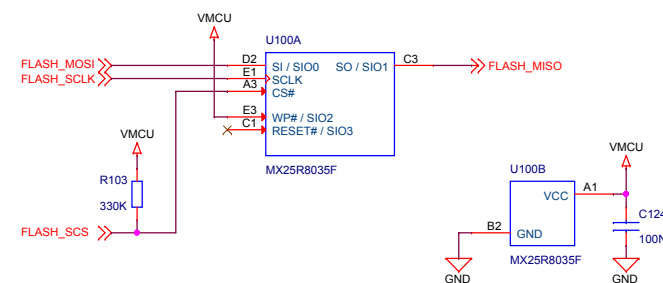
EFR32 Pin Mapping



EFR32 I/O Port Pins



Serial Flash



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Monday, April 02, 2012

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EFR32 I/O Port Connections

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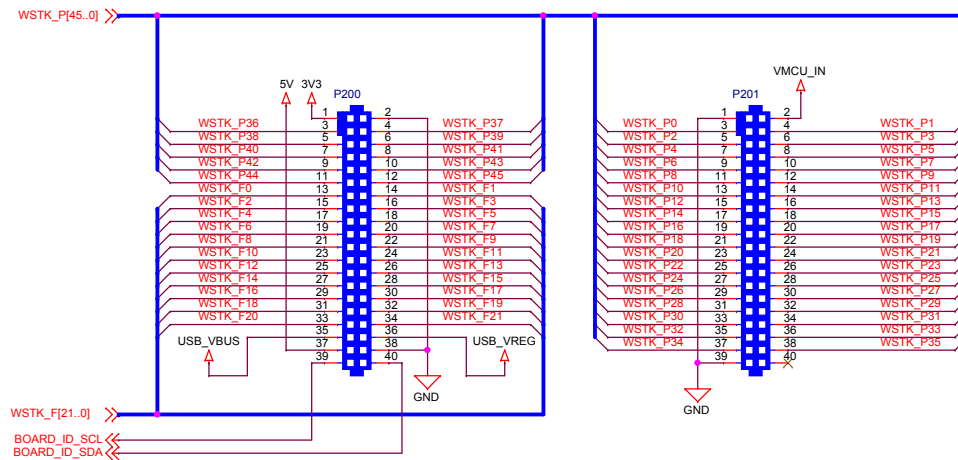
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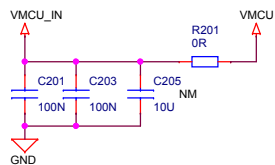
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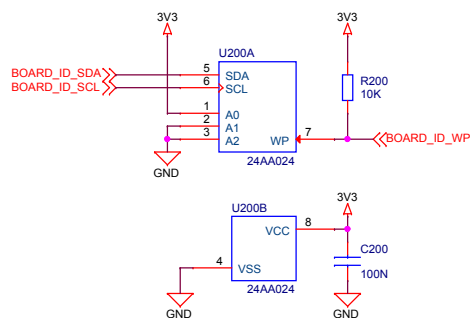
WSTK Connectors



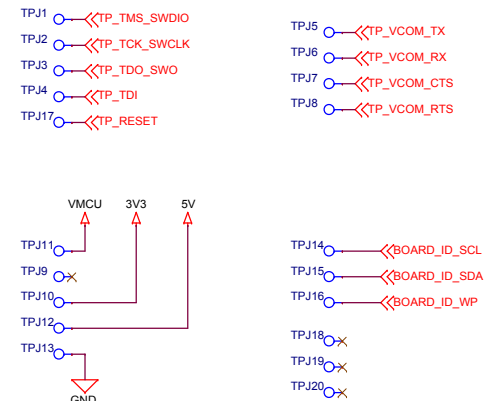
WSTK Power Decoupling




Board Identification



Test Points



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