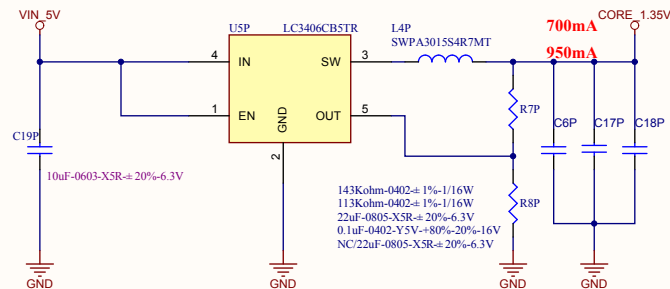
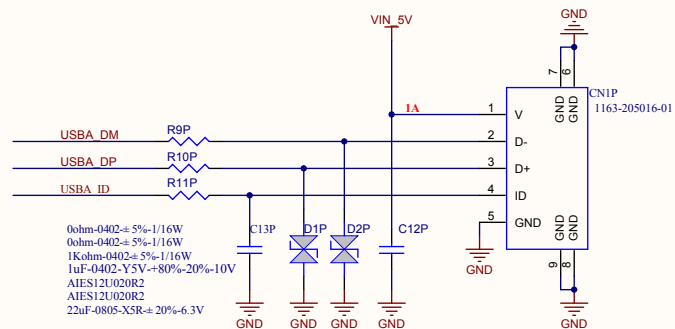
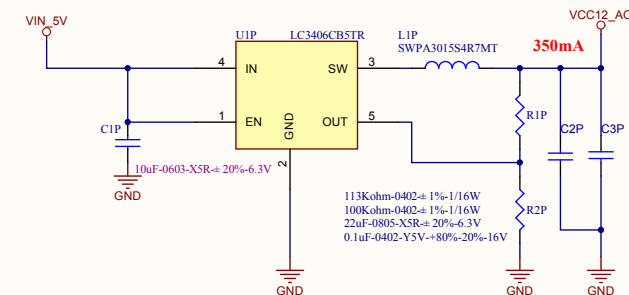
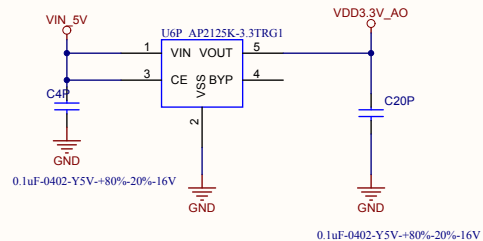
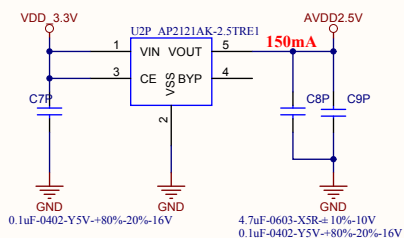


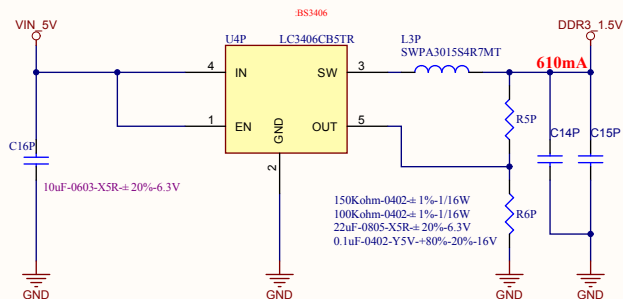
USB



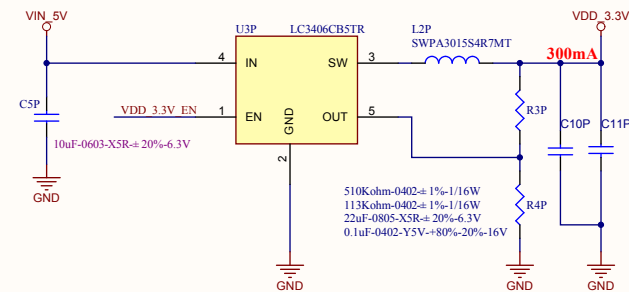
1.344V



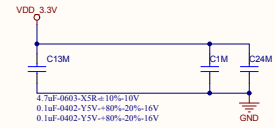
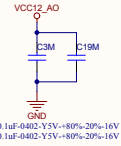
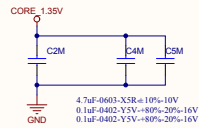
1.278V



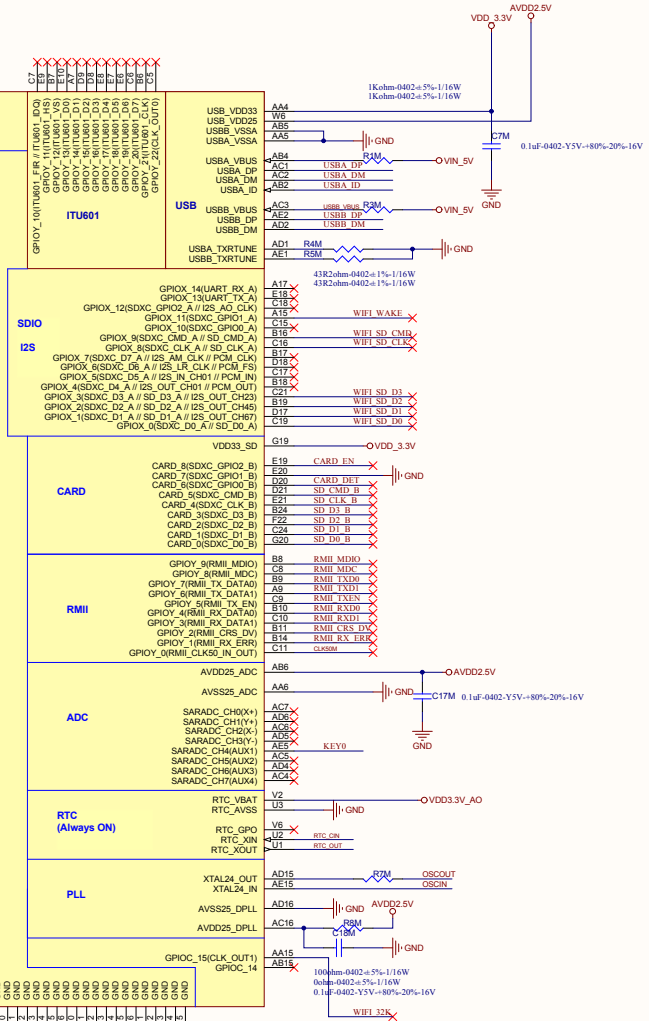
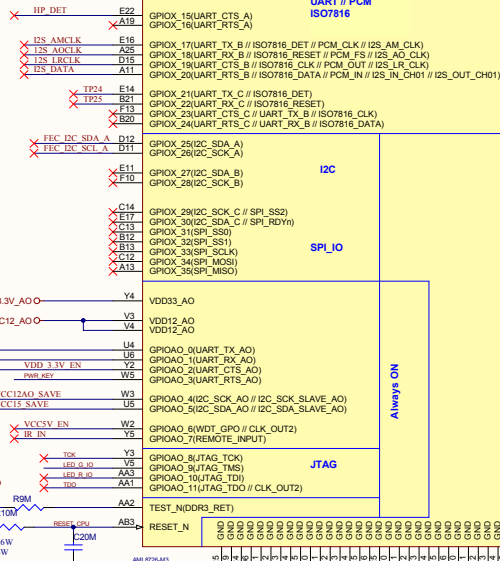
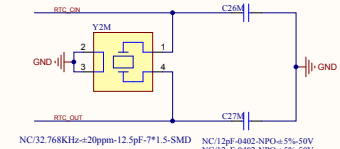
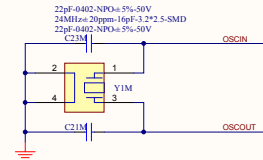
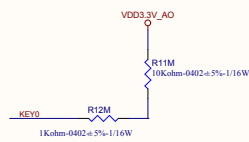
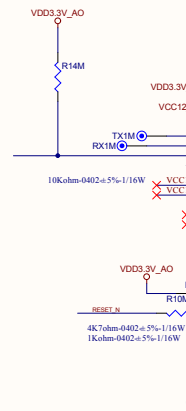
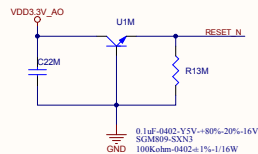
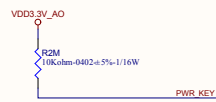
1.50V



3.308V



PCB

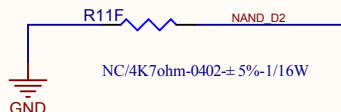


Power ON Config

NAND_D6 NAND_D5
1 1 Boot from SD_B

NAND_ALE
1 JTAG ENABLE
0 JTAG DISABLE

NAND_D7
1 ROM BOOT (NAND/iNand/SD_card)
0 NAND Boot From SPI_A
1 ROM BOOT (NAND/iNand/SD_card)
0 Boot From SPI_A
NAND_D4 NAND_D3 NAND_D2
1 1 1 Boot from nand without RB
1 1 0 Nand with RB
1 0 X SPI nor flash romboot
0 1 1 Boot from iNAND uses SDIOB
0 1 0 Boot from iNAND uses SDIOB
0 0 1 Boot from iNAND uses SDIOA



NAND_D2&D3&D4&D5&D6&D7

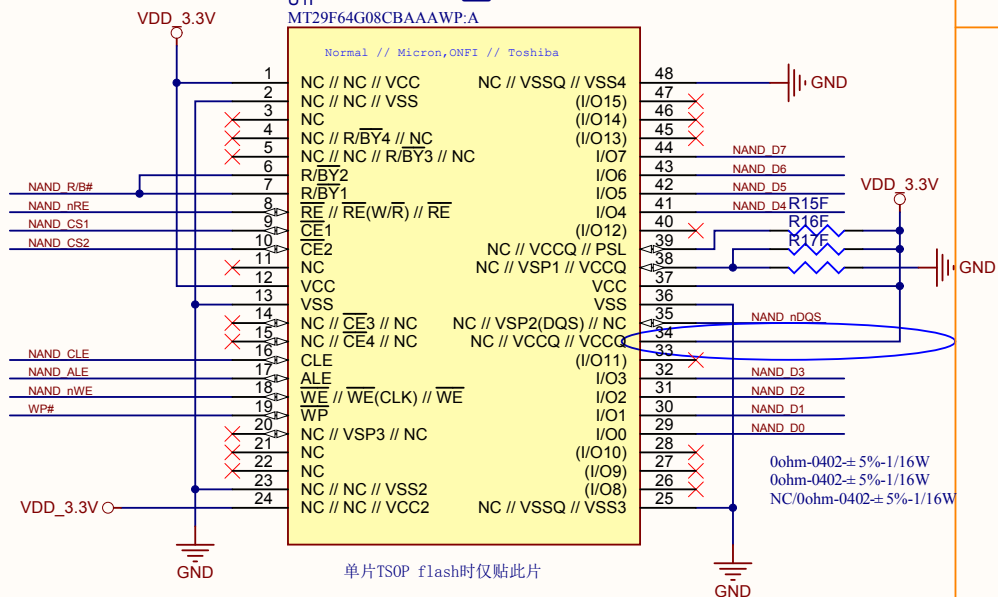
ALE

Boot from nand without RB

TSOP_NAND

U1F
MT29F64G08CBAAWP:A

Normal // Micron, ONFI // Toshiba



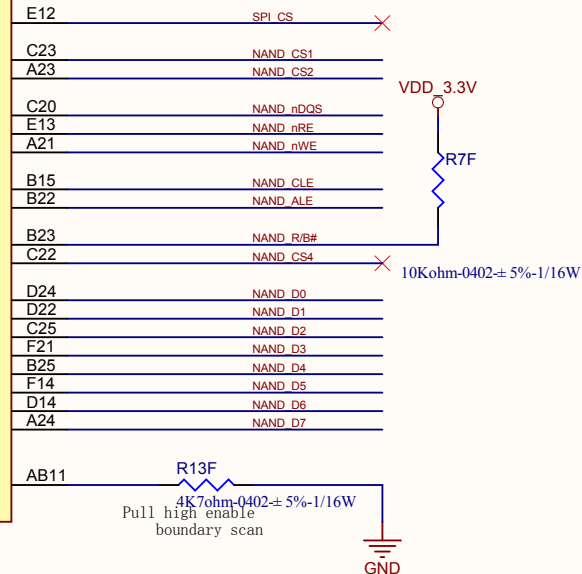
单片TSOP flash时仅贴此片

U5C AML8726-M3

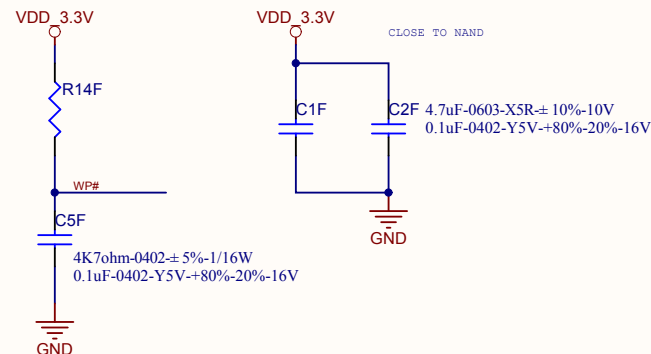
NAND&iNAND&SPI FLASH I/F

BOOT_17(SPI_NOR_CS_n_A)
BOOT_8(NAND_CE0)
BOOT_9(NAND_CE1)
(Lib Version:1121)
BOOT_16(NAND_DQS)
BOOT_15(NAND_Ren_WR)
BOOT_14(NAND_WEn_CLK // SPI_NOR_C_A)
BOOT_13(NAND_CLE // SPI_NOR_Q_A)
BOOT_12(NAND_ALE // SPI_NOR_D_A)
BOOT_10(NAND_CE2 // NAND_RB0 // SDXC_CMD_C)
BOOT_11(NAND_CE3 // NAND_RB1 // SDXC_CLK_C)
BOOT_0(NAND_IO_0 // SDXC_D0_C)
BOOT_1(NAND_IO_1 // SDXC_D1_C)
BOOT_2(NAND_IO_2 // SDXC_D2_C)
BOOT_3(NAND_IO_3 // SDXC_D3_C)
BOOT_4(NAND_IO_4 // SDXC_D4_C)
BOOT_5(NAND_IO_5 // SDXC_D5_C)
BOOT_6(NAND_IO_6 // SDXC_D6_C)
BOOT_7(NAND_IO_7 // SDXC_D7_C)

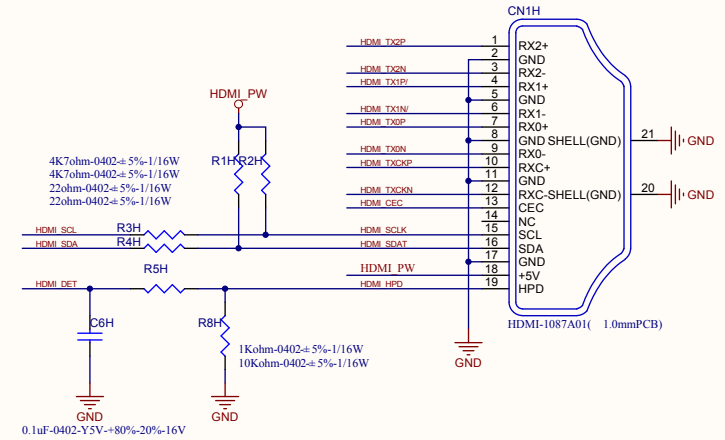
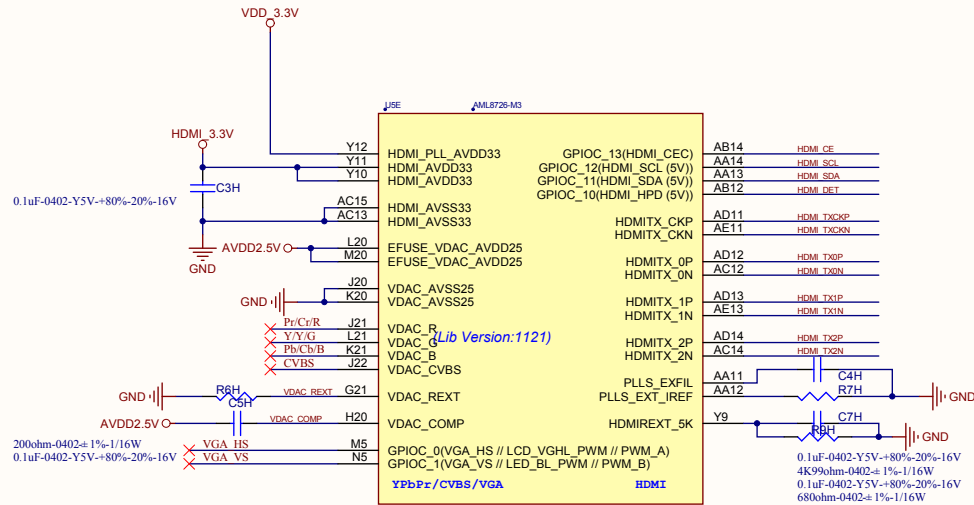
BSD_EN



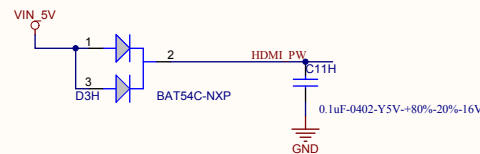
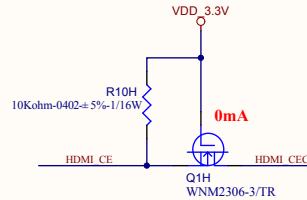
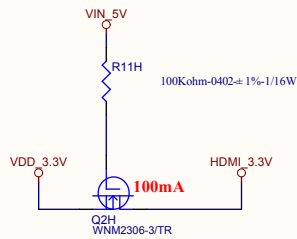
Flash



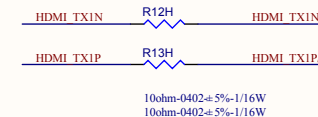
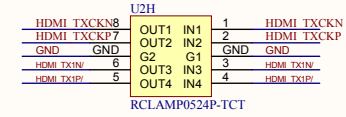
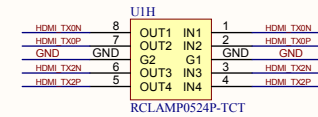
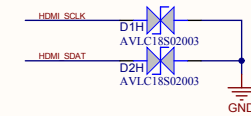
HDMI



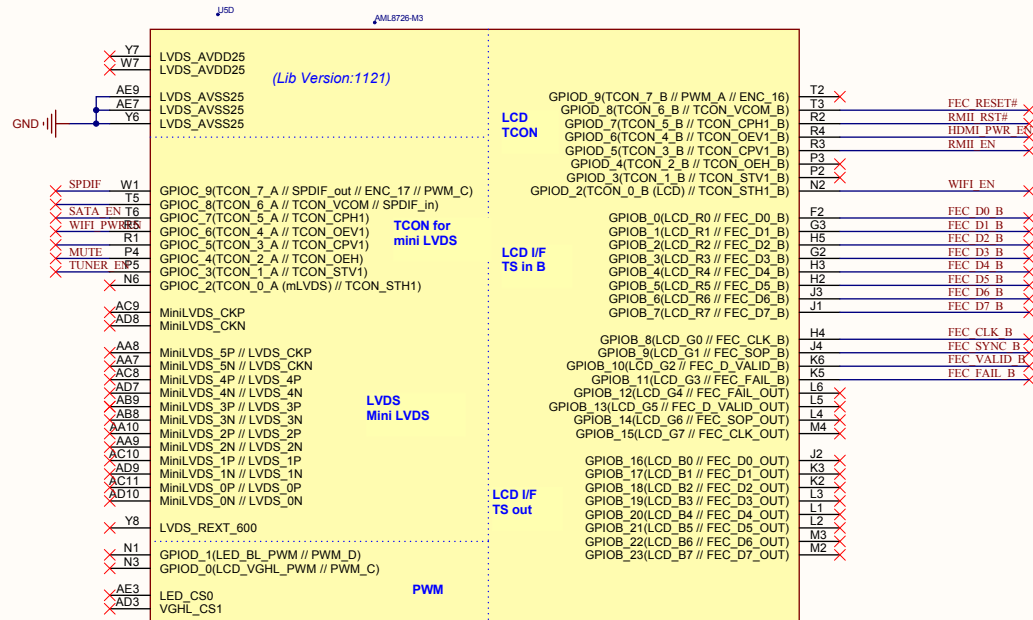
HDMI



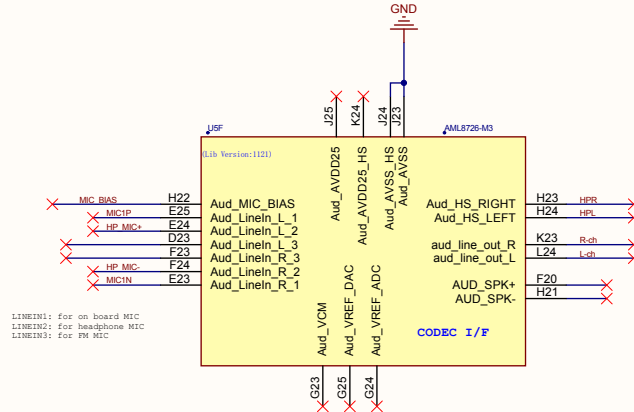
ESD



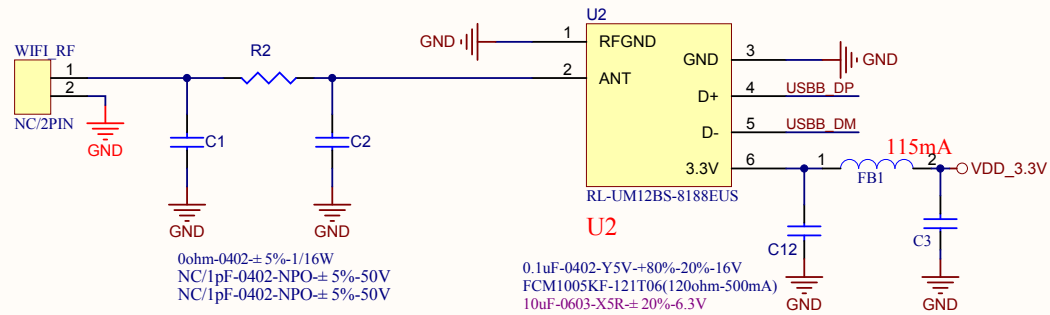
GPIO



Audio



Wi-Fi



LED

