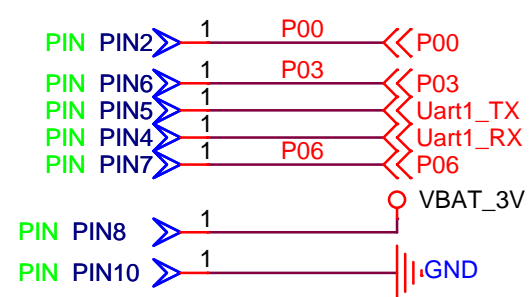


SPI Flash



Table 1: Pin assignment and booting sequence from external devices

Pin	STEP	SPI MASTER	STEP	SPI MASTER	STEP	UART	STEP	SPI SLAVE	STEP	I2C
P0_0	1	SCK	2	SCK	3	TX	7	SCK	8	SCL
P0_1				CS		RX				SDA
P0_2				MISO	4	TX			9	SCL
P0_3		CS		MOSI		RX		CS		SDA
P0_4					5	TX			10	SCL
P0_5		MOSI				RX		MISO		SDA
P0_6		MISO			6	TX		MOSI	11	SCL
P0_7						RX				SDA



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