REVISION HISTORY

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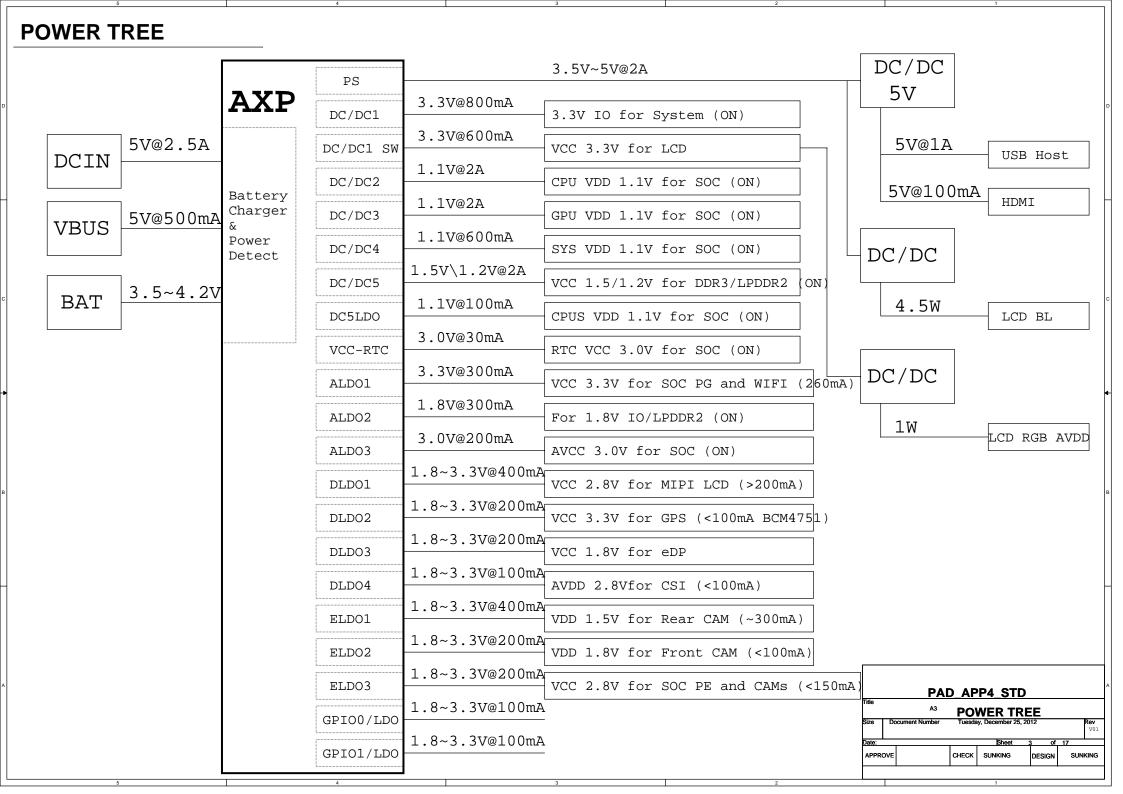
P15: AUDIO IN/OUT

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evision	Description	Date	Drawn	Checked	Approved
PAD_APP4_STD_V1_10		2012-08-28	YT		
				PAD_APP4_	STD
		Title	•		
		Size		ber Tuesday, Decemb	ON HISTORY Der 25, 2012
		<u>Dat</u>		E.	eet 1 of 17



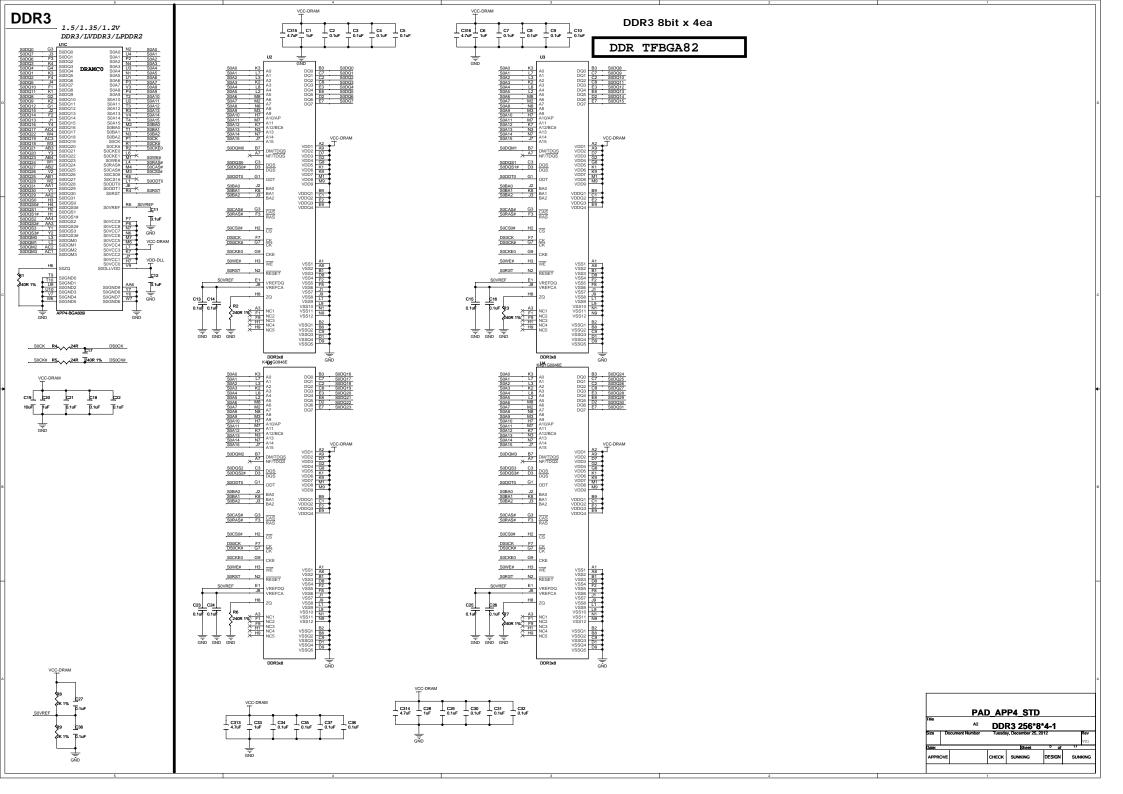
GPIO ASSIGNMENT

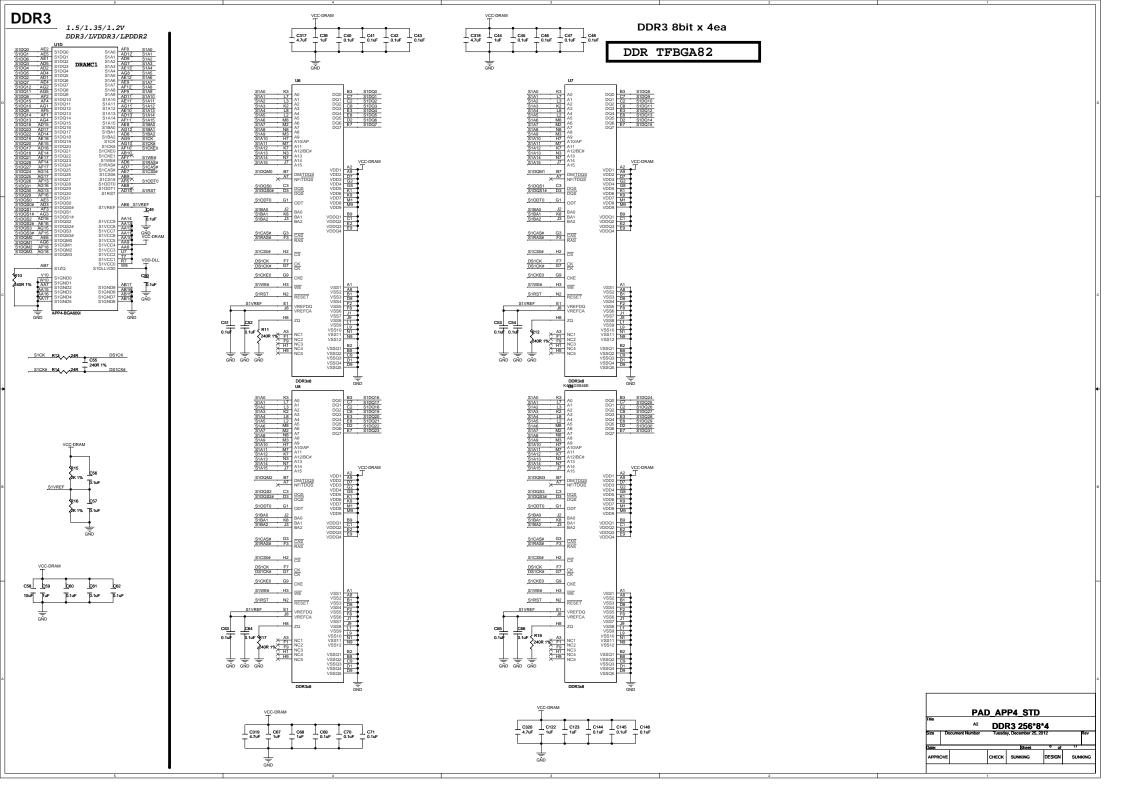
No.	Define	Function	No.	Define	Function	No.	Define	Function	No.	Define	Function	No.	Define	Function
PA0	GPS-RST	GPS	PB0	BB-UART-RTS		PG0	WL-SDIO-CLK		PH0	NAND1-WE		PL0	PMU-SCK	PMU
PA1	GPS-PWR	GFS	PB1	BB-PCM-CLK		PG1	WL-SDIO-CMD		PH1	NAND1-ALE		PL1	PMU-SDA	FMO
PA2	CTP-WAKE	CTP	PB2	BB-PCM-SYNC		PG2	WL-SDIO-D0		PH2	NAND1-CLE		PL2	SUART-TX	DEBUG
PA3	CTP-INT	CII	PB3	BB-PCM-IN	BB	PG3	WL-SDIO-D1	WIFI	PH3	NAND1-CE1	NAND	PL3	BB-VBAT-EN	ВВ
PA4	GPS-UART-RX		PB4	BB-UART-CTS	DD DD	PG4	WL-SDIO-D2		PH4	NAND1-CE0		PL4	IR-RX	IR
PA5	GPS-UART-TX	GPS	PB5	BB-UART-RX		PG5	WL-SDIO-D3		PH5	NAND1-RE		PL5	STMS	
PA6	GPS-UART-CTS		РВ6	BB-UART-TX		PG6 B1	BT-UART-RX		РНб	NAND1-RB0		PL6	STCK	DEBUG
PA7	GPS-UART-RTS		PB7	BB-PCM-OUT		PG7	BT-UART-TX	BT	PH7	NAND1-RB1		PL7	STDO	
PA8	SD0-DET	SD Card				PG8	BT-UART-CTS		PH8	NAND1-DQS		PL8	STDI	
PA9	GS-INT					PG9	BT-UART-RTS		РН9	TMS	DEBUG	PM0	BB-HOST-WAKE	BB
PA10	GY-INT	Sensors				PG10	WL-ENABLE		PH10	TCK	_ DEBOG	PM1	BB-ON	ВВ
PA11	CP-INT					PG11	WL-WAKE	WIFI	PH11	TDO		PM2		
PA12	LS-INT					PG12	WL-HOST-WAKE		PH12	TDI		РМ3	BB-PWRON	
PA13	USB0-DRV					PG13	BT-PCM-CLK		PH13	LCD-PWM	LCD	PM4	BB-WAKE	BB
PA14	USB0-VBUSDET	USB				PG14	BT-PCM-SYNC		PH14	TWI0-SCK	CAMERA	PM5	BB-RF-DIS	
PA15	USB0-IDDET					PG15	BT-PCM-OUT	BT	PH15	TWI0-SDA		РМ6	BB-RST-N	
PA16	DMIC-CLK	DMIC				PG16	BT-PCM-IN	БІ	PH16	TWI1-SCK	CTP	РМ7	СК32КО	CLOCK
PA17	DMIC-DIN	DMIC				PG17	BT-ENABLE		PH17	TWI1-SDA				
PA18	SPK-SHDN	AUDIO				PG18	BT-WAKE		PH18	TWI2-SCK	Sensors			
PA19	LCD-SDA	LCD-eDP							PH19	TWI2-SDA	Selisors			
PA20	LCD-SCL	LCD CDI							PH20	UART-TX	DEBUG			
PA21									PH21	UART-RX				
PA22									PH22					
PA23									PH23					
PA24									PH24					
PA25	LCD-BL-EN	LCD							PH25					
PA26	USB1-DRV	IICD							PH26					
PA27	USB-ICTRL	USB							PH27					
									PH28					
									PH29	NAND1-CE2	27227			
										NAND1-CE3	NAND		DAD ADD C	
											7		PAD APP4 S	TD

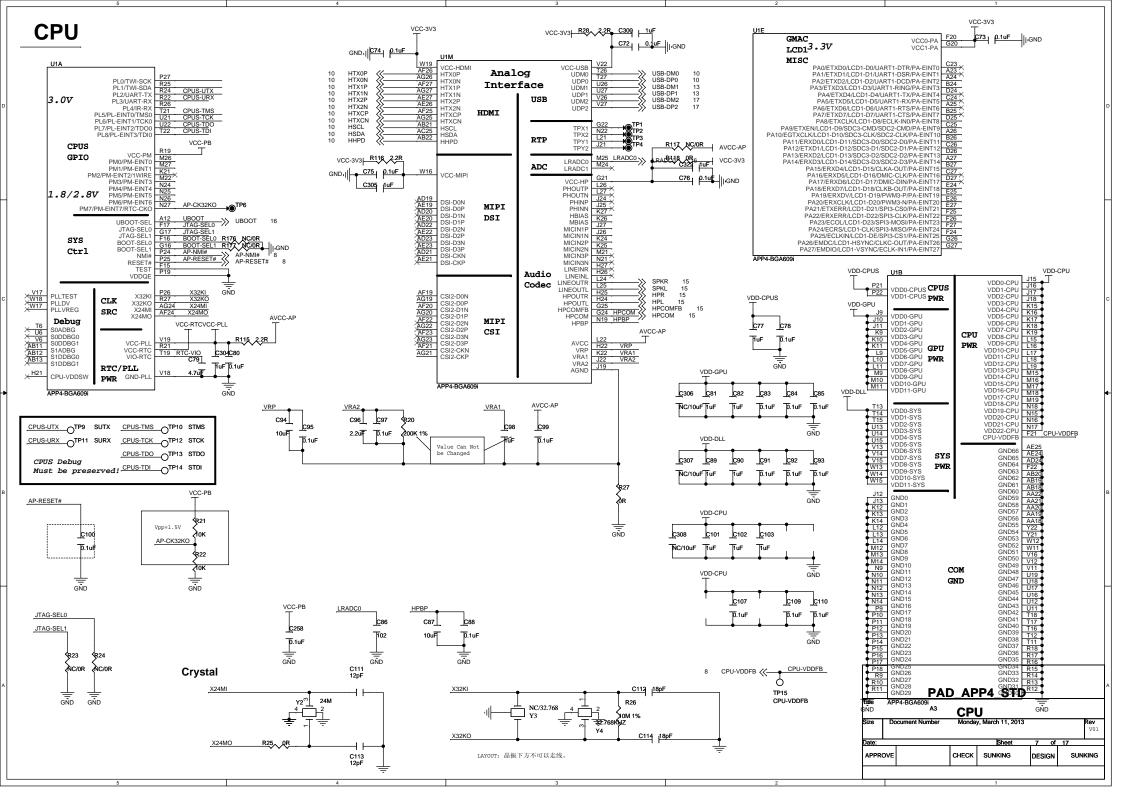
NOTE:

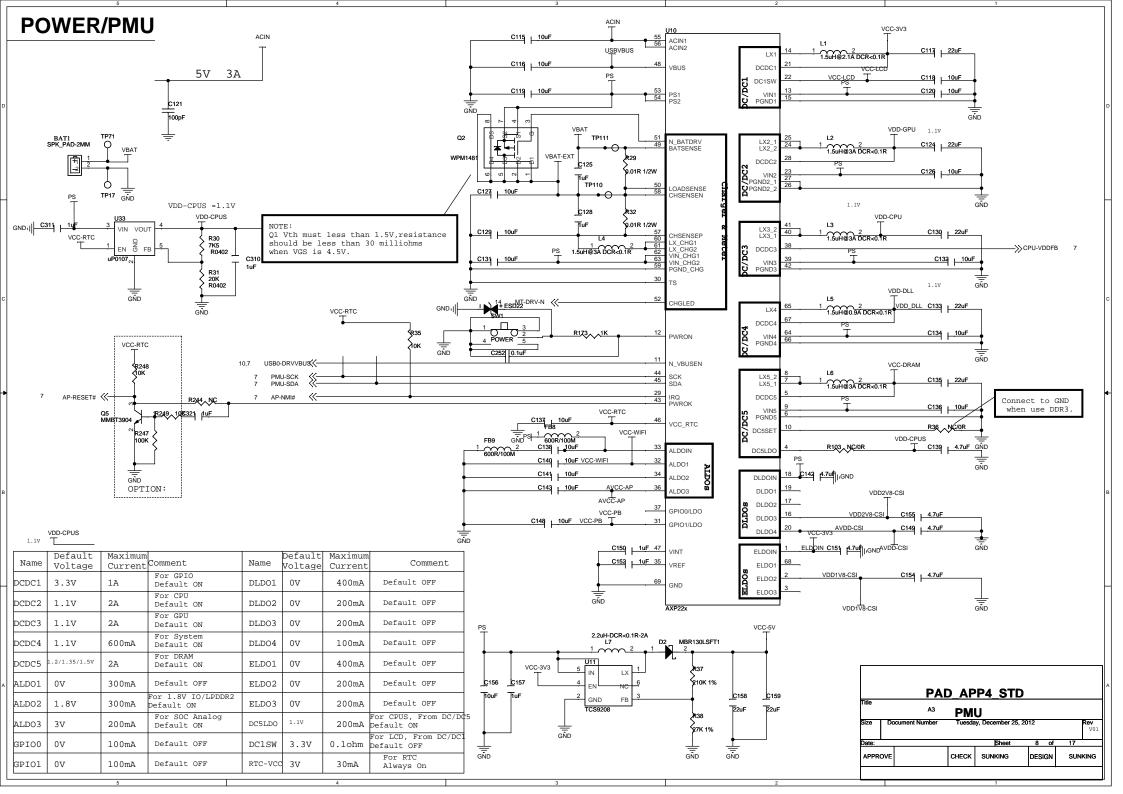
GPIO PA21, PA22, PA23, PA24, PH22, PH23, PH24, PH25, PH26, PH27, PH28 can be used.

_	OF TO AGGIGNMENT													
Size	Do	cum	ent N	lumbe	er	Tue	esda	y, Decer		, 201	2			Rev V01
Date:									Sheet		4	of	17	
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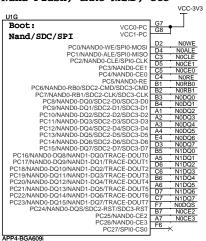


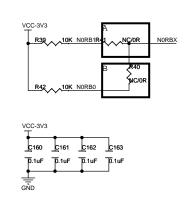




NAND

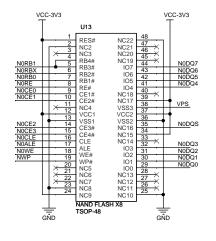
3.3V Nand Flash, eMMC-NAND, etc

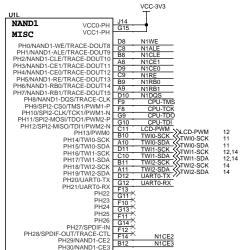


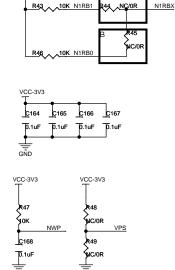


NC/0R

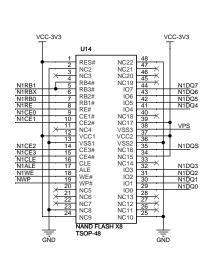
N1RBX







VCC-3V3



	U15		
OA1 OD41 OD41 OD41 OD41 OD41 OD41 OD40 OD40 OD40 OD40 OD40 OD40 OD40 OD40	N/U1 N/U2 N/U2 N/U3 N/U4 N/U5 N/U6 N/U6 N/U6 N/U7 N/U8 N/U10 N/U11 N/U11 N/U12 N/U13 N/U14 N/U15 N/U16 N/U16 N/U17 N/U18 N/U18 N/U19 N/U20 N/U11 N/U21 N/U22 N/U23 N/U24 DQ0 DQ1 DQ1 DQ2 DQ3 DQ4 DQ5 DQ6 DQ7 NC/TOGGI	VCCQ1 VCCQ3 VCCQ4 VCCQ5 VCCQ6 VCCQ6 VCC1 VCC2 VSSQ1 VSSQ2 VSSQ3 VSSQ4 VSSQ5 VSSQ5 VSSQ6 VSSQ6 VSSQ6 VSSQ6 VSSQ7 VSSQ7 VSSQ7 VSSQ7 VSSQ6 VSSQ6 VSSQ6 VSSQ7 VS	OD1 OF1 OB9 OB9 OG9 N8 N8 N2 B7 M7 OB1 OG1 OG9 F7 L4 SN B3 SN B3 SN B3 SN B3 SN B6 N7 M7 OB1 OG1 OG9 OF9 F7 L4 A4 N1CLE F3 NWP F3 NWP F4 NICE F3 NWP F4 NICE F3 NWP F4 NICE F3 NWP F5 NWP F5 NWP F6 F7 F7 F7 NWP F8

VCC-3V3

TSSOP CE Count(1)	Resistor A	Resistor B
1	Disconnect	Disconnect
2	Connect	Disconnect
4	Disconnect	Connect

VCC-3V3 TWI0 pull-up by VCC-PE in Camera Page Pull-Up TWI1-SCK Resistor TWI1-SDA R51 2.2K for TWIs R52 TWI2-SCK

TWI2-SDA

R53 2.2K

APP4-BGA609i

CPU-TMS TP18 TMS -O^{TP19} TCK CPU-TCK TP20 TDO CPU-TDO Debug Pins for CPU CPU-TDI -OTP21 TDI UART0-TX OTP22 UTX OTP23 URX UART0-RX

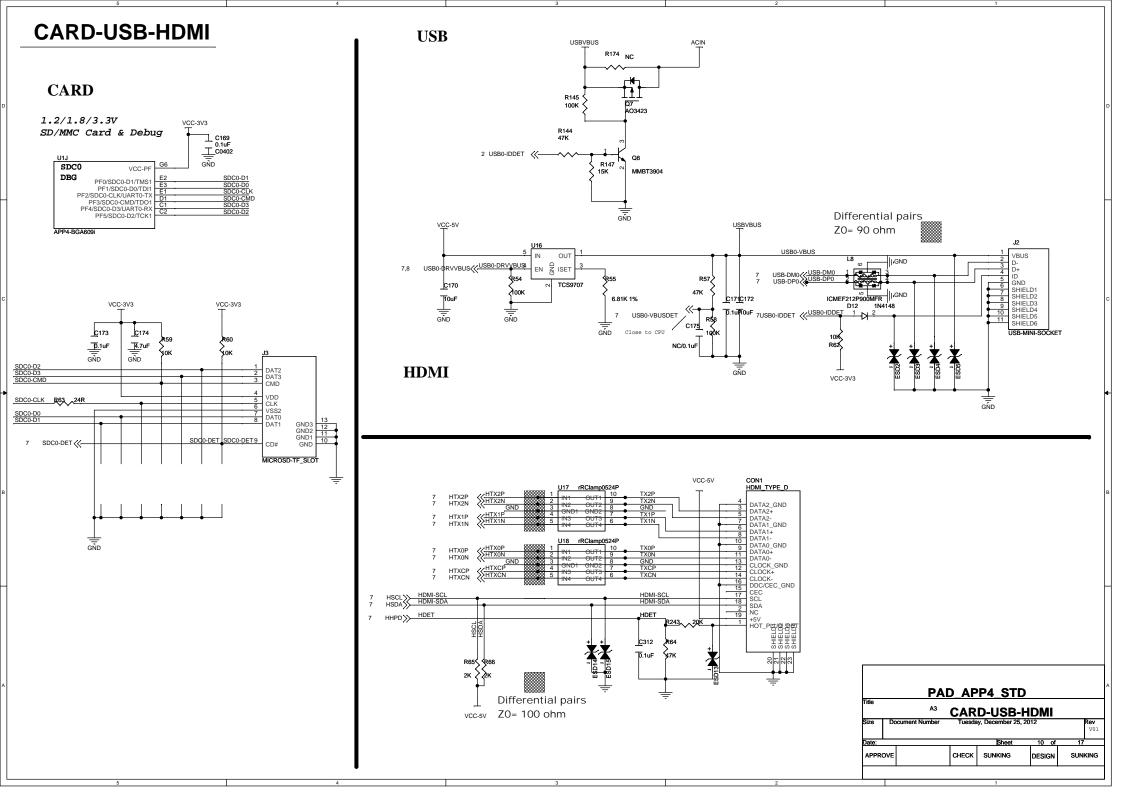
(1) TSSOP CE Count: Chip Select Count of one TSSOP Nand Flash, can be 1/2/4.

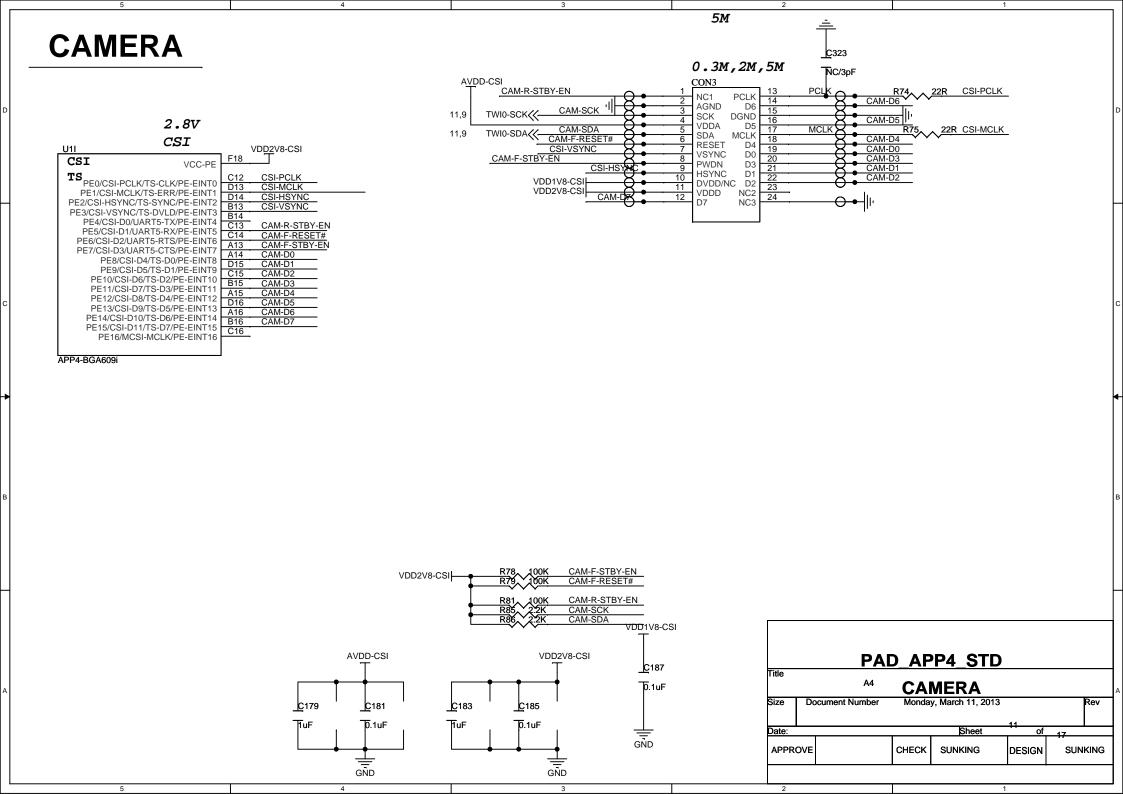
(2) Refer to the Datasheet of Nand Flash to determine VPS should be Prind-up

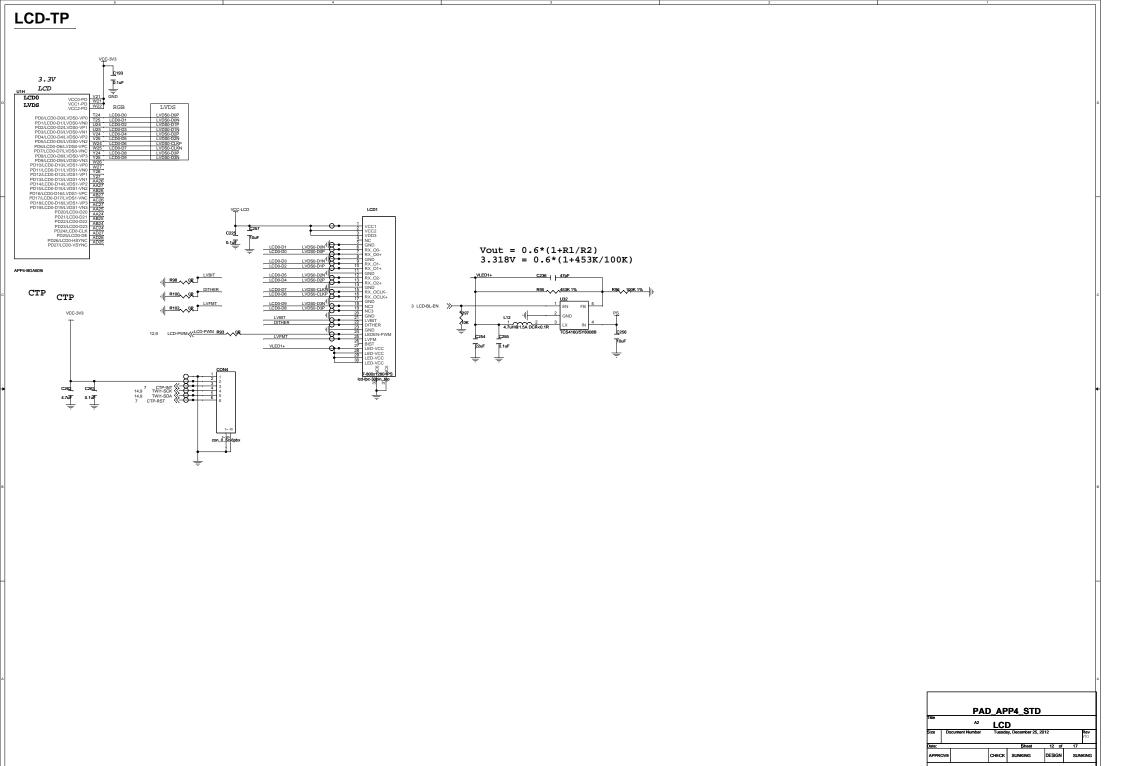
(3) Both Toggle NAND and TSOP NAND can be layout together.

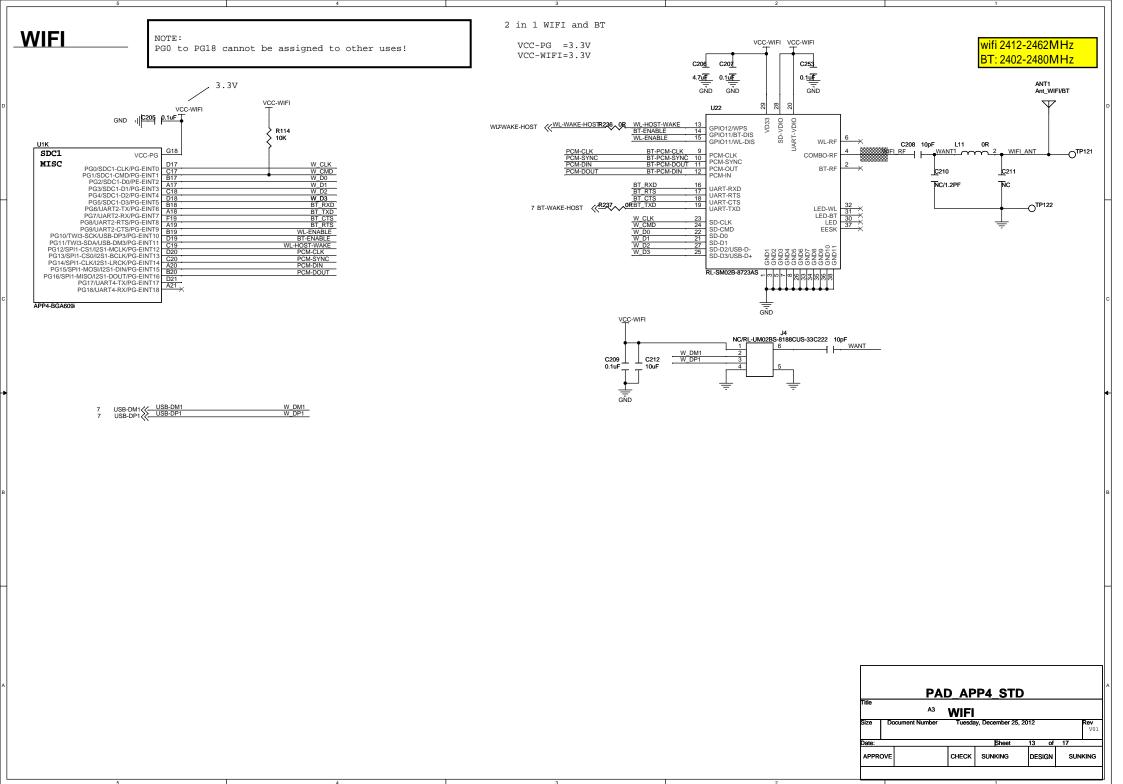
PAD APP4 STD

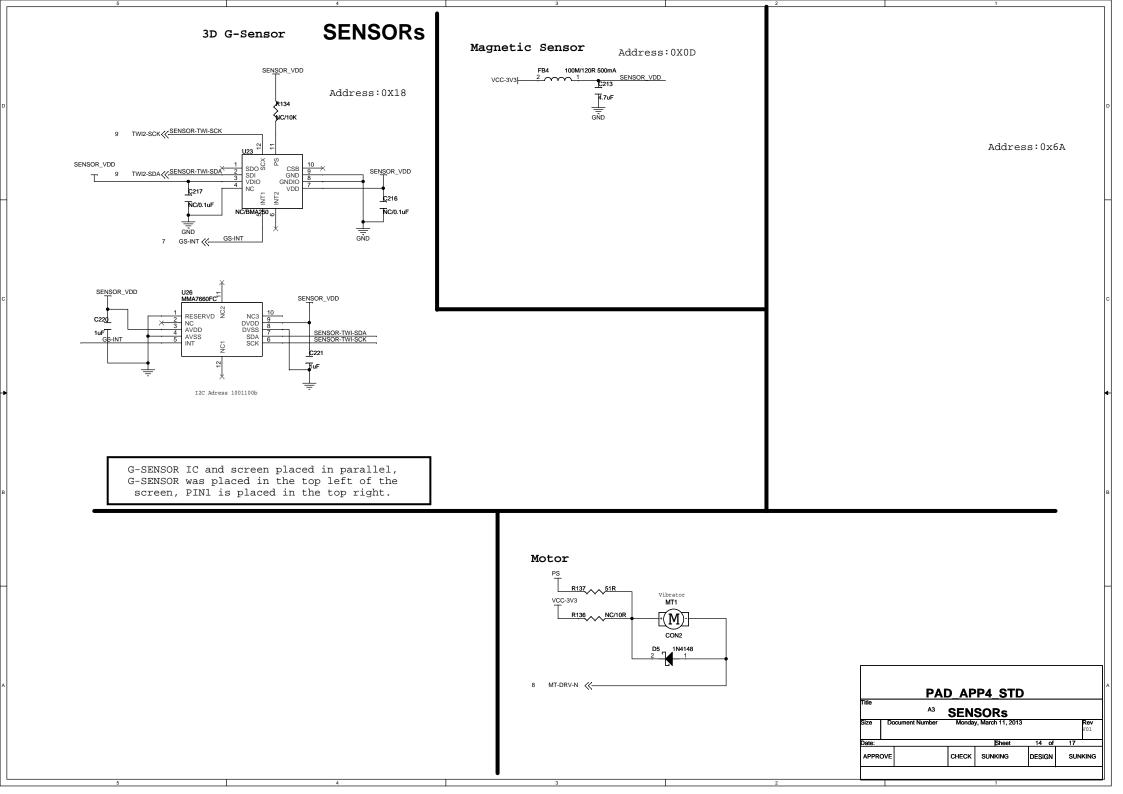
NAND V01 Sheet 9 of 17 APPROVE CHECK SUNKING DESIGN SUNKING

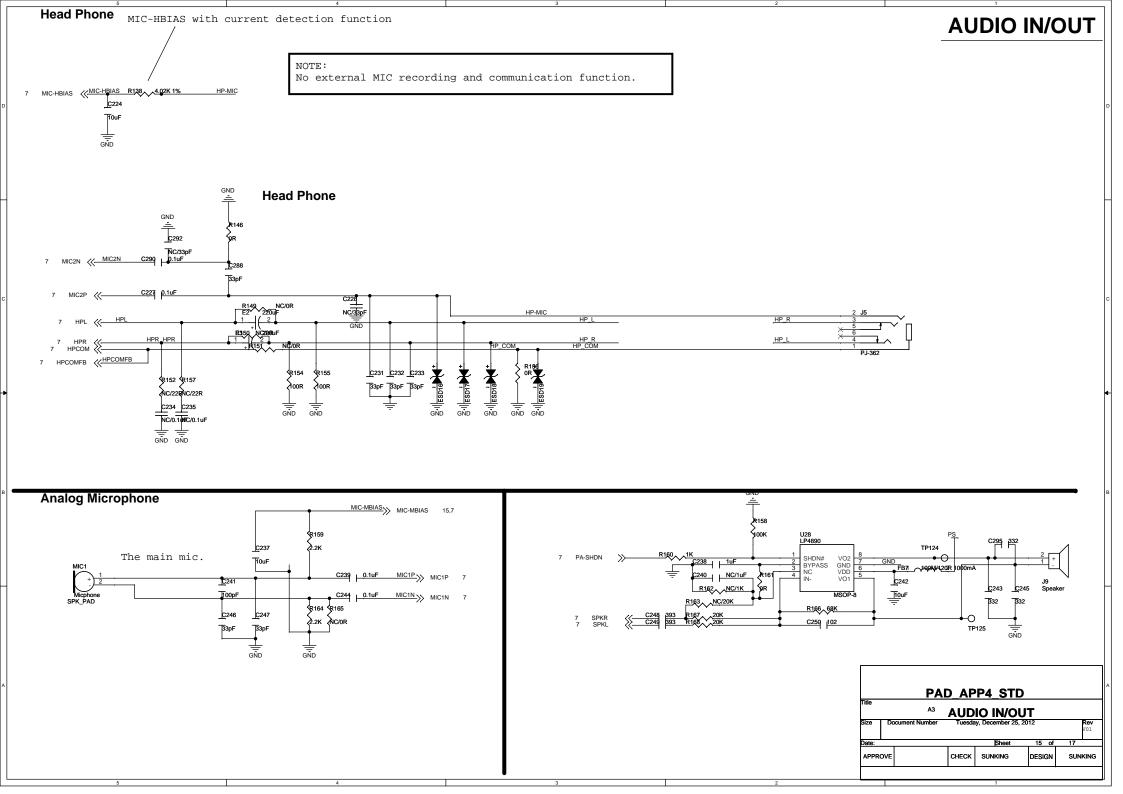


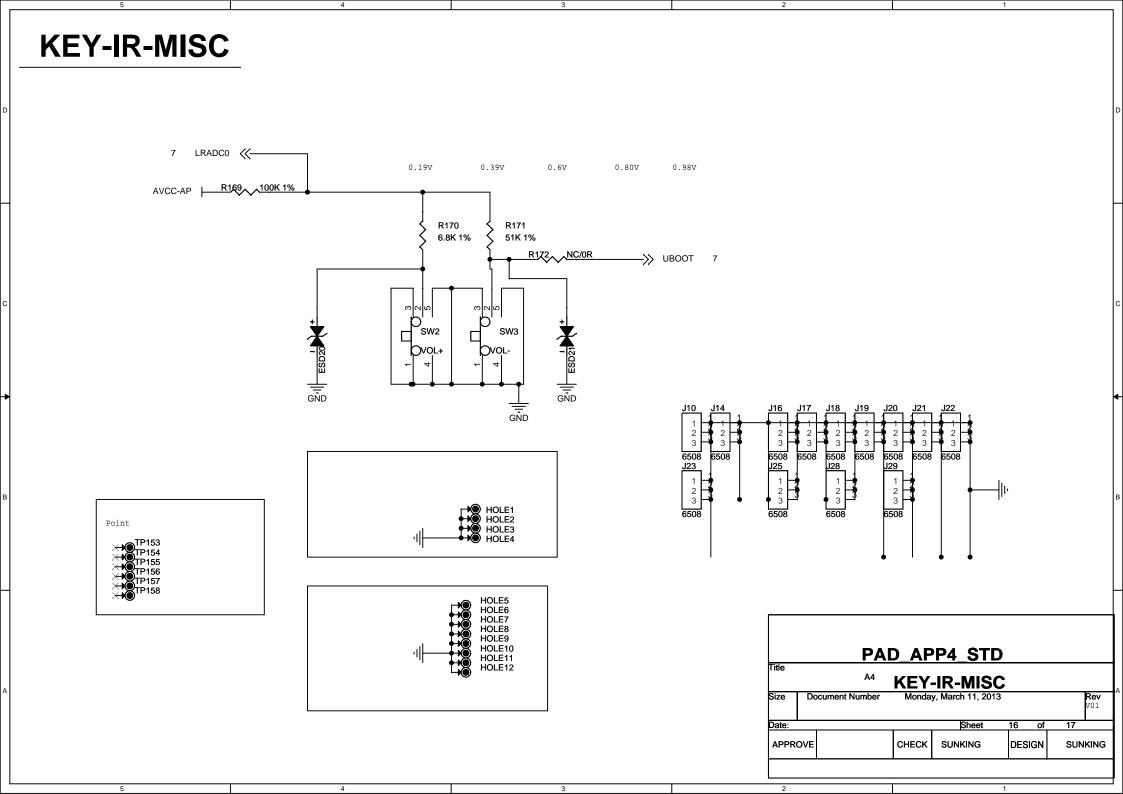












BASEBAND

NOTE:

Not supported BASEBAND function !

Note: VCCB>=VCCA.

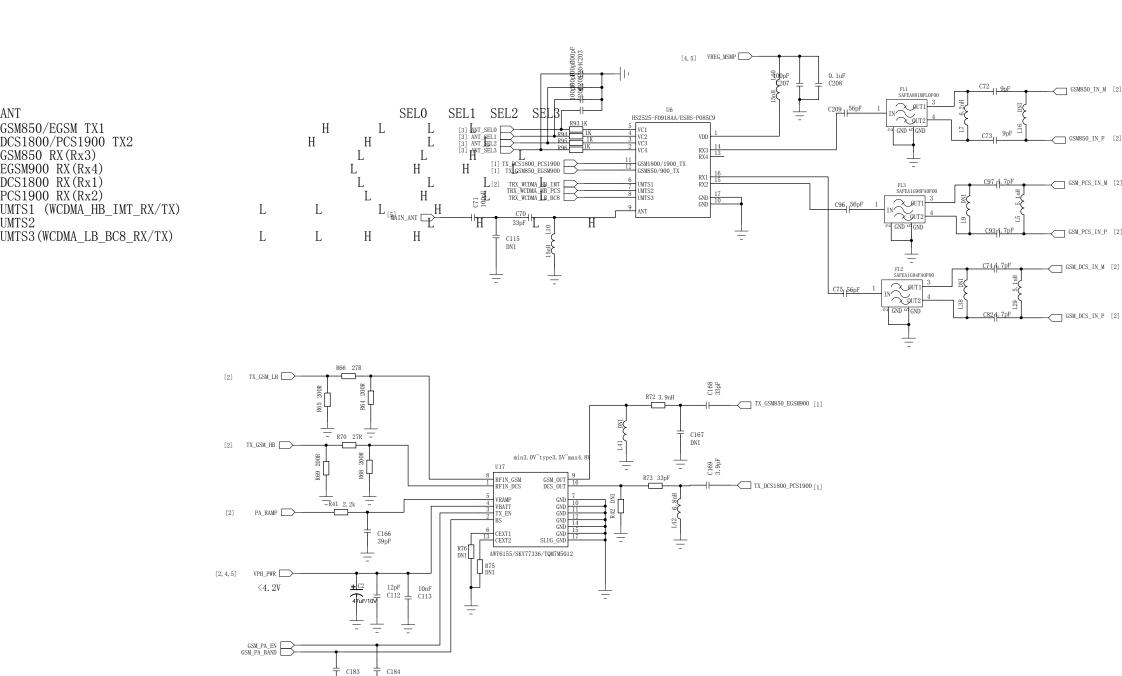
Note:

VCCB>=VCCA.

PAD_APP4_STD
Filte

A3 BASEBAND
Size Bocument Number Monday, March 11, 2013
Pate: Sheet 17 of 17

APPROVE CHECK SUNKING DESIGN SUNKING



100pF

100pF

