

Regarding Requirements of Paragraph 15.212(a)(1)**Shielding 15.212(a)(1)(i)**

The oscillator/transmitter circuitry is housed under a metal shield soldered to the circuit board.

Buffered Modulation/Data Input 15.212(a)(1)(ii)

All signals are buffered as required.

Power Regulation 15.212(a)(1)(iii)

The RF components are powered by a locally regulated 3.0 V source.

Antenna Construction 15.212(a)(1)(iv)

The antenna is integral to the printed circuit board as an etched trace.

Test Configuration 15.212(a)(1)(v)

The device was tested stand-alone with no external shielding. No ferrites were used on the attached cable. The connecting cable measured 20 cm of ribbon cable to the programming interface card and with power extended up to 70 cm to the power supply. A commercial grade wall-plugging DC power supply provided USB power to the board that in turn passed power to the EUT.

Labeling 15.212(a)(1)(vi)

The module is labeled with the FCC ID using a permanently applied printed label as depicted in other supporting documentation. The user manual contains instructions for external labeling when the module is enclosed in a final configuration.

Applicable Requirements on Final Configuration 15.212(a)(1)(vii)

The user manual includes instructions that explain the requirements of marking and the instructions that need to be conveyed to the final configuration.

Compliance to RF Exposure Requirements in Final Configuration 15.212(a)(1)(viii)

The user manual includes instructions on the warnings required for the final configuration.
