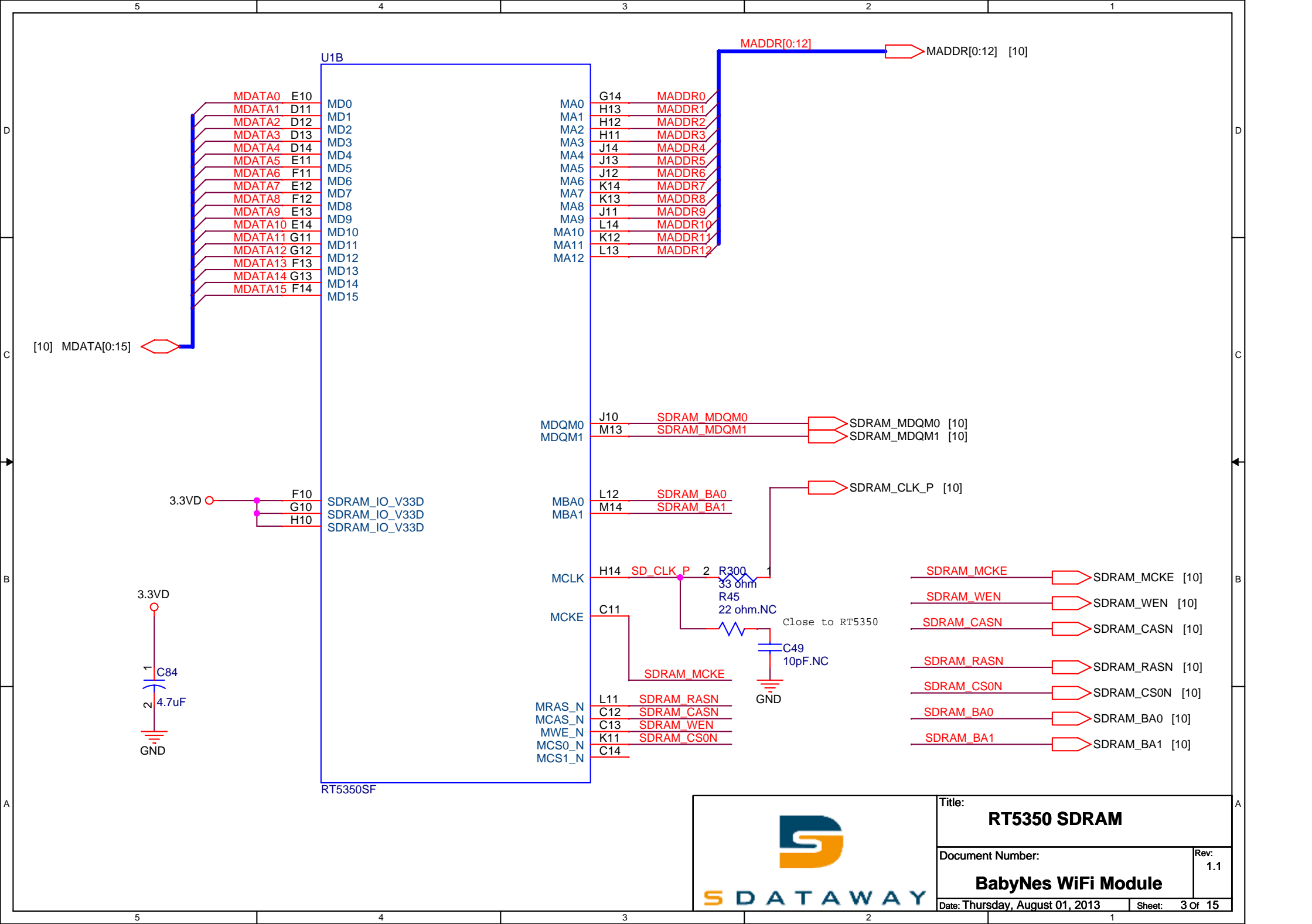
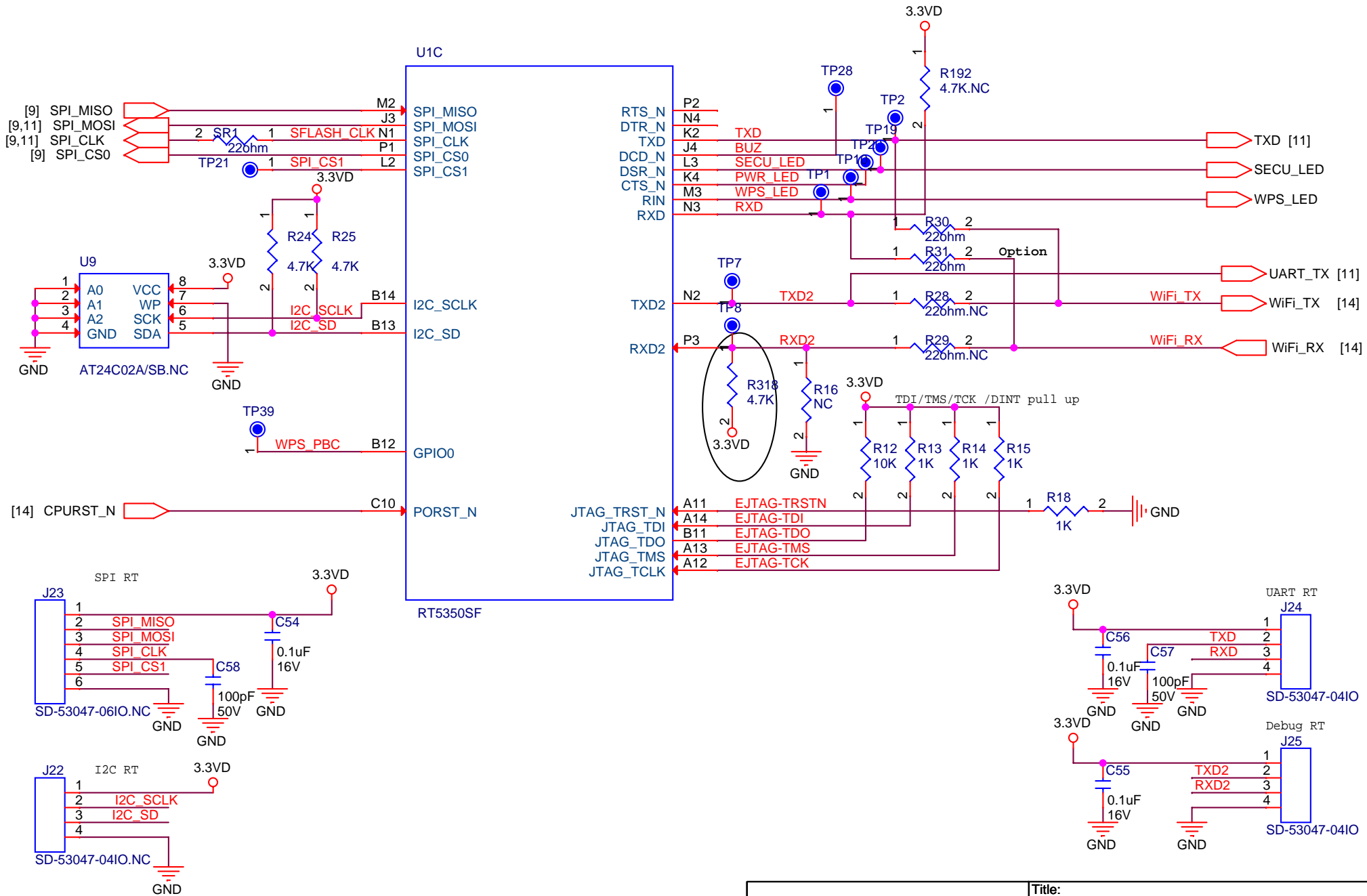
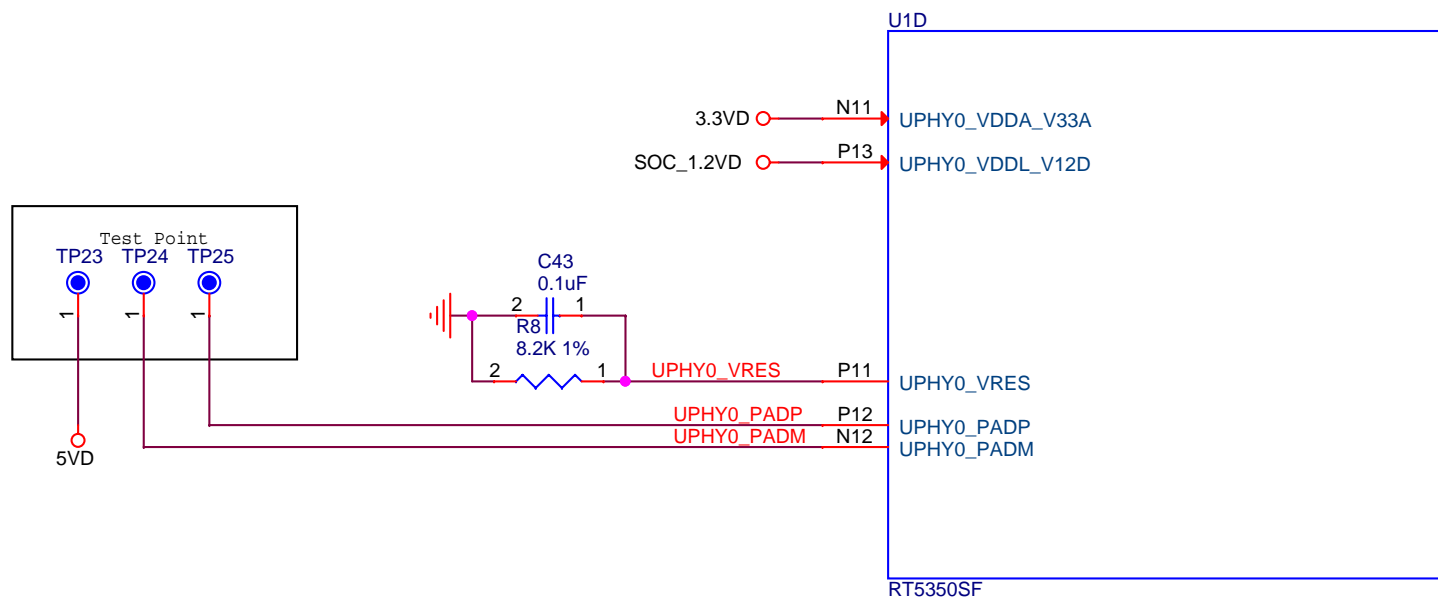


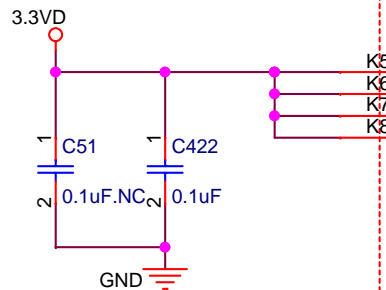
Title: RT5350 RF Power	
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Close to RT5350

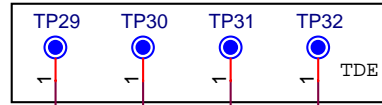


U1E

EPHY_V33A
EPHY_V33A
EPHY_V33A
EPHY_V33A

EPHY_TXN_p4
EPHY_TXP_p4
EPHY_RXN_p4
EPHY_RXP_p4

M9
L9
N9
P9



TDE Test Point

EPHY_TXN_p3
EPHY_TXP_p3
EPHY_RXN_p3
EPHY_RXP_p3

N8
P8
M8
L8



SOC_1.8VD

EPHY_TXN_p2
EPHY_TXP_p2
EPHY_RXN_p2
EPHY_RXP_p2

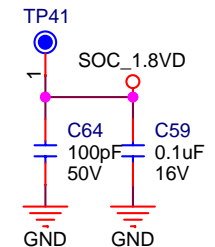
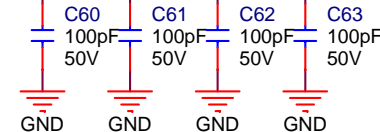
M7
L7
N7
P7

EPHY_TXN_p1
EPHY_TXP_p1
EPHY_RXN_p1
EPHY_RXP_p1

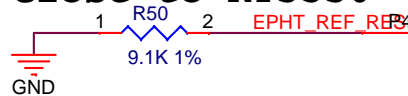
N6
P6
L6
M6

EPHY_TXN_p0
EPHY_TXP_p0
EPHY_RXN_p0
EPHY_RXP_p0

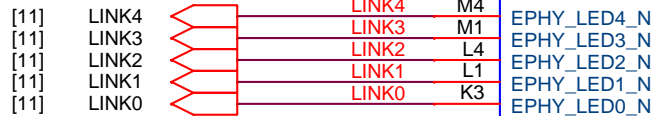
N5
P5
L5
M5




Close to RT5350

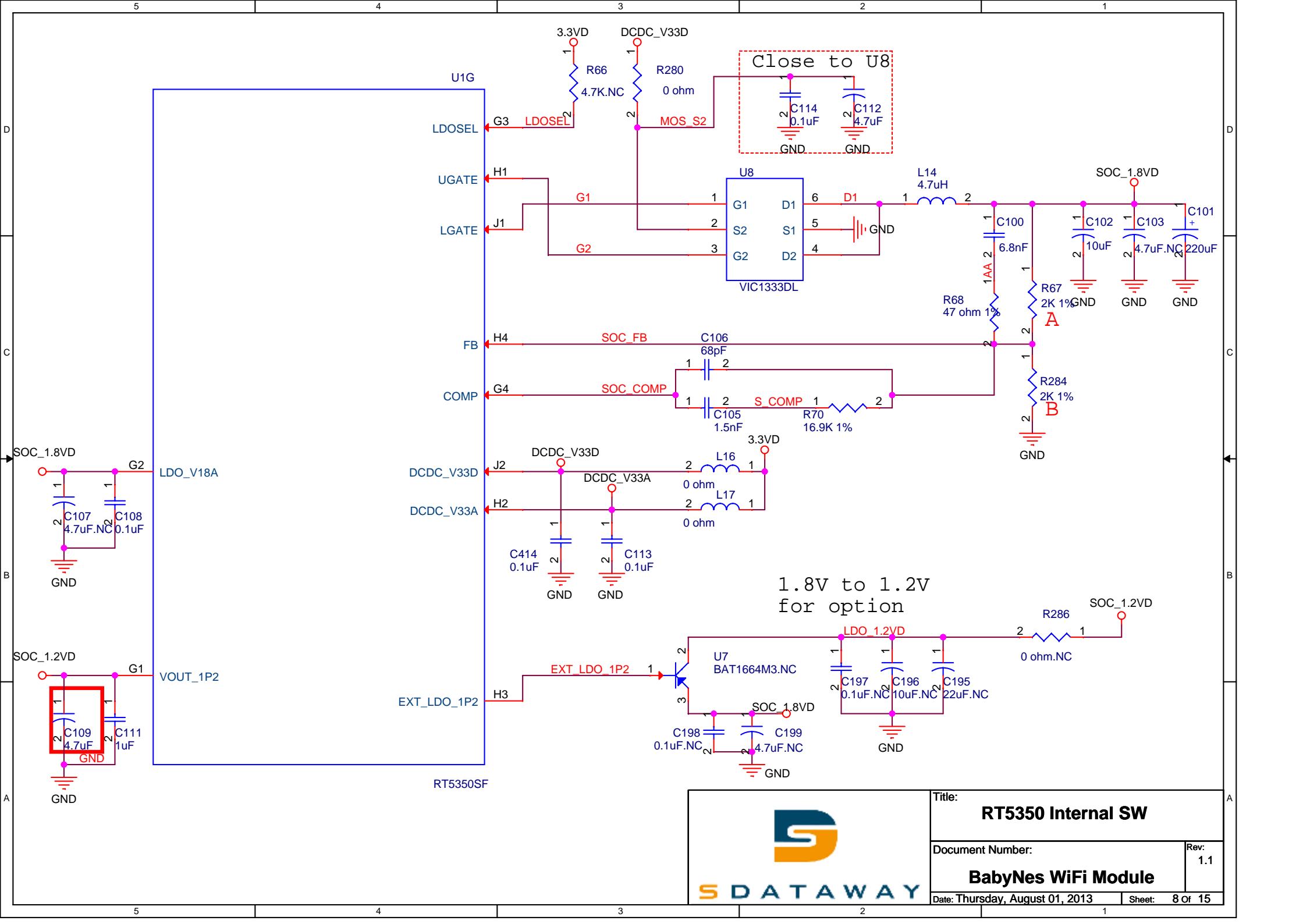


EPHY_REF_RES



RT5350SF

		Title: RT5350 ETH	
		Document Number: BabyNes WiFi Module	Rev: 1.1
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Title: RT5350 Internal SW		
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[3] MDATA[0:15]

MDATA[0:15]

[3] SDRAM_MCKE

SDRAM_MCKE

[3] SDRAM_CLK_P

[3] SDRAM_CS0N

[3] SDRAM_WEN

[3] SDRAM_RASN

[3] SDRAM_CASN

[3] SDRAM_BA0

[3] SDRAM_BA1

[3] MADDR[0:12]

MADDR[0:12]

[3] SDRAM_MDQM[0:1]

SDRAM_MDQM[0:1]

3.3VD

3.3VD_SD

C386

C12

10uF.NC

0.1uF.NC

GND

C445

C387

C388

C389

C390

C391

C392

0.1uF.NC

0.1uF

0.1uF

0.1uF

0.1uF

0.1uF

0.1uF

GND

GND

U35

SDRAM TSOP 54P


W9825G6JH-6



Title: SDRAM		
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RT5350 Boot Up Strapping

Pin Name	Description	Value=0	Value=1
SPI_CLK	XTAL_FREQ_HI	20MHz	40MHz
WLAN_LED_N	Big Endian	Little Endian	Big Endian
EPHY_LED4_N	DRAM_FROM_EE	from boot strapping	from EEPROM
{EPHY_LED3_N, EPHT_LED2_N}	DRAM_SIZE	INIC/AP(SDR) 0: 2MB/8MB 1: 8MB/16MB 2: 16MB/32MB 3: 32MB/64MB	
{EPHY_LED1_N, EPHT_LED0_N}	CPU_CLK_SEL	CPU clock select 0: 360MHz 1: Reserved 2: 320MHz 3: 300MHz	
{SPI_MOSI, TXD2, TXD}	CHIP_MODE[2:0]	A vector to set chip function/test/debug modes 0 : Normal mode(boot fromSPI serial flash) 1 : iNIC-USB mode 2 : Reserved 3 : Reserved 4 : Reserved 5 : iNIC-PHY mode 6 : SCAN mode 7 : TEST/DEBUG mode	

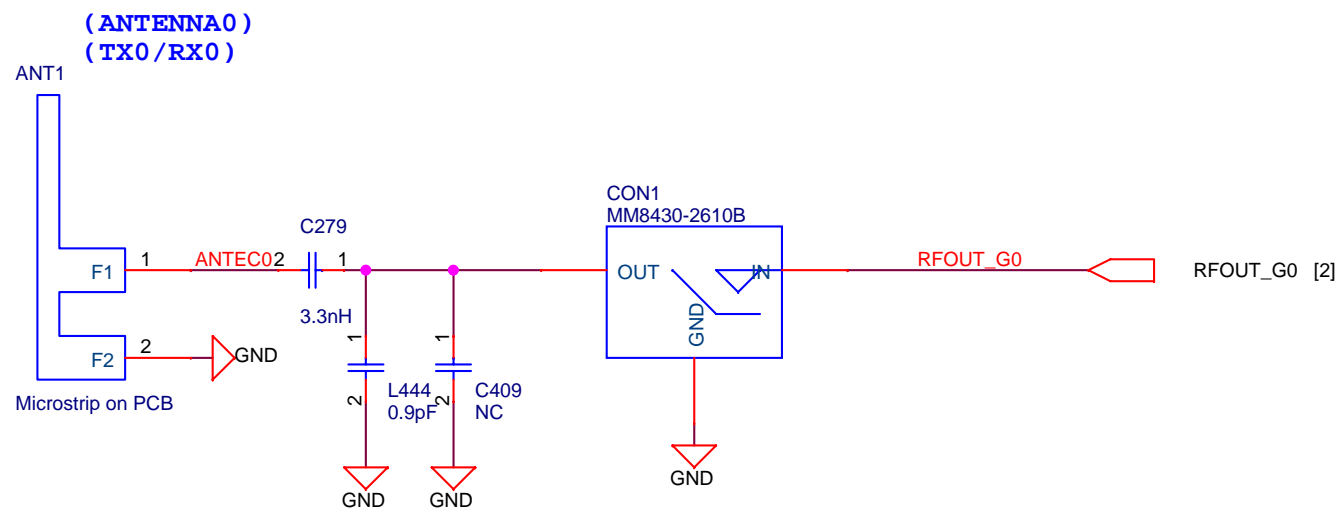
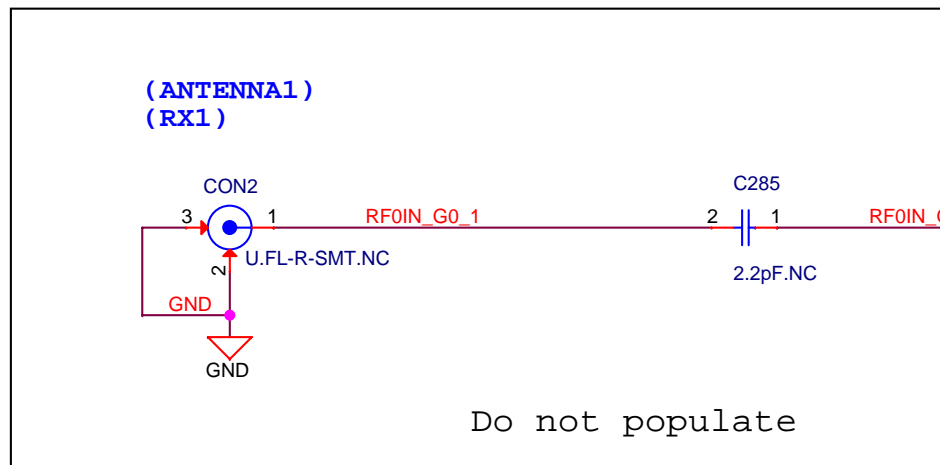


Title: RT5350 config

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Title: Front END		
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