

Title		
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A4		
Date:	13-Nov-2013	Sheet of
File:	C:\Users\Administrator\Desktop\MyDesign\Drawn By:	

ZM2102 Z-Wave Module

Schematics:

Schematic

Page 2 Z-Wave

PCB1
280100482
PCB, ZM2102 Z-Wave Module, PCB1, Rev2

DOC1
954012900
Schematic, ZM2102 Z-Wave Module, PCB1, Rev2

Layout Constrains

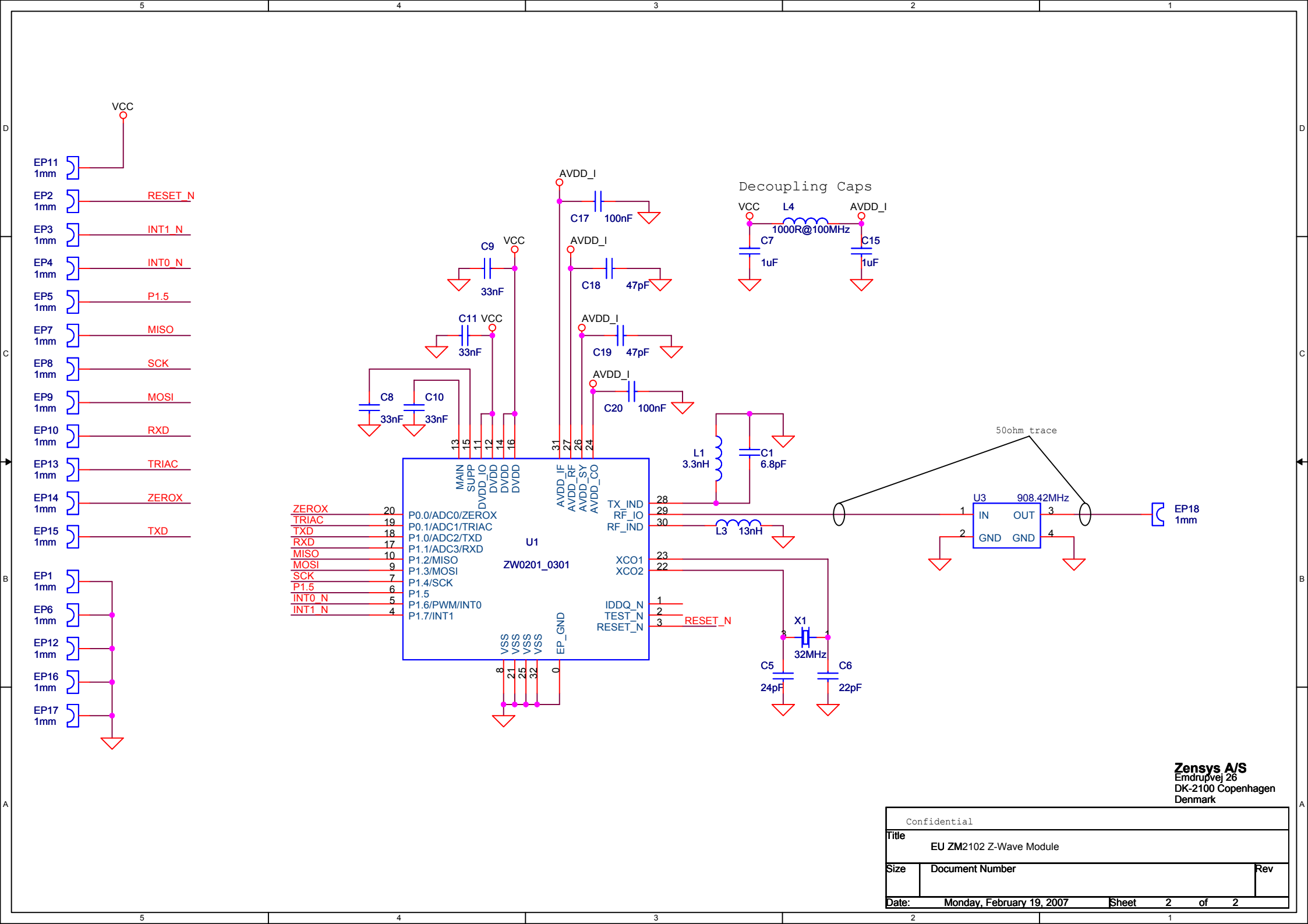
- 1: C11 is connected to U1 pin 11 and 12
- 2: C10 is connected to U1 pin 13
- 3: C9 is connected to U1 pin 14 and 16
- 4: C8 is connected to U1 pin 15
- 5: Locate all decoupling caps as close the U1 as possible
- 6: The trace from U3 to U1 and from U3 to EP18 must be 50ohm
- 7: Place 2 vias from C7 to Layer 3
- 8: Place 2 vias from C15 to Layer 3
- 9: Place 2 vias from L4 to Layer 3

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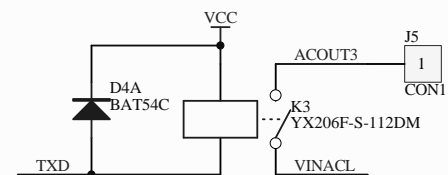
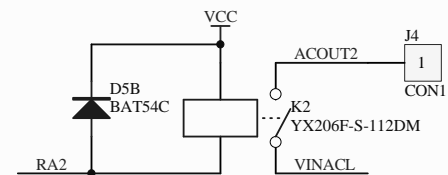
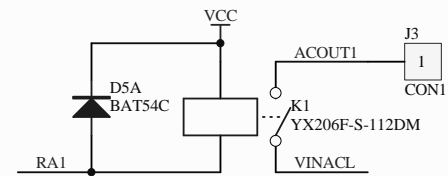
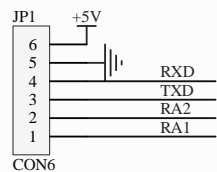
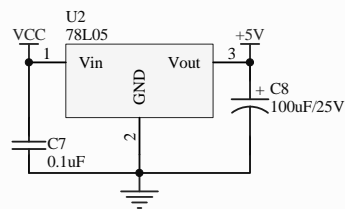
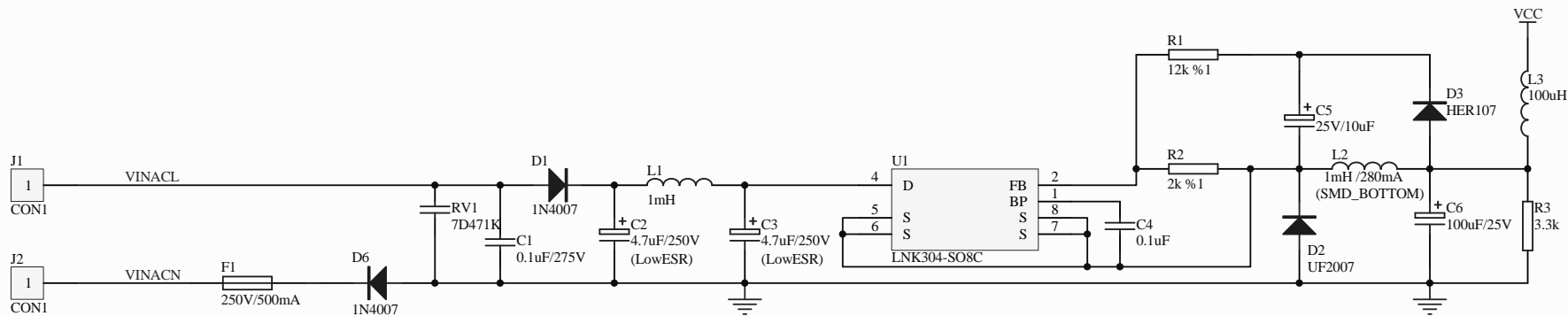
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Confidential			
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