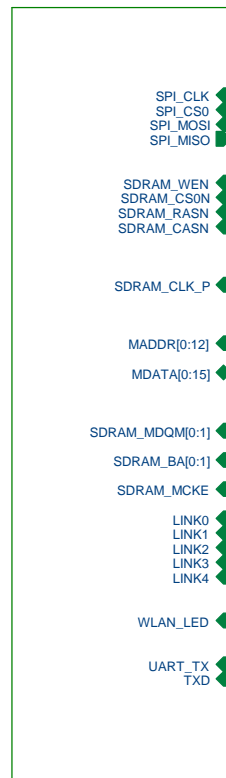


2-MEMORY



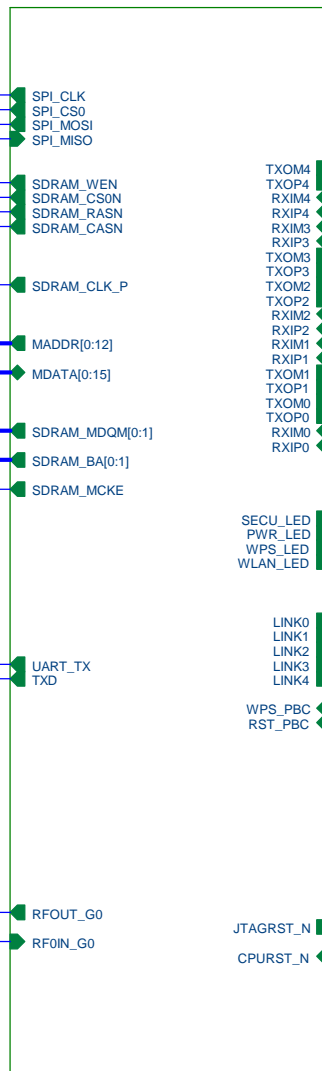
2-MEMORY

3-WLAN



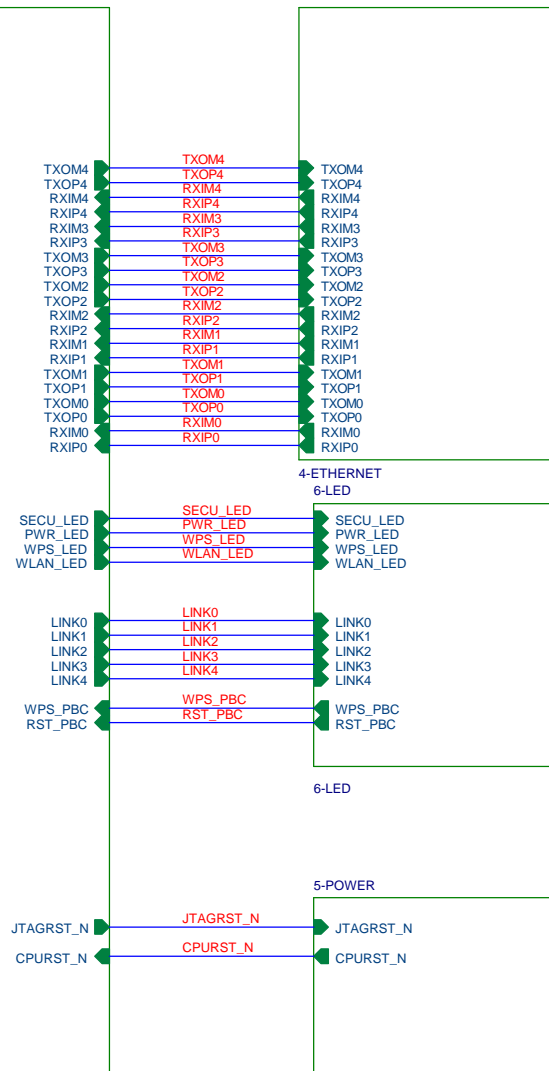
3-WLAN

1-CPU-RT5350



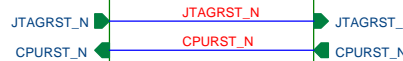
1-CPU-RT5350

4-ETHERNET




6-LED

5-POWER

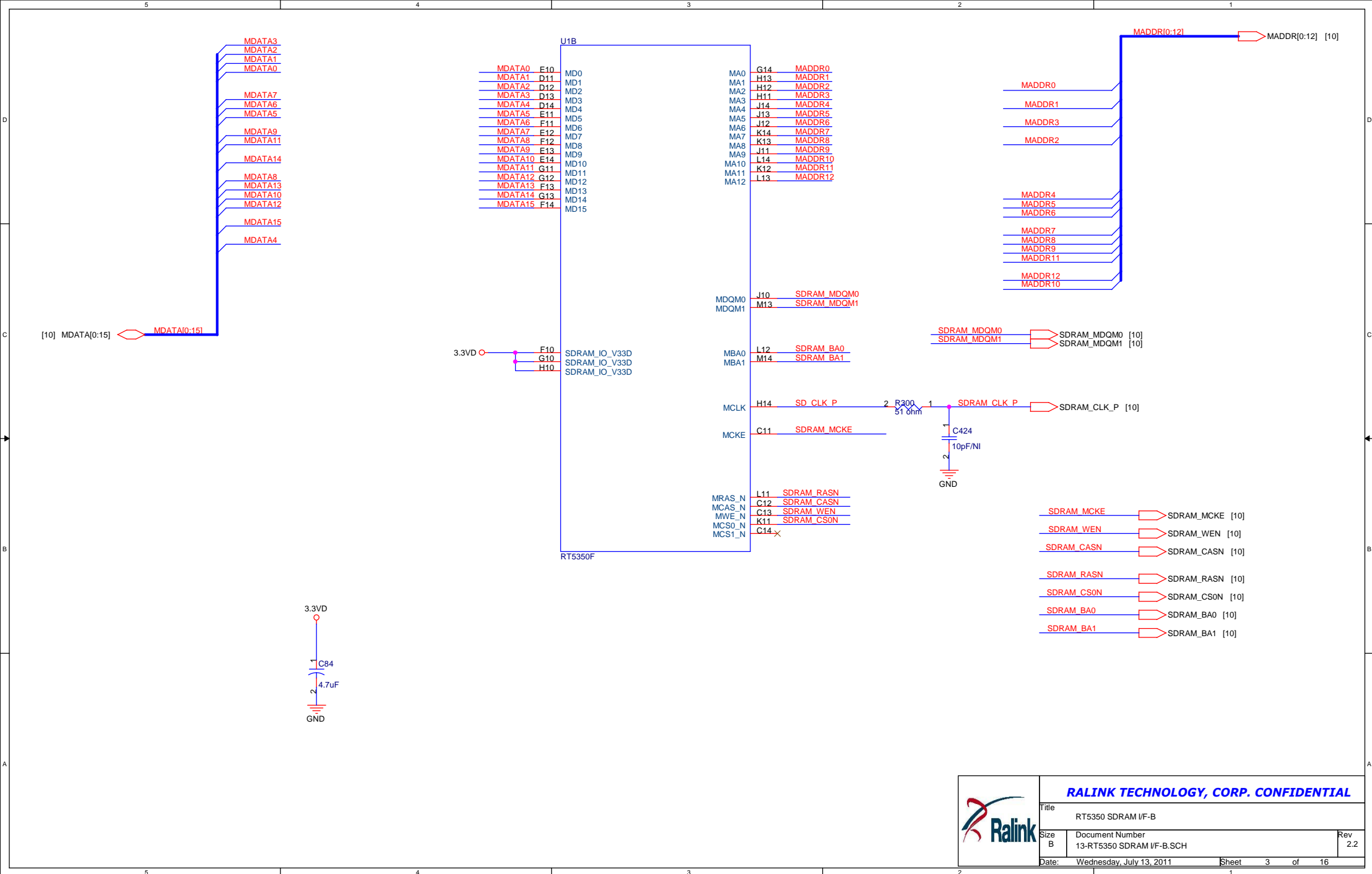


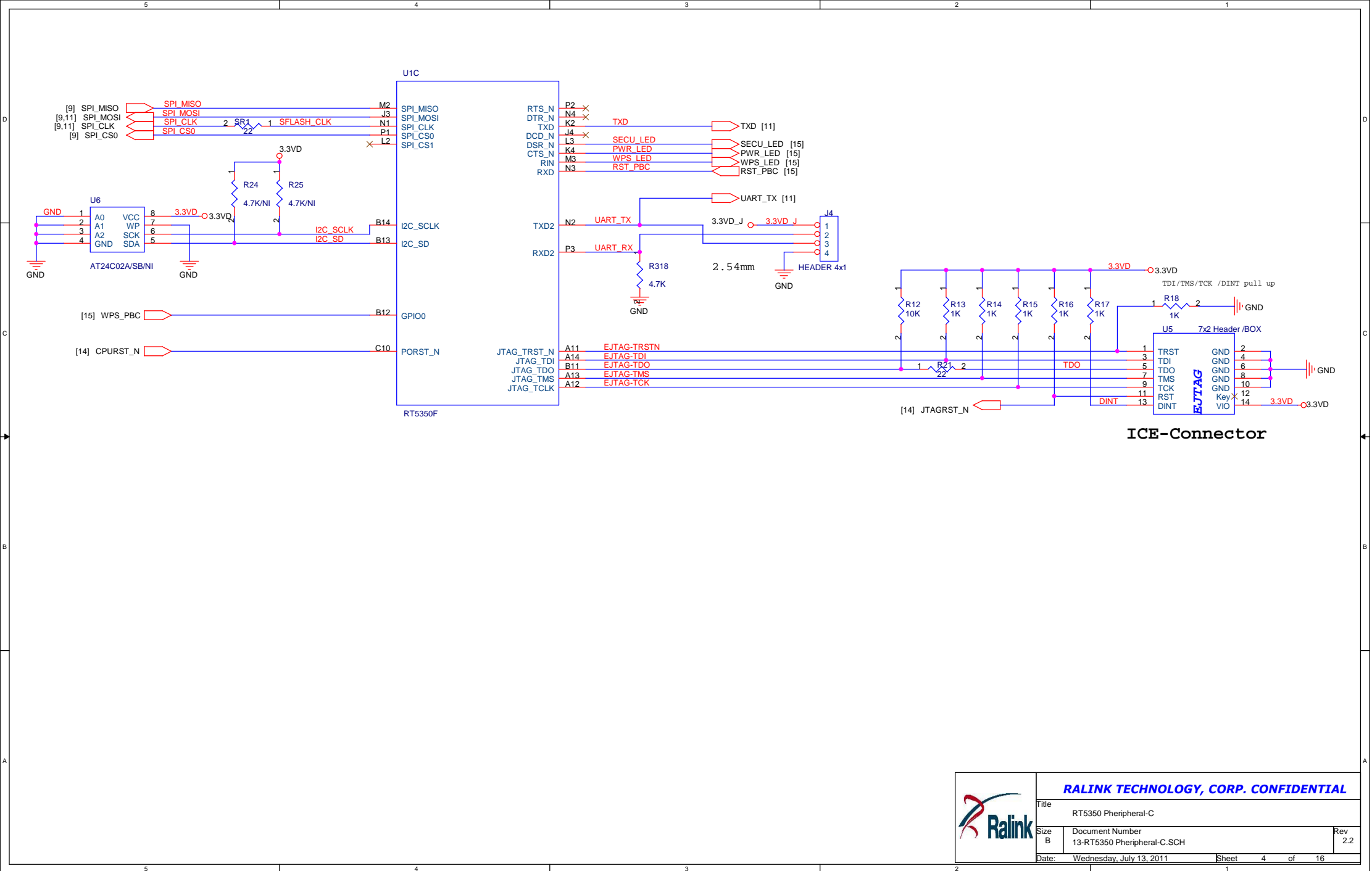
5-POWER

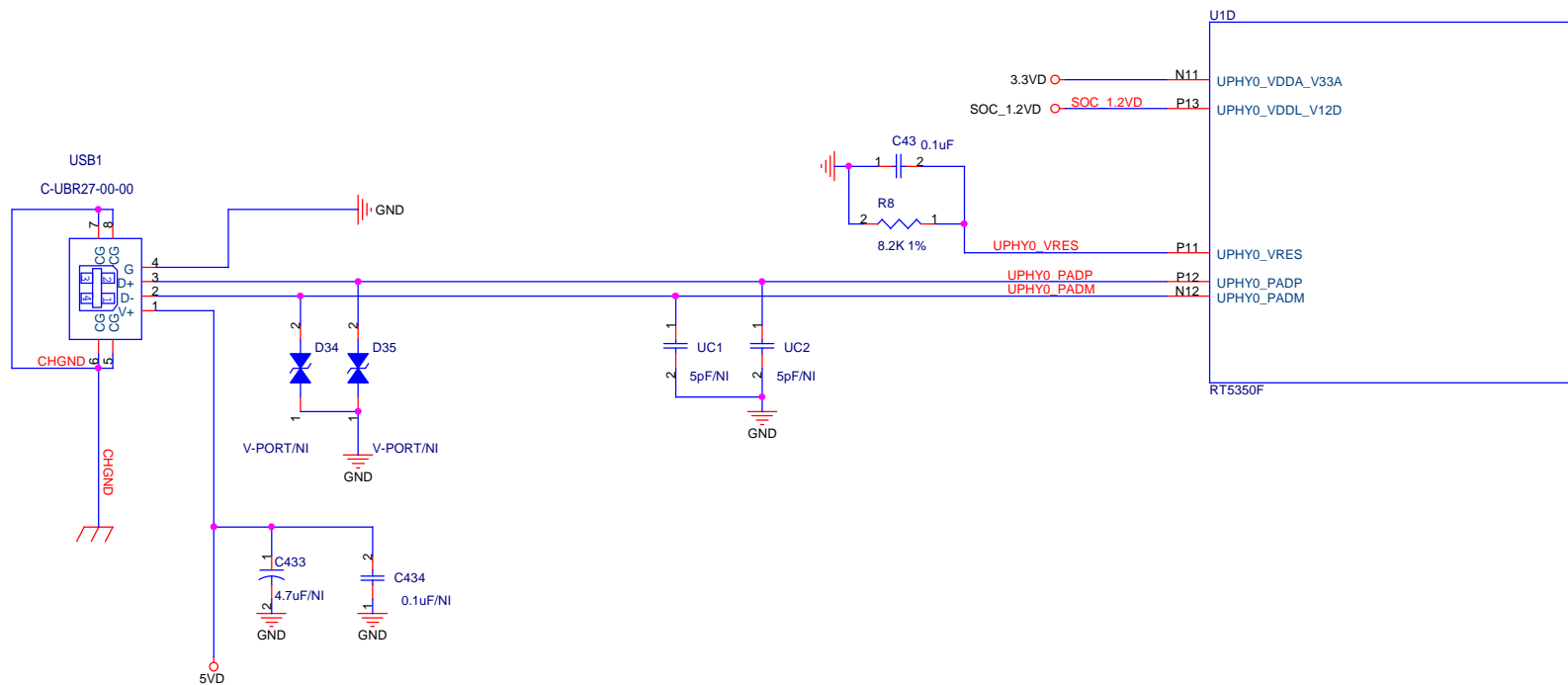
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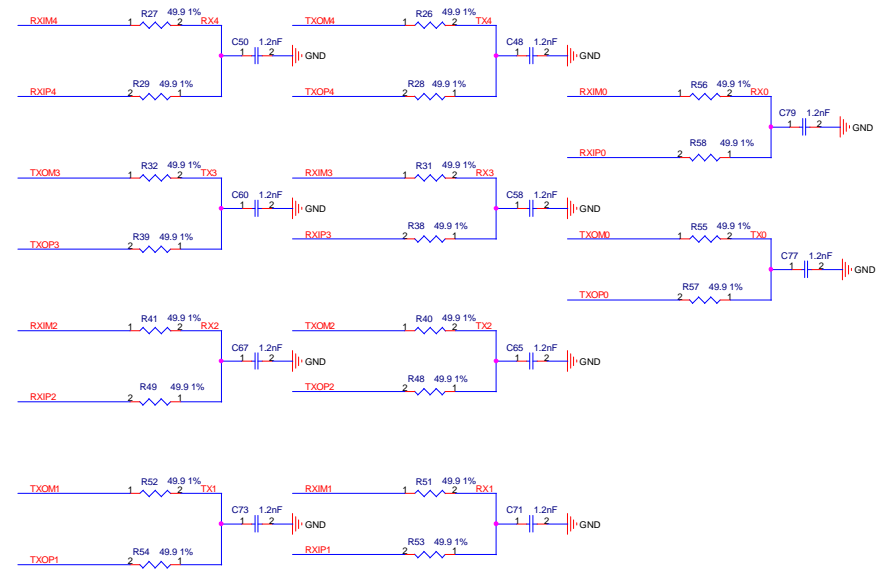
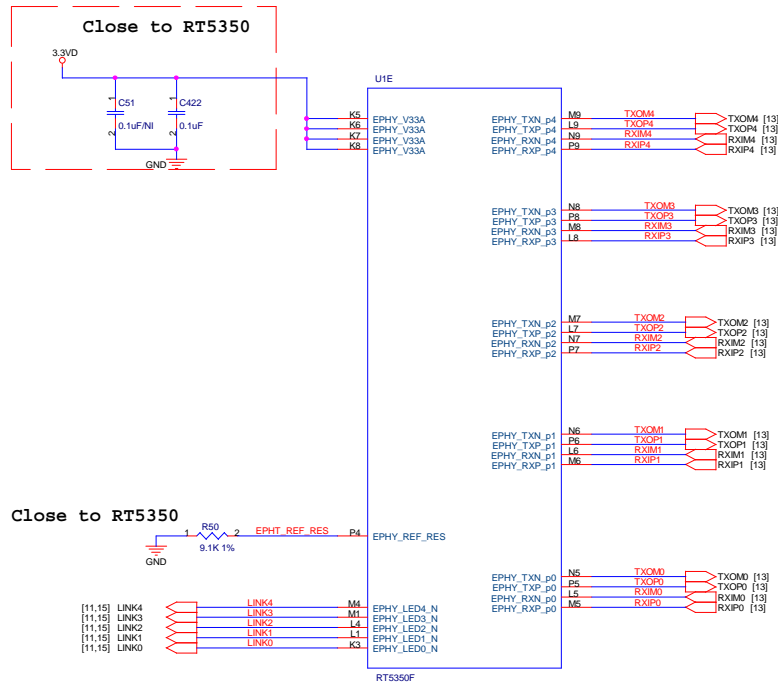
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		Title 11n 2.4G AP-RT5350			
		Size B	Document Number AP-RT5350-V22.SCH		Rev 2.2
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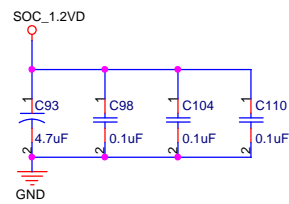
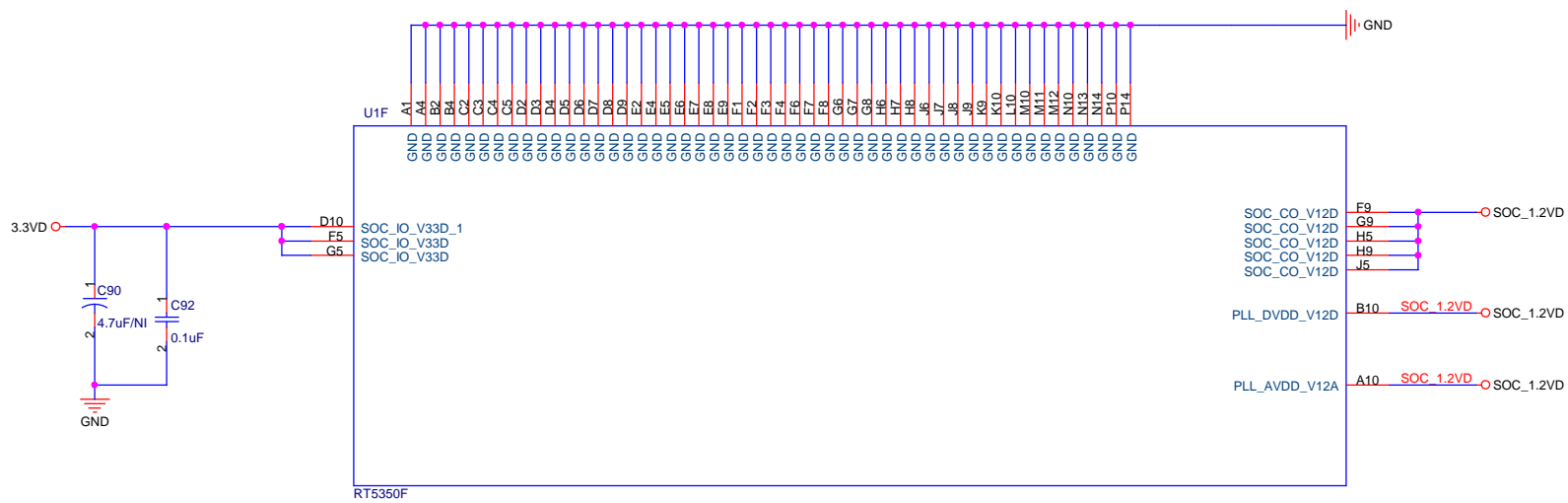


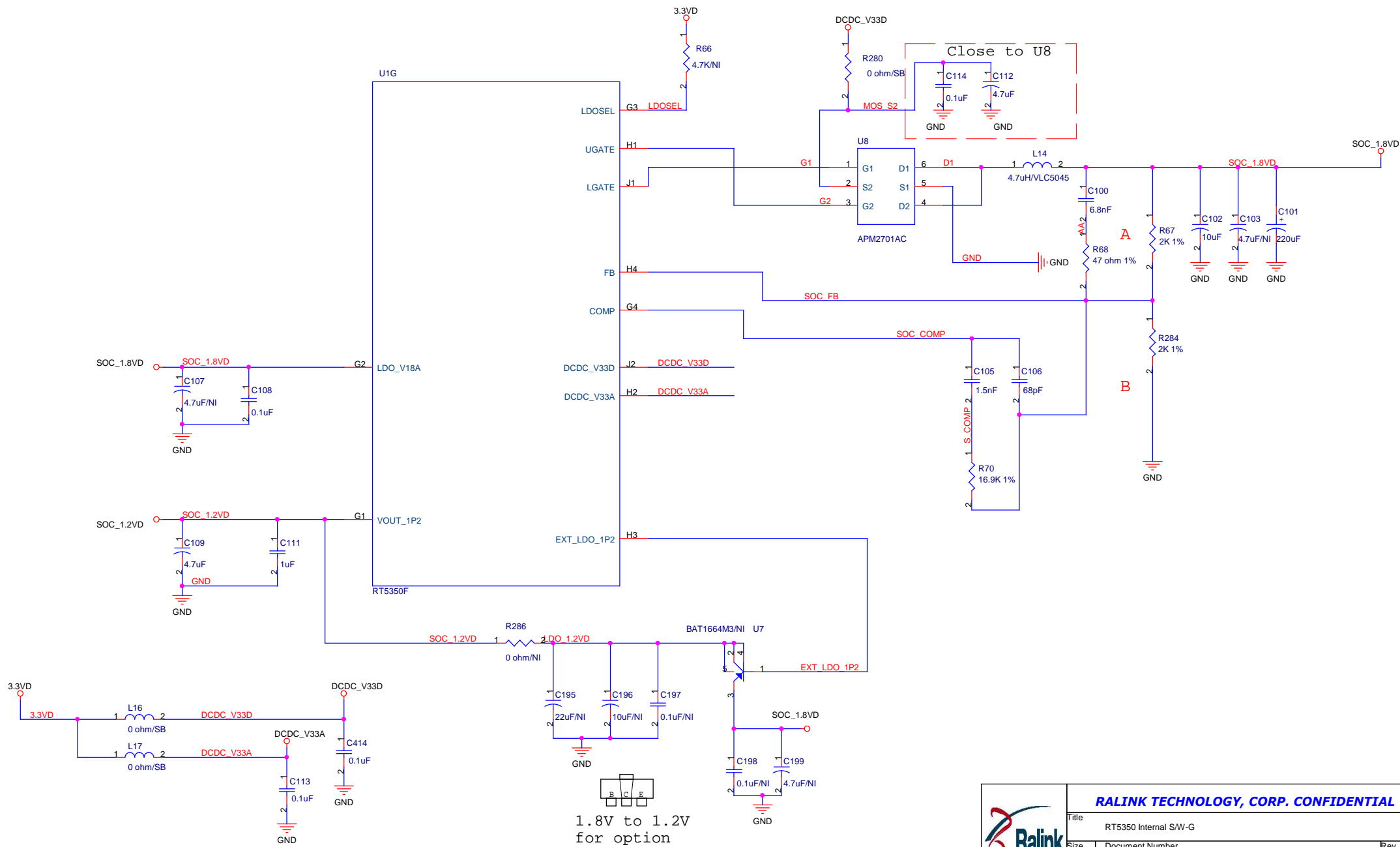






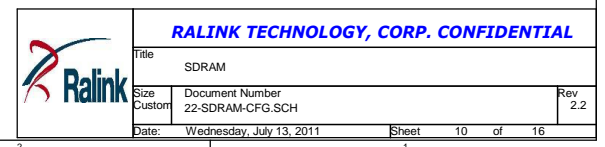


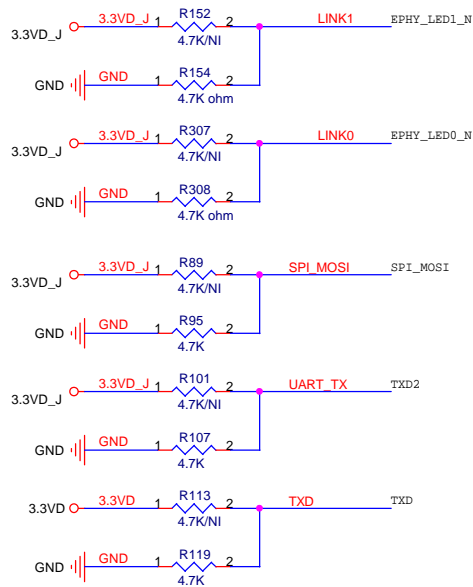
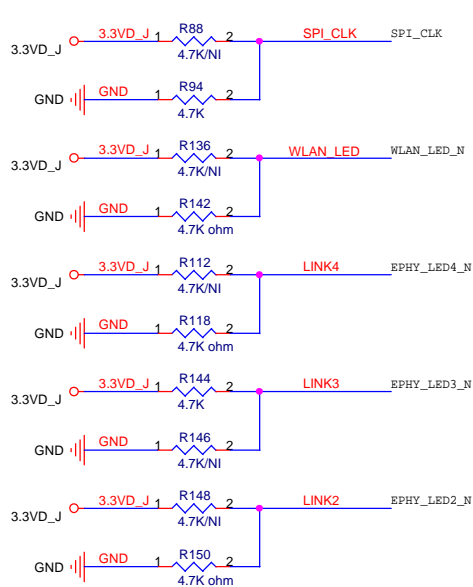
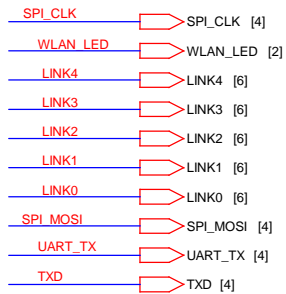






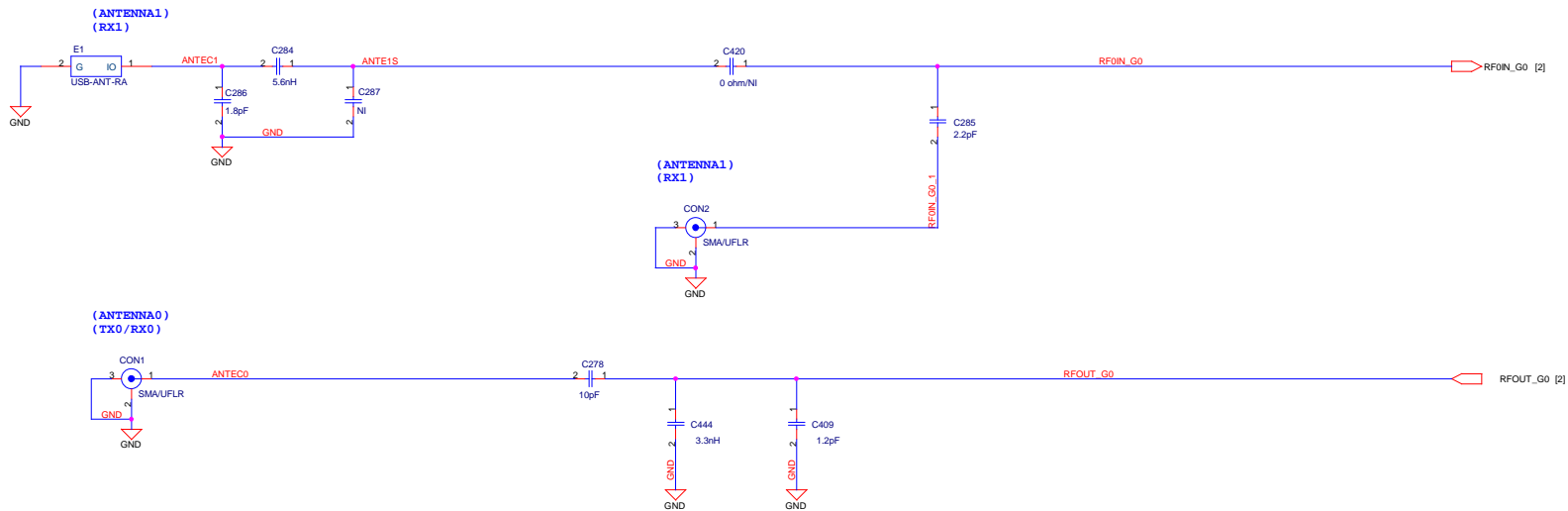






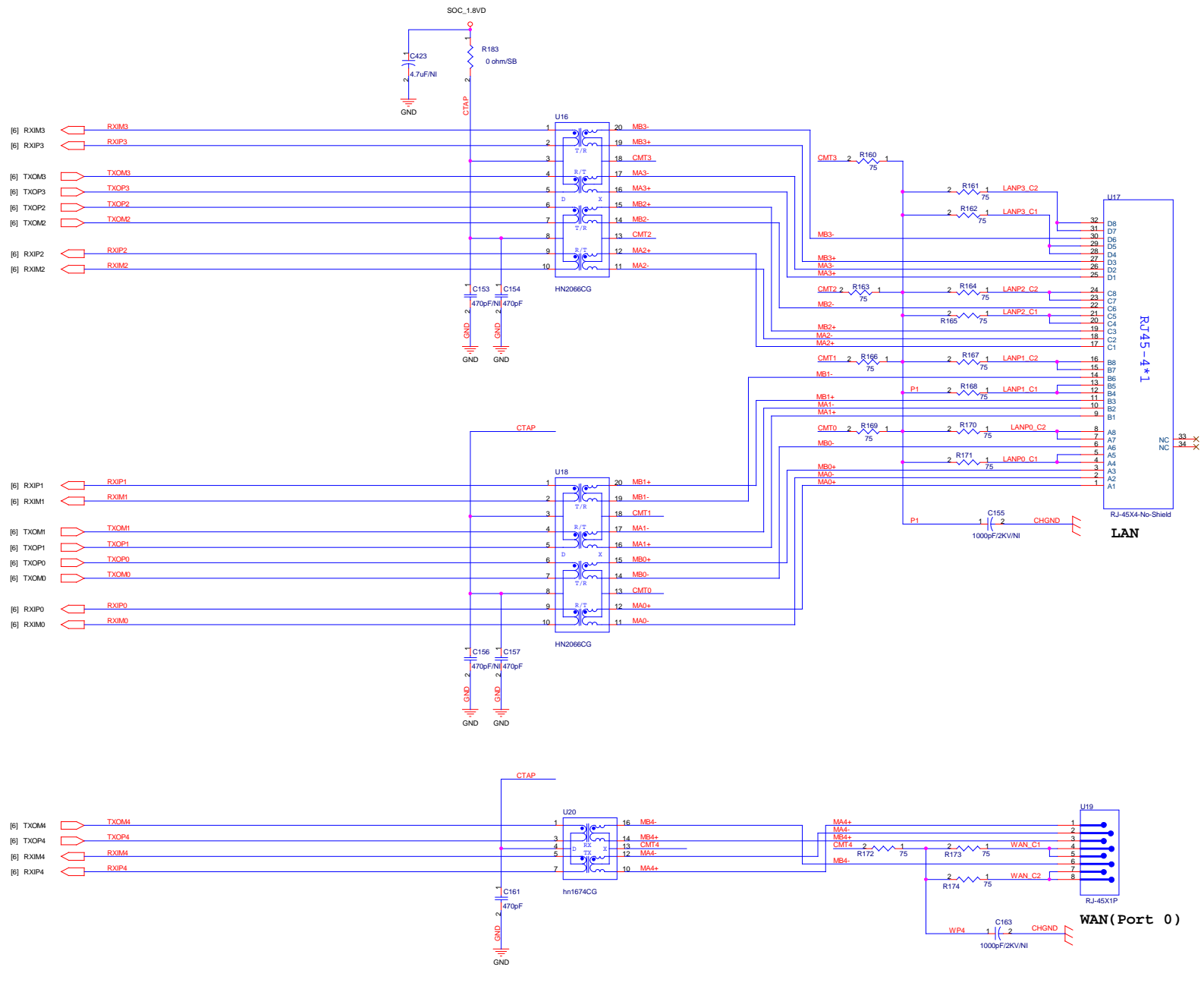
## RT5350 Boot Up Strapping

Pin Name	Description	Value=0	Value=1
SPI_CLK	XTAL_FREQ_HI	20MHz	40MHz
WLAN_LED_N	Big Endian	Little Endian	Big Endian
EPHY_LED4_N	DRAM_FROM_EE	from boot strapping	from EEPROM
{EPHY_LED3_N, EPHT_LED2_N}	DRAM_SIZE	INIC/AP(SDR) 00: 2MB/8MB 01: 8MB/16MB 10: 16 MB/32 MB, 32 MB*2 11: 32MB	
{EPHY_LED1_N, EPHT_LED0_N}	CPU_CLK_SEL	CPU clock select 00: 360MHz 01: Reserved 10: 320MHz 11: 300MHz	
{SPI_MOSI, TXD2, TXD}	CHIP_MODE[2:0]	A vector to set chip function/test/debug modes 000 : Normal mode(boot fromSPI serial flash) 001 : iNIC-USB mode 010 : Reserved 011 : Reserved 100 : Reserved 101 : iNIC-PHY mode 110 : SCAN mode 111 : TEST/DEBUG mode	

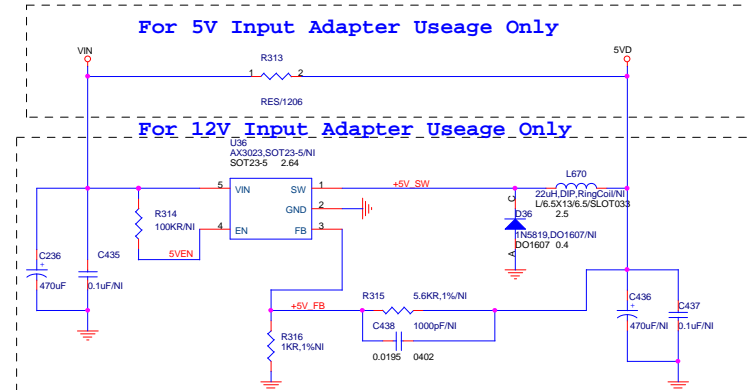
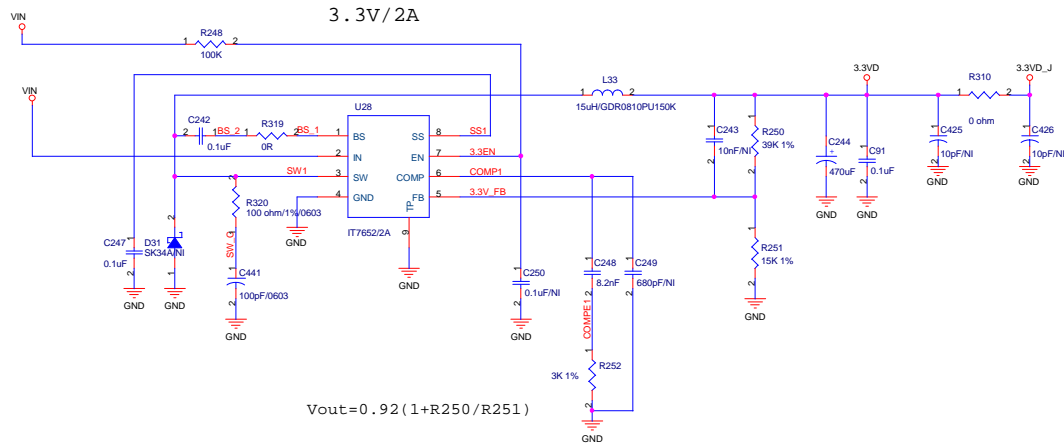
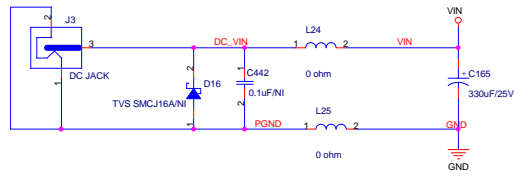


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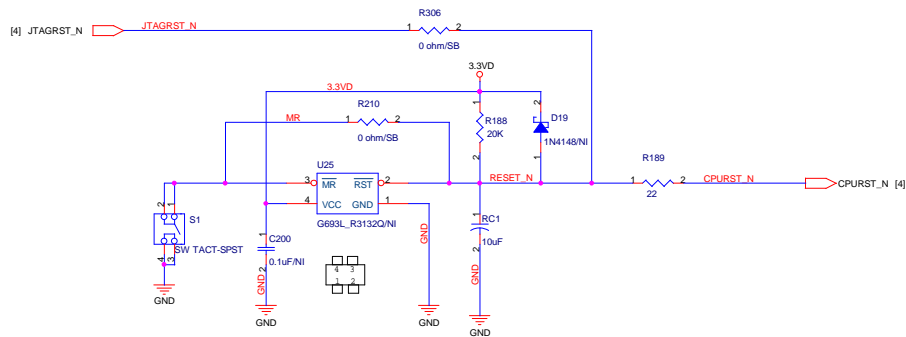
Title	Front End		
Size	Document Number		
C	31-Front End.SCH	Rev	2.2
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## 5V DC ADAPTER



## Reset Circuit




GPIO13

[4] SECU\_LED  SECU\_LED

SMD LED

SECLED

SECURITY LED

[2] WLAN\_LED  WLAN\_LED

WLED

Wireless ACT LED


GPIO9

[4] PWR\_LED  PWR\_LED

PWRLED


System/Power LED

SECU\_LED

[6] LINK4  LINK4


WANLED

WAN Port LED

[6] LINK3  LINK3


LANLED3

LAN3 Port LED

[6] LINK2  LINK2


LANLED2

LAN2 Port LED

[6] LINK1  LINK1

LANLED1


LAN1 Port LED

[6] LINK0  LINK0

LANLED0

LAN0 Port LED

GPIO0

[4] WPS\_PBC  WPS\_PBC

WPS\_PBC J

WPS PBC

GPIO14

[4] WPS\_LED  WPS\_LED

WPSLED+

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Title		LED
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B	6-LED.SCH	2.2
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