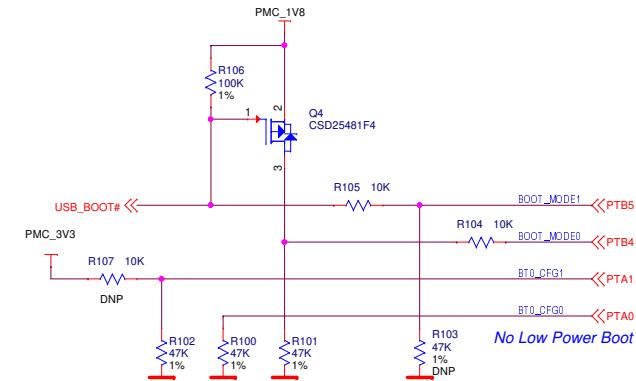
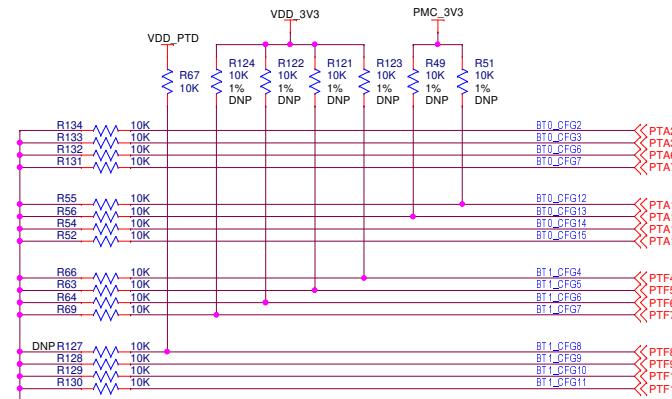


BOOT CONFIGURATION



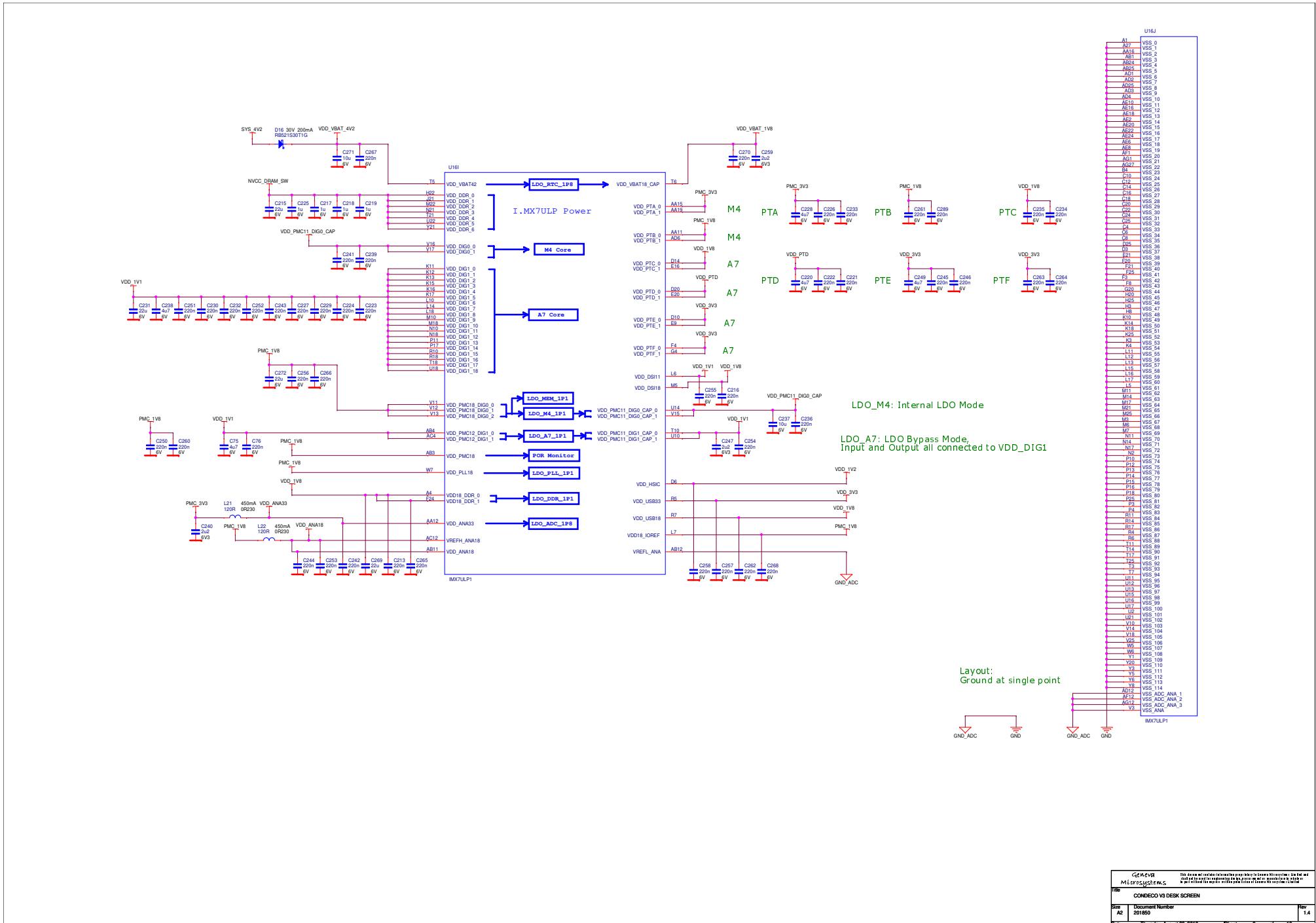
SW4-1
ON Serial Downloader
OFF Internal Boot

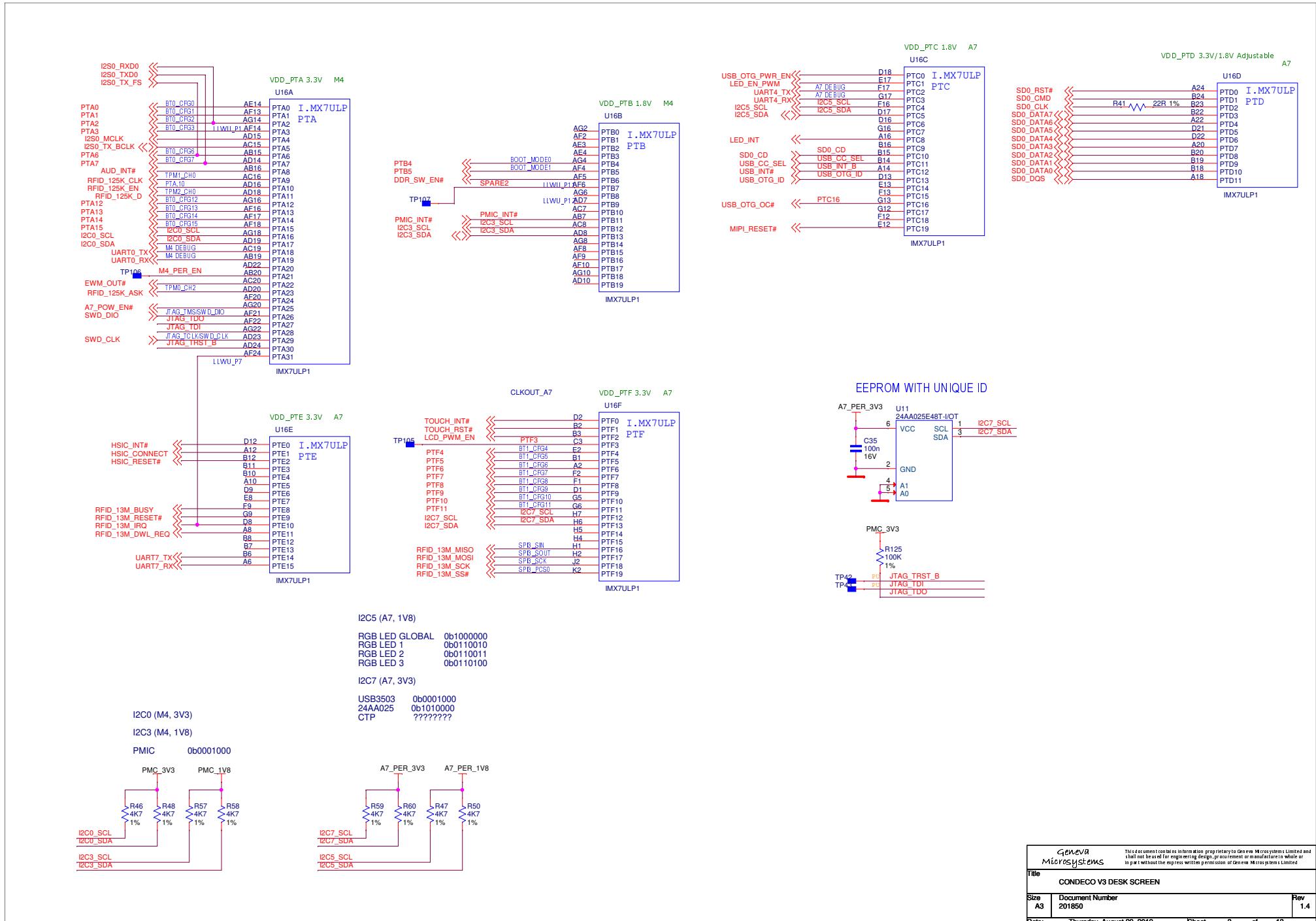
SW4-2
ON Dual A7/eMMC/SD and M4/QSPI
OFF Single A7 eMMC/SD

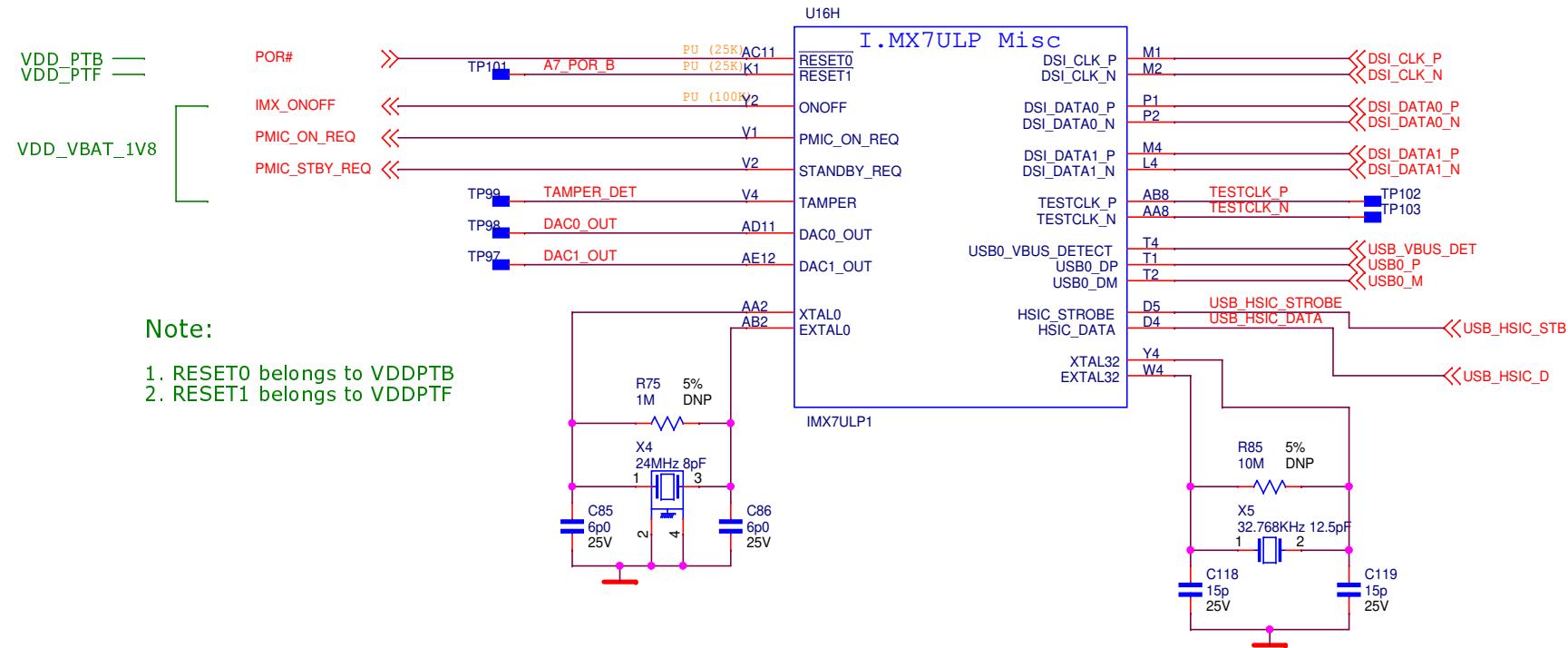
FUSE MAP

	0	0	0	0	0	0	0	0	1	0
TYPE	BT0_CFG15	BT0_CFG14	BT0_CFG13	BT0_CFG12	BT0_CFG7	BT0_CFG6	BT0_CFG3	BT0_CFG2	BT0_CFG1	BT0_CFG0
QSPI	QSPI instance 00 - QSPI0 Others - reserved	QSPI device type 00 - 3B read supported 01 - Hyperflash 1.8V 10 - Hyperflash 3.0 11 - 4B read supported	External OSC Freq Selection 00 - 24Mhz 01 - 30Mhz 10 - 19.2 Mhz 11 - 26Mhz (Not supported by ROM in ULP1 TO1.0)	M4 boot interface 0- QSPI Others - reserved	Infinite Loop 0- Debug USE only Others - reserved	Dual Boot 0- Boot from eMMC/SD 1 - Boot from A7/eMMC/SD and M4/QSPI	LP Boot 0 - No Low Power Boot 1 = Boot from M4 with A7 on demand			
SD/eSD										
MMC/eMMC										

	0	0	0	1	0	0	0	0
TYPE	BT1_CFG11	BT1_CFG10	BT1_CFG9	BT1_CFG8	BT1_CFG7	BT1_CFG6	BT1_CFG5	BT1_CFG4
QSPI	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved
SD/eSD	A7 boot interface 000 - USDHC0 001 - USDHC1 others - reserved	USDHC device type 0 - eMMC 1 - SD	SD Speed 000 - Normal 001 - High Others - reserved	SD Loopback Clock Source Sel (for SDR50 and SDR104 only) 0' - through SD pad 1' - direct	Bus width 00 - 4 bit 01 - 8 bit 10 - 4bit DDR 11 - 8 bit DDR	Speed 0 - Normal 1 - High	eMMC fast boot 0 - disable 1 - enable	
MMC/eMMC								





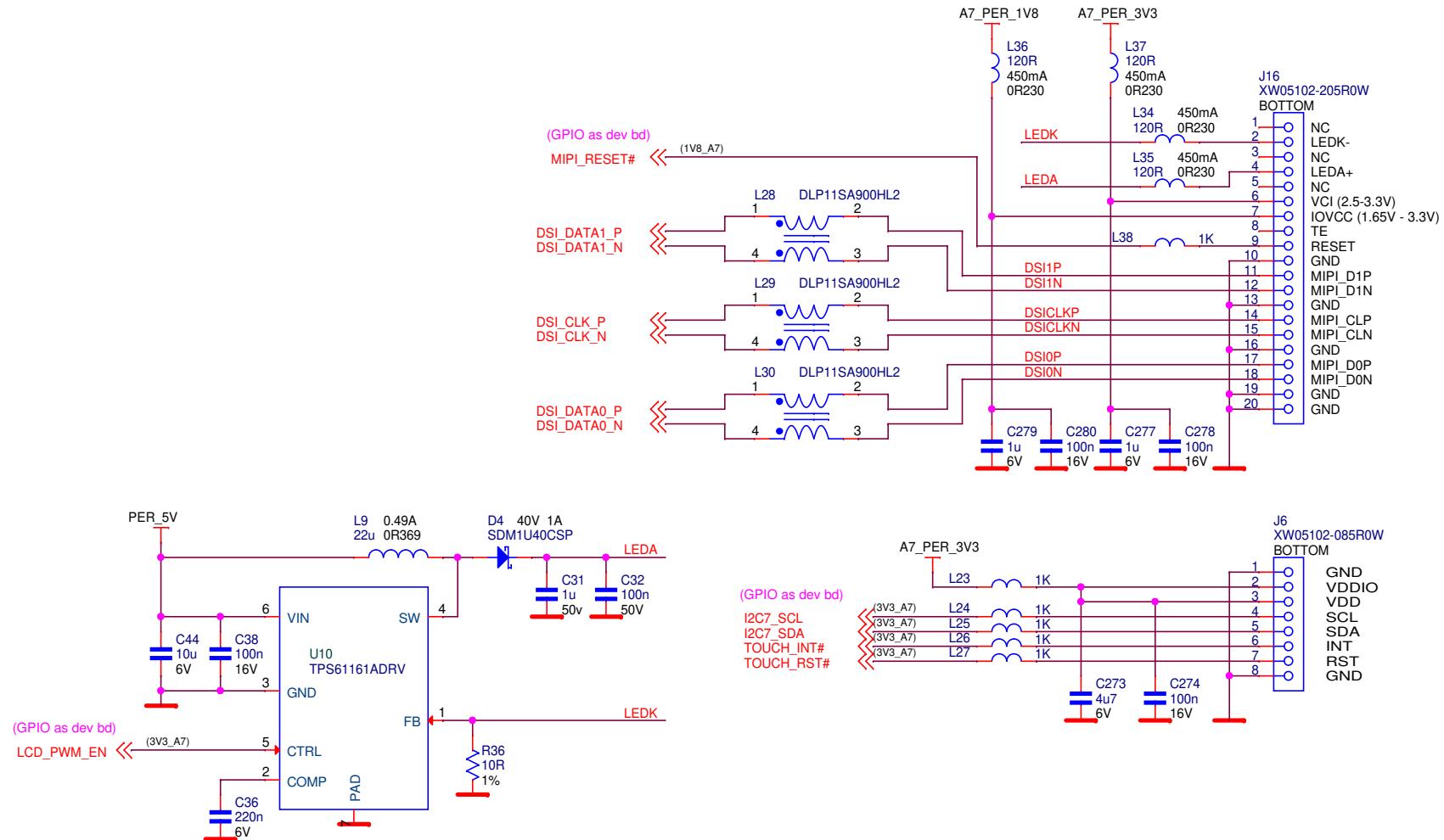


Note:

1. RESET0 belongs to VDDPTB
2. RESET1 belongs to VDDPTF

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Size A4	Document Number 201850	Rev 1.4
Date: Thursday, August 29, 2019	Sheet 4	of 13

3" RGB LCD PANEL



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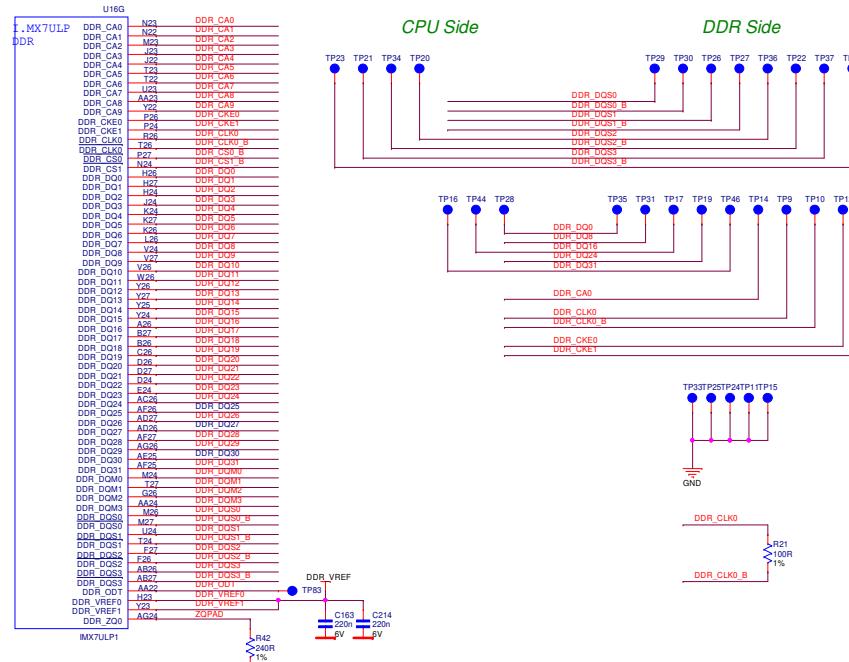
Title CONDECO DESKSCREEN V2

Size A4 Document Number 201850

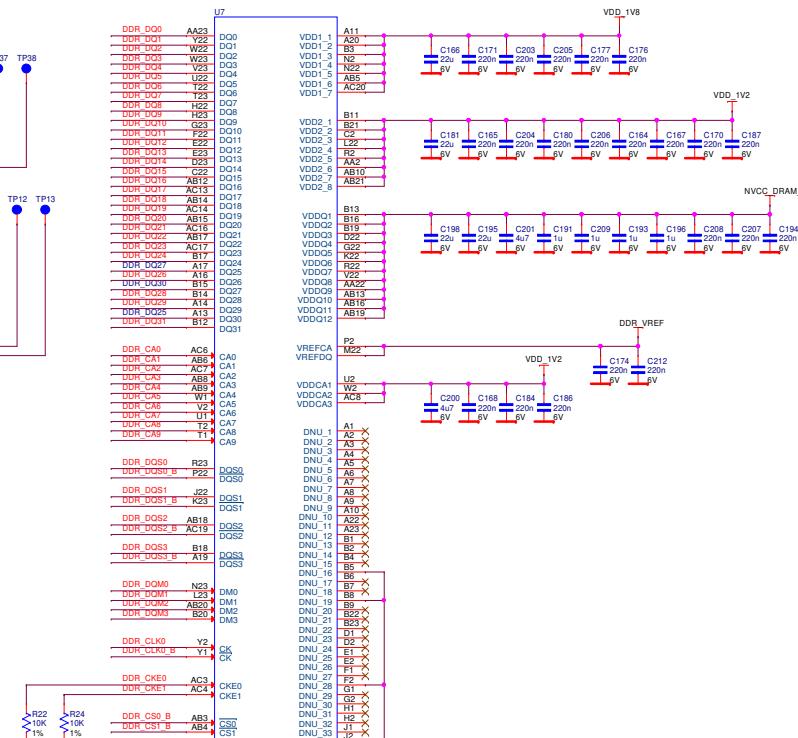
Rev 1.4

Date: Thursday, August 29, 2019

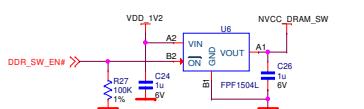
Sheet 5 of 13



LPDDR3 MEMORY



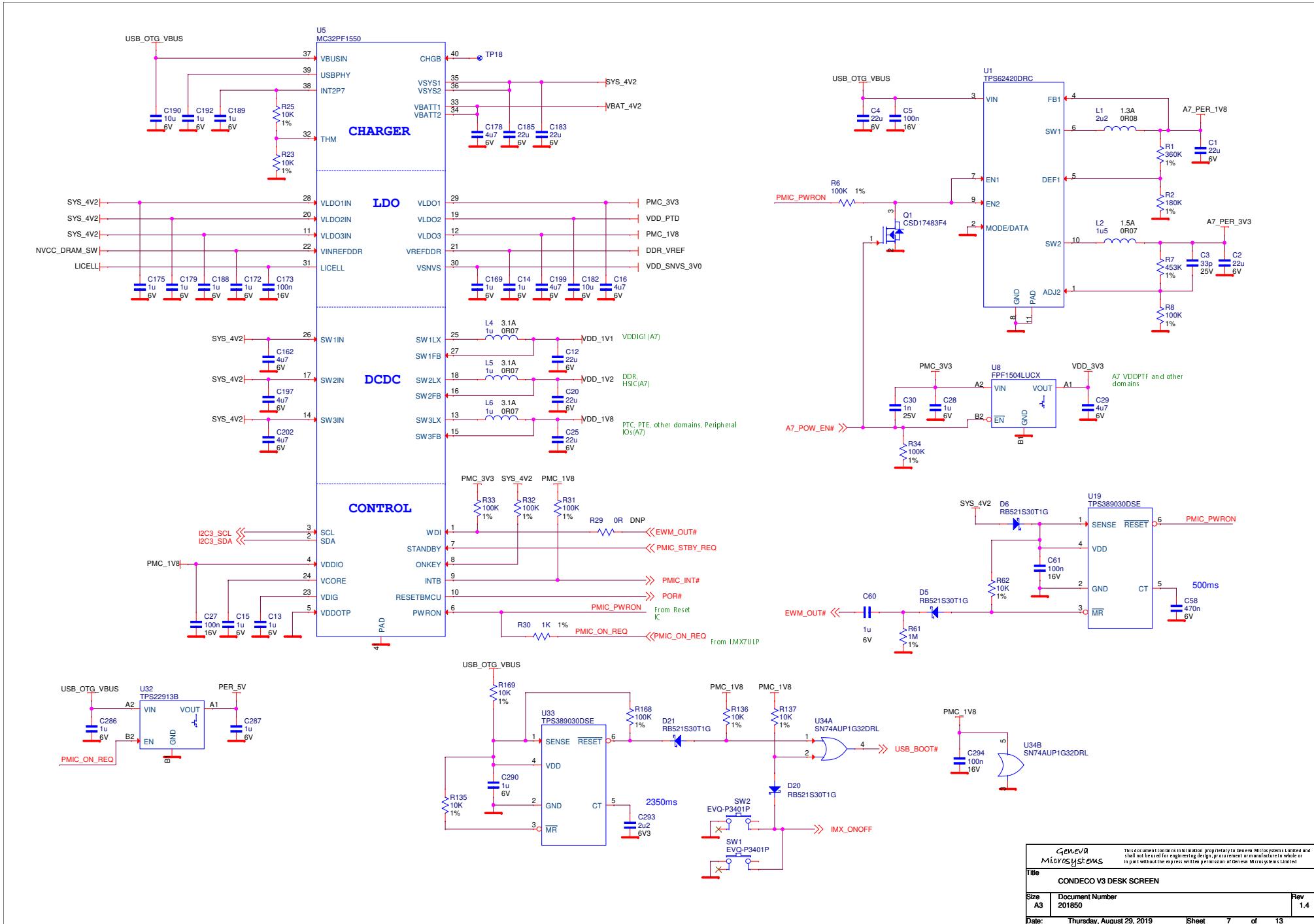
LPDDR3 Power Switch



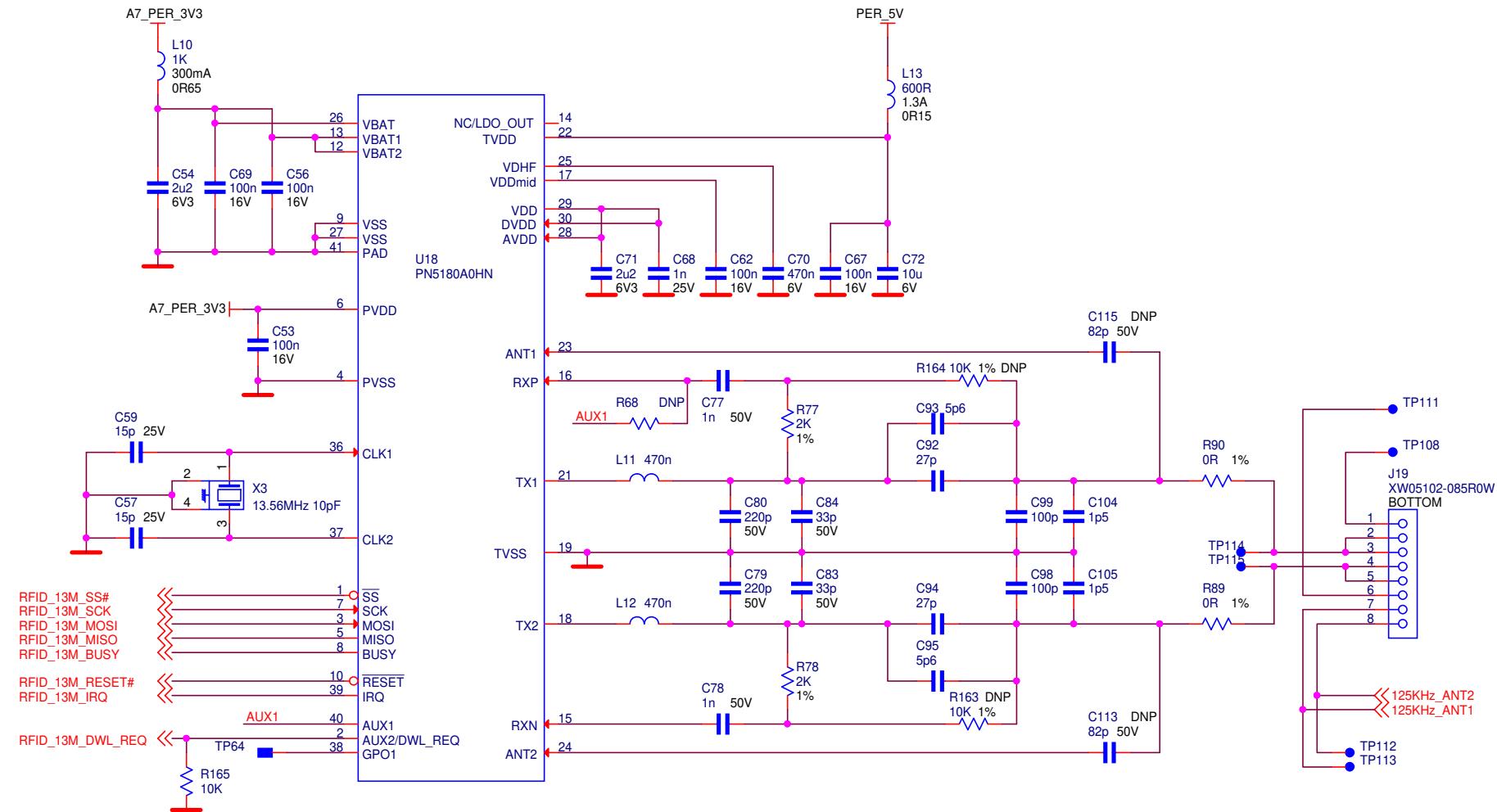
Note:
Low enable:
Default low to output power for ULP and LPDDR3 VDDQ;
Set high to disable power when LPDDR3 is in self refresh mode.

1GB NT6CL256T32C0
Total Memory

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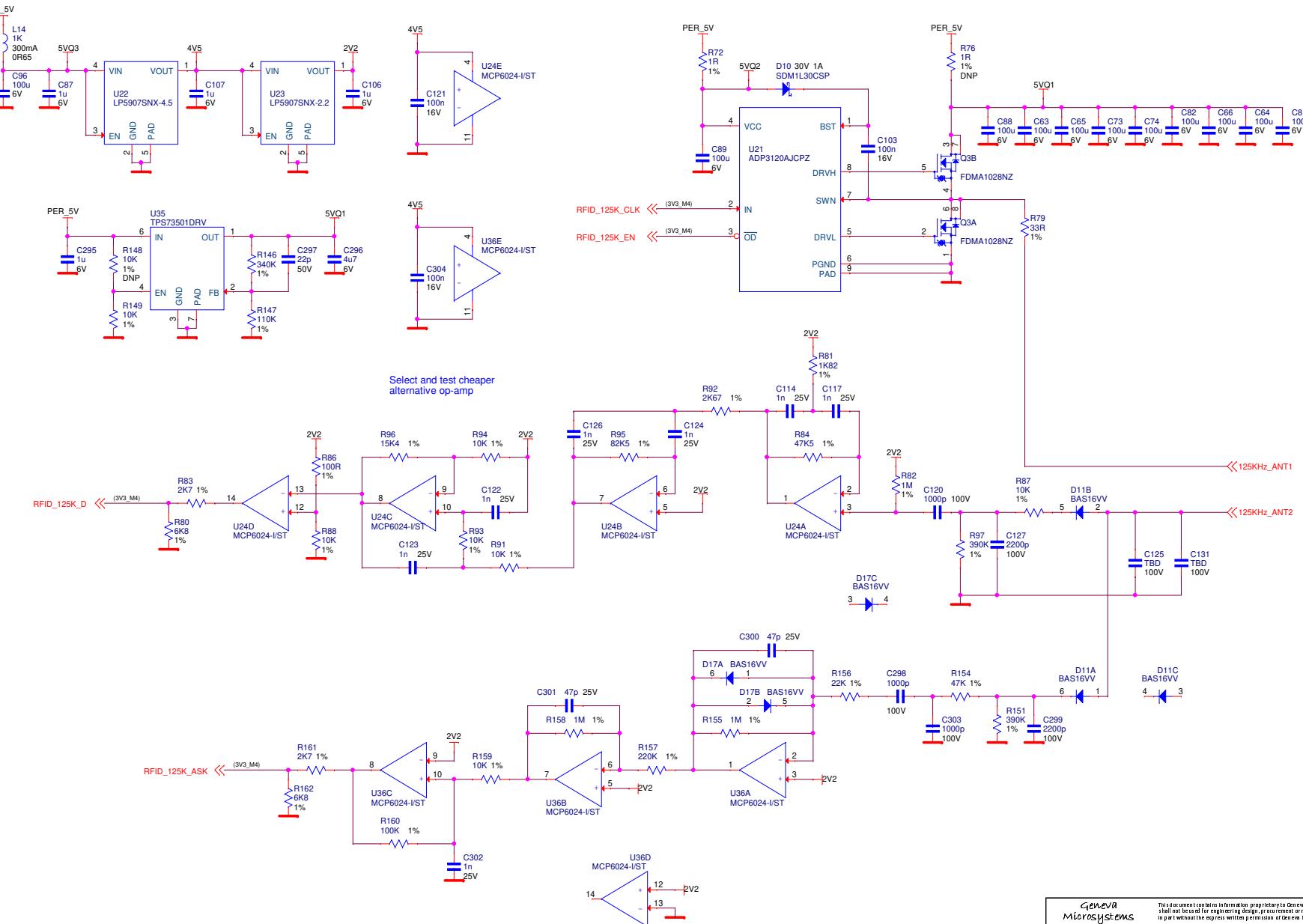
13.56MHz NFC/RFID



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Size	Document Number	Rev
A4	201850	1.4

Date: Thursday, August 29, 2019 Sheet 8 of 13

125KHz RFID



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1

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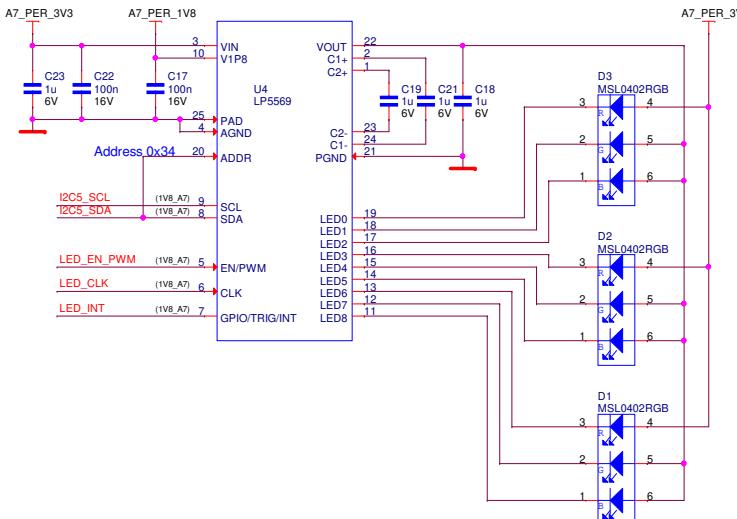
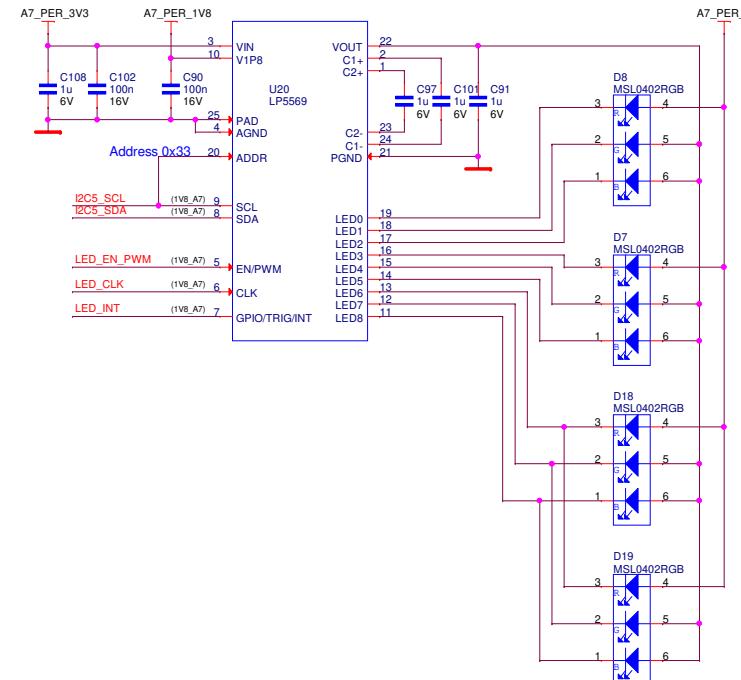
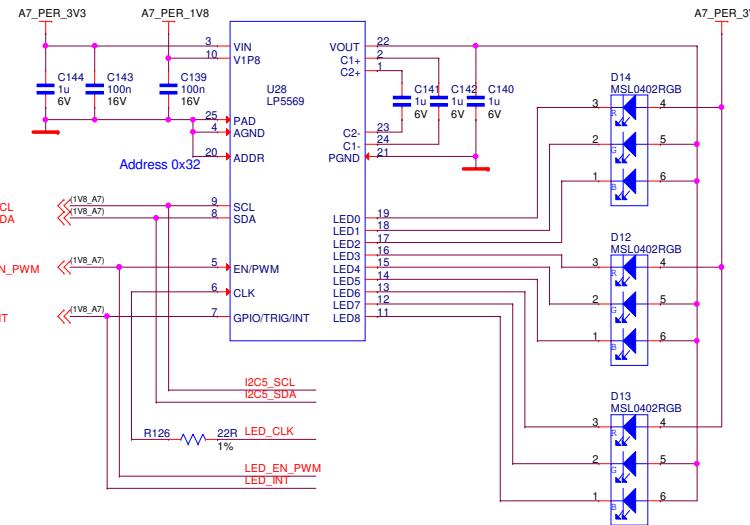
13

Date: Thursday, 7 August

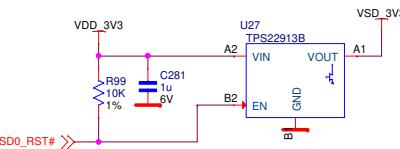
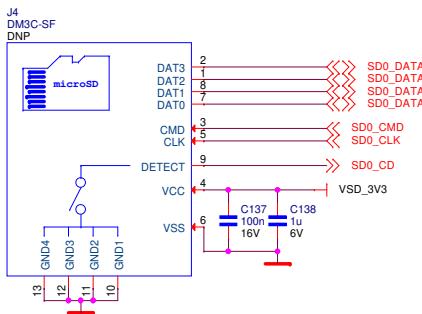
18

RGB LED

This first device must have its CLK set to output so that all are synchronised.

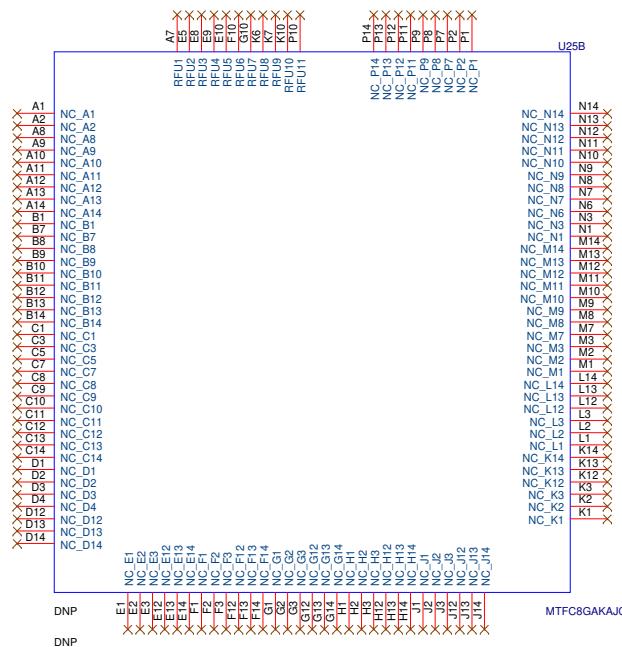


SD AND eMMC



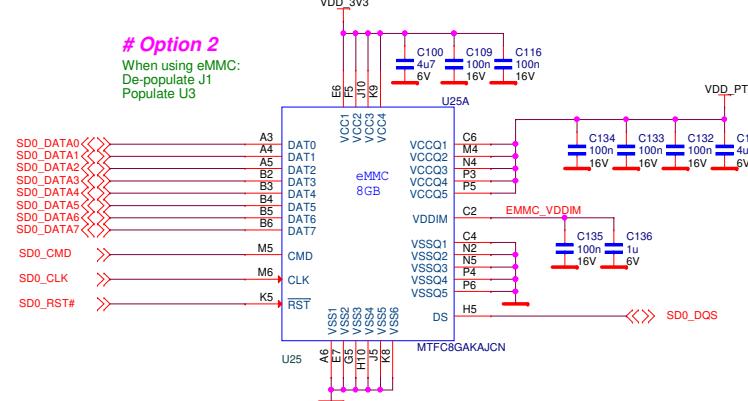
Note:

1. ROM Code will reset the SD power during boot up through SD_RST#;

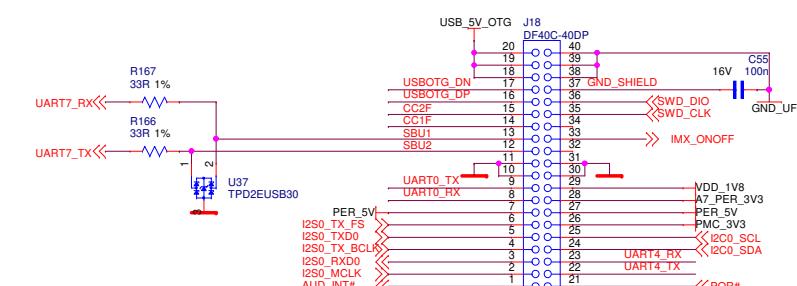
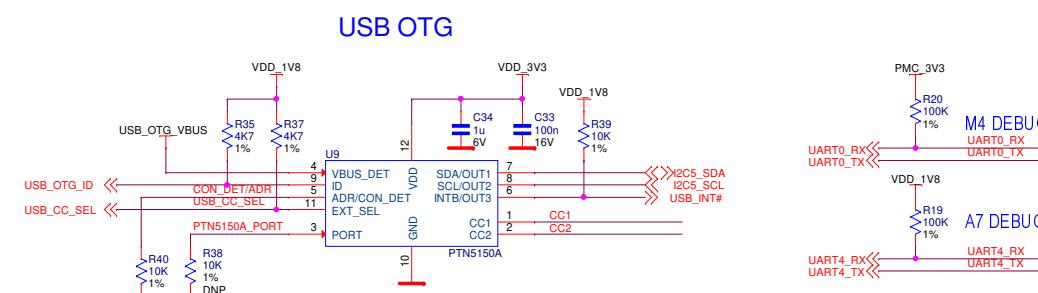
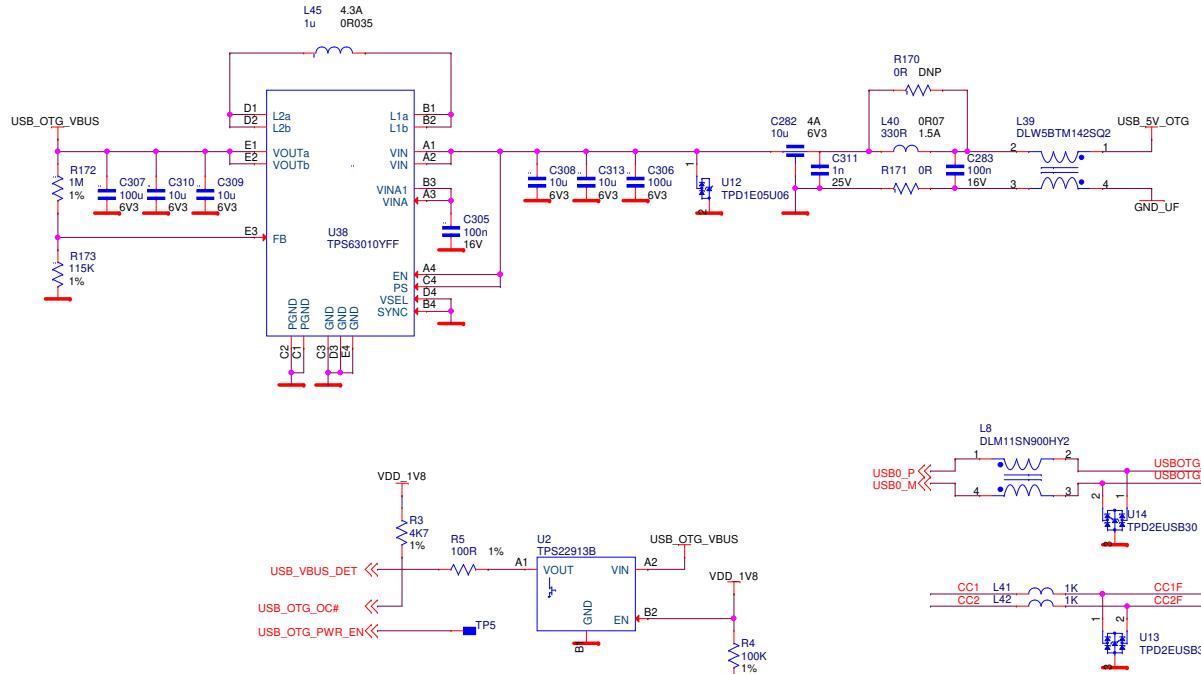


Option 2

When using eMMC:
De-populate J1
Populate U3



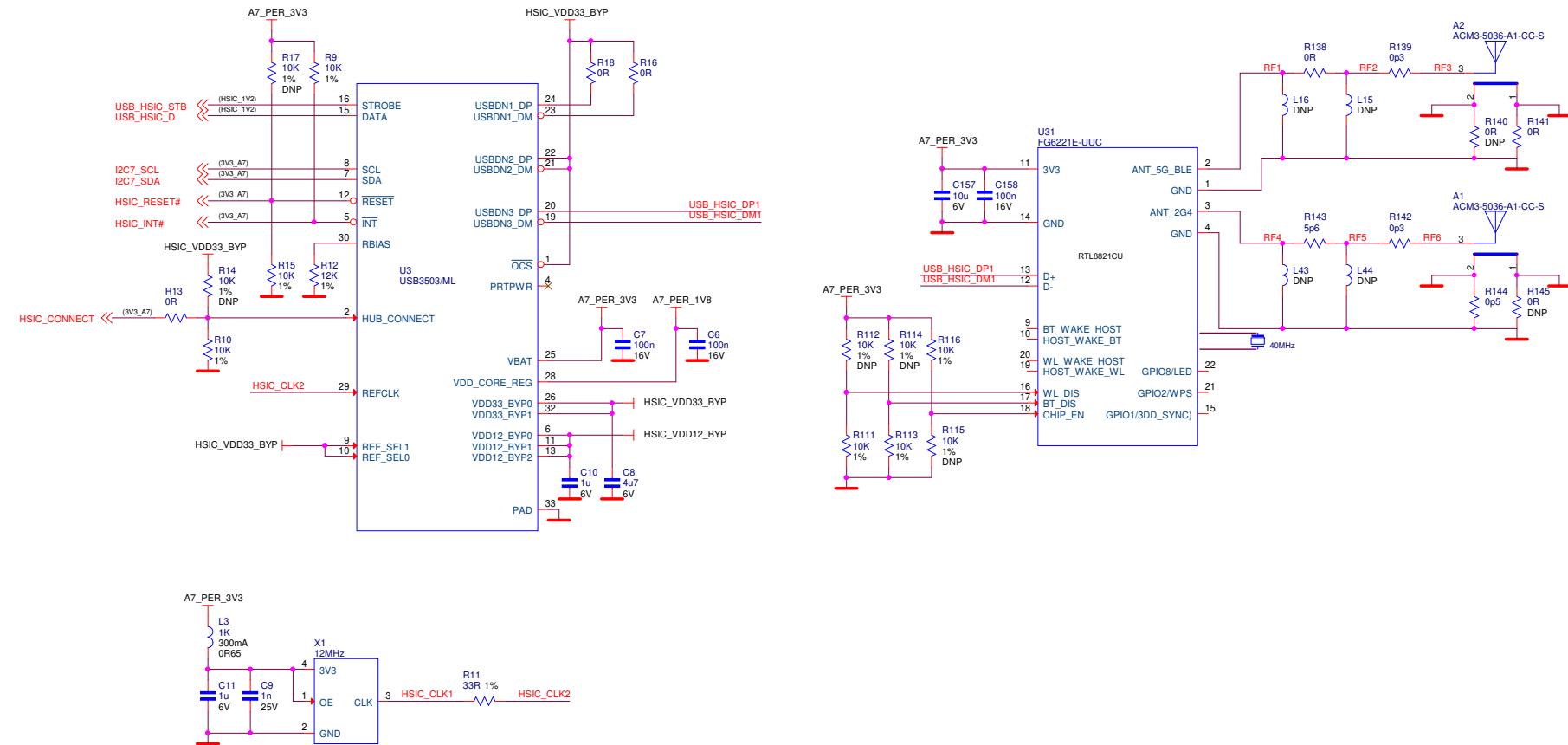
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Rev	1.4	Date	Thursday, August 29, 2019



S1
3p2MM S2
3p2MM

S3
3MM S4
3MM S5
3MM S6
3M

WIFI AND BLE



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