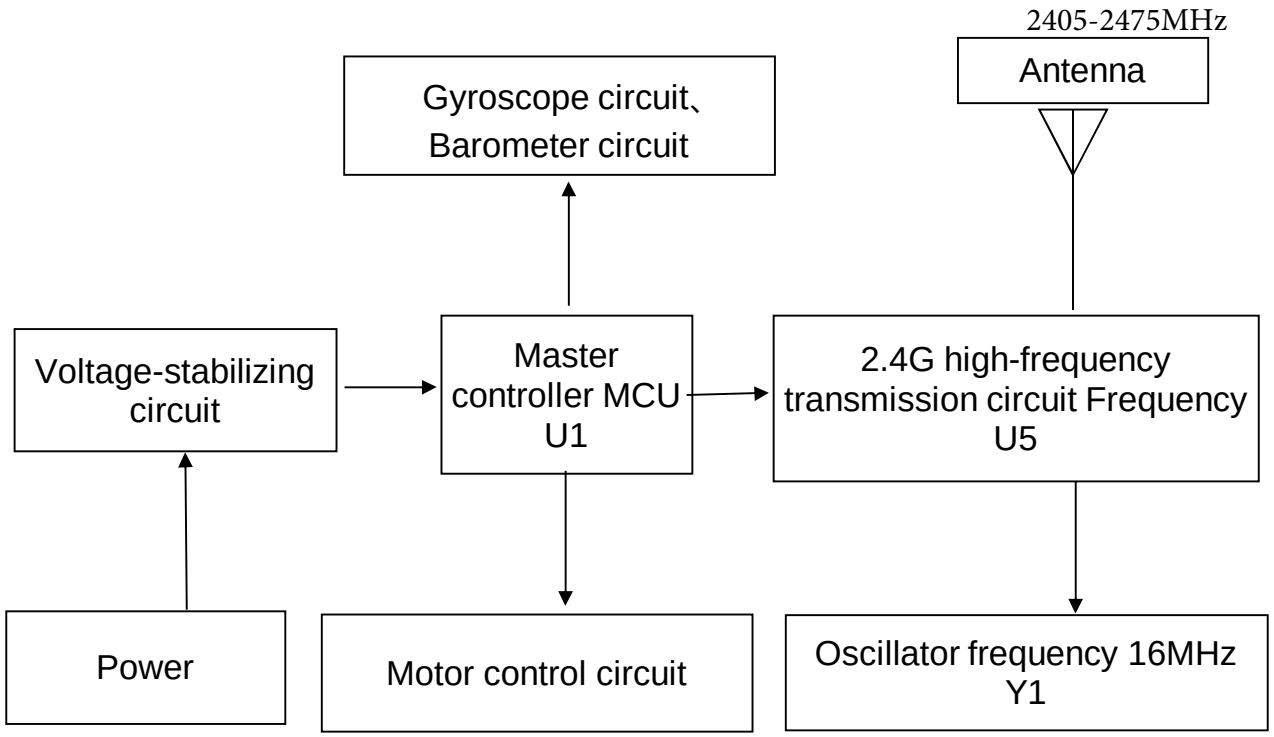
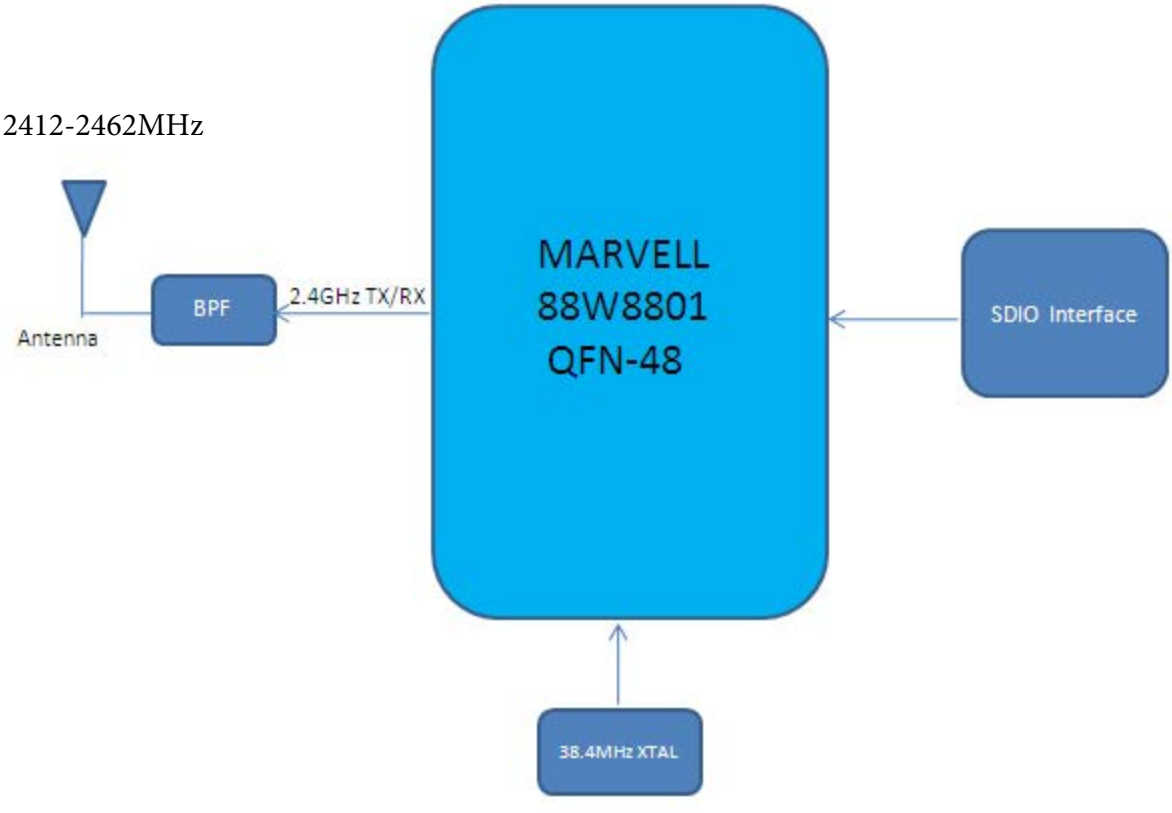
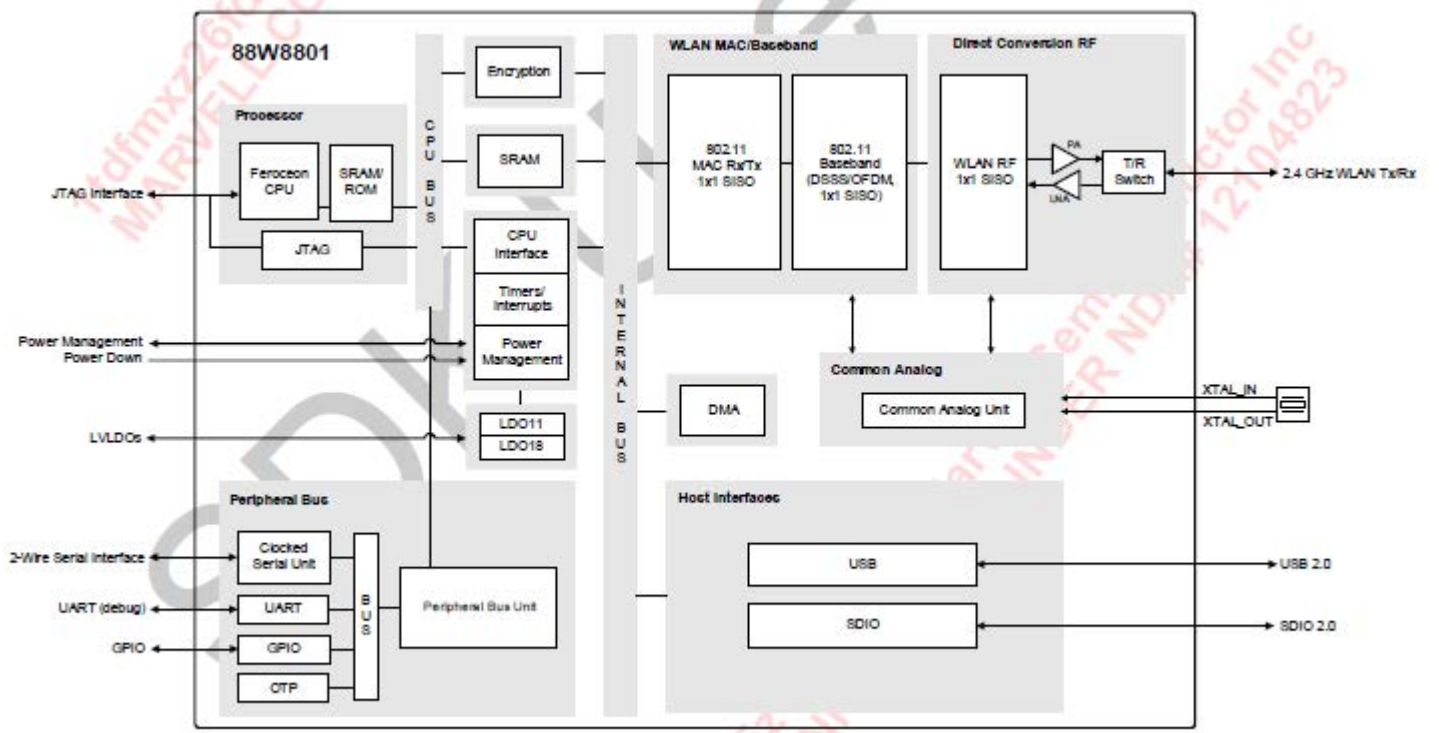
Block Diagram(RX)



2412-2462MHz




88W8801
Processor
Feroceon CPU
SRAM/ ROM
JTAG
JTAG Interface
CPU BUS
Encryption
SRAM
CPU Interface
Timers/ Interrupts
Power Management
LDO11
LDO18
INTERNAL BUS
WLAN MAC/Baseband
802.11 MAC Rx/Tx 1x1 SISO
802.11 Baseband (DSSS/OFDM, 1x1 SISO)
Direct Conversion RF
WLAN RF 1x1 SISO
PA
LNA
T/R Switch
2.4 GHz WLAN Tx/Rx
Power Management
Power Down
LVLDOs
DMA
Common Analog
Common Analog Unit
XTAL_IN
XTAL_OUT
Peripheral Bus
Clocked Serial Unit
UART
GPIO
OTP
BUS
Peripheral Bus Unit
2-Wire Serial Interface
UART (debug)
GPIO
Host Interfaces
USB
SDIO
USB 2.0
SDIO 2.0