

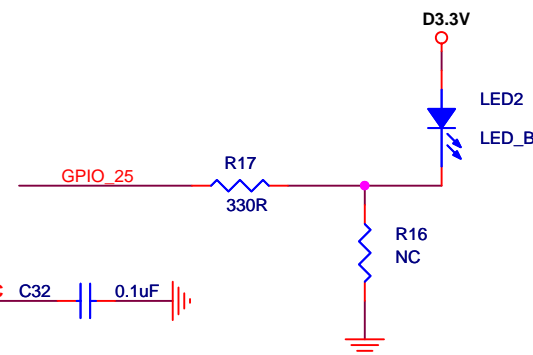
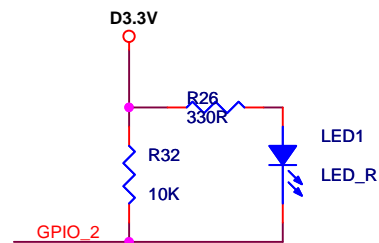
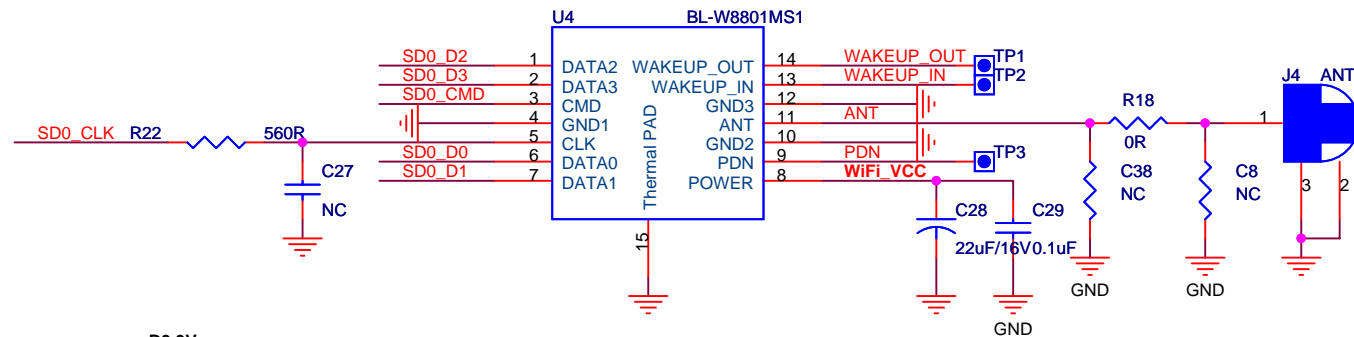
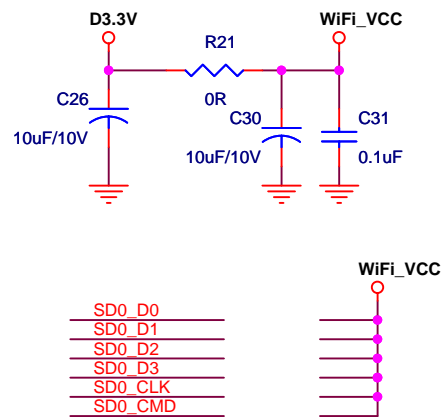
FH8610&Flash		
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Date:	Wednesday, September 28, 2016	Sheet 1 of 3

2 D3.3V
D1.2V
2 GND

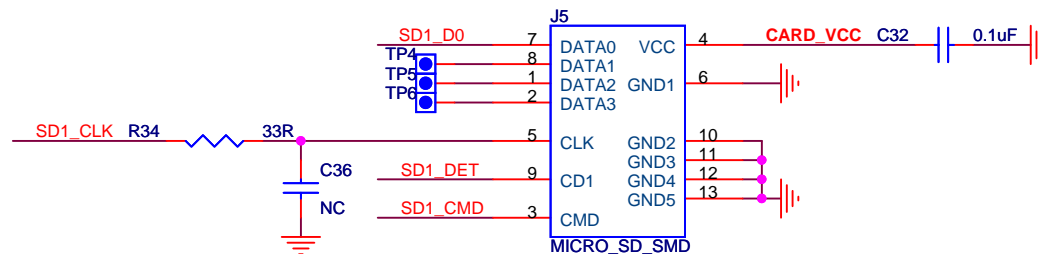
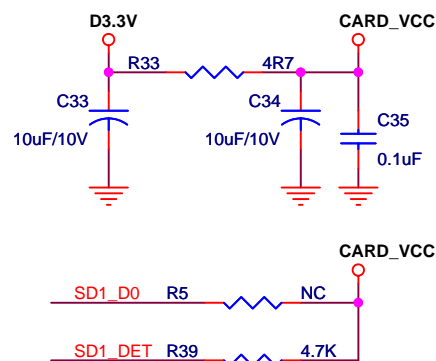
SPI0_CS1

UART1_IN
UART1_OUT

WiFi模组 (SDIO)



SD卡 (1位数据模式)

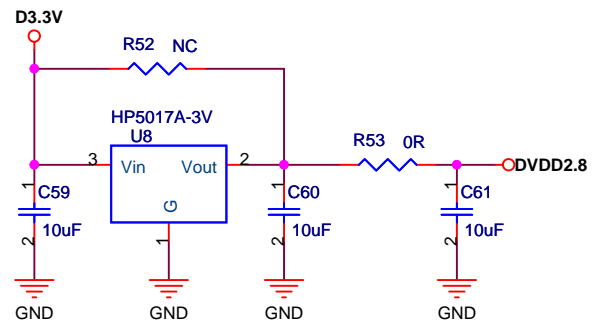
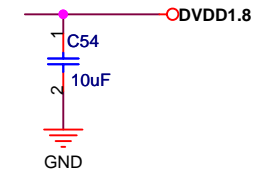
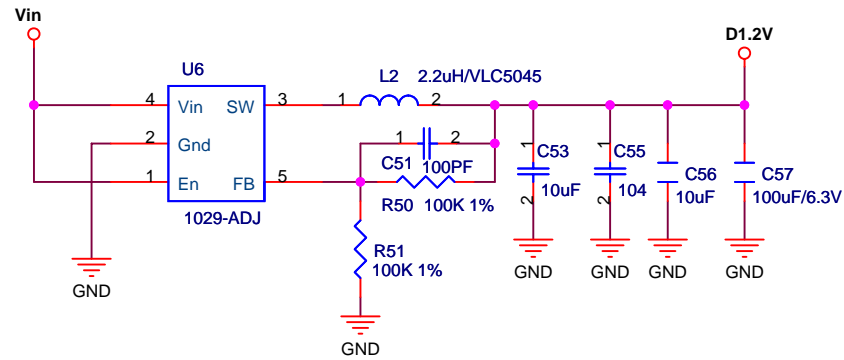
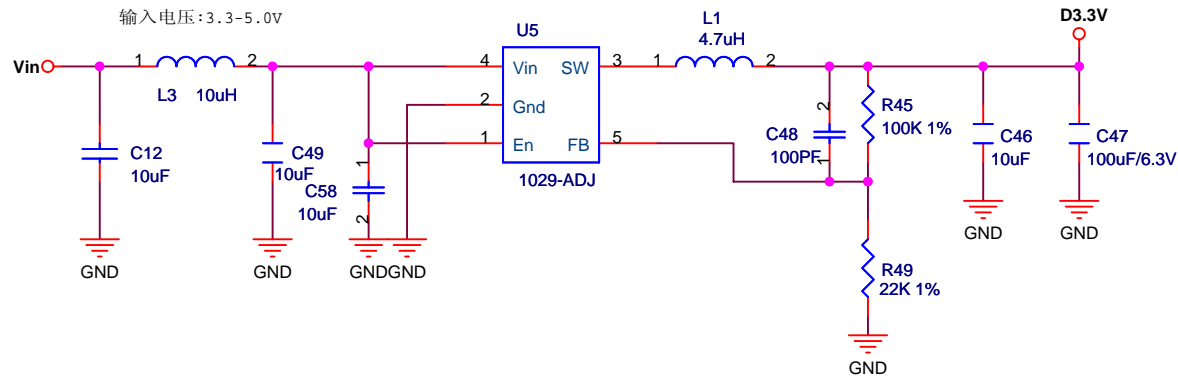


1 SPI0_CS1	» SPI0_CS1
1 GPIO_2	» GPIO_2
1 GPIO_3	» GPIO_3
1 GPIO_10	» GPIO_10
1 GPIO_25	» GPIO_25
1 SD0_D[3:0]	» SD0_D[3:0]
1 SD0_CLK	» SD0_CLK
1 SD0_CMD	» SD0_CMD
1 SD1_D0	» SD1_D0
1 SD1_CLK	» SD1_CLK
1 SD1_CMD	» SD1_CMD
1 SD1_DET	» SD1_DET
1 D3.3V	» D3.3V
1 GND	» GND

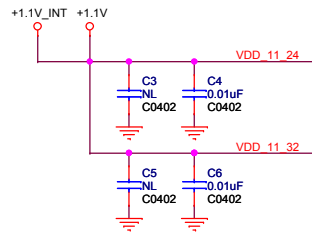
WIFI&TF CARD+MIC

Size A4	Document Description	Rev A
Date: Wednesday, September 28, 2016		Sheet 2 of 3

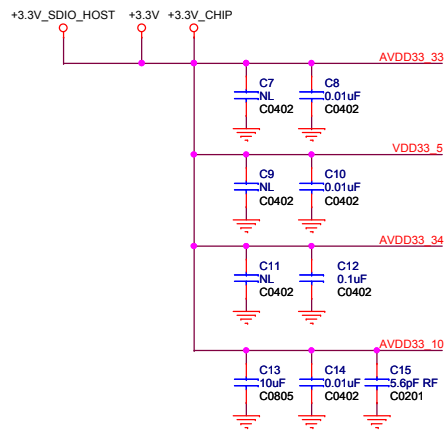
Power Supply



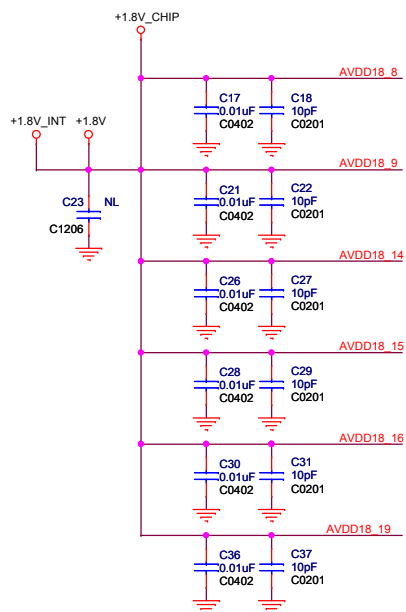
POWER DECOUPLING



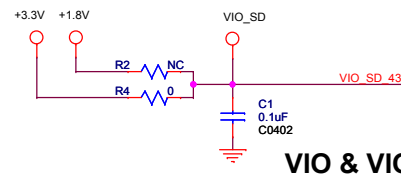
1.1V



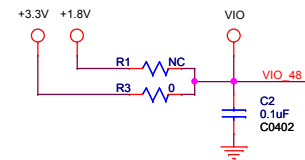
3.3V



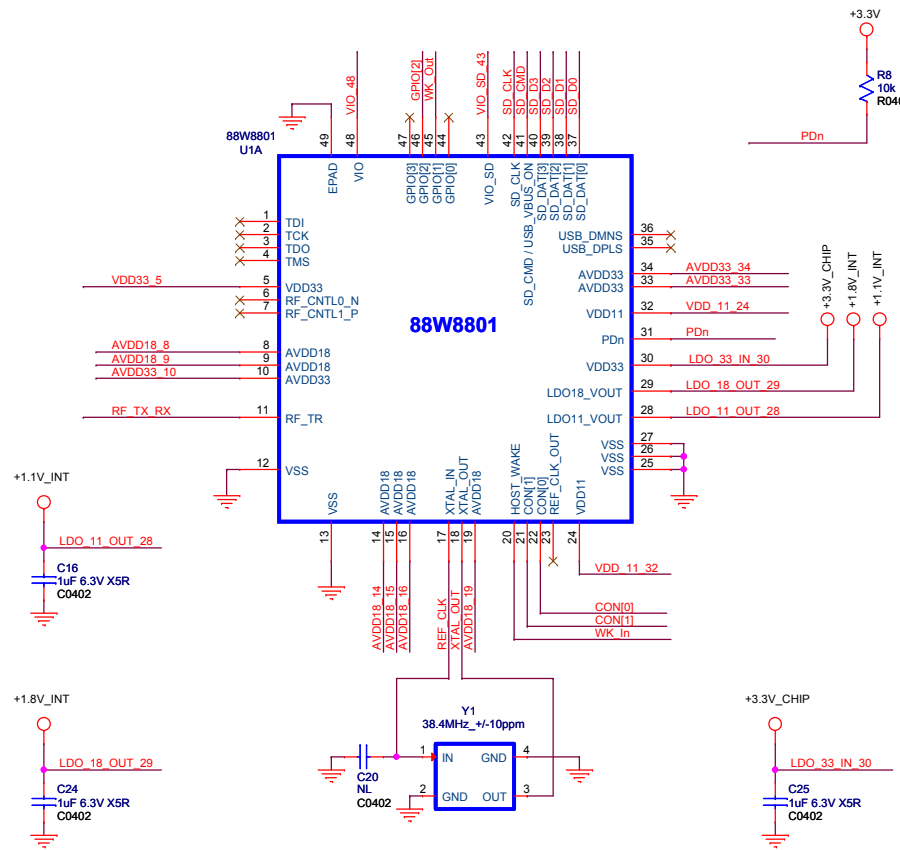
1.8V



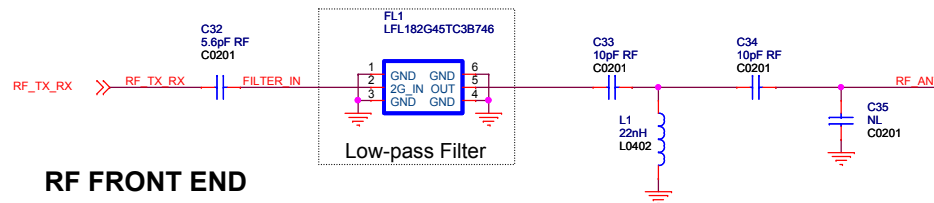
VIO & VIO_SD



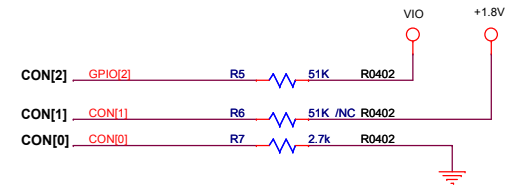
88W8801 SOC



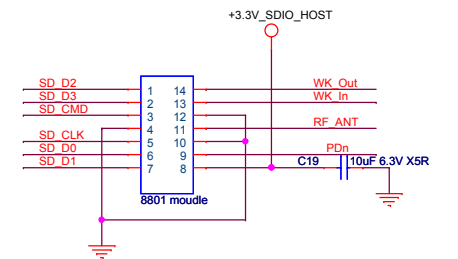
RF FRONT END



CON	FUNCTION
2	Reference clock frequency selection 1 - 38.4 MHz (Default)
1:0	Boot options: 10 - SDIO



SOC Strap Config



SDIO HOST, Moudle Pin

