1 Annex B – EUT pictures

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1.2 EUT pictures - BG2 (S2)



Picture 1: EUT left side



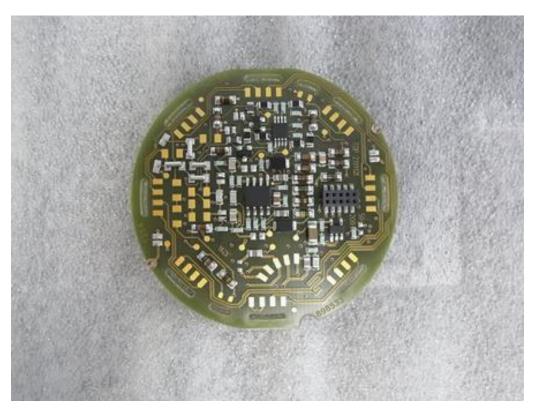
Picture 2: EUT right side



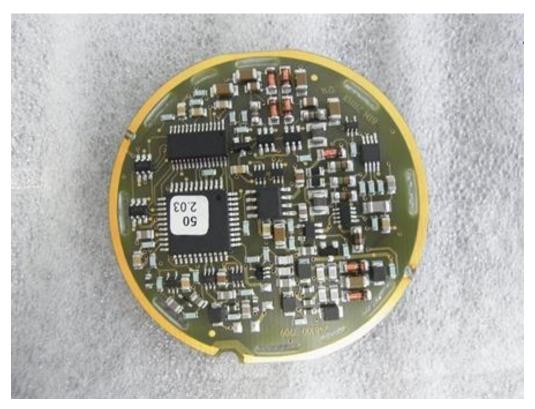
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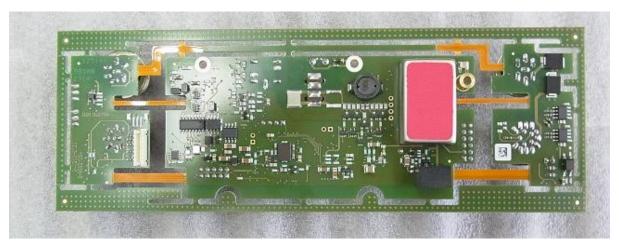
Picture 3: PCB rotor front side



Picture 4: PCB rotor rear side



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Picture 5: PCB stator front side



Picture 6: PCB stator rear side

FCC ID: 2ADAT-T40S2TOS6

IC: 12438A-T40S2TOS6

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) This device may not cause harmful interference, and
- (2) this device must accept any interference received, including interference that may cause undesired operation.

Picture 7: Label



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1.3 EUT pictures - BG3 (S3)



Picture 8: EUT left side



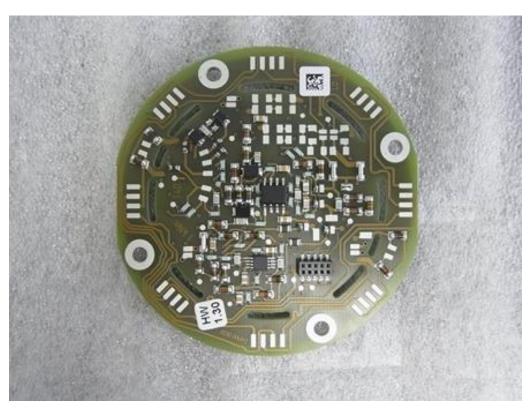
Picture 9: EUT right side



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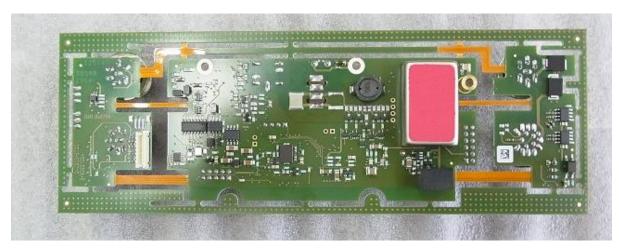
Picture 10: PCB rotor front side



Picture 11: PCB rotor rear side



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Picture 12: PCB stator front side



Picture 13: PCB stator rear side

FCC ID: 2ADAT-T40S2TOS6

IC: 12438A-T40S2TOS6

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) This device may not cause harmful interference, and
- (2) this device must accept any interference received, including interference that may cause undesired operation.

Picture 14: Label



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1.4 EUT pictures - BG4 (S4)



Picture 15: EUT left side



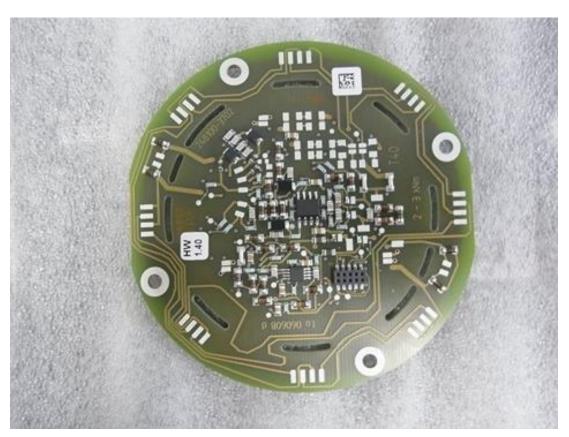
Picture 16: EUT right side



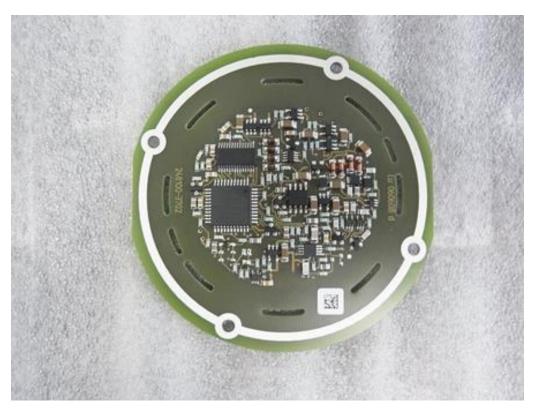
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Picture 17: PCB rotor front side



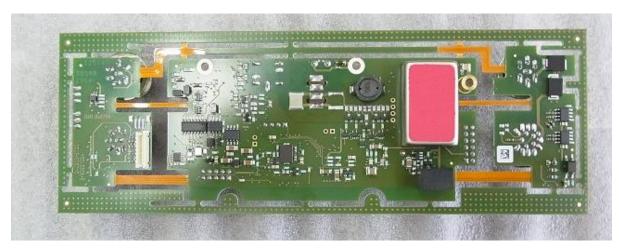
Picture 18: PCB rotor rear side



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Picture 19: PCB stator front side



Picture 20: PCB stator rear side

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IC: 12438A-T40S2TOS6

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- (2) this device must accept any interference received, including interference that may cause undesired operation.

Picture 21: Label



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1.5 EUT pictures - BG5 (S5)



Picture 22: EUT left side



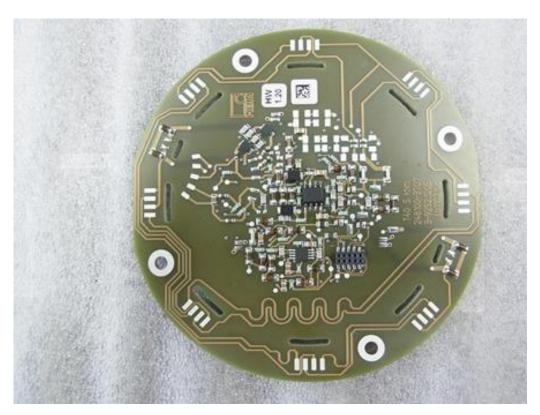
Picture 23: EUT right side



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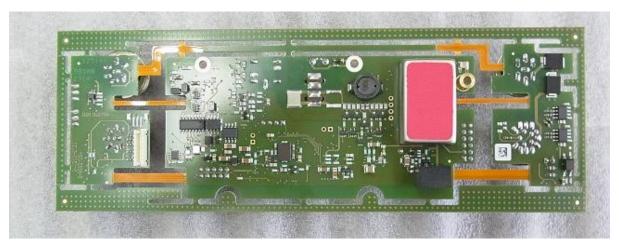
Picture 24: PCB rotor front side



Picture 25: PCB rotor rear side



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Picture 26: PCB stator front side



Picture 27: PCB stator rear side

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IC: 12438A-T40S2TOS6

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions:

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- (2) this device must accept any interference received, including interference that may cause undesired operation.

Picture 28: Label



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1.6 EUT pictures - BG6 (S6)



Picture 29: EUT left side



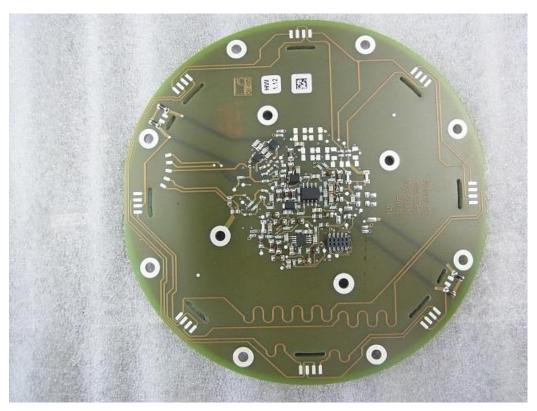
Picture 30: EUT right side



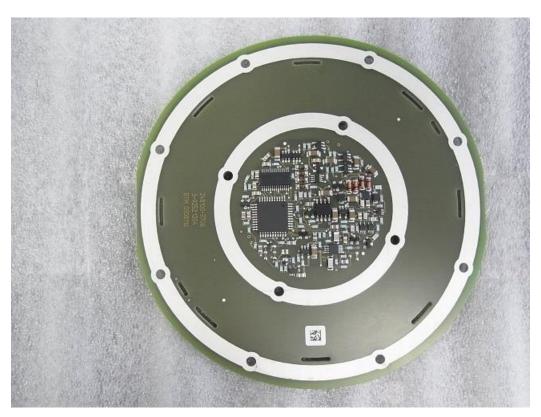
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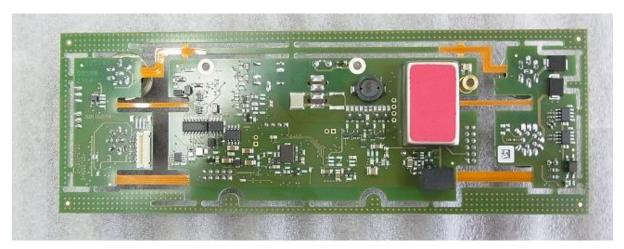
Picture 31: PCB rotor front side



Picture 32: PCB rotor rear side



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Picture 33: PCB stator front side



Picture 34: PCB stator rear side

FCC ID: 2ADAT-T40S2TOS6

IC: 12438A-T40S2TOS6

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- (2) this device must accept any interference received, including interference that may cause undesired operation.

Picture 35: Label

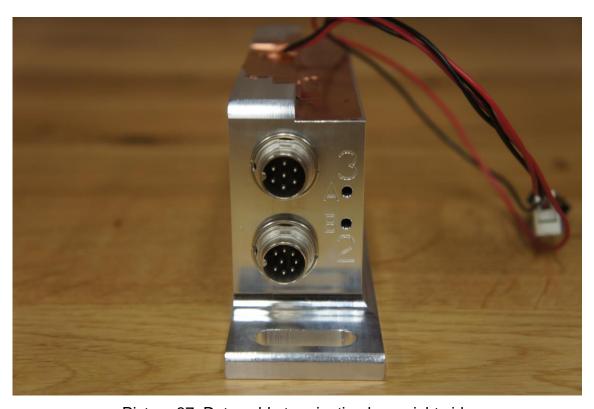


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1.7 EUT pictures - perpheral devices



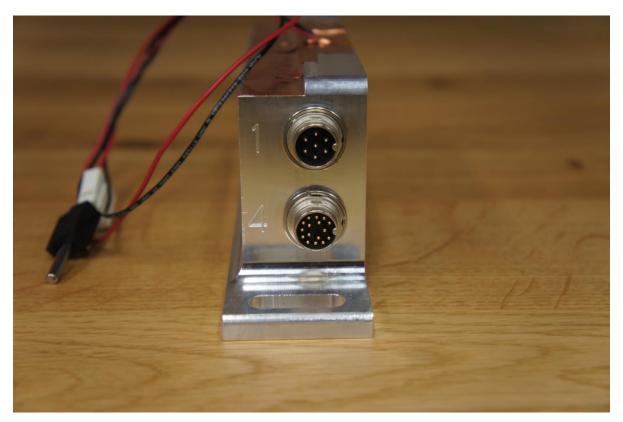
Picture 36: Data cable termination box - front



Picture 37: Data cable termination box - right side



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Picture 38: Data cable termination box - left side



Picture 39: DC power supply



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