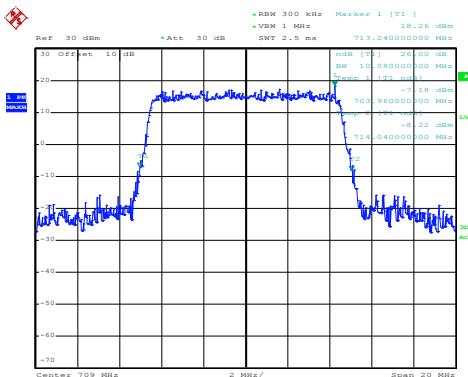


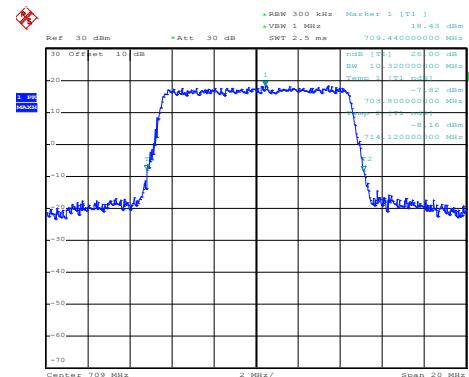
-26dBc bandwidth

BW: 10MHz

### 16QAM



### QPSK

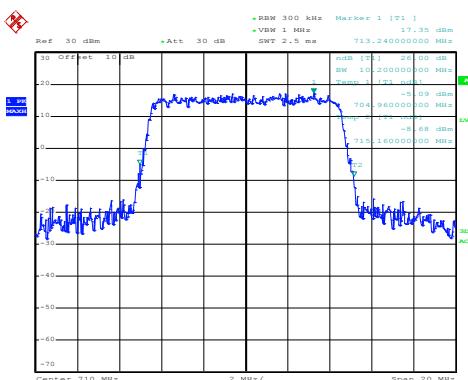


Date: 18.OCT.2017 09:39:36

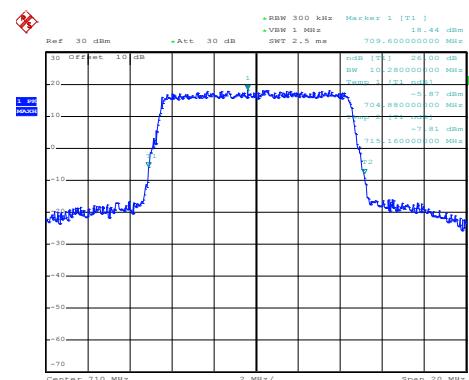
Date: 18.OCT.2017 09:39:29

### Lowest channel

### 16QAM



### QPSK

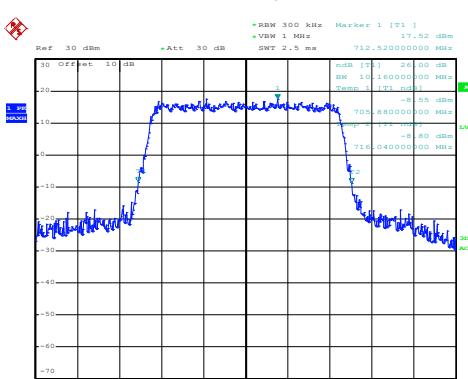


Date: 18.OCT.2017 09:42:34

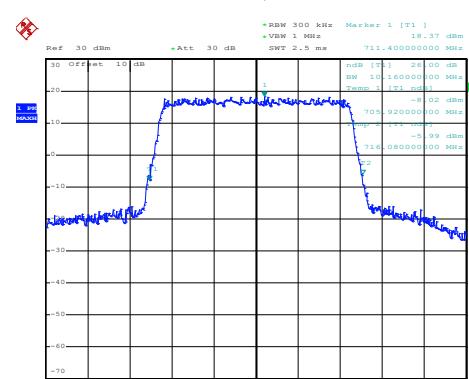
Date: 18.OCT.2017 09:42:28

### Middle channel

### 16QAM



### QPSK



Date: 18.OCT.2017 09:43:10

Date: 18.OCT.2017 09:43:04

### Highest channel

## 6.4 Out of band emission at antenna terminals

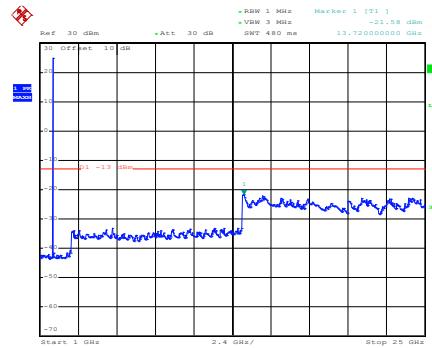
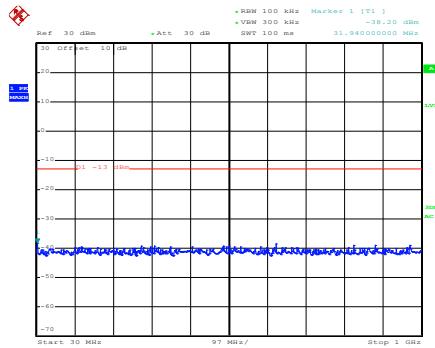
Test Requirement:	Part 22.917(a), FCC Part 24.238 (a), part 27.53(g), part 27.53(h), Part 27.53(m),
Test Method:	ANSI/TIA-603-D 2010
Limit:	Band 2, Band 4, Band5, Band12, Band 17: The power of any emission outside a licensee's frequency block shall be attenuated below the transmitter power (P) in watts by at least $43 + 10 \log_{10}(P)$ dB (-13 dBm). Band7: For mobile digital stations, the attenuation factor shall be not less than $40 + 10 \log (P)$ dB on all frequencies between the channel edge and 5 megahertz from the channel edge, $43 + 10 \log (P)$ dB on all frequencies between 5 megahertz and X megahertz from the channel edge, and $55 + 10 \log (P)$ dB on all frequencies more than X megahertz from the channel edge, where X is the greater of 6 megahertz or the actual emission bandwidth as defined in paragraph (m)(6) of this section. In addition, the attenuation factor shall not be less than $43 + 10 \log (P)$ dB on all frequencies between 2490.5 MHz and 2496 MHz and $55 + 10 \log (P)$ dB at or below 2490.5 MHz.
Test Setup:	<p>System simulator</p> <p>Spectrum Analyzer</p> <p>Splitter</p> <p>ATT</p> <p>EUT</p>
Test Procedure:	<ol style="list-style-type: none"> <li>The RF output of the transceiver was connected to a spectrum analyzer through appropriate attenuation.</li> <li>The resolution bandwidth of the spectrum analyzer was set at 100 kHz when below 1GHz, 1MHz when above 1 GHz; sufficient scans were taken to show the out of band Emissions if any up to 10th harmonic.</li> <li>For the out of band: Set the RBW=100 kHz, VBW=300 kHz when below 1 GHz, RBW =1 MHz, VBW=3 MHz when above 1 GHz, Start=30MHz, Stop= 10th harmonic.</li> <li>Band Edge Requirements: In the 1 MHz bands immediately outside and adjacent to the frequency block, a resolution bandwidth of at least 1 percent of the emission bandwidth of the fundamental emission of the transmitter may be employed to measure the out of band Emissions.</li> </ol>
Test Instruments:	Refer to section 5.8 for details
Test mode:	Refer to section 5.3 for details
Test results:	Passed

Test plots as follows:

**Conducted spurious emission  
LTE band 2, 1.4MHz**

16 QAM & RB Size 1

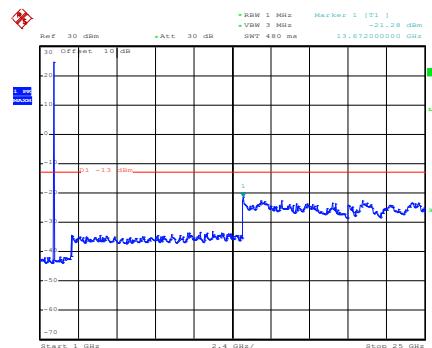
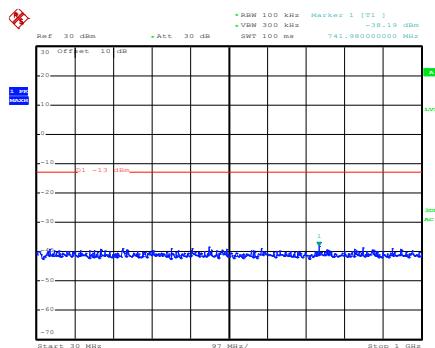
Lowest channel



30MHz~1GHz

1GHz~25GHz

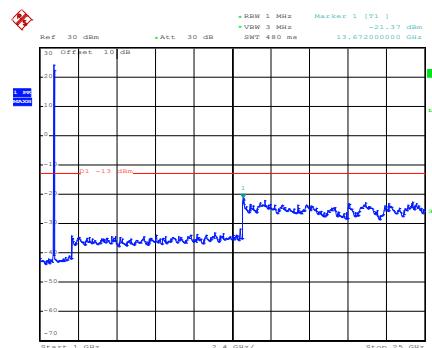
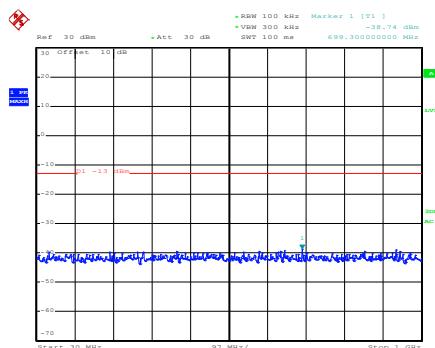
Middle channel



30MHz~1GHz

1GHz~25GHz

High channel

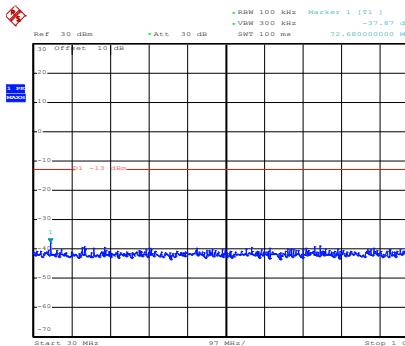


30MHz~1GHz

1GHz~25GHz

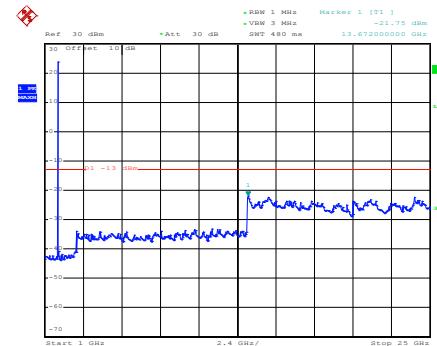
## 16 QAM & RB Size 6

### Lowest channel



Date: 18.OCT.2017 15:14:52

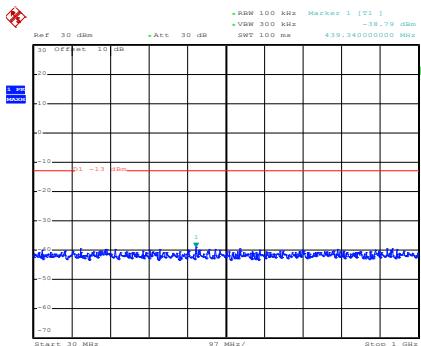
30MHz~1GHz



Date: 20.OCT.2017 15:37:02

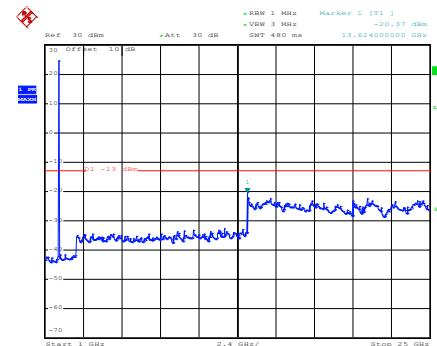
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:15:38

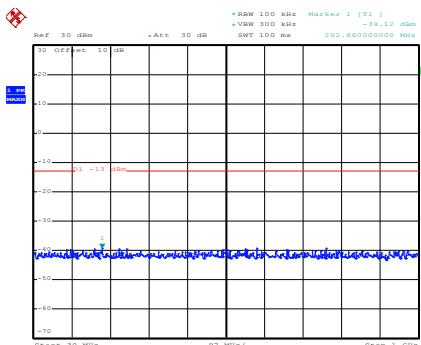
30MHz~1GHz



Date: 20.OCT.2017 15:38:16

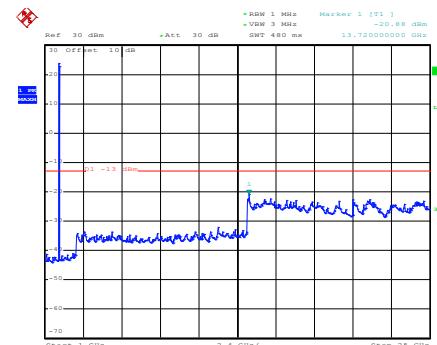
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:16:21

30MHz~1GHz

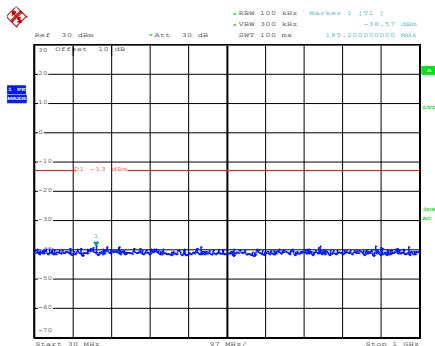


Date: 20.OCT.2017 15:39:26

1GHz~25GHz

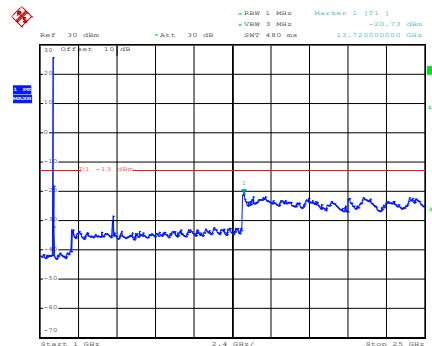
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 15:14:16

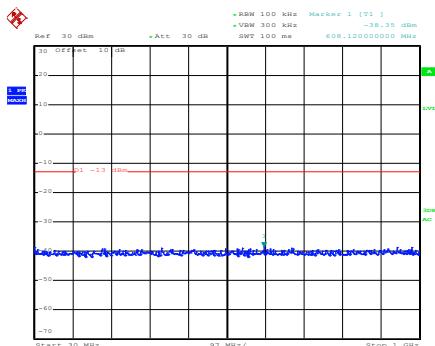
30MHz~1GHz



Date: 20.OCT.2017 15:34:46

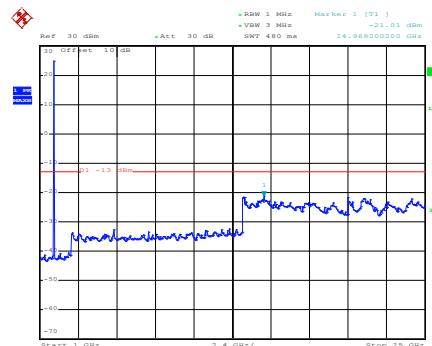
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:15:06

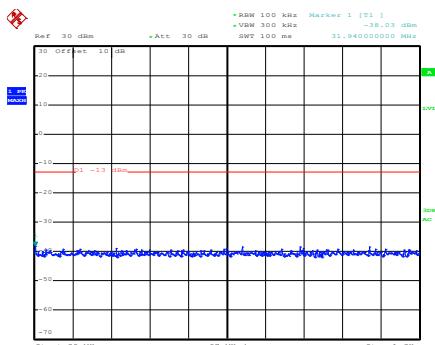
30MHz~1GHz



Date: 20.OCT.2017 15:37:29

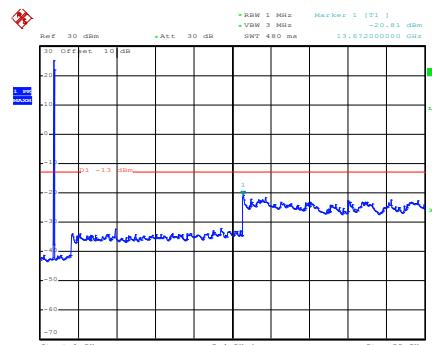
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:15:52

30MHz~1GHz

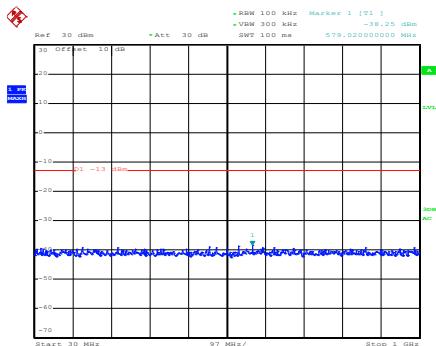


Date: 20.OCT.2017 15:38:41

1GHz~25GHz

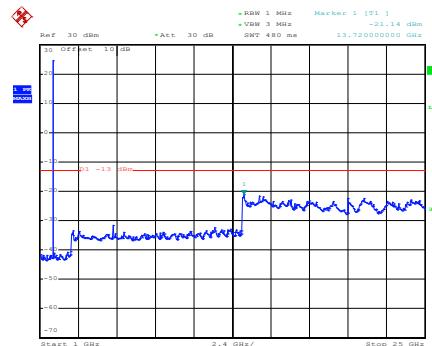
## QPSK & RB Size 6

### Lowest channel



Date: 18.OCT.2017 15:14:48

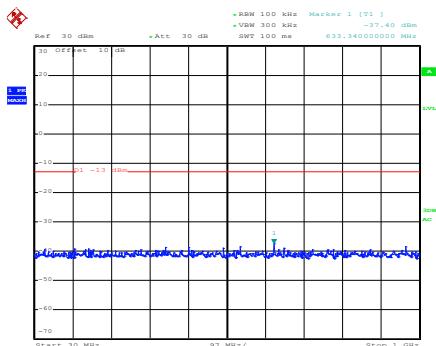
30MHz~1GHz



Date: 20.OCT.2017 15:36:54

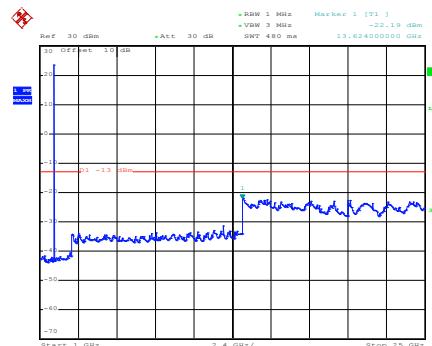
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:15:34

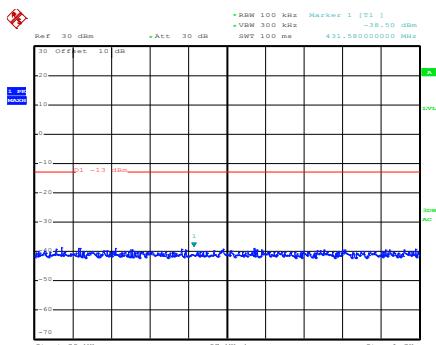
30MHz~1GHz



Date: 20.OCT.2017 15:38:09

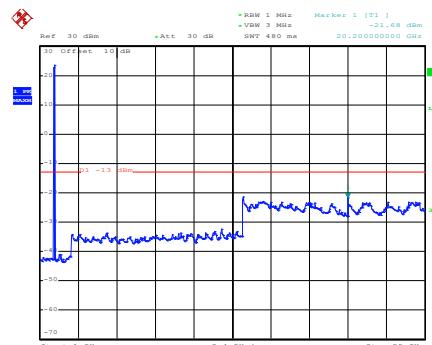
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:16:17

30MHz~1GHz



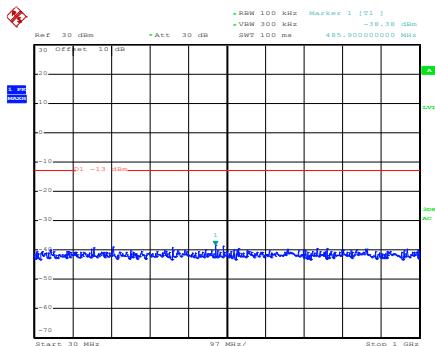
Date: 20.OCT.2017 15:39:19

1GHz~25GHz

## 3MHz

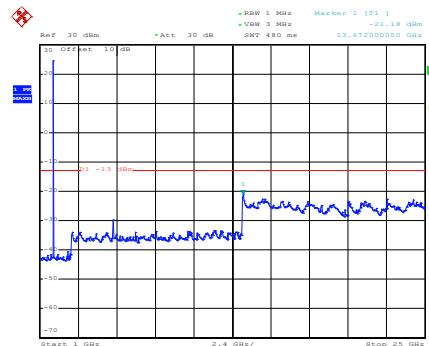
### 16 QAM & RB Size 1

#### Lowest channel



Date: 18.OCT.2017 15:16:43

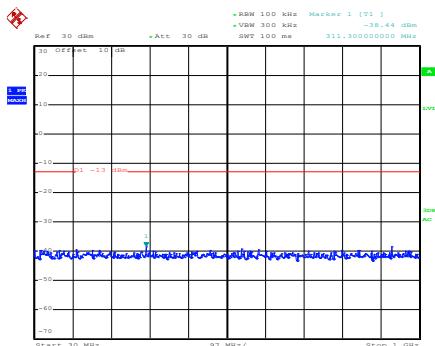
30MHz~1GHz



Date: 20.OCT.2017 15:40:18

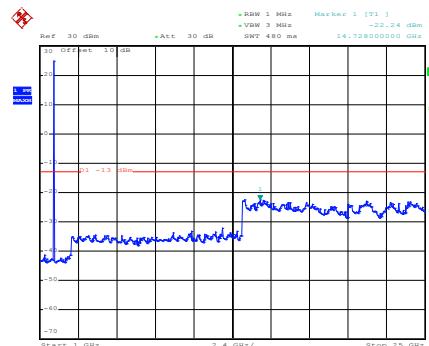
1GHz~25GHz

#### Middle channel



Date: 18.OCT.2017 15:17:26

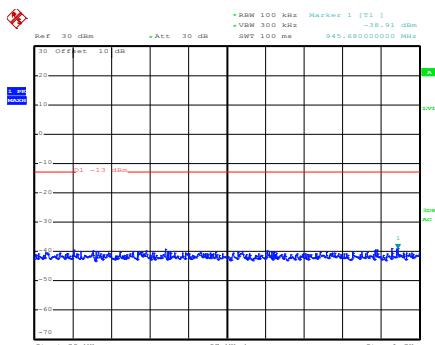
30MHz~1GHz



Date: 20.OCT.2017 15:41:23

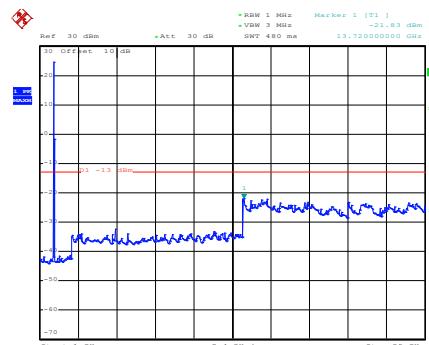
1GHz~25GHz

#### High channel



Date: 18.OCT.2017 15:18:21

30MHz~1GHz

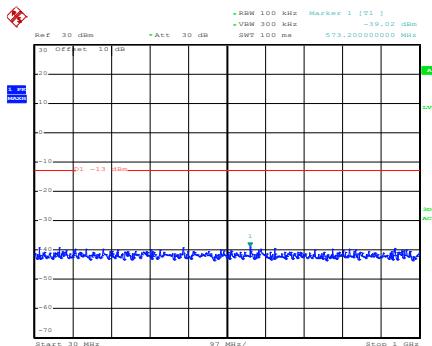


Date: 20.OCT.2017 15:42:54

1GHz~25GHz

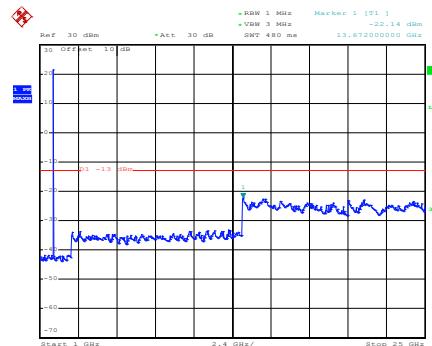
## 16 QAM & RB Size 15

### Lowest channel



Date: 18.OCT.2017 15:17:09

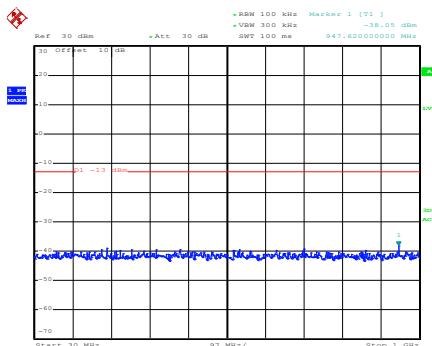
30MHz~1GHz



Date: 20.OCT.2017 15:40:56

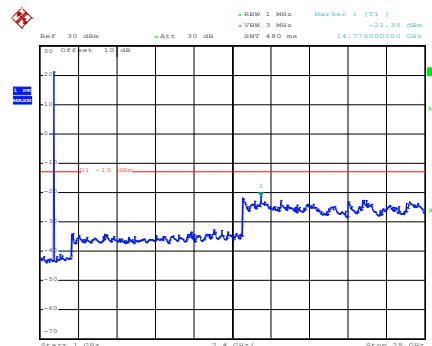
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:17:50

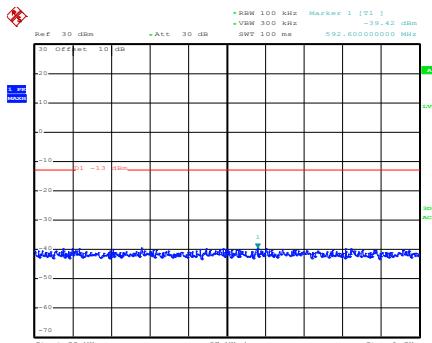
30MHz~1GHz



Date: 20.OCT.2017 15:42:22

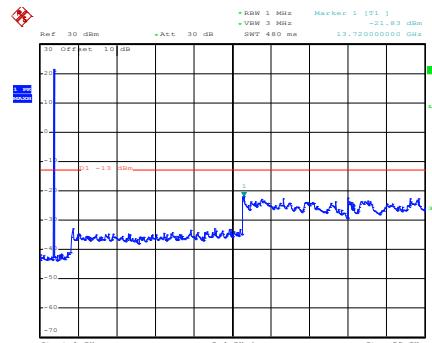
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:18:47

30MHz~1GHz

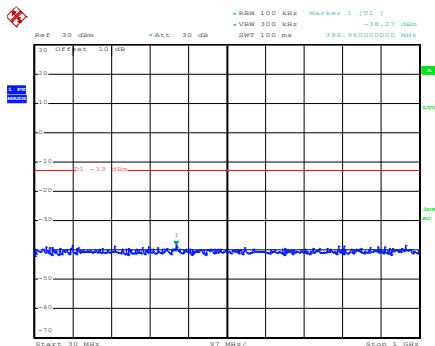


Date: 20.OCT.2017 15:43:32

1GHz~25GHz

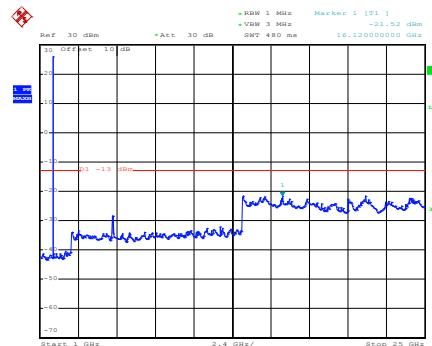
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 15:16:39

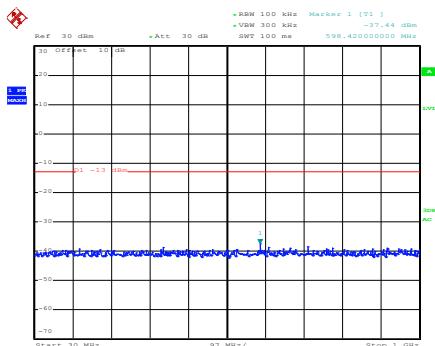
30MHz~1GHz



Date: 20.OCT.2017 15:40:07

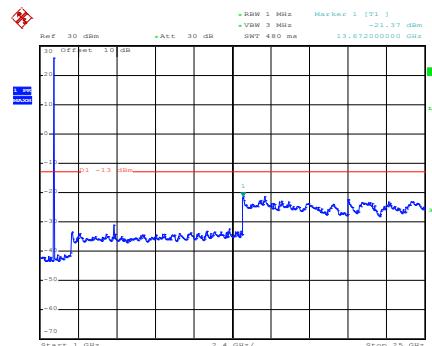
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:17:21

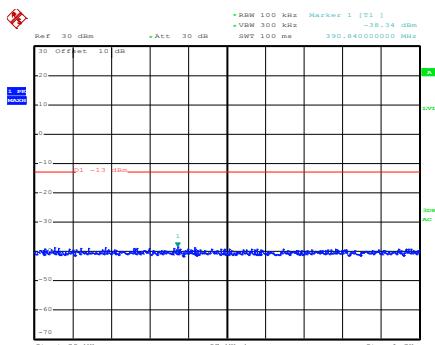
30MHz~1GHz



Date: 20.OCT.2017 15:41:14

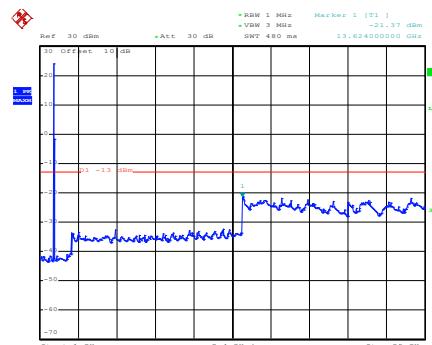
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:18:15

30MHz~1GHz

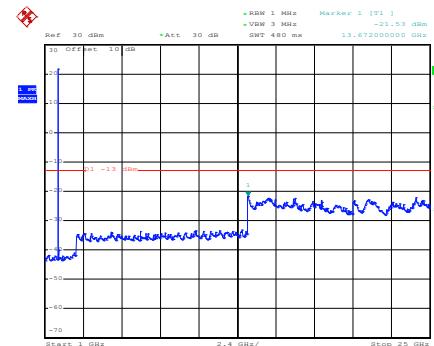
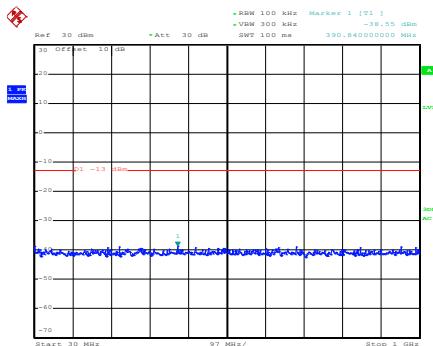


Date: 20.OCT.2017 15:42:45

1GHz~25GHz

## QPSK & RB Size 15

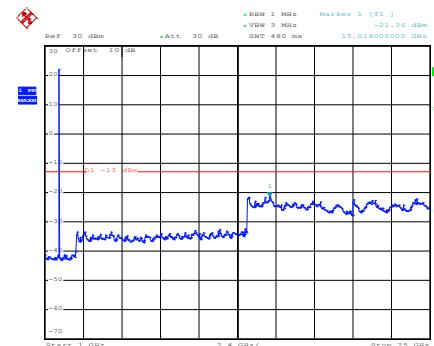
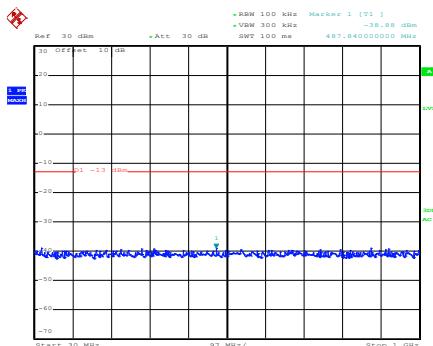
### Lowest channel



30MHz~1GHz

1GHz~25GHz

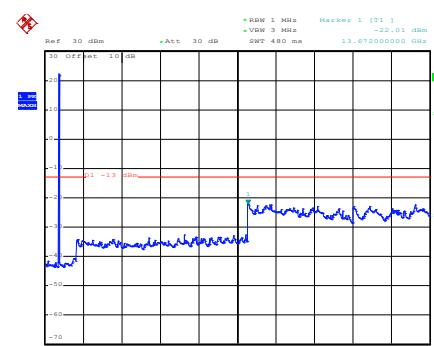
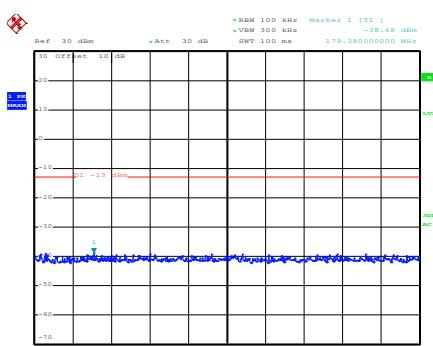
### Middle channel



30MHz~1GHz

1GHz~25GHz

### High channel



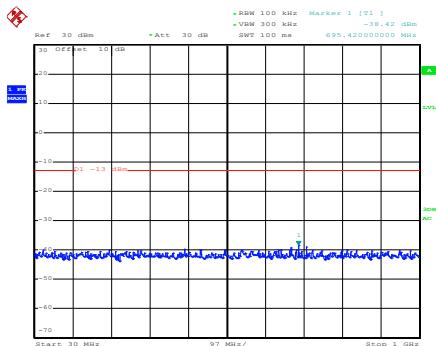
30MHz~1GHz

1GHz~25GHz

**5MHz**

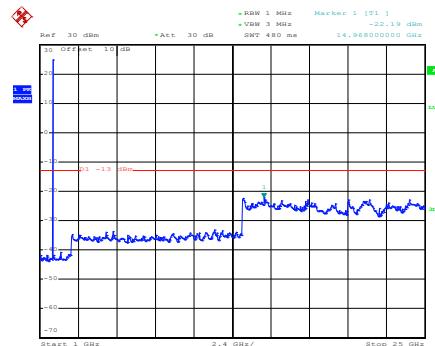
## 16 QAM & RB Size 1

## Lowest channel



Date: 18.OCT.2017 15:19:04

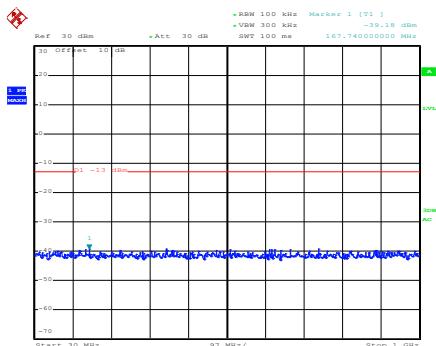
30MHz~1GHz



Date: 20.OCT.2017 15:44:26

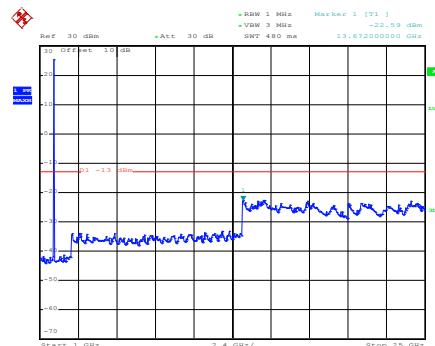
1GHz~25GHz

## Middle channel



Date: 18.OCT.2017 15:19:48

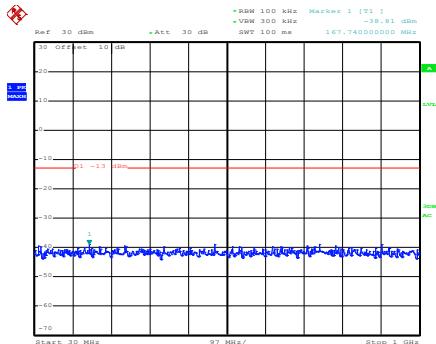
30MHz~1GHz



Date: 20.OCT.2017 15:45:39

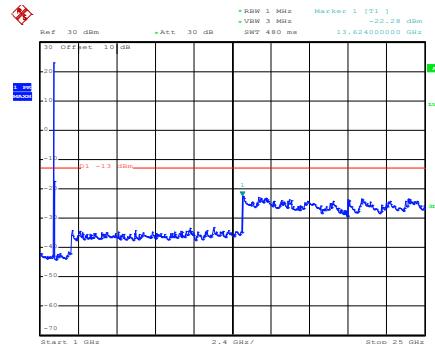
1GHz~25GHz

## High channel



Date: 18.OCT.2017 15:20:29

30MHz~1GHz

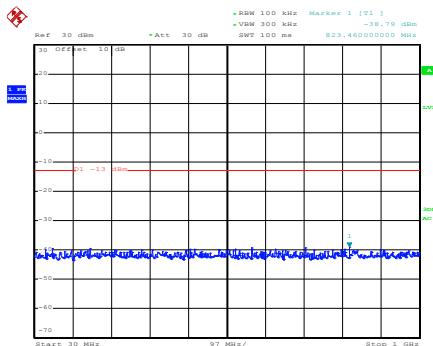


Date: 20.OCT.2017 15:46:49

1GHz~25GHz

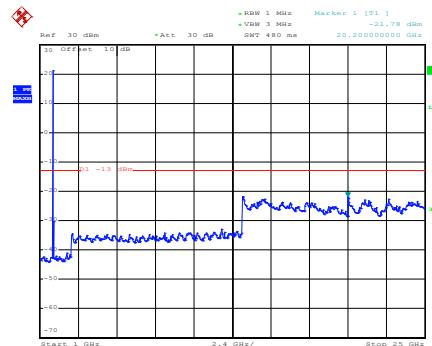
## 16 QAM & RB Size 25

### Lowest channel



Date: 18.OCT.2017 15:19:30

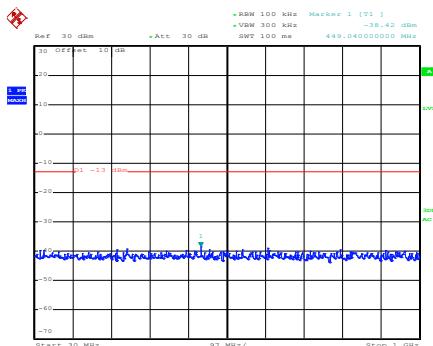
30MHz~1GHz



Date: 20.OCT.2017 15:45:07

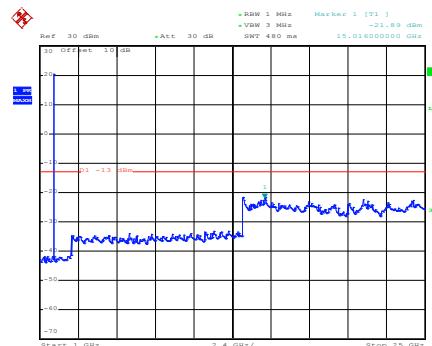
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:20:14

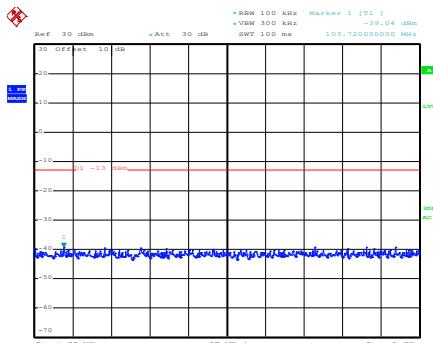
30MHz~1GHz



Date: 20.OCT.2017 15:46:22

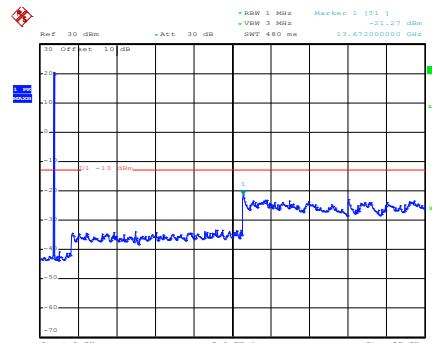
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:20:54

30MHz~1GHz

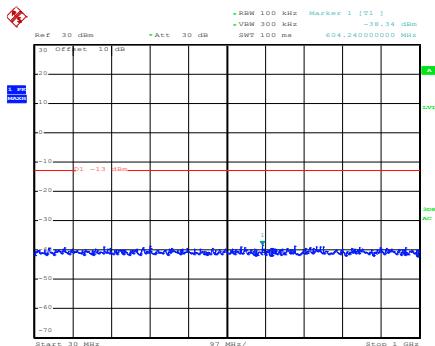


Date: 20.OCT.2017 15:47:32

1GHz~25GHz

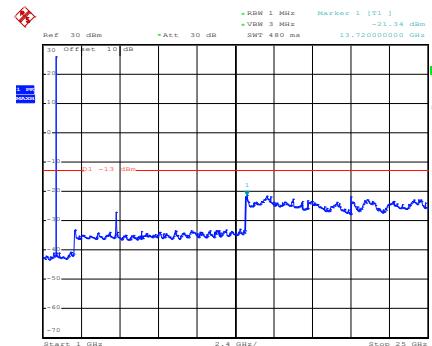
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 15:19:00

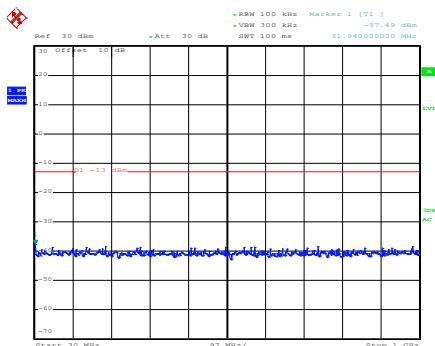
30MHz~1GHz



Date: 20.OCT.2017 15:44:10

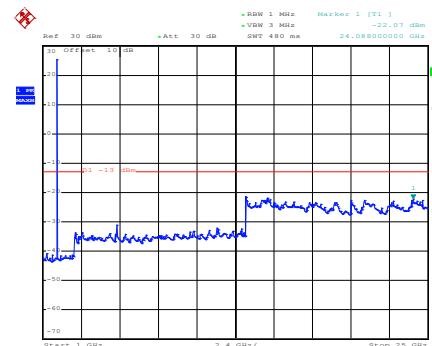
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:19:42

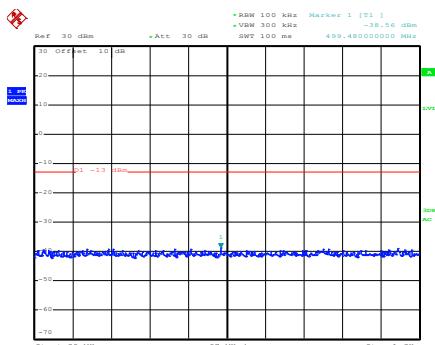
30MHz~1GHz



Date: 20.OCT.2017 15:45:30

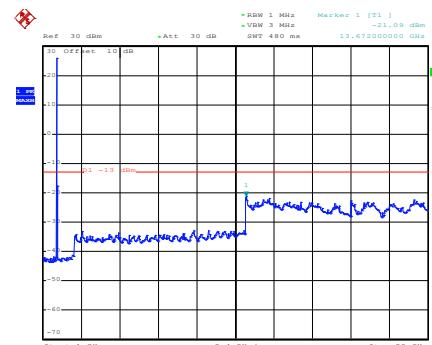
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:20:25

30MHz~1GHz

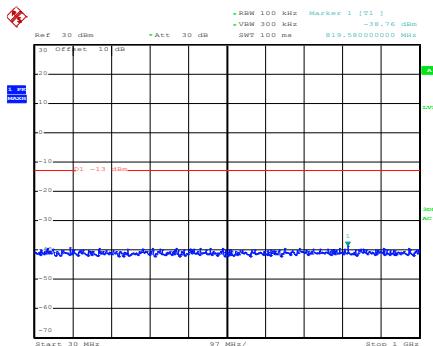


Date: 20.OCT.2017 15:46:41

1GHz~25GHz

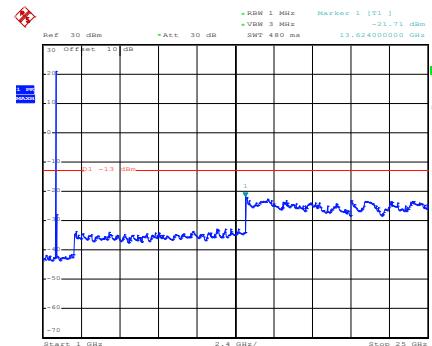
## QPSK & RB Size 25

### Lowest channel



Date: 18.OCT.2017 15:19:25

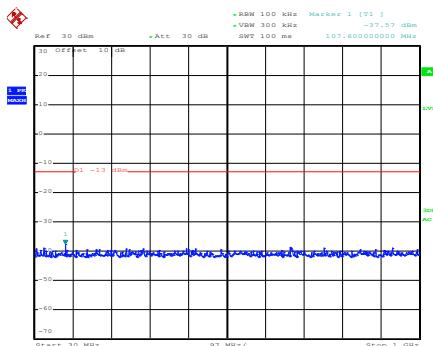
30MHz~1GHz



Date: 20.OCT.2017 15:45:00

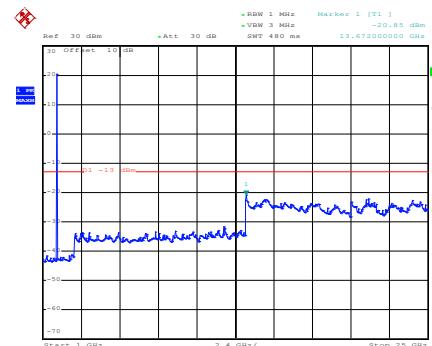
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:20:10

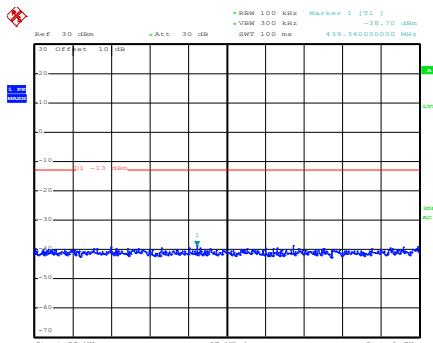
30MHz~1GHz



Date: 20.OCT.2017 15:46:13

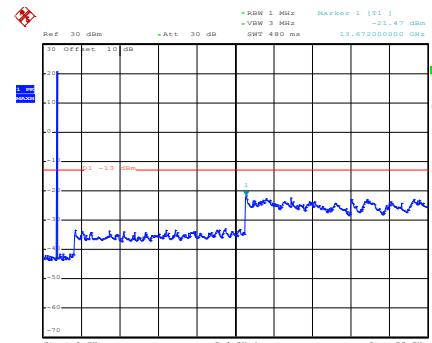
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:20:50

30MHz~1GHz



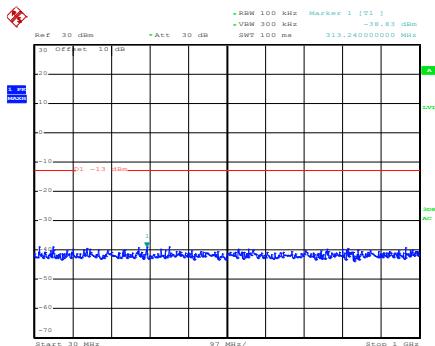
Date: 20.OCT.2017 15:47:24

1GHz~25GHz

## 10MHz

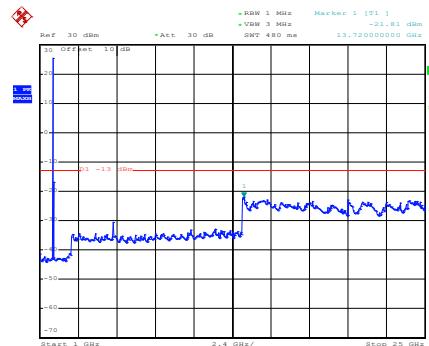
### 16 QAM & RB Size 1

#### Lowest channel



Date: 18.OCT.2017 15:21:12

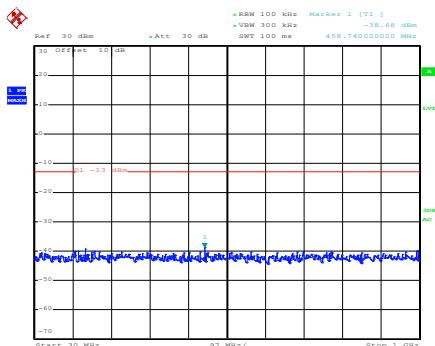
30MHz~1GHz



Date: 20.OCT.2017 15:48:21

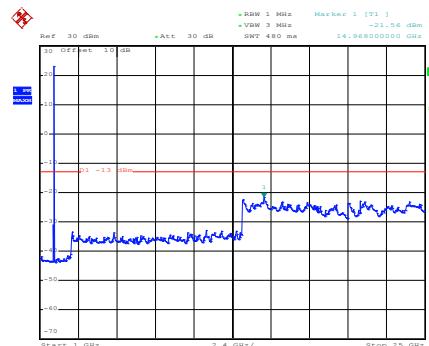
1GHz~25GHz

#### Middle channel



Date: 18.OCT.2017 15:21:56

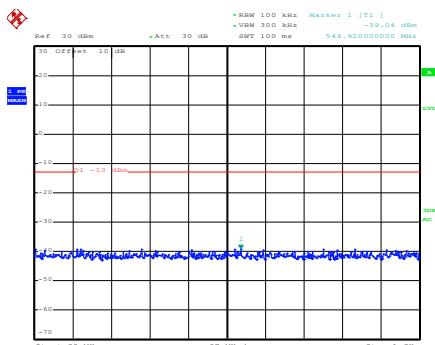
30MHz~1GHz



Date: 20.OCT.2017 15:49:28

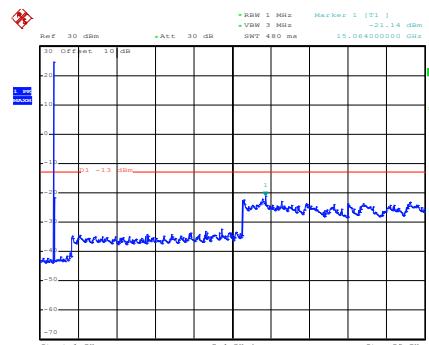
1GHz~25GHz

#### High channel



Date: 18.OCT.2017 15:22:41

30MHz~1GHz

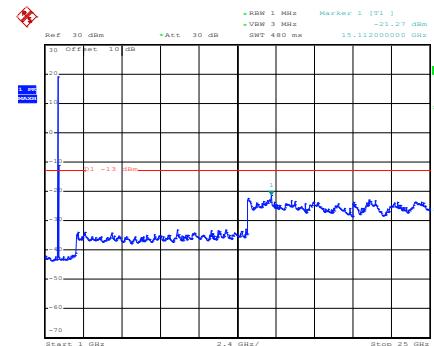
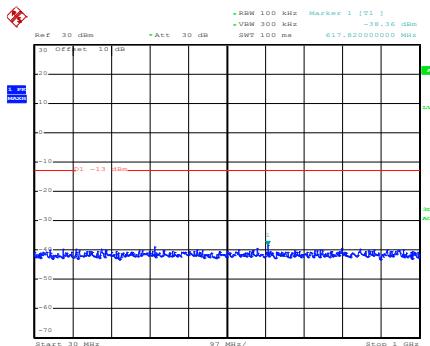


Date: 20.OCT.2017 15:50:32

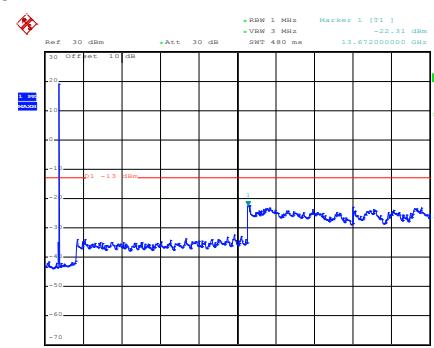
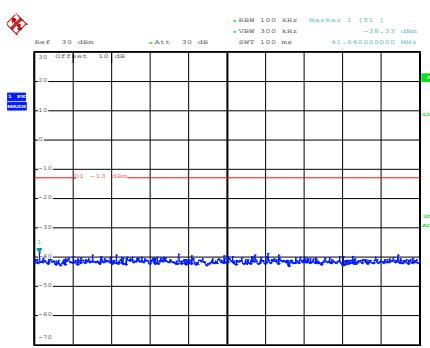
1GHz~25GHz

## 16 QAM & RB Size 50

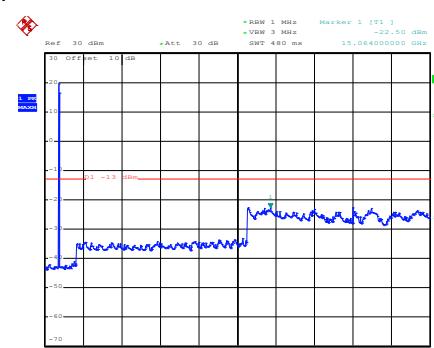
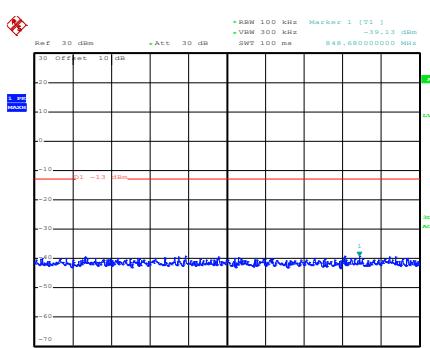
### Lowest channel



### Middle channel

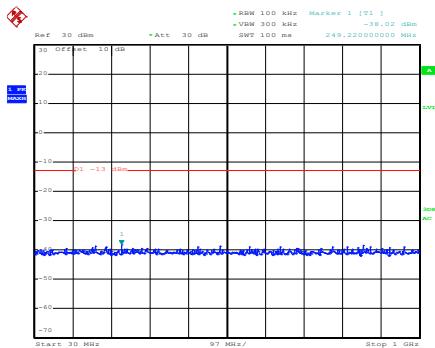


### High channel

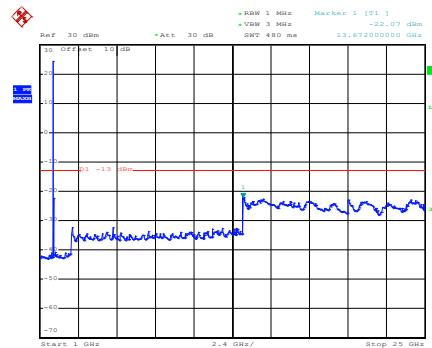


## QPSK & RB Size 1

### Lowest channel

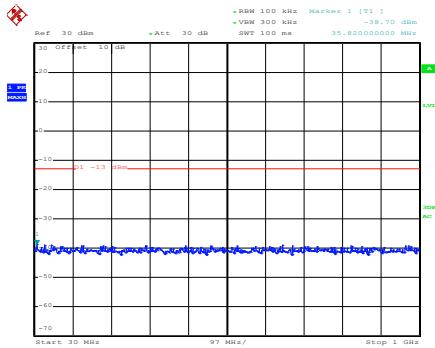


30MHz~1GHz

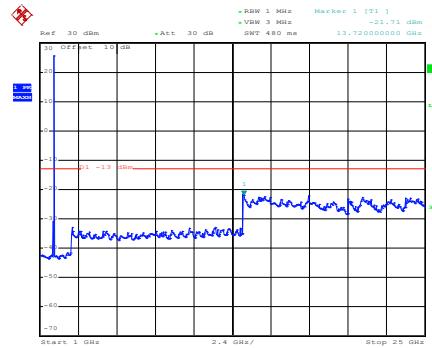


1GHz~25GHz

### Middle channel

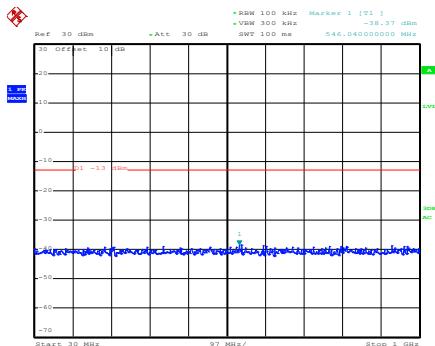


30MHz~1GHz

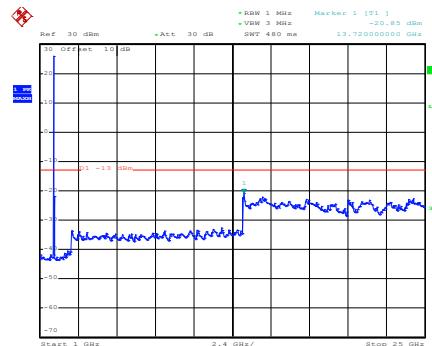


1GHz~25GHz

### High channel



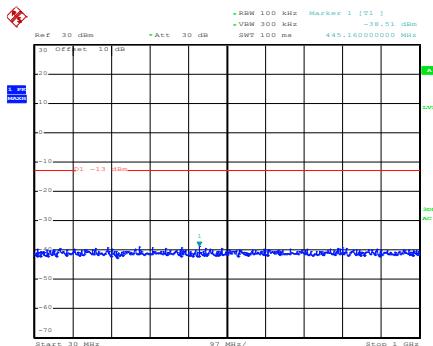
30MHz~1GHz



1GHz~25GHz

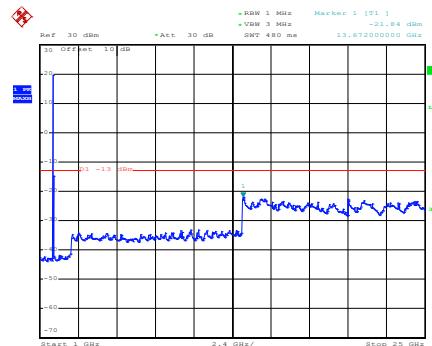
## QPSK & RB Size 50

### Lowest channel



Date: 18.OCT.2017 15:21:35

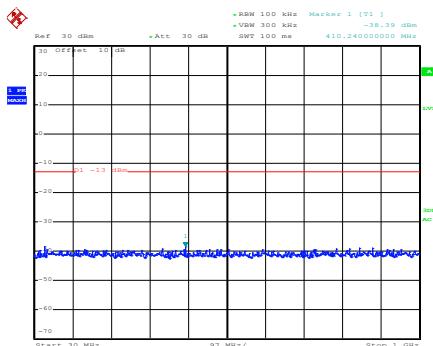
30MHz~1GHz



Date: 20.OCT.2017 15:48:54

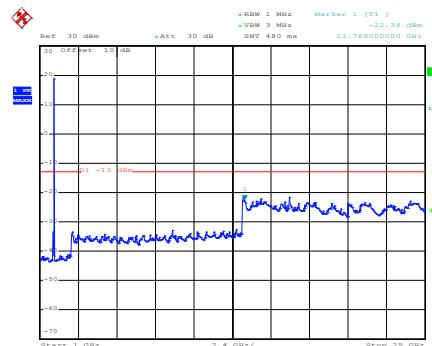
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:22:17

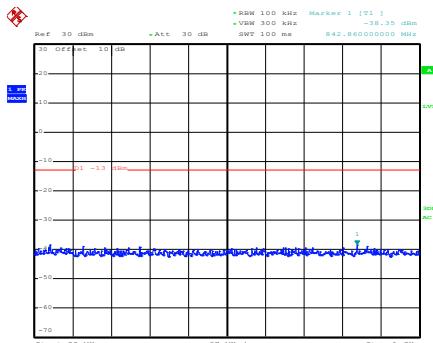
30MHz~1GHz



Date: 20.OCT.2017 15:50:00

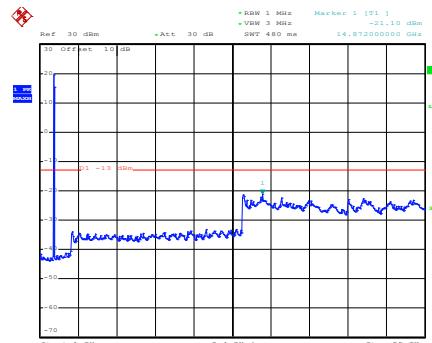
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:23:02

30MHz~1GHz



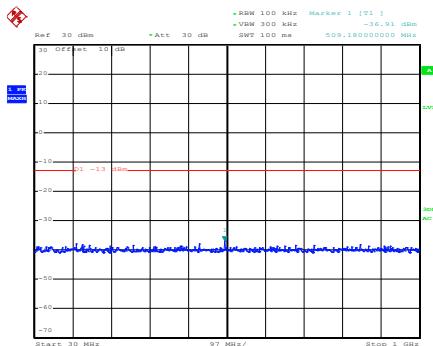
Date: 20.OCT.2017 15:51:05

1GHz~25GHz

## 15MHz

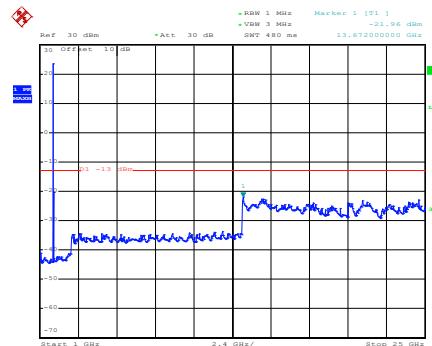
### 16 QAM & RB Size 1

#### Lowest channel



Date: 18.OCT.2017 15:23:57

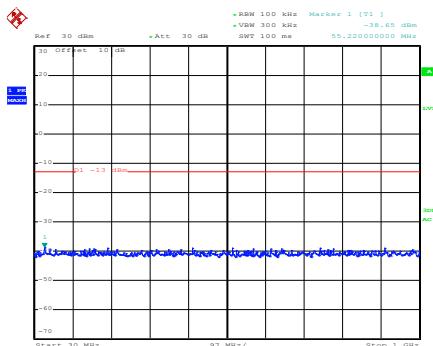
#### 30MHz~1GHz



Date: 20.OCT.2017 15:51:56

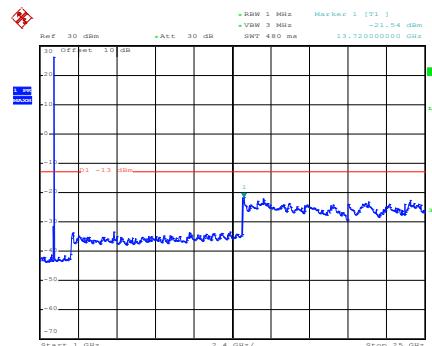
#### 1GHz~25GHz

#### Middle channel



Date: 18.OCT.2017 15:24:43

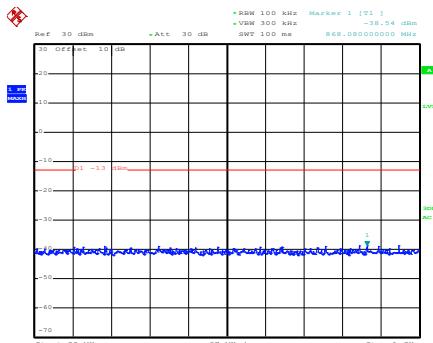
#### 30MHz~1GHz



Date: 20.OCT.2017 15:52:59

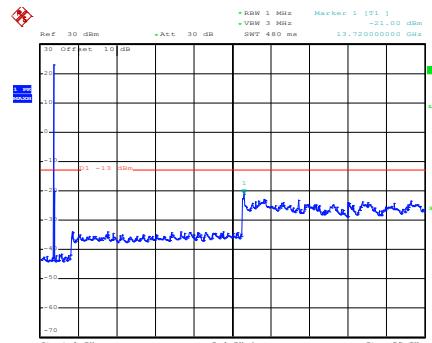
#### 1GHz~25GHz

#### High channel



Date: 18.OCT.2017 15:25:24

#### 30MHz~1GHz

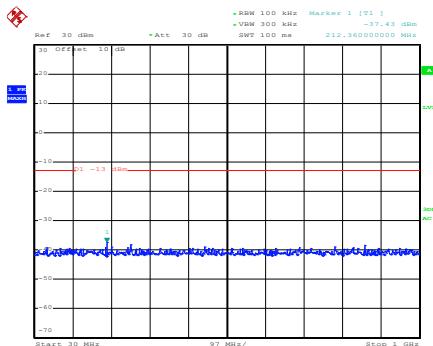


Date: 20.OCT.2017 15:54:04

#### 1GHz~25GHz

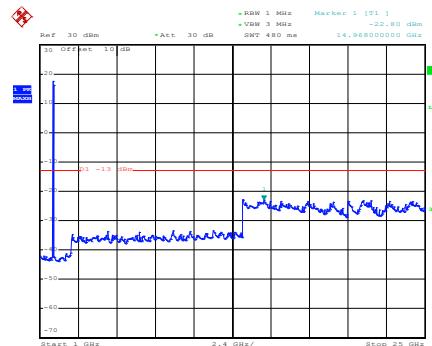
## 16 QAM & RB Size 75

### Lowest channel



Date: 18.OCT.2017 15:24:27

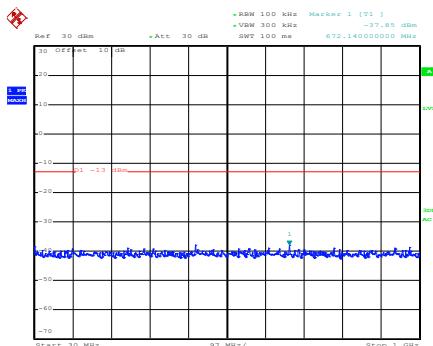
30MHz~1GHz



Date: 20.OCT.2017 15:52:34

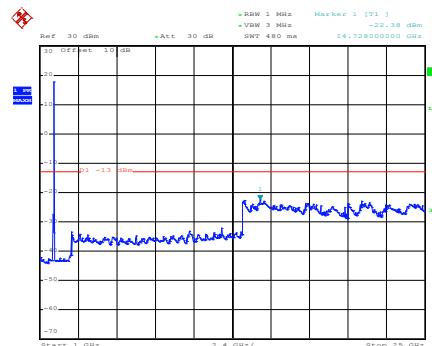
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:25:08

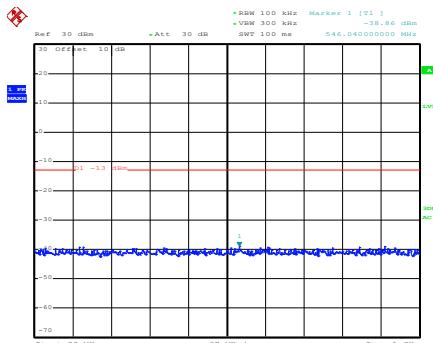
30MHz~1GHz



Date: 20.OCT.2017 15:53:37

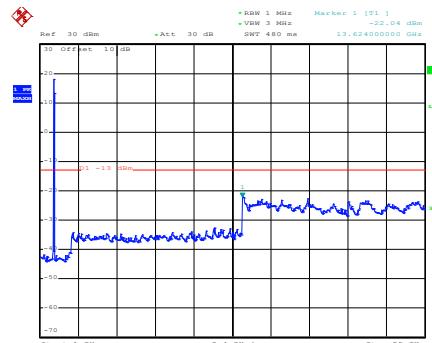
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:25:50

30MHz~1GHz

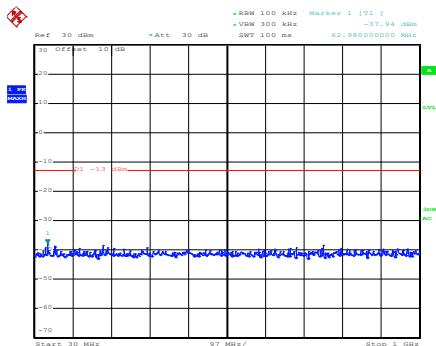


Date: 20.OCT.2017 15:54:42

1GHz~25GHz

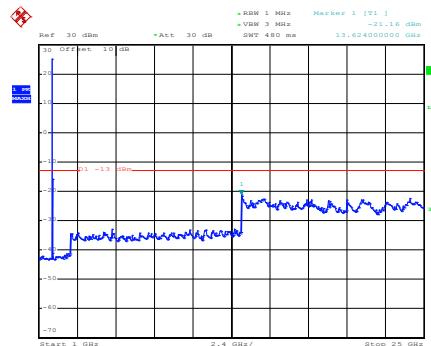
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 15:24:04

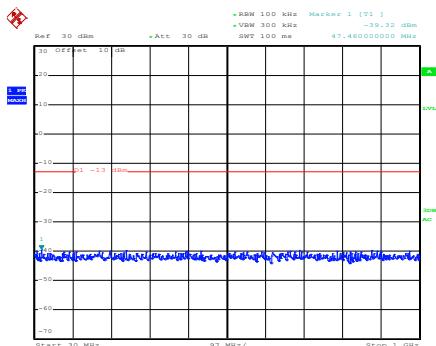
30MHz~1GHz



Date: 20.OCT.2017 15:51:49

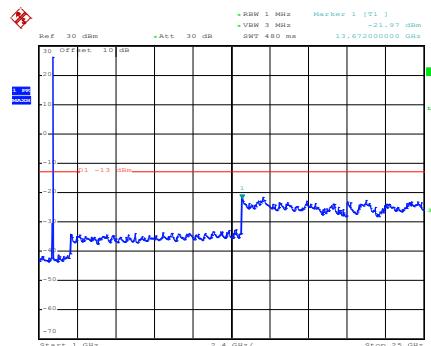
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:24:47

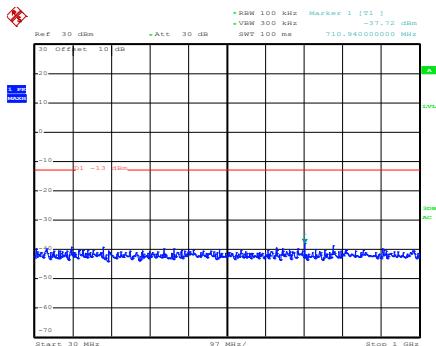
30MHz~1GHz



Date: 20.OCT.2017 15:52:51

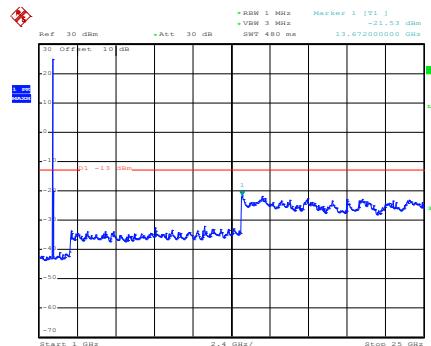
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:25:27

30MHz~1GHz

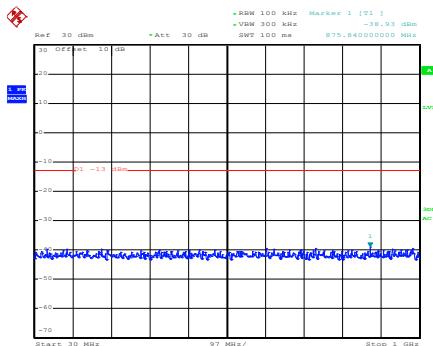


Date: 20.OCT.2017 15:53:56

1GHz~25GHz

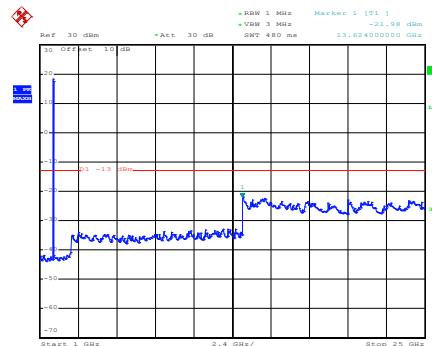
## QPSK & RB Size 75

### Lowest channel



Date: 18.OCT.2017 15:24:31

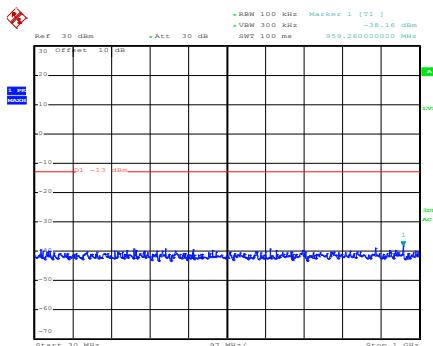
30MHz~1GHz



Date: 20.OCT.2017 15:52:27

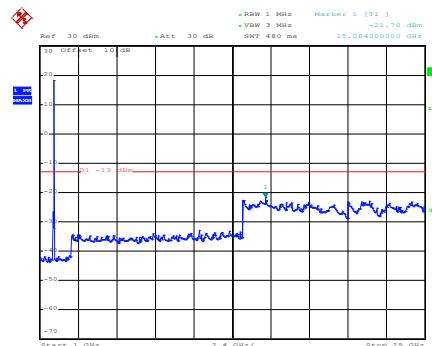
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:25:13

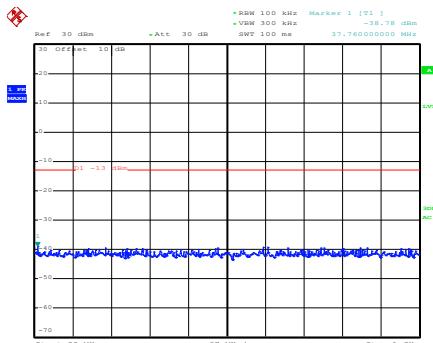
30MHz~1GHz



Date: 20.OCT.2017 15:53:30

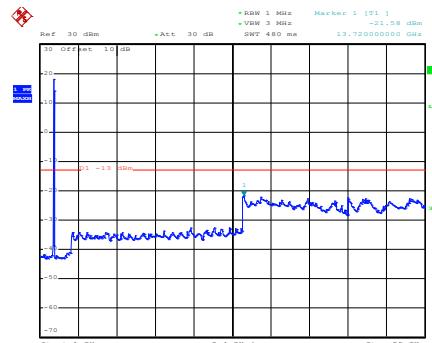
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:25:55

30MHz~1GHz



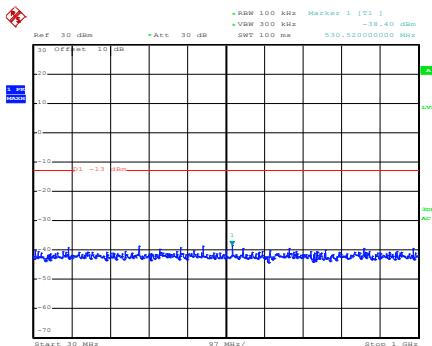
Date: 20.OCT.2017 15:54:56

1GHz~25GHz

## 20MHz

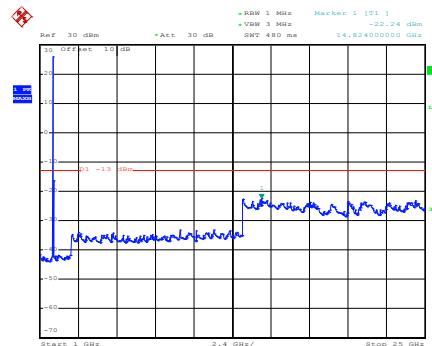
### 16 QAM & RB Size 1

#### Lowest channel



Date: 18.OCT.2017 15:26:16

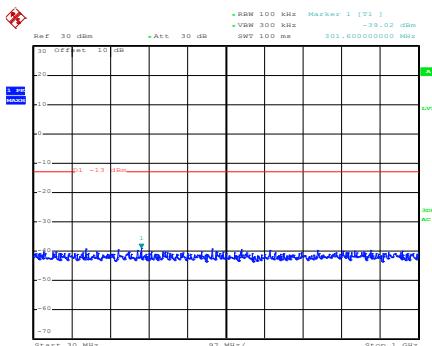
#### 30MHz~1GHz



Date: 20.OCT.2017 15:55:47

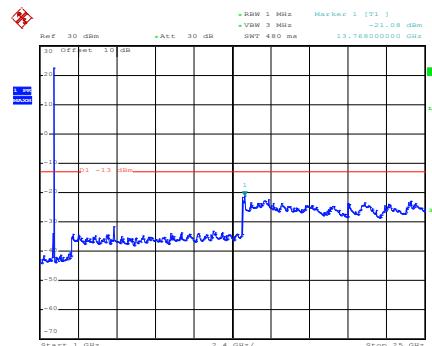
#### 1GHz~25GHz

#### Middle channel



Date: 18.OCT.2017 15:27:03

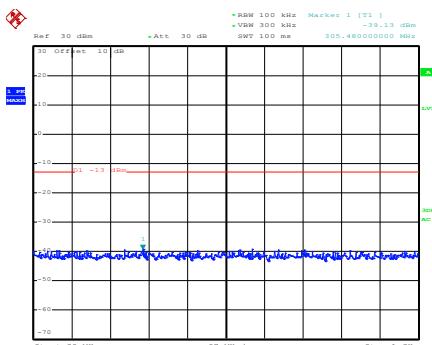
#### 30MHz~1GHz



Date: 20.OCT.2017 15:56:56

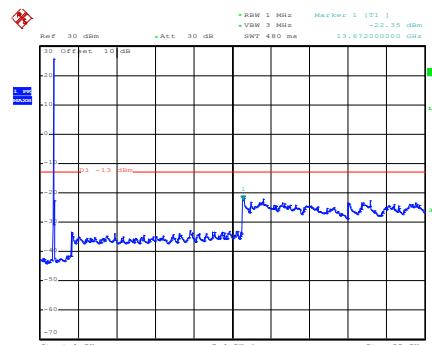
#### 1GHz~25GHz

#### High channel



Date: 18.OCT.2017 15:27:54

#### 30MHz~1GHz

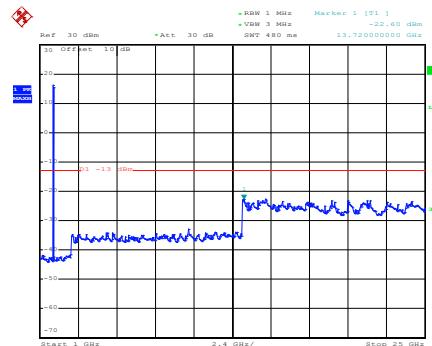
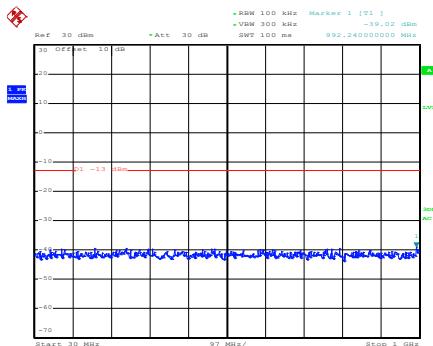


Date: 20.OCT.2017 15:58:07

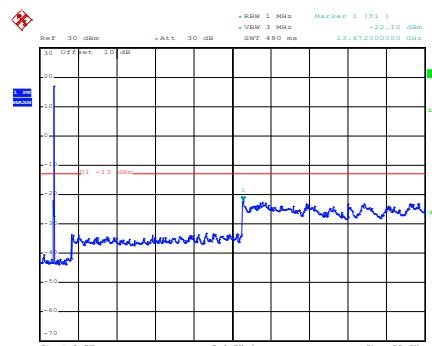
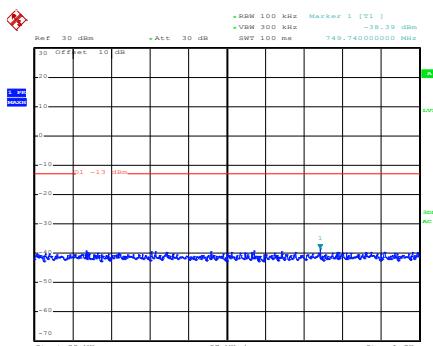
#### 1GHz~25GHz

## 16 QAM & RB Size 100

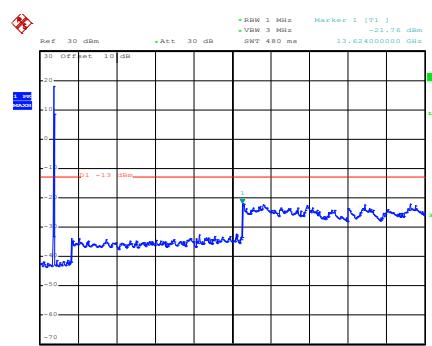
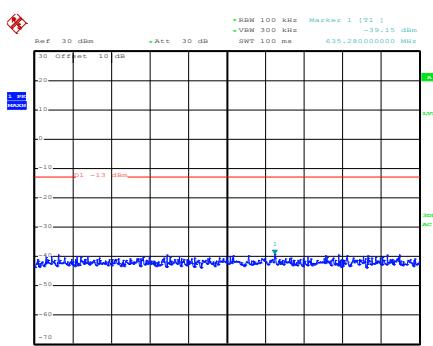
### Lowest channel



### Middle channel

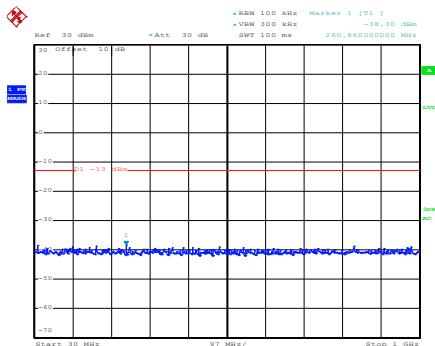


### High channel



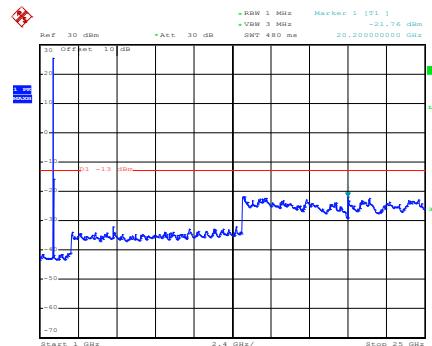
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 15:26:10

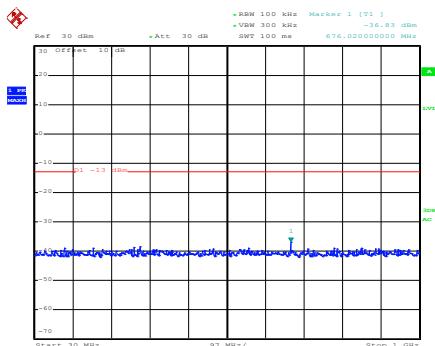
30MHz~1GHz



Date: 20.OCT.2017 15:55:34

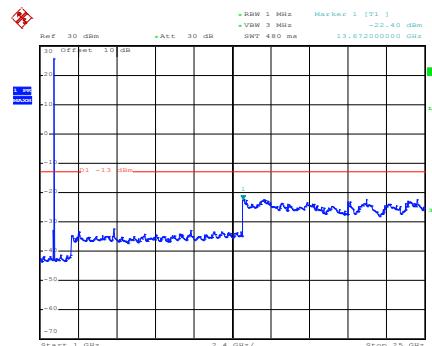
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:26:56

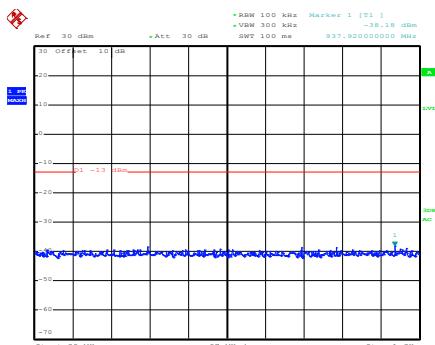
30MHz~1GHz



Date: 20.OCT.2017 15:56:47

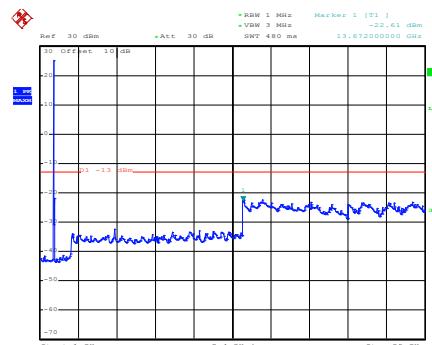
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:27:42

30MHz~1GHz

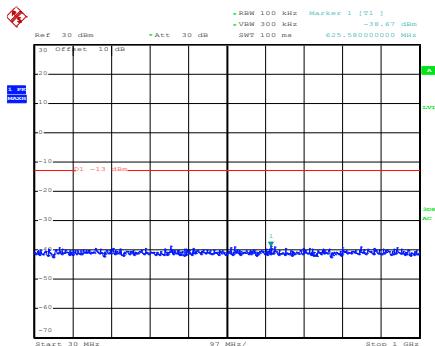


Date: 20.OCT.2017 15:57:56

1GHz~25GHz

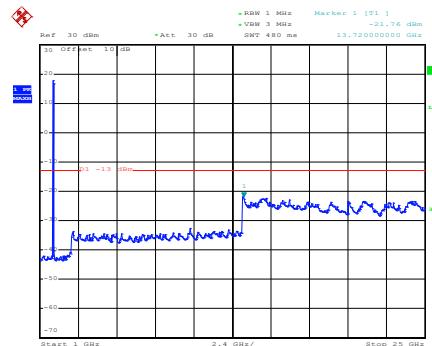
## QPSK & RB Size 100

### Lowest channel



Date: 18.OCT.2017 15:26:39

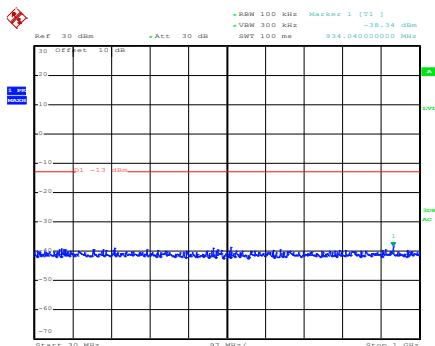
30MHz~1GHz



Date: 20.OCT.2017 15:56:22

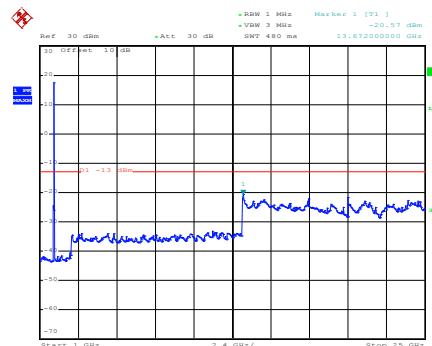
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:27:24

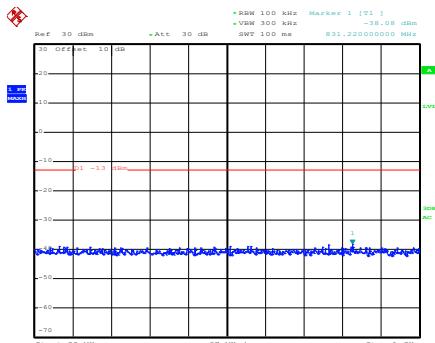
30MHz~1GHz



Date: 20.OCT.2017 15:57:31

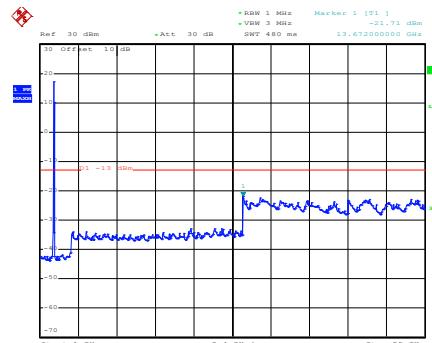
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:28:22

30MHz~1GHz



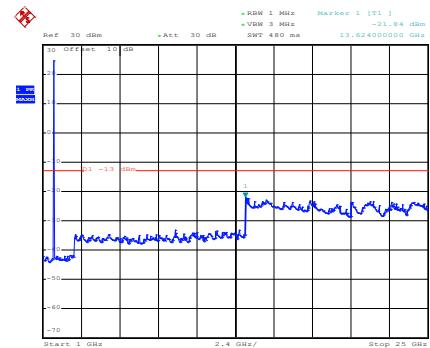
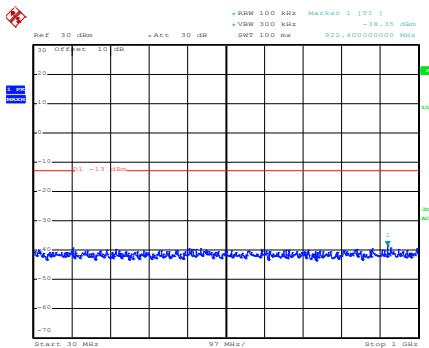
Date: 20.OCT.2017 15:58:36

1GHz~25GHz

## LTE band 4, 1.4MHz

16 QAM & RB Size 1

Lowest channel



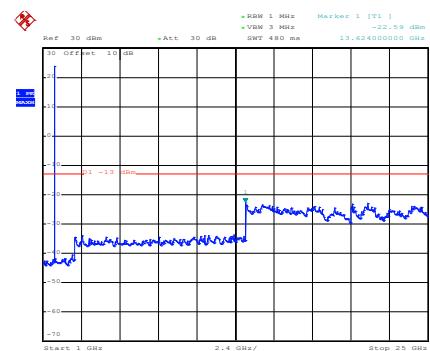
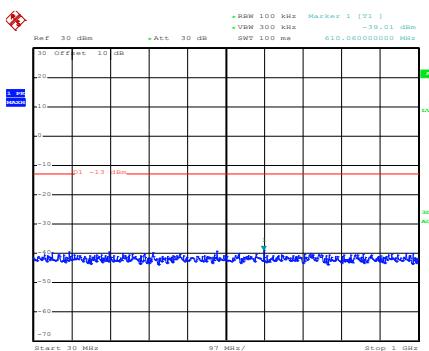
Date: 18.OCT.2017 15:30:59

Date: 20.OCT.2017 16:09:09

30MHz-1GHz

1GHz~25GHz

Middle channel



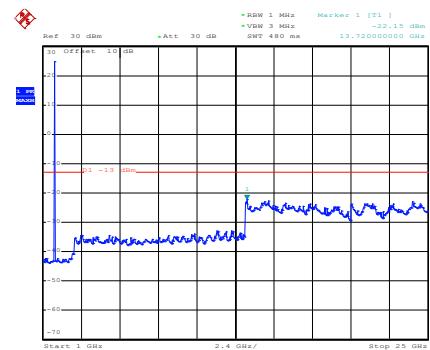
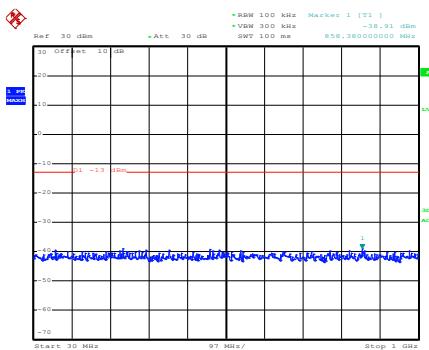
Date: 18.OCT.2017 15:31:40

Date: 20.OCT.2017 16:10:31

30MHz~1GHz

1GHz~25GHz

High channel



Date: 18.OCT.2017 15:32:44

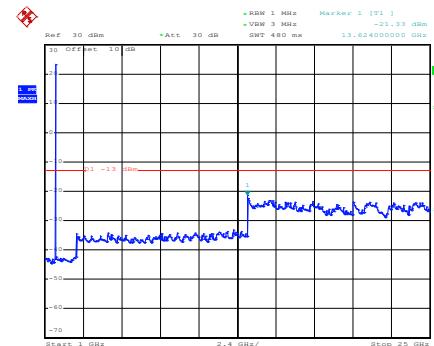
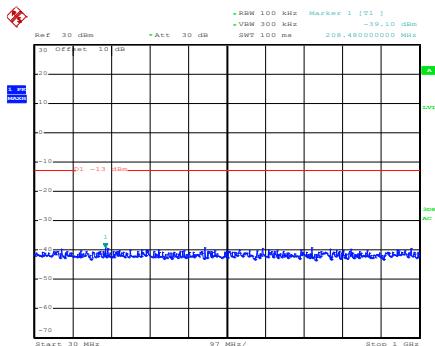
Date: 20.OCT.2017 16:11:37

30MHz~1GHz

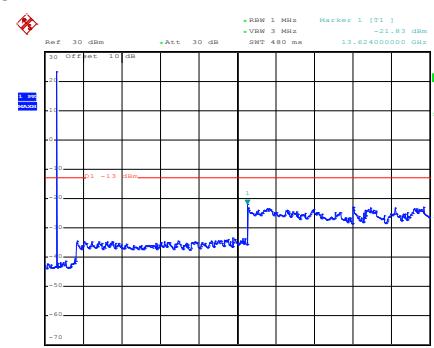
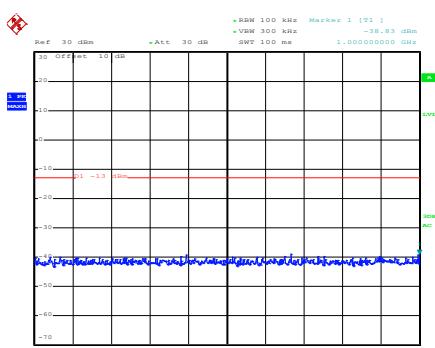
1GHz~25GHz

## 16 QAM & RB Size 6

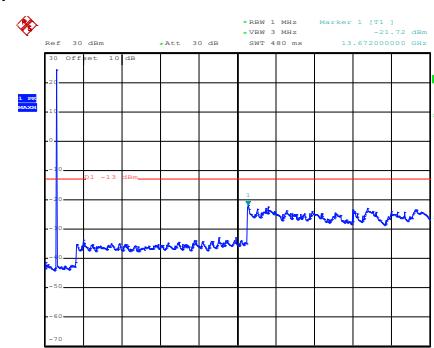
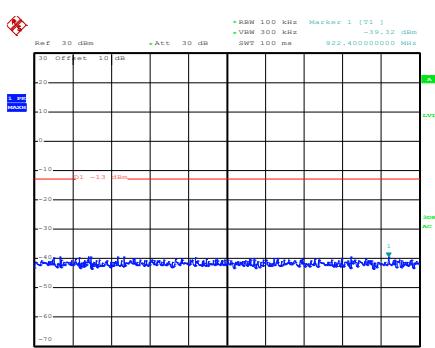
### Lowest channel



### Middle channel

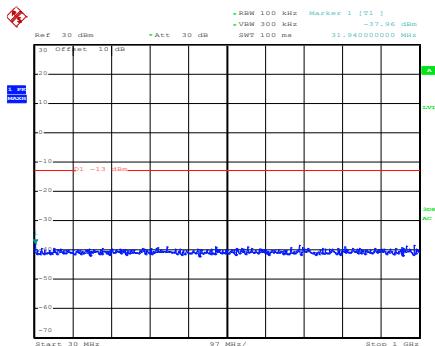


### High channel



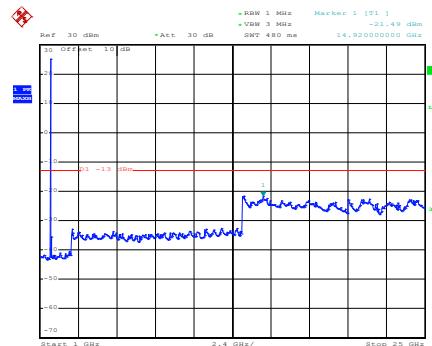
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 15:30:54

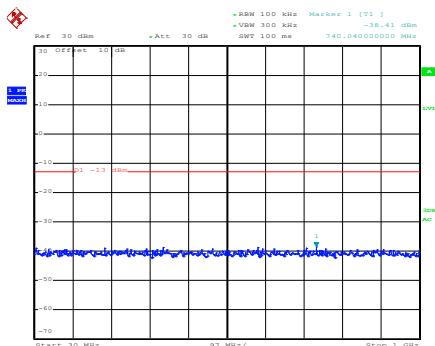
30MHz~1GHz



Date: 20.OCT.2017 16:08:59

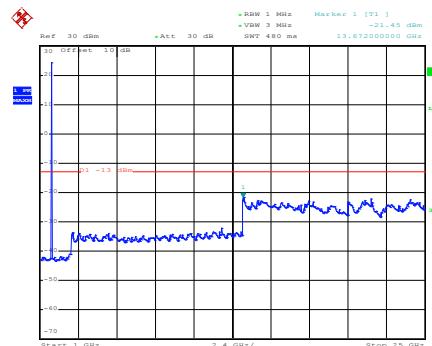
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:31:36

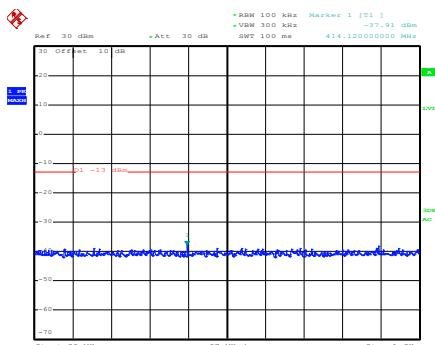
30MHz~1GHz



Date: 20.OCT.2017 16:10:24

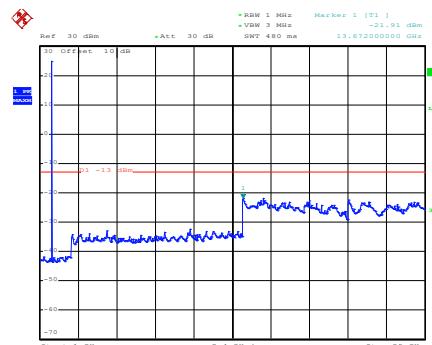
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:32:40

30MHz~1GHz

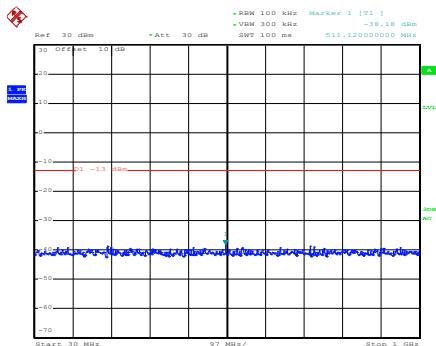


Date: 20.OCT.2017 16:11:28

1GHz~25GHz

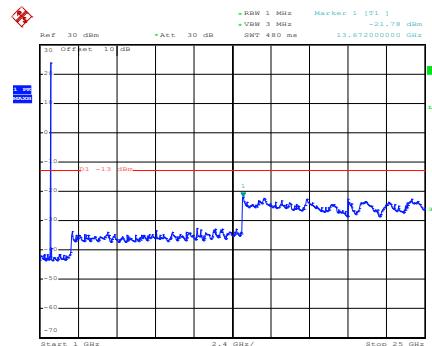
## QPSK & RB Size 6

### Lowest channel



Date: 18.OCT.2017 15:31:21

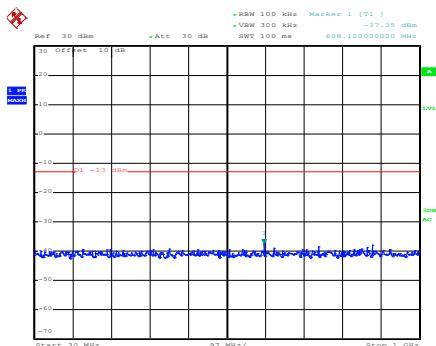
30MHz~1GHz



Date: 20.OCT.2017 16:09:53

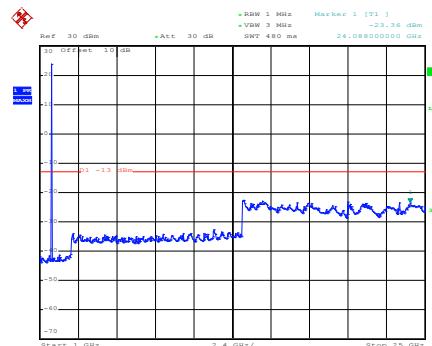
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:32:07

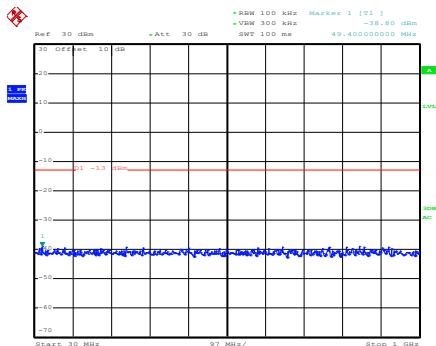
30MHz~1GHz



Date: 20.OCT.2017 16:11:00

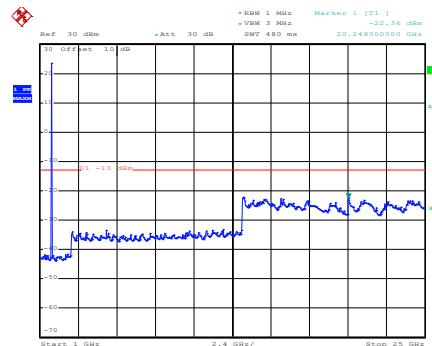
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:33:02

30MHz~1GHz



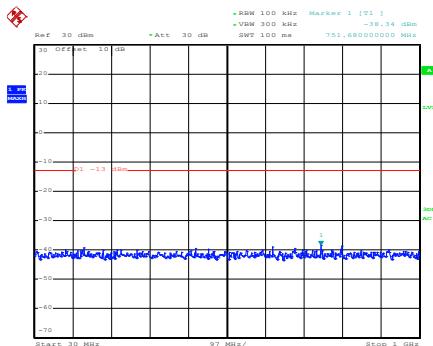
Date: 20.OCT.2017 16:12:04

1GHz~25GHz

## 3MHz

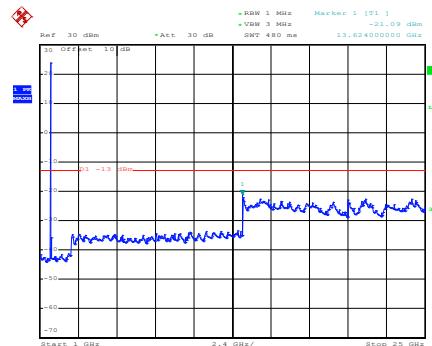
### 16 QAM & RB Size 1

#### Lowest channel



Date: 18.OCT.2017 15:33:28

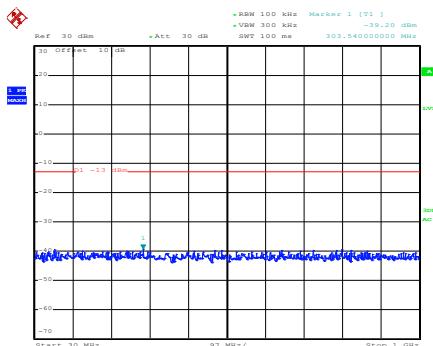
30MHz~1GHz



Date: 20.OCT.2017 16:14:07

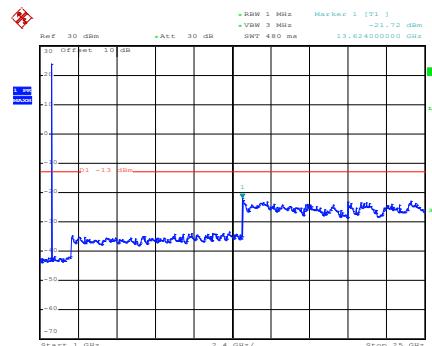
1GHz~25GHz

#### Middle channel



Date: 18.OCT.2017 15:34:07

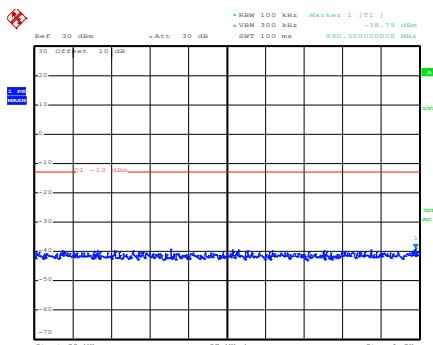
30MHz~1GHz



Date: 20.OCT.2017 16:15:31

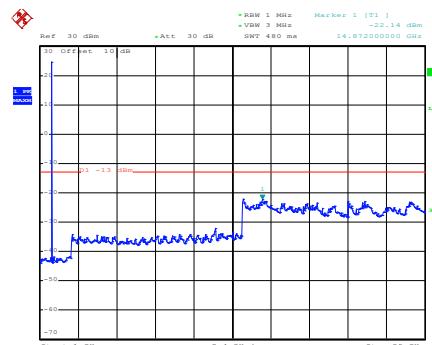
1GHz~25GHz

#### High channel



Date: 18.OCT.2017 15:34:53

30MHz~1GHz

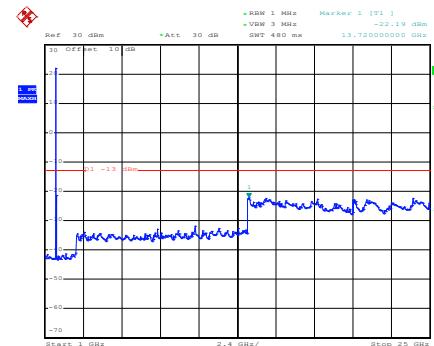
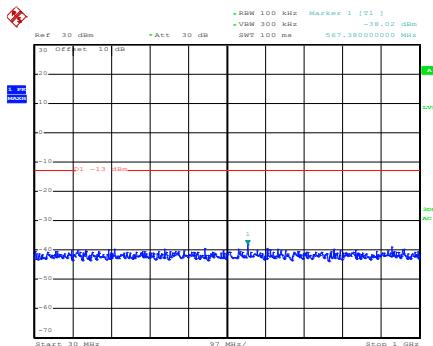


Date: 20.OCT.2017 16:16:34

1GHz~25GHz

## 16 QAM & RB Size 15

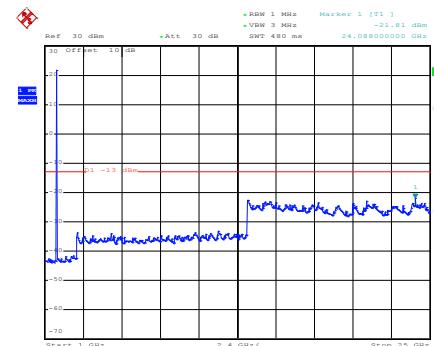
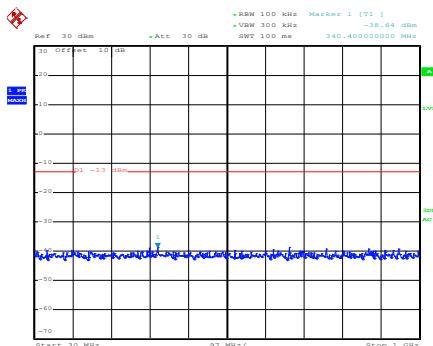
### Lowest channel



30MHz~1GHz

1GHz~25GHz

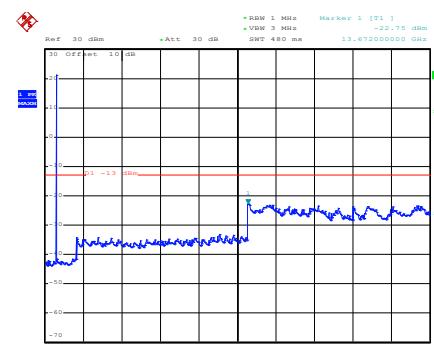
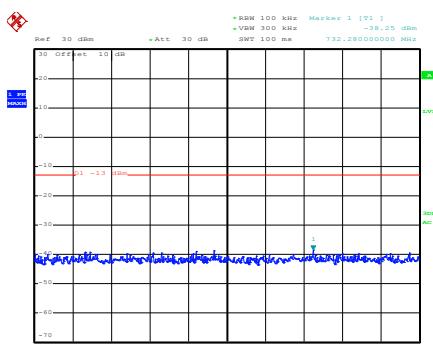
### Middle channel



30MHz~1GHz

1GHz~25GHz

### High channel

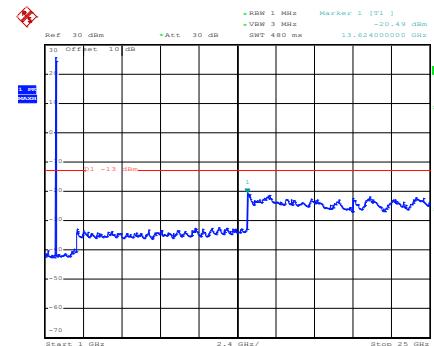
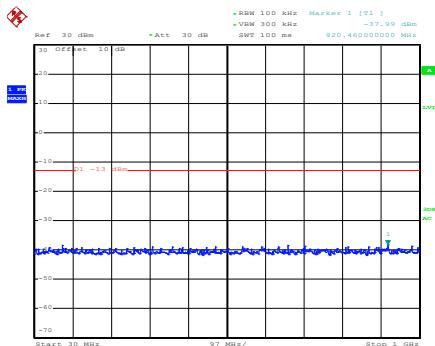


30MHz~1GHz

1GHz~25GHz

## QPSK & RB Size 1

### Lowest channel



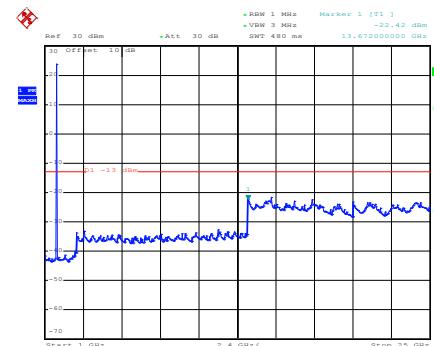
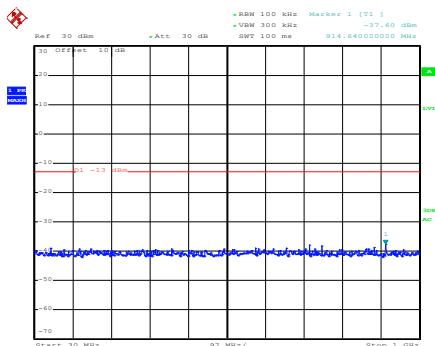
Date: 18.OCT.2017 15:33:24

30MHz~1GHz

Date: 20.OCT.2017 16:13:58

1GHz~25GHz

### Middle channel



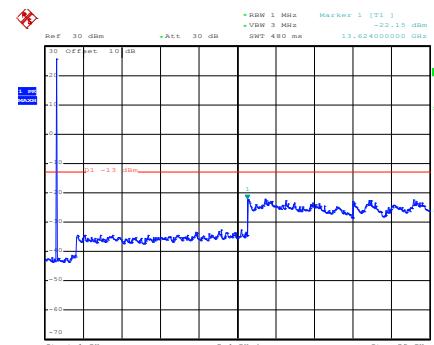
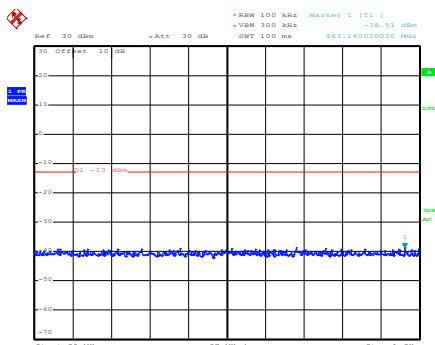
Date: 18.OCT.2017 15:34:03

30MHz~1GHz

Date: 20.OCT.2017 16:15:23

1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:34:48

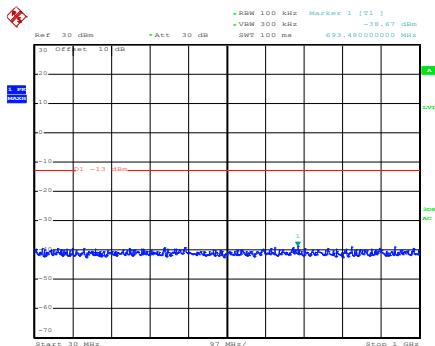
30MHz~1GHz

Date: 20.OCT.2017 16:16:25

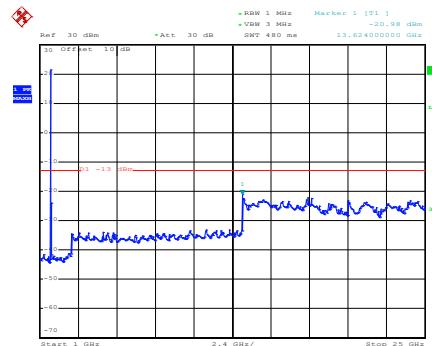
1GHz~25GHz

## QPSK & RB Size 15

### Lowest channel

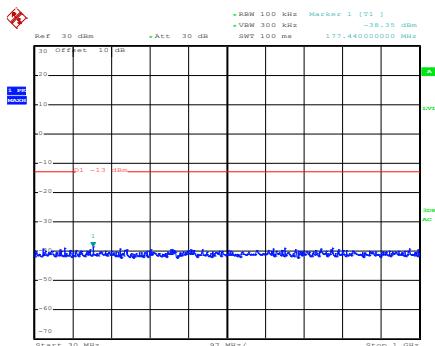


30MHz~1GHz

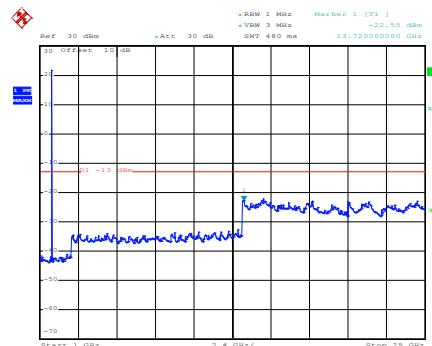


1GHz~25GHz

### Middle channel

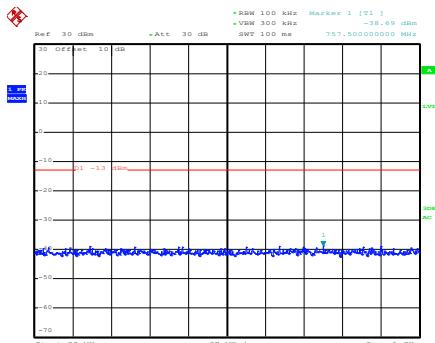


30MHz~1GHz

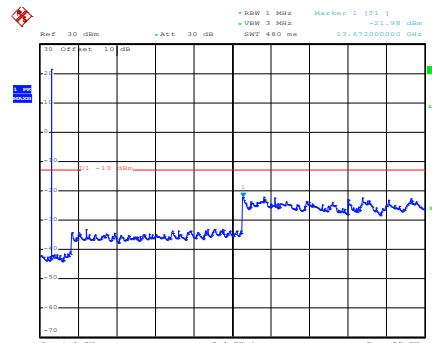


1GHz~25GHz

### High channel



30MHz~1GHz

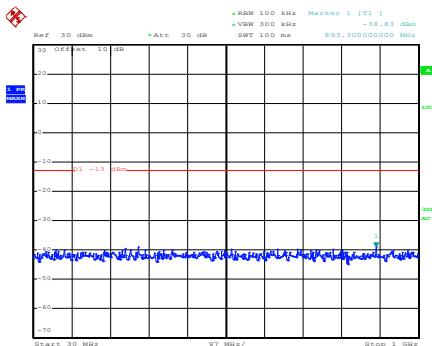


1GHz~25GHz

## 5MHz

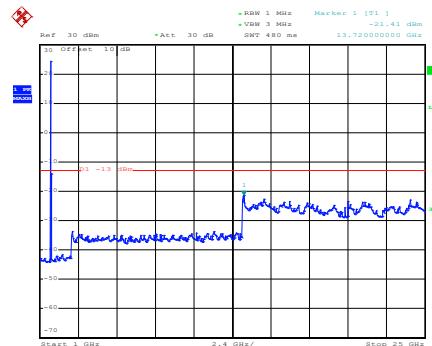
### 16 QAM & RB Size 1

#### Lowest channel



Date: 18.OCT.2017 15:35:46

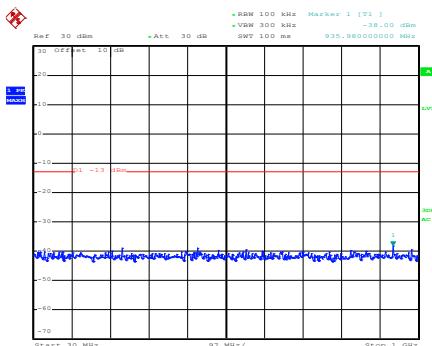
30MHz~1GHz



Date: 20.OCT.2017 16:17:55

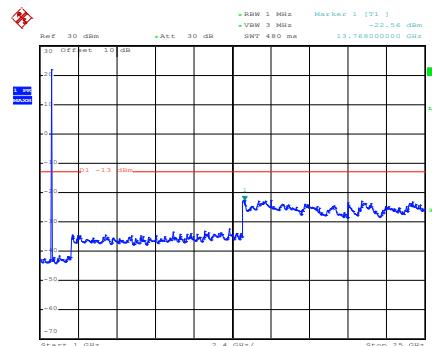
1GHz~25GHz

#### Middle channel



Date: 18.OCT.2017 15:36:40

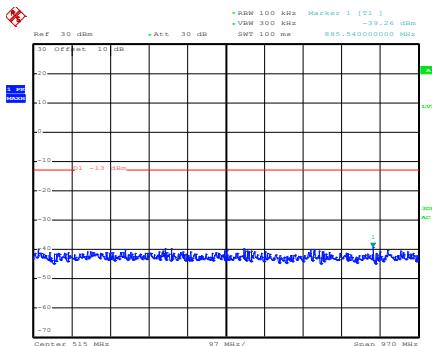
30MHz~1GHz



Date: 20.OCT.2017 16:18:59

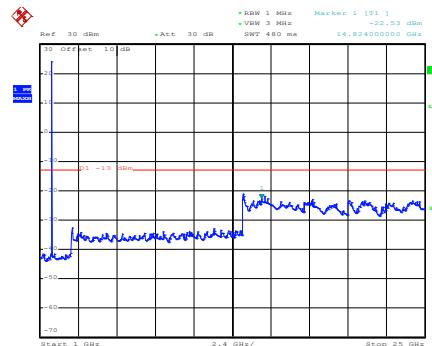
1GHz~25GHz

#### High channel



Date: 18.OCT.2017 15:48:30

30MHz~1GHz

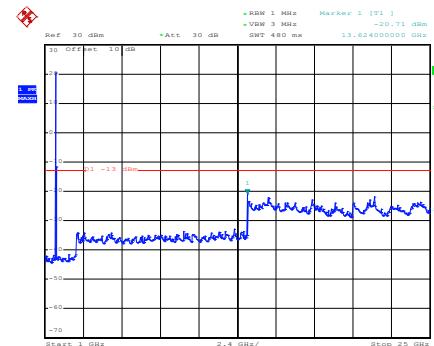
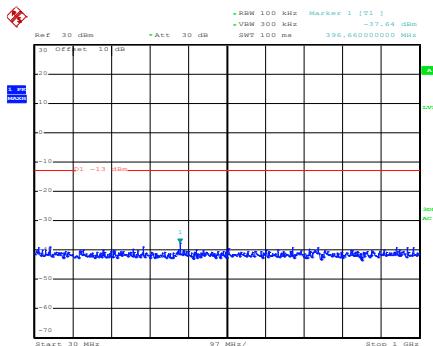


Date: 20.OCT.2017 16:20:14

1GHz~25GHz

## 16 QAM & RB Size 25

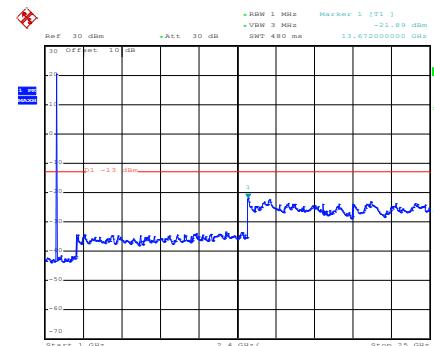
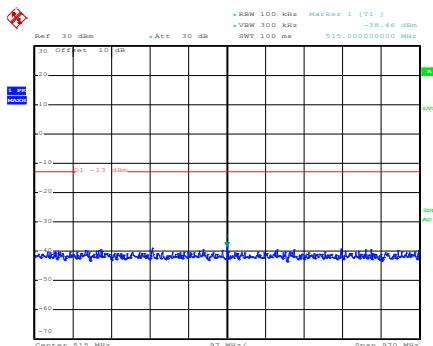
### Lowest channel



30MHz~1GHz

1GHz~25GHz

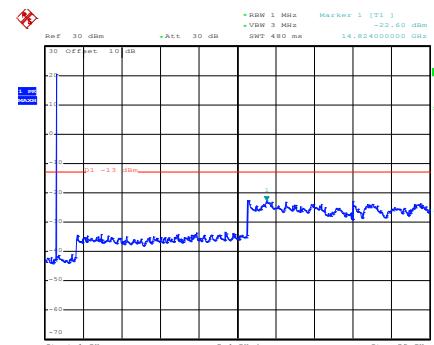
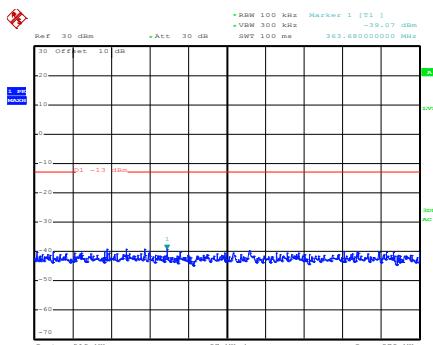
### Middle channel



30MHz~1GHz

1GHz~25GHz

### High channel

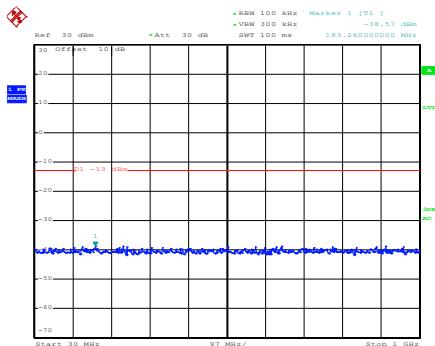


30MHz~1GHz

1GHz~25GHz

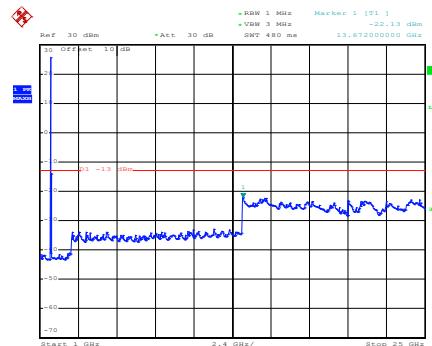
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 15:35:42

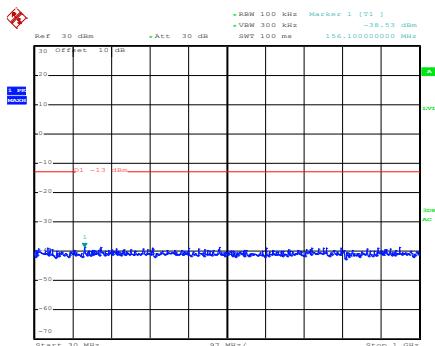
30MHz~1GHz



Date: 20.OCT.2017 16:17:48

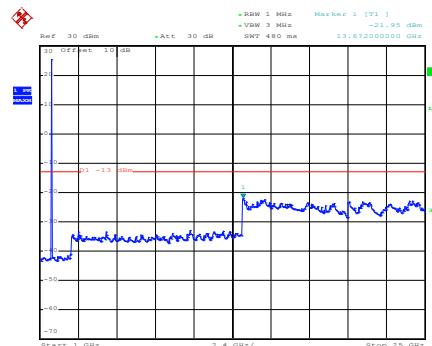
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:36:35

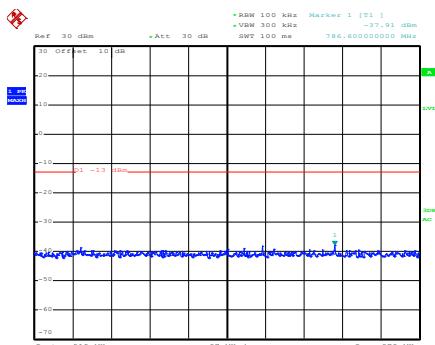
30MHz~1GHz



Date: 20.OCT.2017 16:18:50

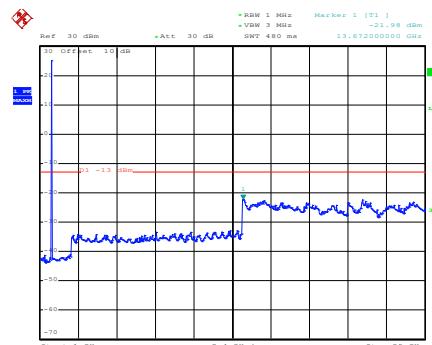
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:48:27

30MHz~1GHz

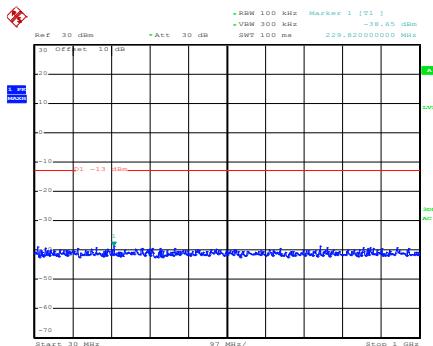


Date: 20.OCT.2017 16:20:05

1GHz~25GHz

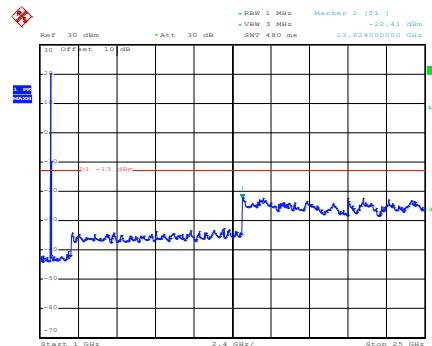
## QPSK & RB Size 25

### Lowest channel



Date: 18.OCT.2017 15:36:05

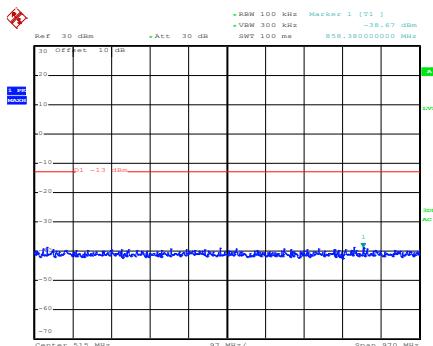
30MHz~1GHz



Date: 20.OCT.2017 16:18:25

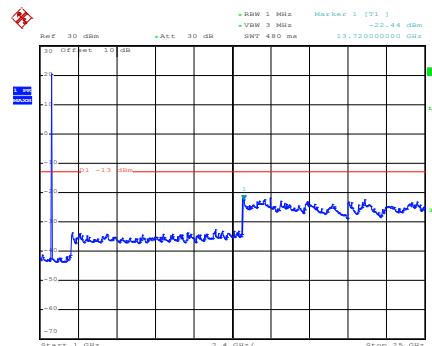
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:48:11

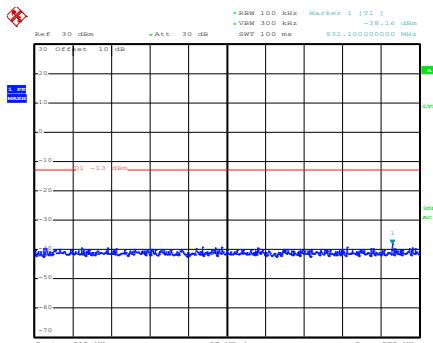
30MHz~1GHz



Date: 20.OCT.2017 16:19:40

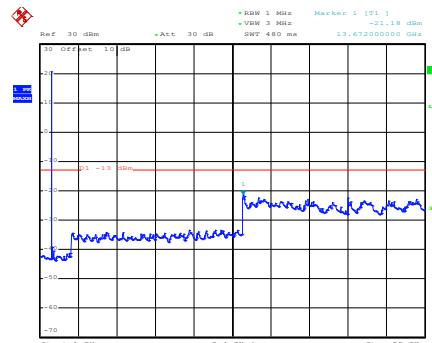
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:48:37

30MHz~1GHz



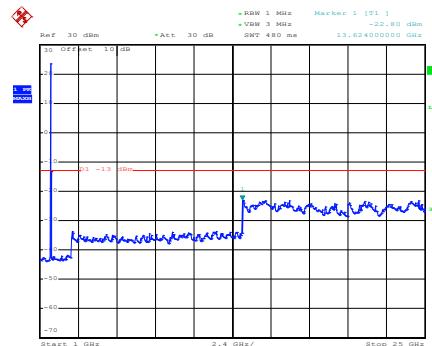
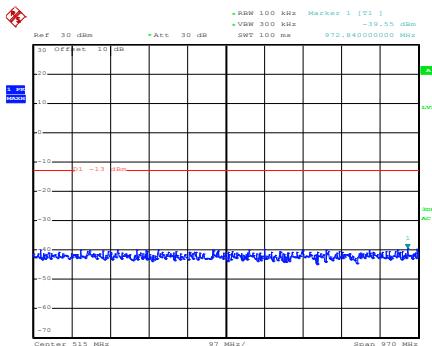
Date: 20.OCT.2017 16:20:51

1GHz~25GHz

## 10MHz

### 16 QAM & RB Size 1

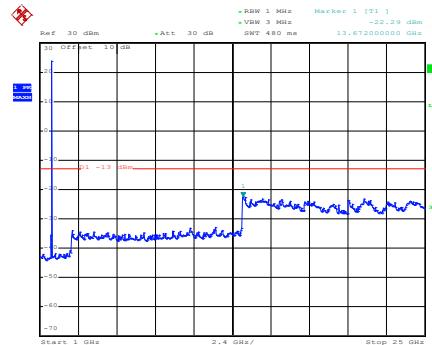
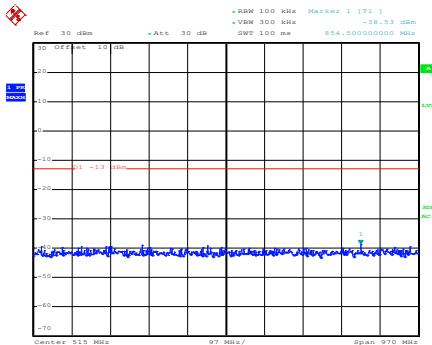
#### Lowest channel



30MHz~1GHz

1GHz~25GHz

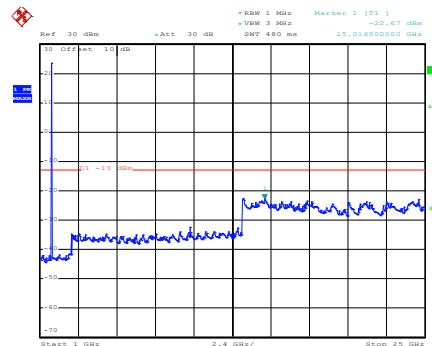
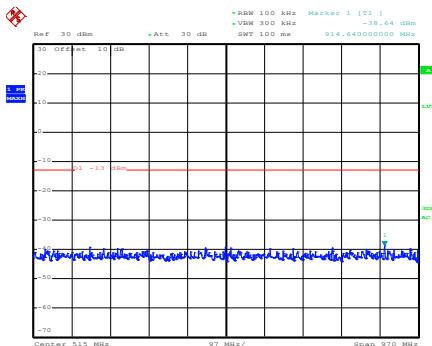
#### Middle channel



30MHz~1GHz

1GHz~25GHz

#### High channel

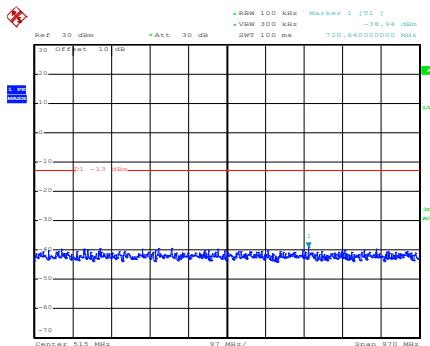


30MHz~1GHz

1GHz~25GHz

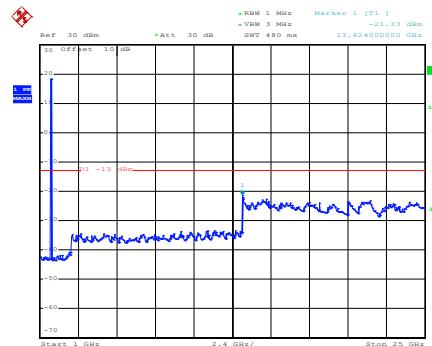
## 16 QAM & RB Size 50

### Lowest channel



Date: 18.OCT.2017 15:49:20

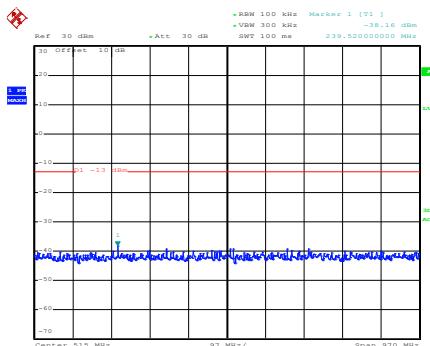
30MHz~1GHz



Date: 20.OCT.2017 16:22:25

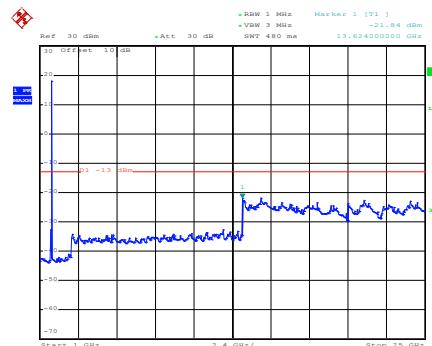
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:50:19

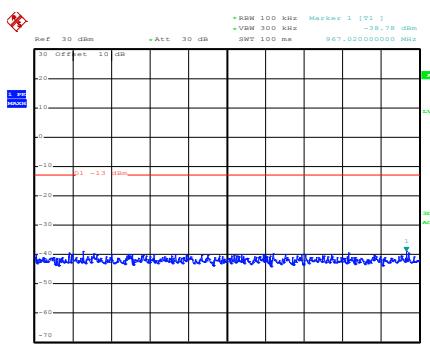
30MHz~1GHz



Date: 20.OCT.2017 16:24:39

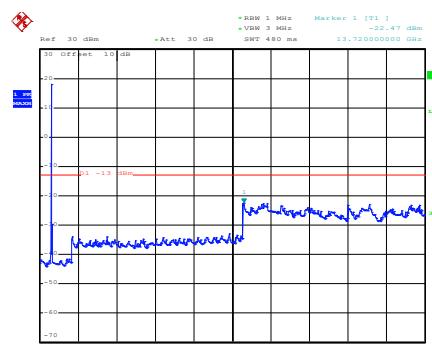
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:51:13

30MHz~1GHz

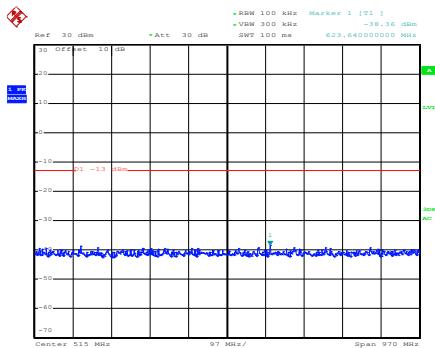


Date: 20.OCT.2017 16:25:51

1GHz~25GHz

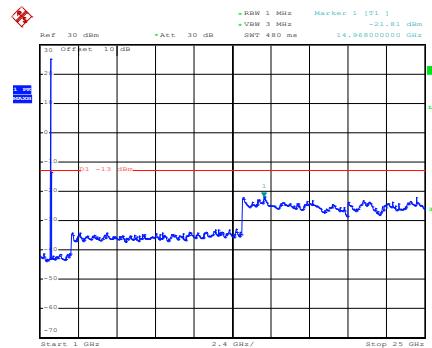
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 15:49:27

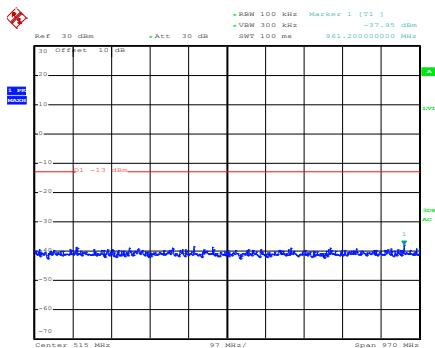
30MHz~1GHz



Date: 20.OCT.2017 16:21:39

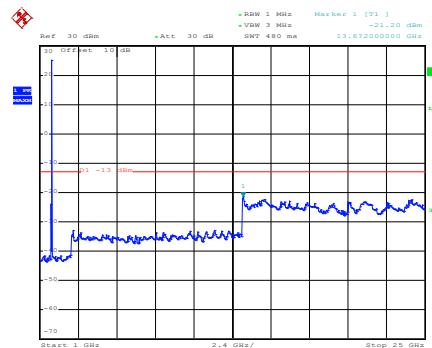
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:49:42

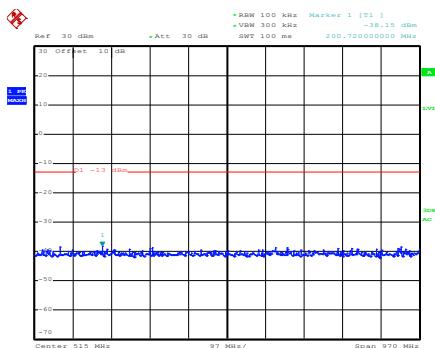
30MHz~1GHz



Date: 20.OCT.2017 16:23:49

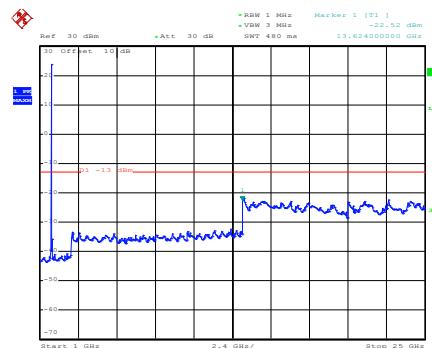
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:50:31

30MHz~1GHz

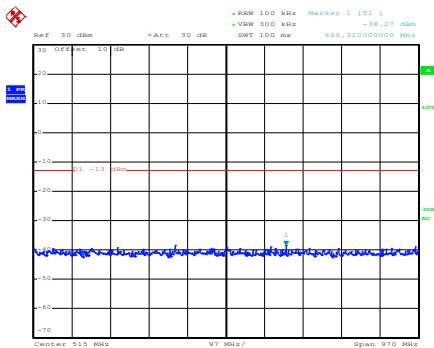


Date: 20.OCT.2017 16:25:02

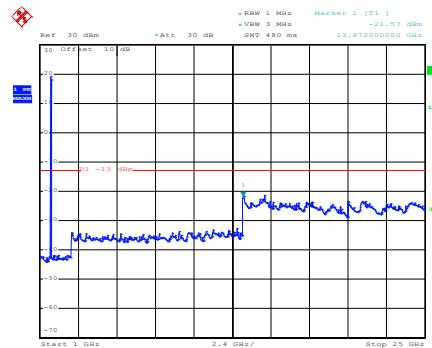
1GHz~25GHz

## QPSK & RB Size 50

### Lowest channel

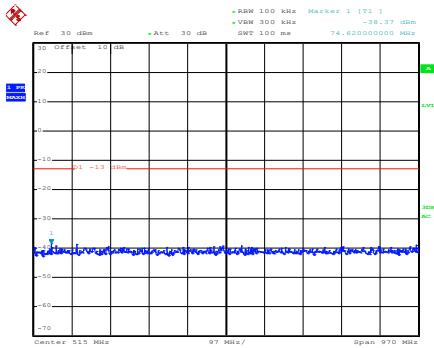


30MHz~1GHz

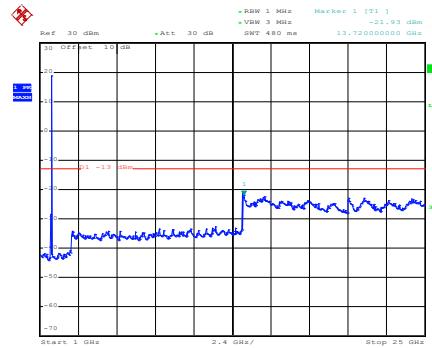


1GHz~25GHz

### Middle channel

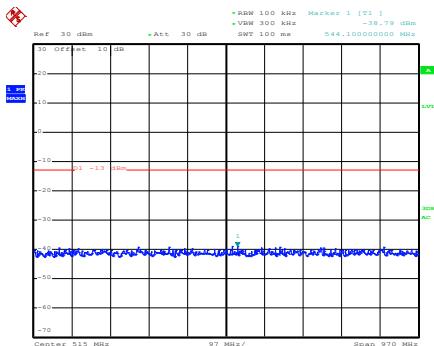


30MHz~1GHz

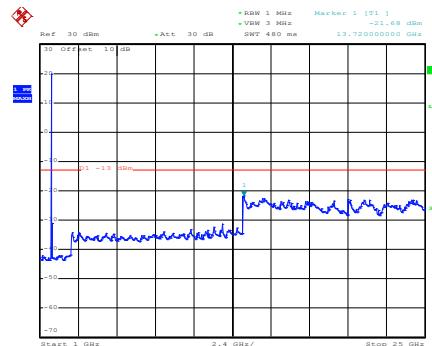


1GHz~25GHz

### High channel



30MHz~1GHz

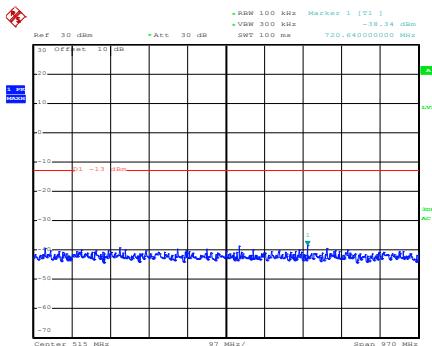


1GHz~25GHz

## 15MHz

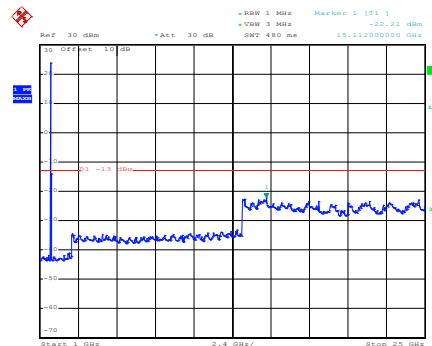
### 16 QAM & RB Size 1

#### Lowest channel



Date: 18.OCT.2017 15:51:28

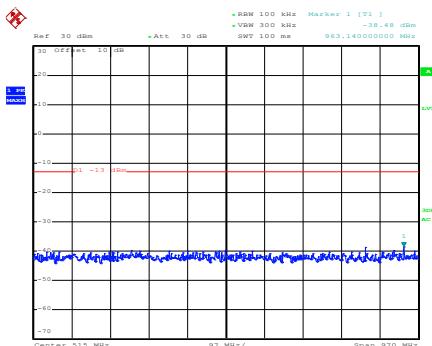
30MHz~1GHz



Date: 20.OCT.2017 16:26:36

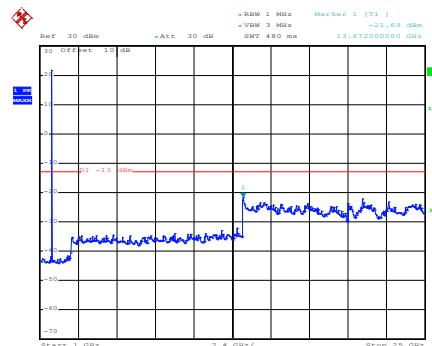
1GHz~25GHz

#### Middle channel



Date: 18.OCT.2017 15:52:06

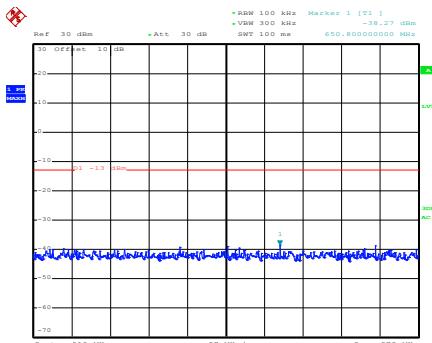
30MHz~1GHz



Date: 20.OCT.2017 16:27:49

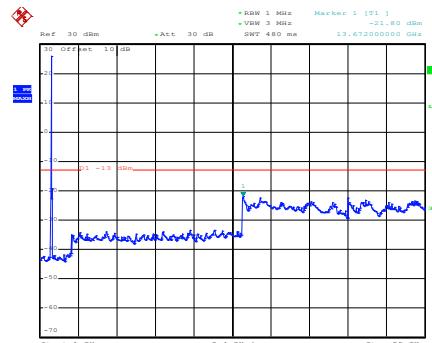
1GHz~25GHz

#### High channel



Date: 18.OCT.2017 15:52:44

30MHz~1GHz

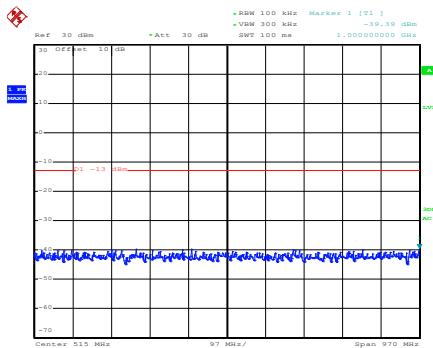


Date: 20.OCT.2017 16:29:03

1GHz~25GHz

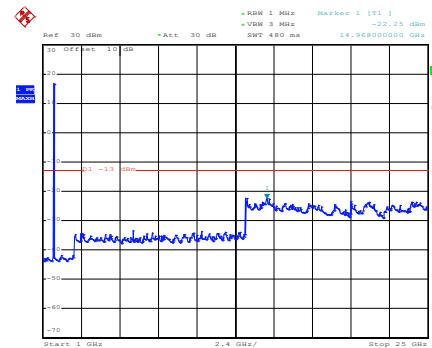
16 QAM & RB Size 75

## Lowest channel



Date: 18.OCT.2017 15:51:51

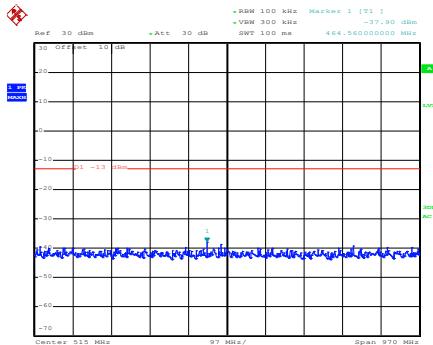
30MHz~1GHz



Date: 20.OCT.2017 16:27:17

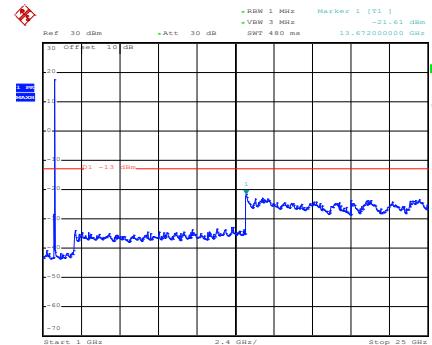
1GHz~25GHz

## Middle channel



Date: 18.OCT.2017 15:52:28

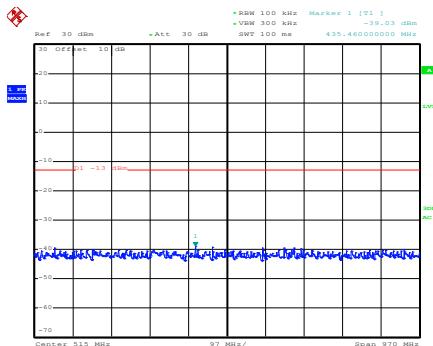
30MHz~1GHz



Date: 20.OCT.2017 16:28:26

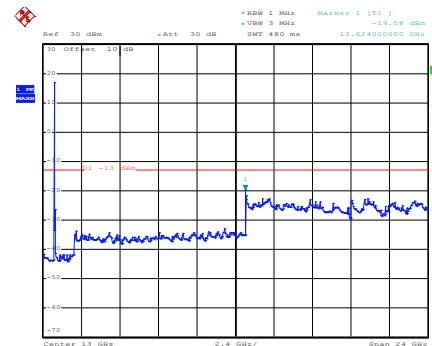
1GHz~25GHz

## High channel



Date: 18.OCT.2017 15:53:06

30MHz~1GHz

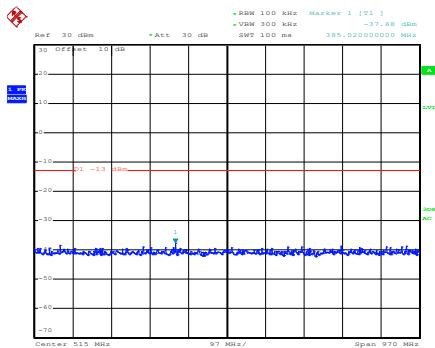


Date: 20.OCT.2017 16:33:03

1GHz~25GHz

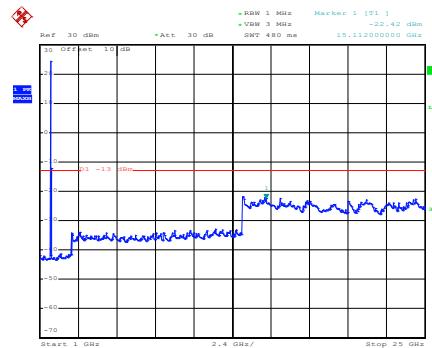
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 15:51:25

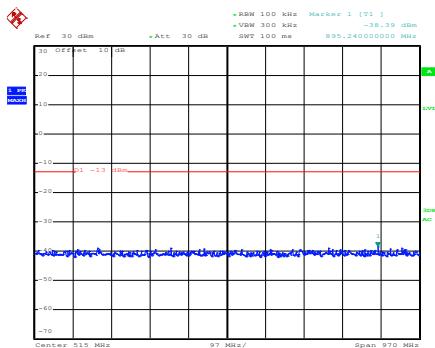
30MHz~1GHz



Date: 20.OCT.2017 16:26:27

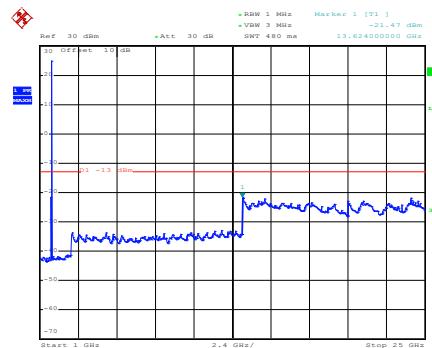
1GHz~25GHz

### Middle channel



Date: 18.OCT.2017 15:52:03

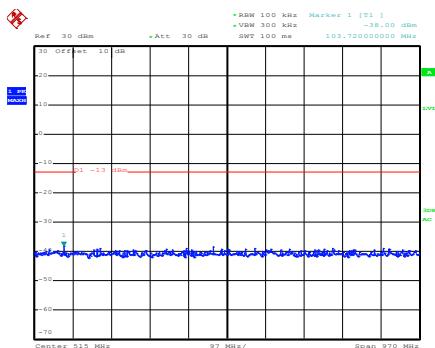
30MHz~1GHz



Date: 20.OCT.2017 16:27:41

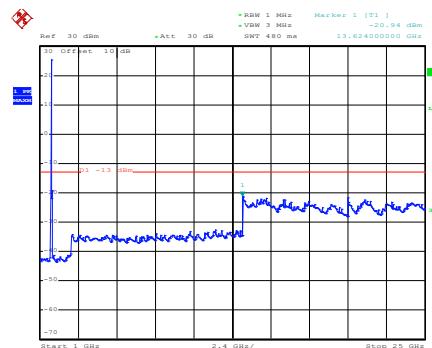
1GHz~25GHz

### High channel



Date: 18.OCT.2017 15:52:40

30MHz~1GHz

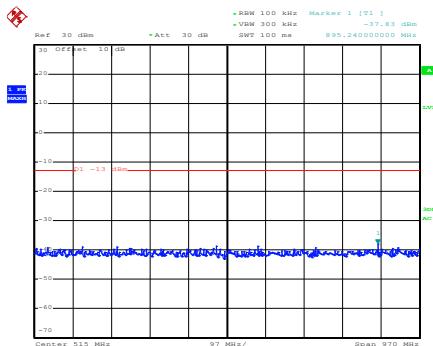


Date: 20.OCT.2017 16:28:53

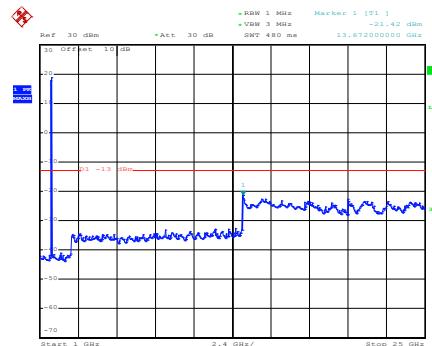
1GHz~25GHz

## QPSK & RB Size 75

### Lowest channel

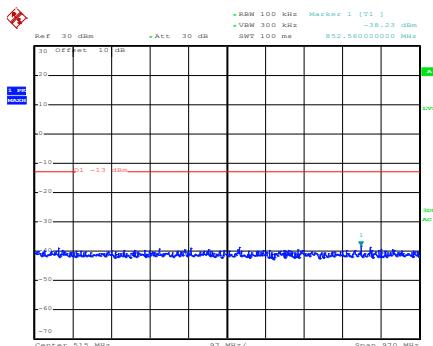


30MHz~1GHz

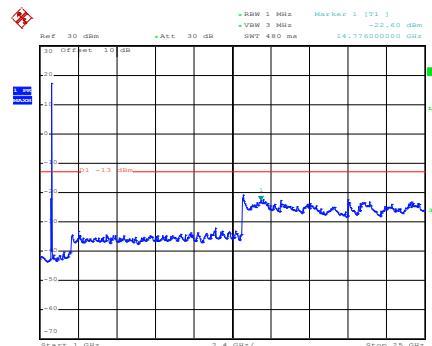


1GHz~25GHz

### Middle channel

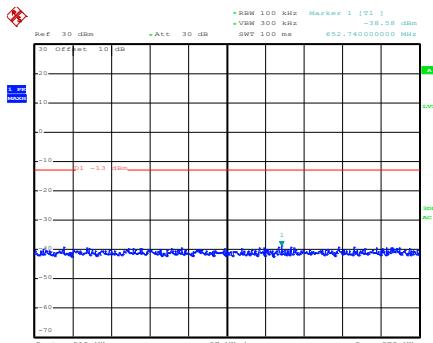


30MHz~1GHz

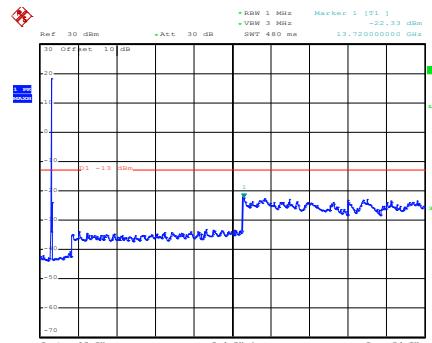


1GHz~25GHz

### High channel



30MHz~1GHz

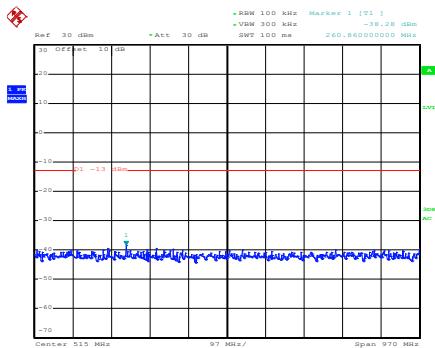


1GHz~25GHz

## 20MHz

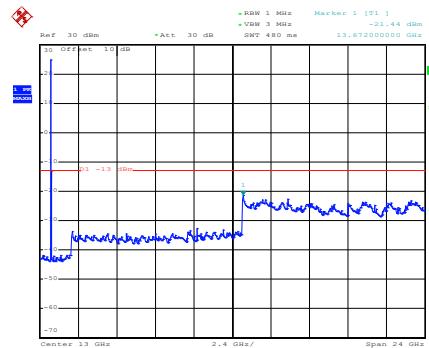
### 16 QAM & RB Size 1

#### Lowest channel



Date: 18.OCT.2017 15:53:40

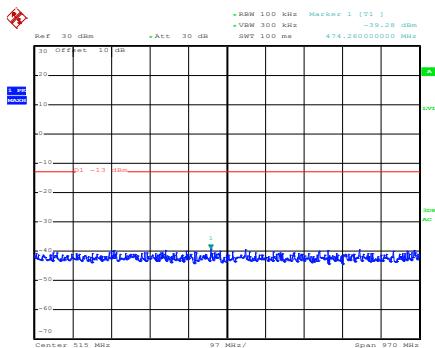
#### 30MHz~1GHz



Date: 20.OCT.2017 16:33:59

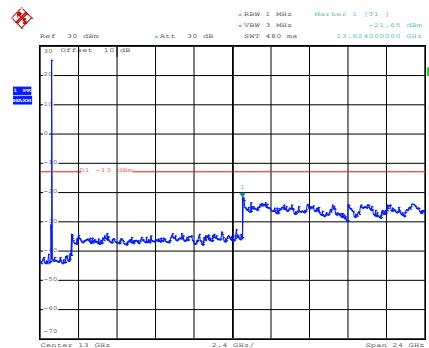
#### 1GHz~25GHz

#### Middle channel



Date: 18.OCT.2017 15:54:24

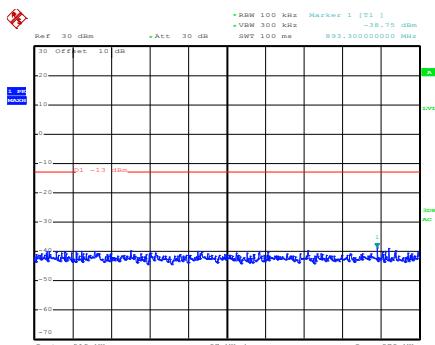
#### 30MHz~1GHz



Date: 20.OCT.2017 16:35:11

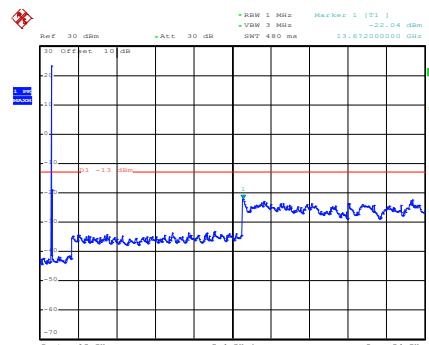
#### 1GHz~25GHz

#### High channel



Date: 18.OCT.2017 15:54:59

#### 30MHz~1GHz

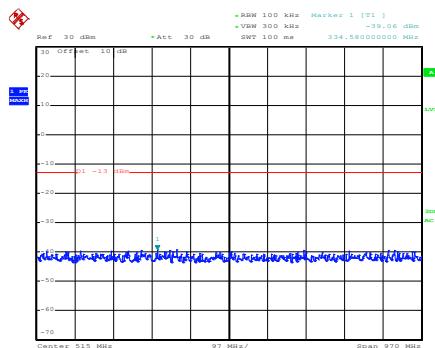


Date: 20.OCT.2017 16:36:17

#### 1GHz~25GHz

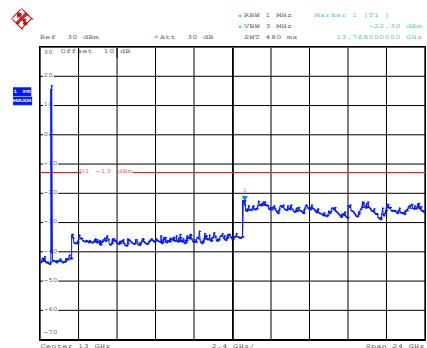
16 QAM & RB Size 100

## Lowest channel



Date: 18.OCT.2017 15:54:06

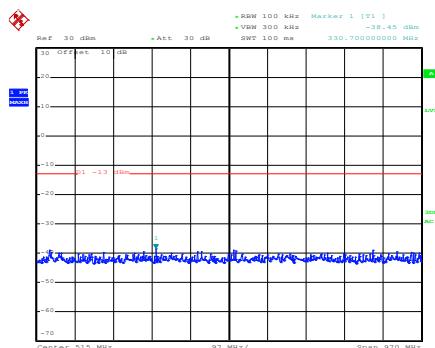
30MHz~1GHz



Date: 20.OCT.2017 16:34:44

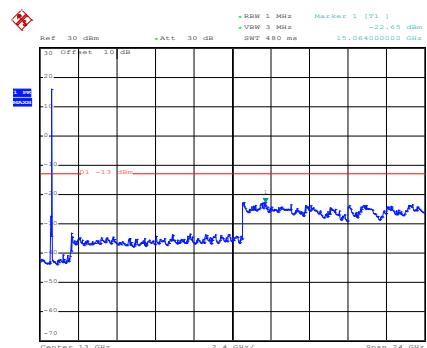
1GHz~25GHz

## Middle channel



Date: 18.OCT.2017 15:54:46

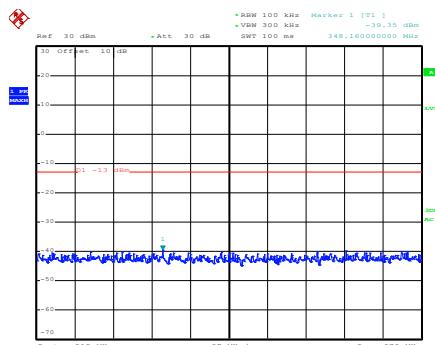
30MHz~1GHz



Date: 20.OCT.2017 16:35:51

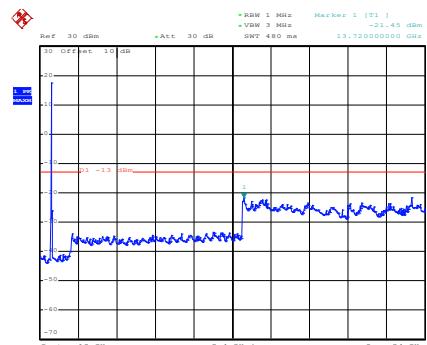
1GHz~25GHz

## High channel



Date: 18.OCT.2017 15:55:21

30MHz~1GHz

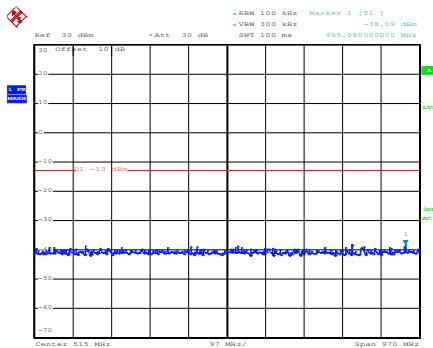


Date: 20.OCT.2017 16:37:00

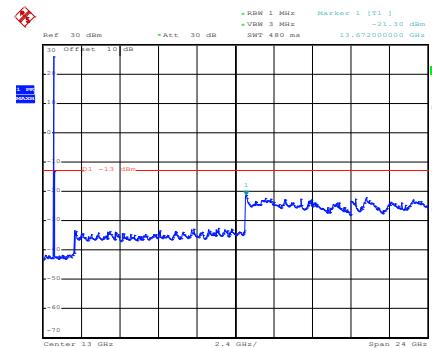
1GHz~25GHz

## QPSK & RB Size 1

### Lowest channel

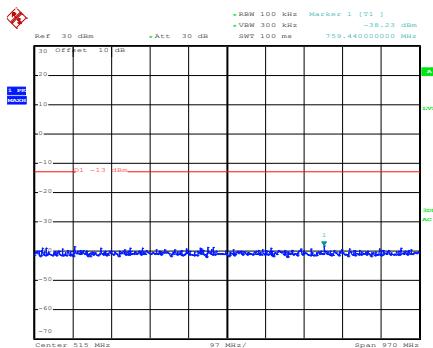


30MHz~1GHz

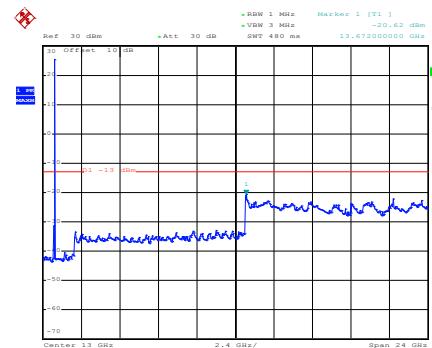


1GHz~25GHz

### Middle channel

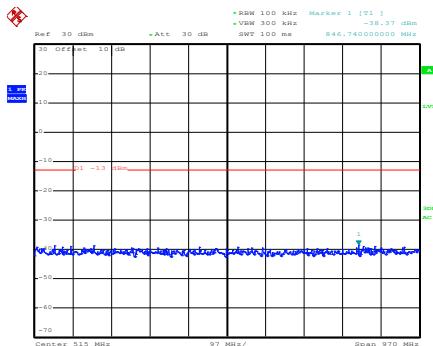


30MHz~1GHz

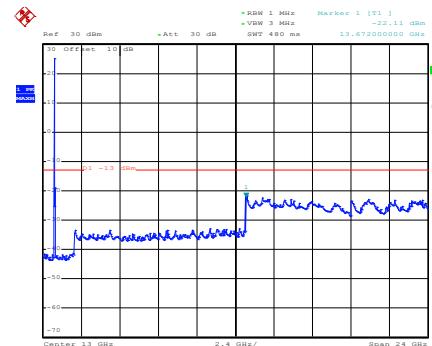


1GHz~25GHz

### High channel



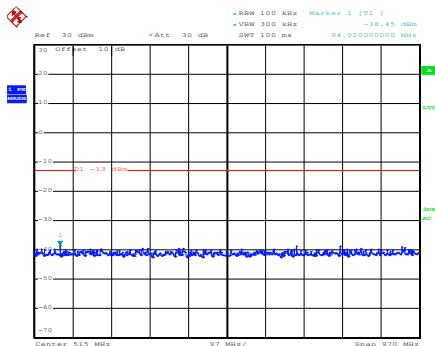
30MHz~1GHz



1GHz~25GHz

## QPSK & RB Size 100

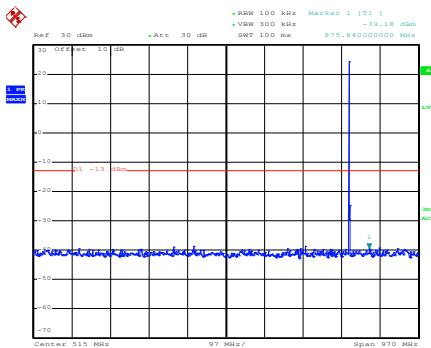
### Lowest channel



## LTE band 5, 1.4MHz

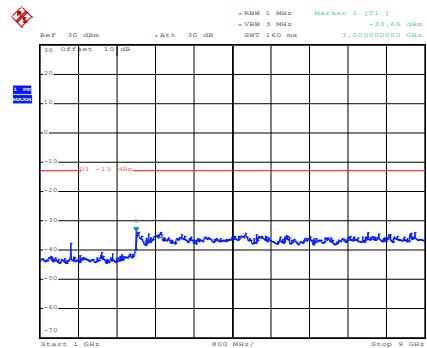
16 QAM & RB Size 1

Lowest channel



Date: 18.OCT.2017 09:49:11

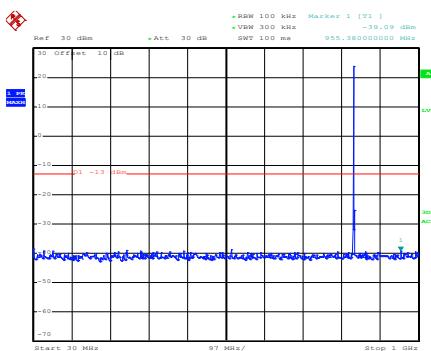
30MHz-1GHz



Date: 20.OCT.2017 16:59:04

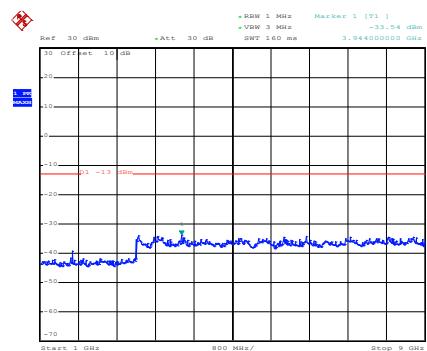
1GHz~9GHz

Middle channel



Date: 18.OCT.2017 09:56:14

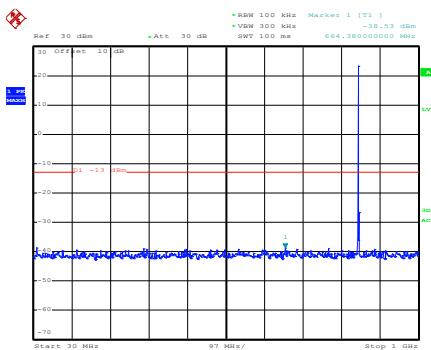
30MHz~1GHz



Date: 20.OCT.2017 17:00:01

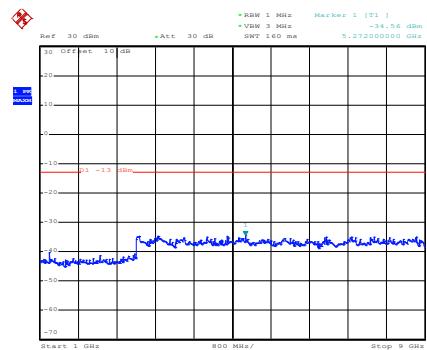
1GHz~9GHz

High channel



Date: 18.OCT.2017 10:00:50

30MHz~1GHz

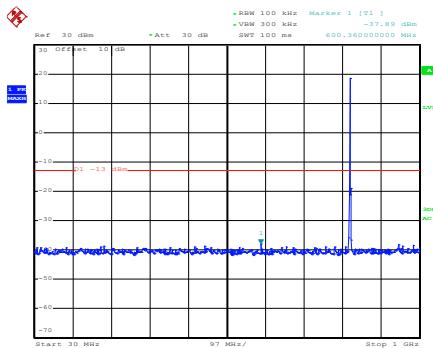


Date: 20.OCT.2017 17:01:08

1GHz~9GHz

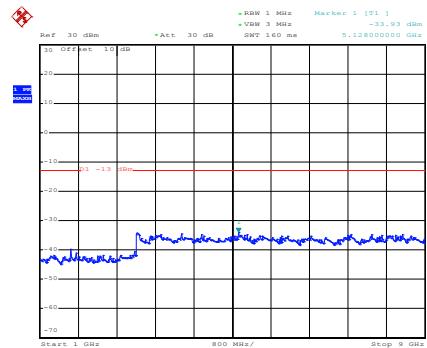
## 16 QAM & RB Size 6

### Lowest channel



Date: 18.OCT.2017 09:55:20

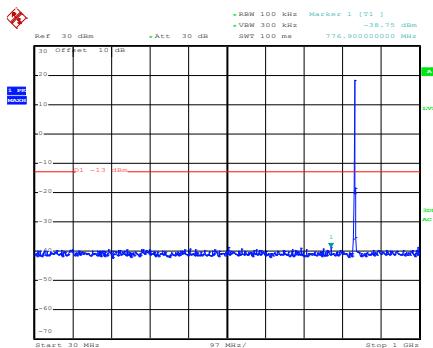
30MHz~1GHz



Date: 20.OCT.2017 16:59:35

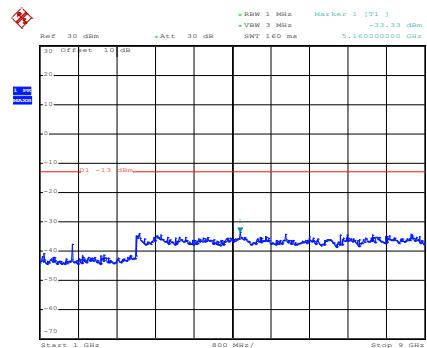
1GHz~9GHz

### Middle channel



Date: 18.OCT.2017 10:00:02

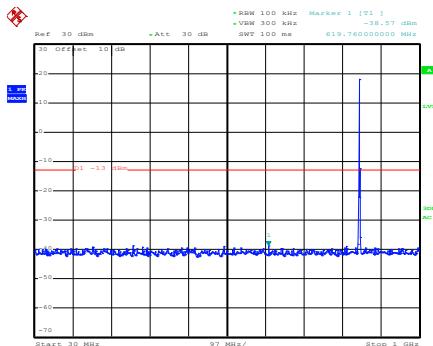
30MHz~1GHz



Date: 20.OCT.2017 17:00:39

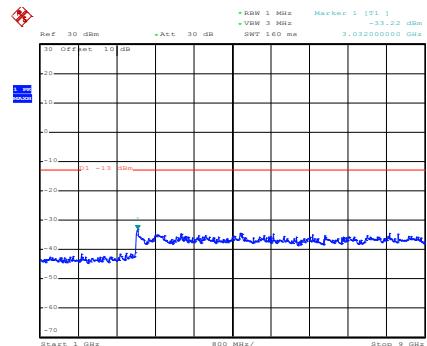
1GHz~9GHz

### High channel



Date: 18.OCT.2017 10:01:40

30MHz~1GHz

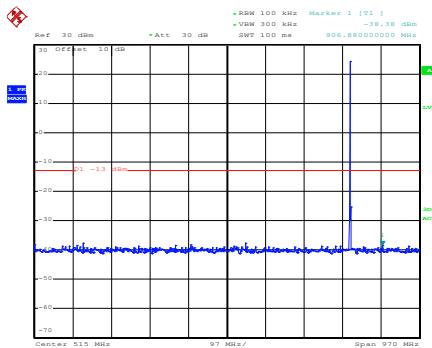


Date: 20.OCT.2017 17:01:39

1GHz~9GHz

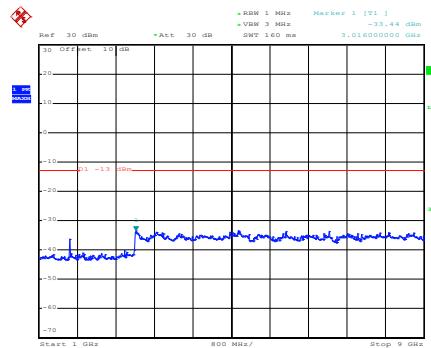
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 09:48:38

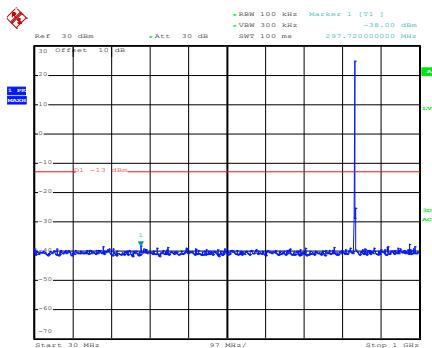
30MHz~1GHz



Date: 20.OCT.2017 16:58:53

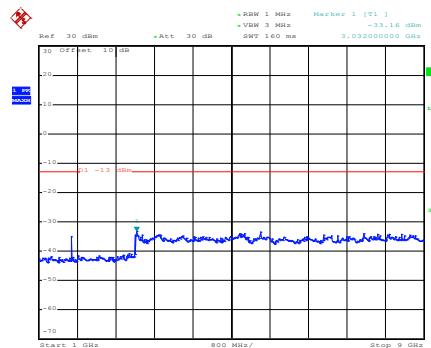
1GHz~9GHz

### Middle channel



Date: 18.OCT.2017 09:56:03

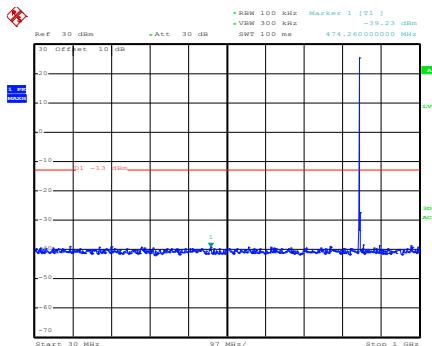
30MHz~1GHz



Date: 20.OCT.2017 16:59:54

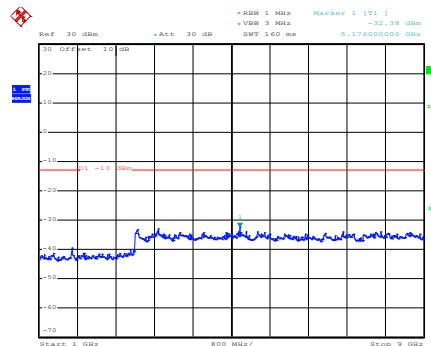
1GHz~9GHz

### High channel



Date: 18.OCT.2017 10:00:37

30MHz~1GHz

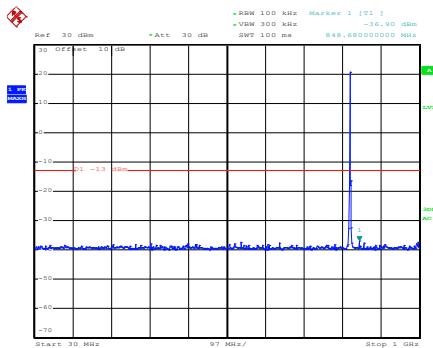


Date: 20.OCT.2017 17:01:02

1GHz~9GHz

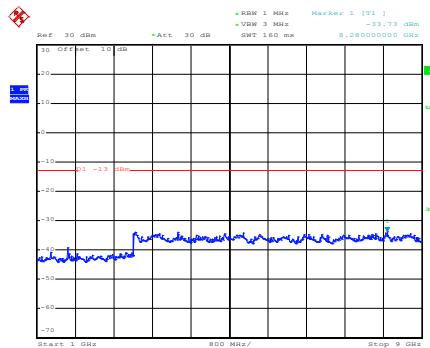
## QPSK & RB Size 6

### Lowest channel



Date: 18.OCT.2017 09:55:00

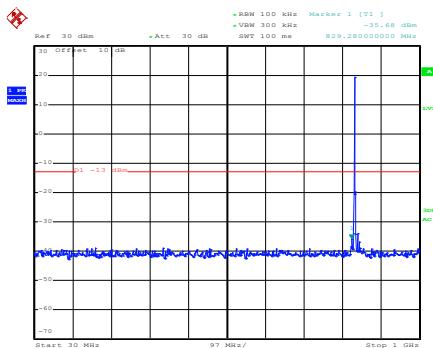
30MHz~1GHz



Date: 20.OCT.2017 16:59:29

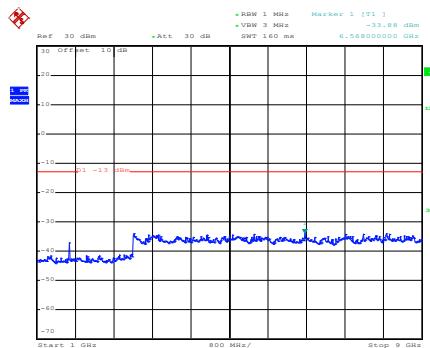
1GHz~9GHz

### Middle channel



Date: 18.OCT.2017 09:59:49

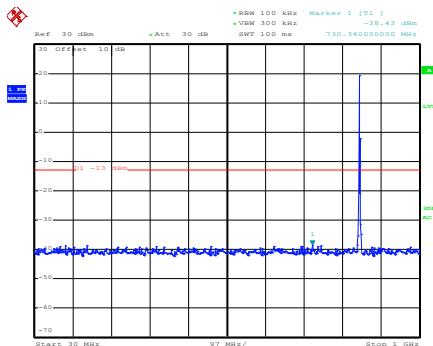
30MHz~1GHz



Date: 20.OCT.2017 17:00:33

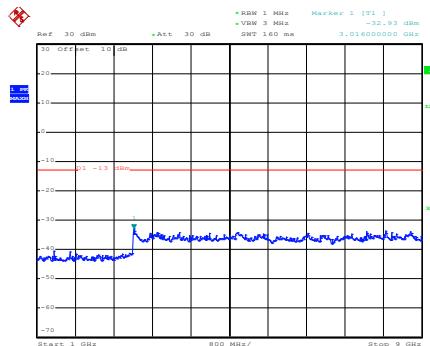
1GHz~9GHz

### High channel



Date: 18.OCT.2017 10:01:32

30MHz~1GHz



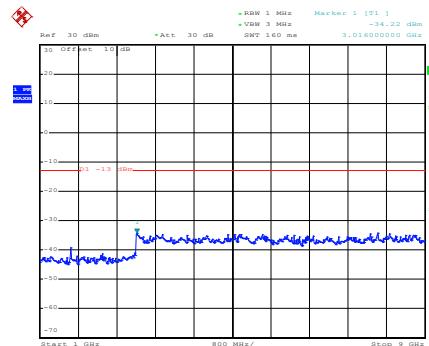
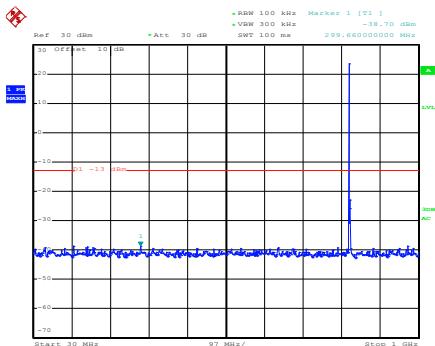
Date: 20.OCT.2017 17:01:35

1GHz~9GHz

## 3MHz

### 16 QAM & RB Size 1

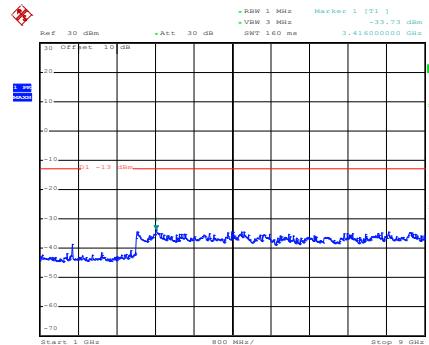
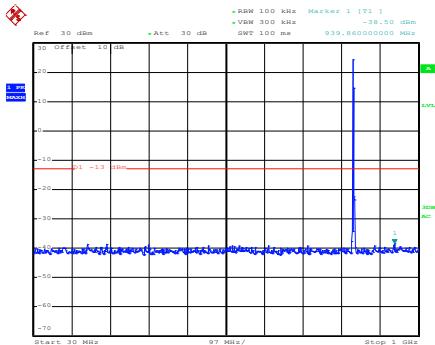
#### Lowest channel



30MHz~1GHz

1GHz~9GHz

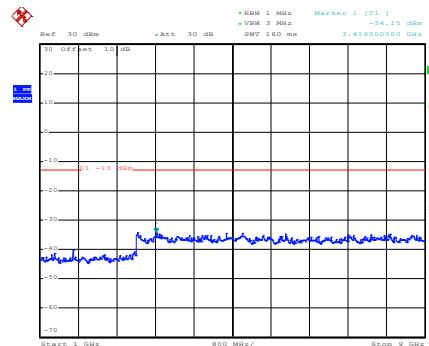
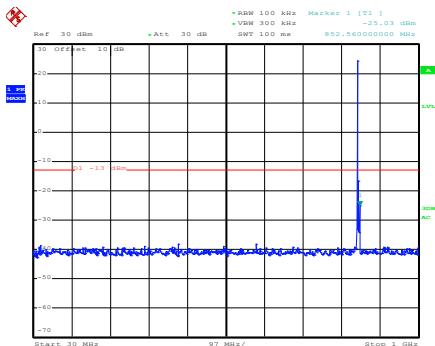
#### Middle channel



30MHz~1GHz

1GHz~9GHz

#### High channel

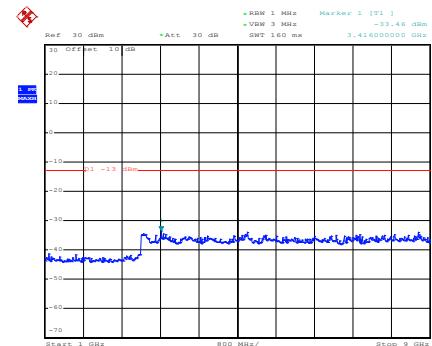
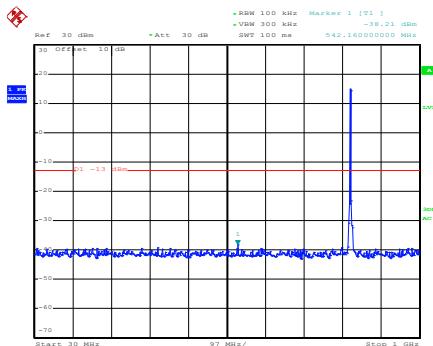


30MHz~1GHz

1GHz~9GHz

## 16 QAM & RB Size 15

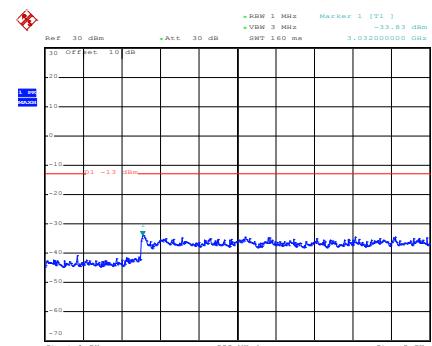
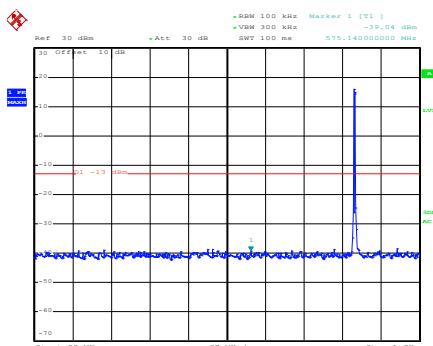
### Lowest channel



30MHz~1GHz

1GHz~9GHz

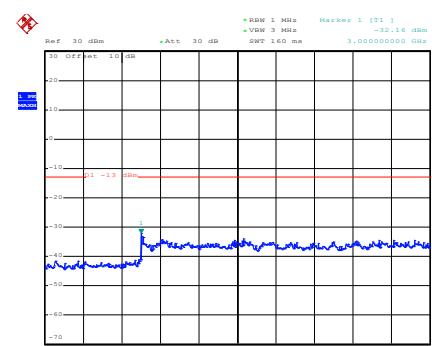
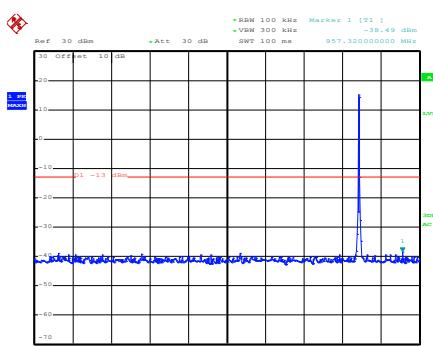
### Middle channel



30MHz~1GHz

1GHz~9GHz

### High channel

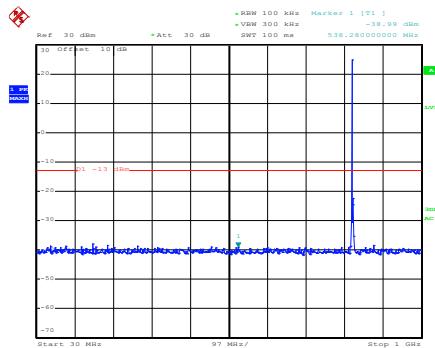


30MHz~1GHz

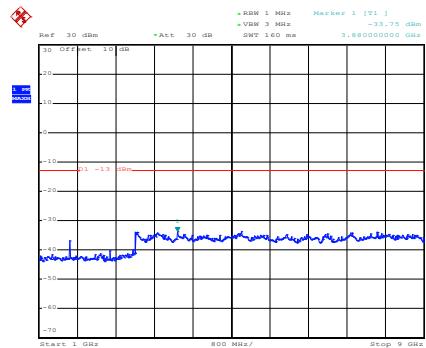
1GHz~9GHz

## QPSK & RB Size 1

### Lowest channel

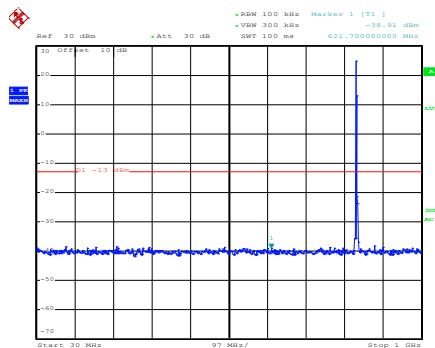


30MHz~1GHz

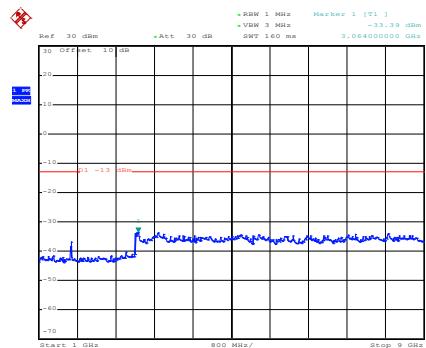


1GHz~9GHz

### Middle channel

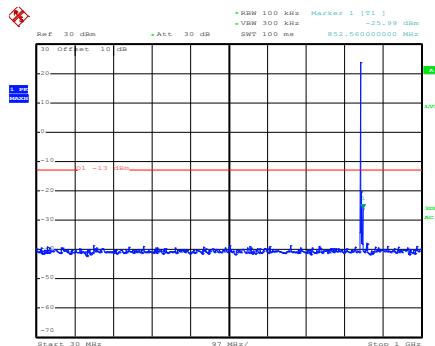


30MHz~1GHz

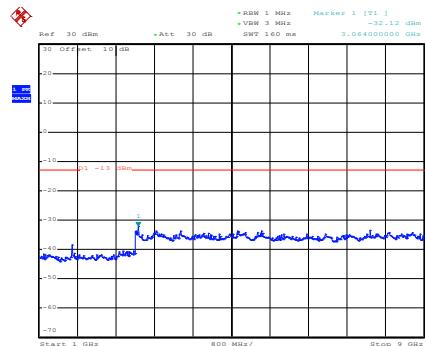


1GHz~9GHz

### High channel



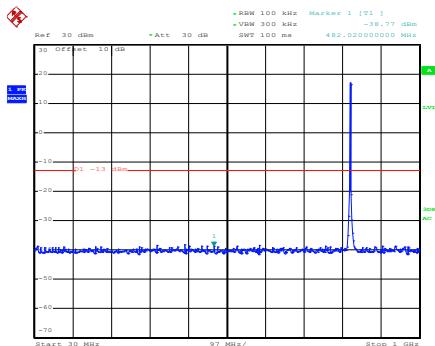
30MHz~1GHz



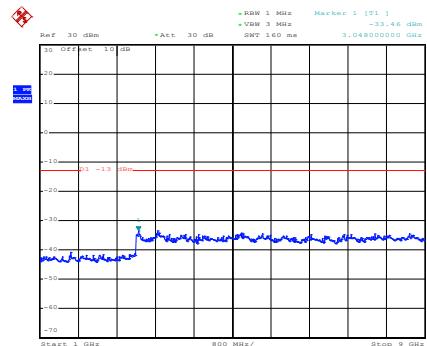
1GHz~9GHz

## QPSK & RB Size 15

### Lowest channel

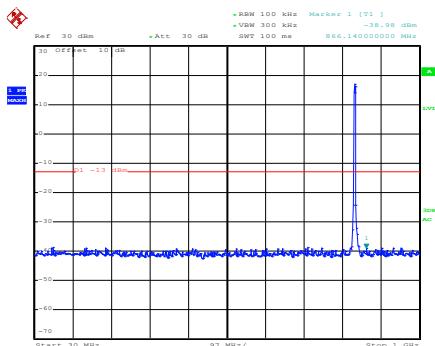


30MHz~1GHz

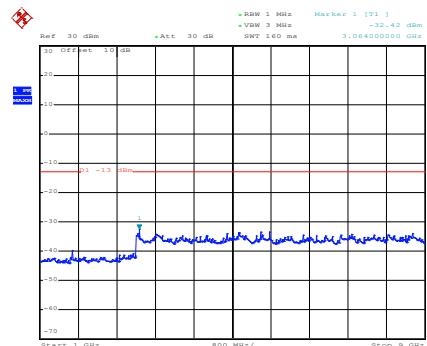


1GHz~9GHz

### Middle channel

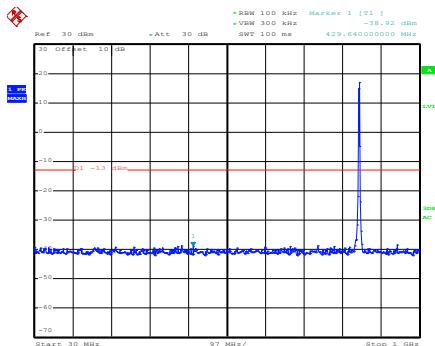


30MHz~1GHz

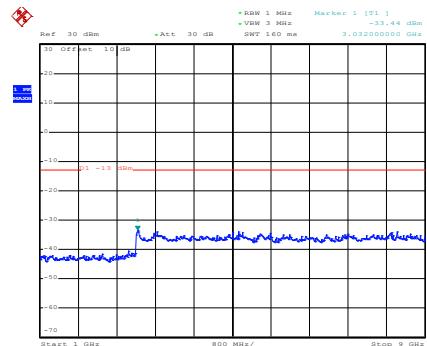


1GHz~9GHz

### High channel



30MHz~1GHz

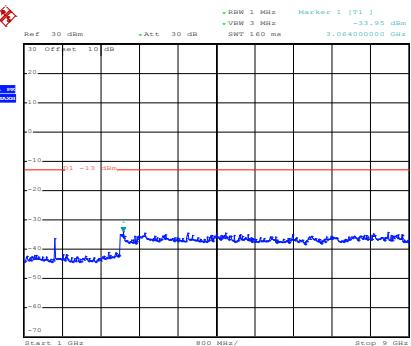
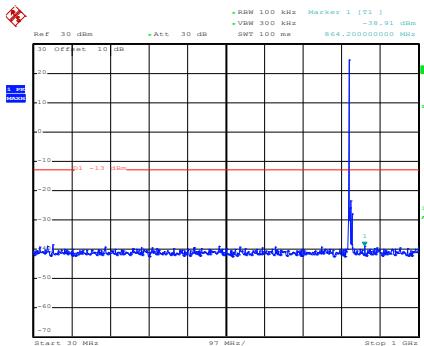


11GHz~9GHz

## 5MHz

### 16 QAM & RB Size 1

#### Lowest channel



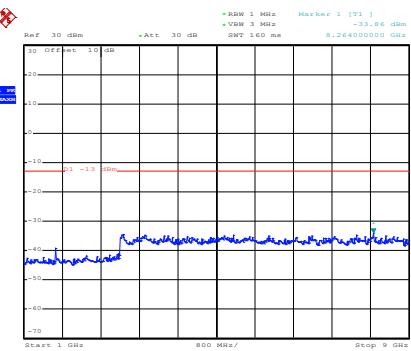
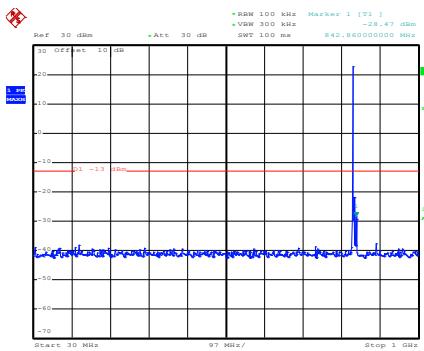
Date: 18.OCT.2017 10:08:30

Date: 20.OCT.2017 17:05:47

#### 30MHz~1GHz

#### 1GHz~9GHz

#### Middle channel



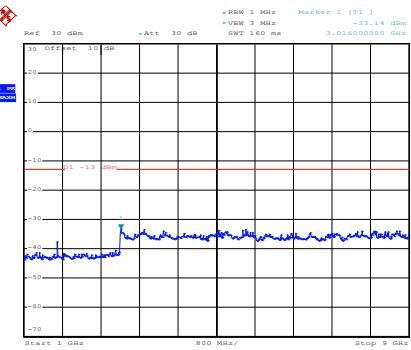
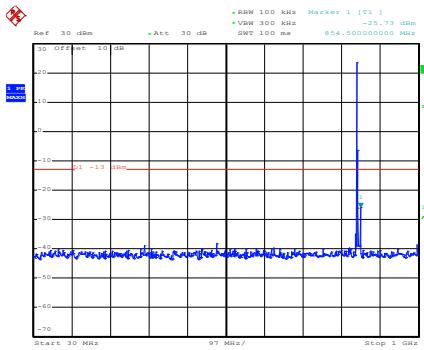
Date: 18.OCT.2017 10:09:47

Date: 20.OCT.2017 17:06:42

#### 30MHz~1GHz

#### 1GHz~9GHz

#### High channel



Date: 18.OCT.2017 10:11:04

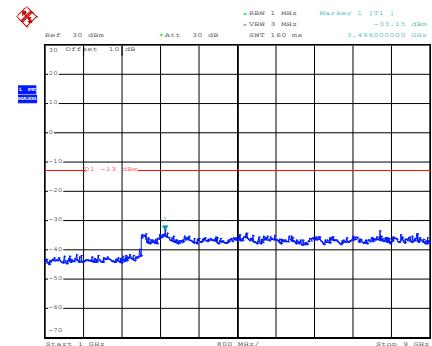
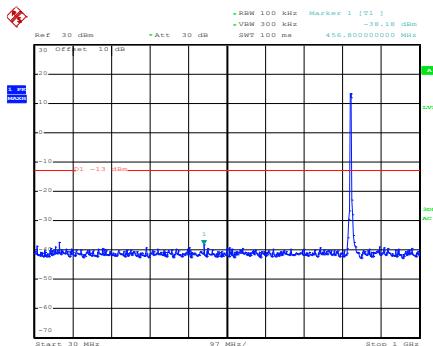
Date: 20.OCT.2017 17:07:37

#### 30MHz~1GHz

#### 1GHz~9GHz

## 16 QAM & RB Size 25

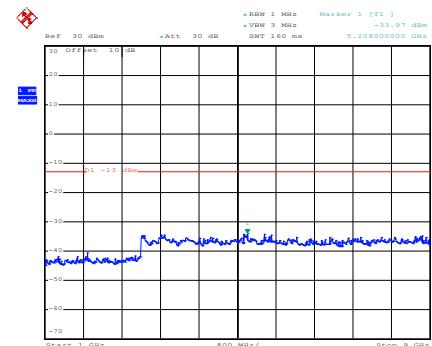
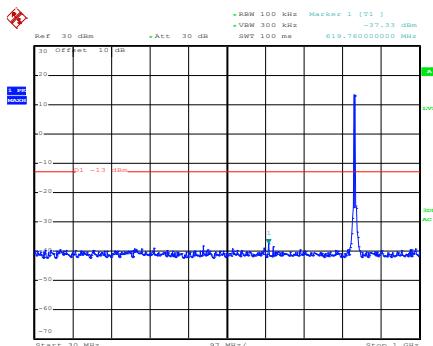
### Lowest channel



30MHz~1GHz

1GHz~9GHz

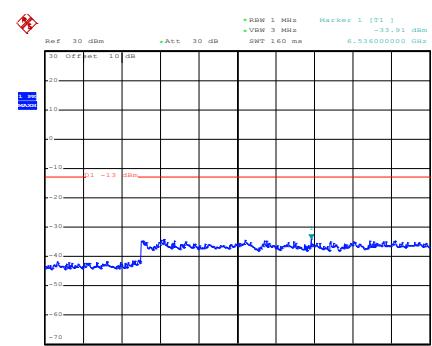
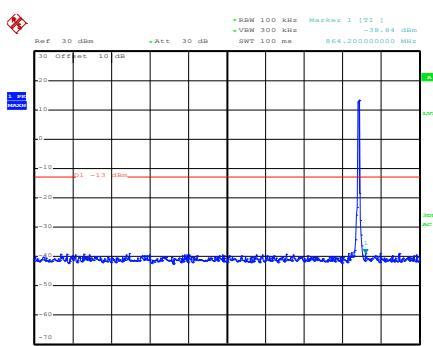
### Middle channel



30MHz~1GHz

1GHz~9GHz

### High channel

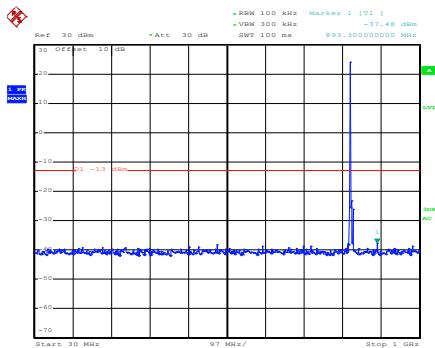


30MHz~1GHz

1GHz~9GHz

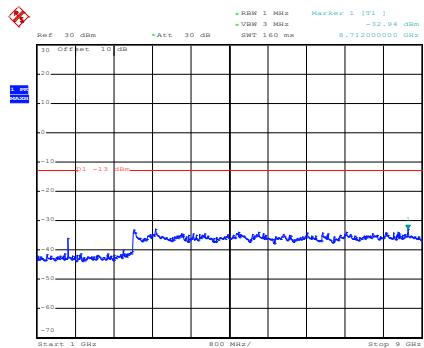
## QPSK & RB Size 1

### Lowest channel



Date: 18.OCT.2017 10:08:20

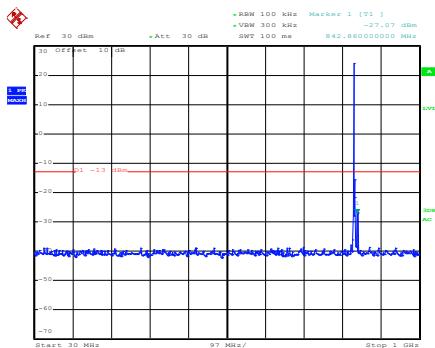
30MHz~1GHz



Date: 20.OCT.2017 17:05:41

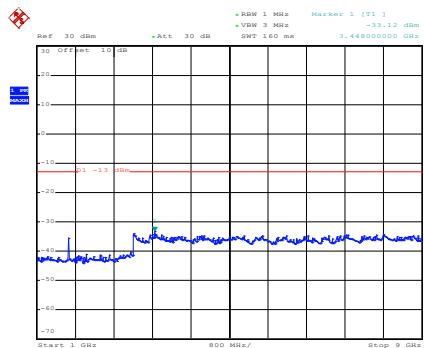
1GHz~9GHz

### Middle channel



Date: 18.OCT.2017 10:09:38

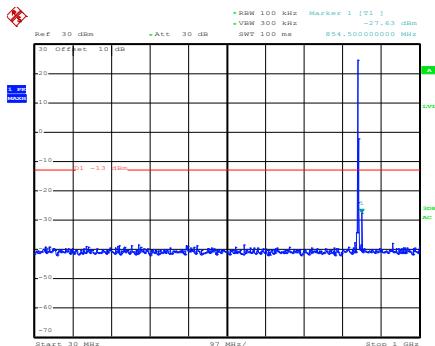
30MHz~1GHz



Date: 20.OCT.2017 17:06:36

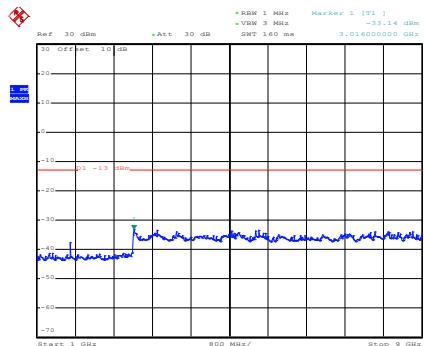
1GHz~9GHz

### High channel



Date: 18.OCT.2017 10:10:59

30MHz~1GHz

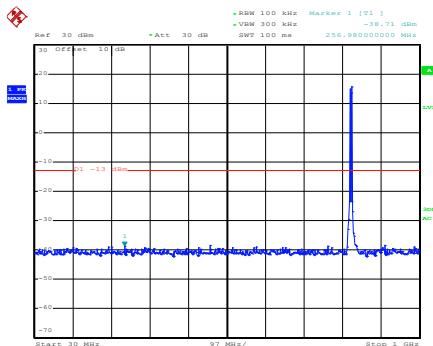


Date: 20.OCT.2017 17:07:32

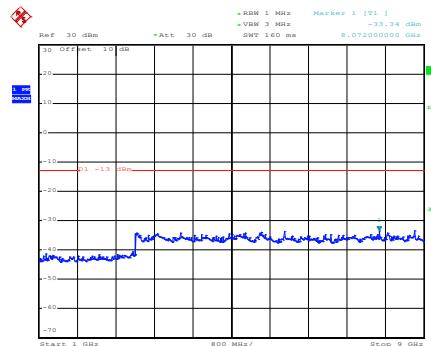
1GHz~9GHz

## QPSK & RB Size 25

### Lowest channel

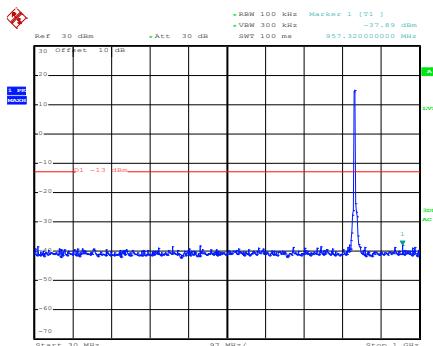


30MHz~1GHz

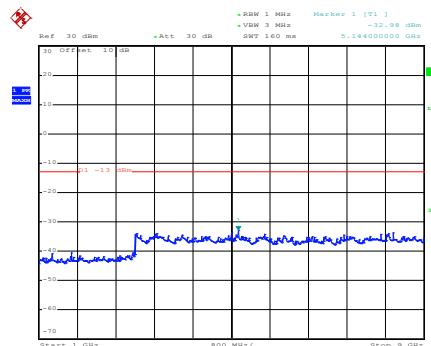


1GHz~9GHz

### Middle channel

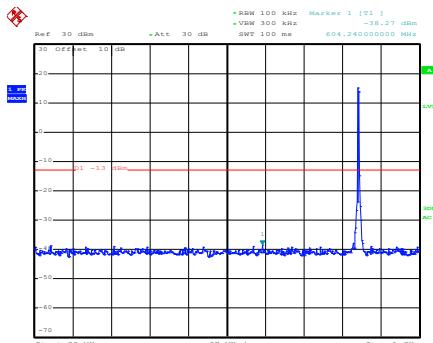


30MHz~1GHz

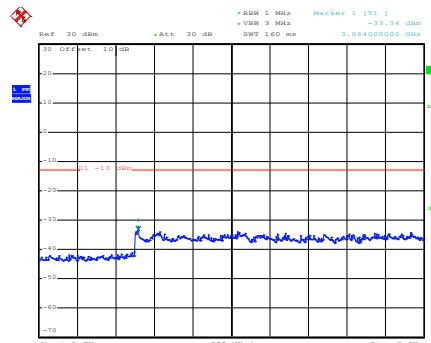


1GHz~9GHz

### High channel



30MHz~1GHz

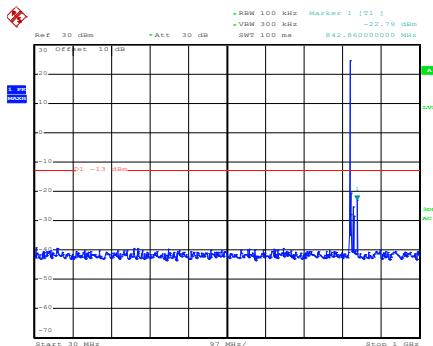


1GHz~9GHz

## 10MHz

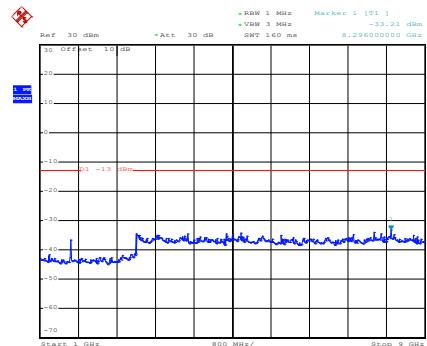
### 16 QAM & RB Size 1

#### Lowest channel



Date: 18.OCT.2017 10:13:13

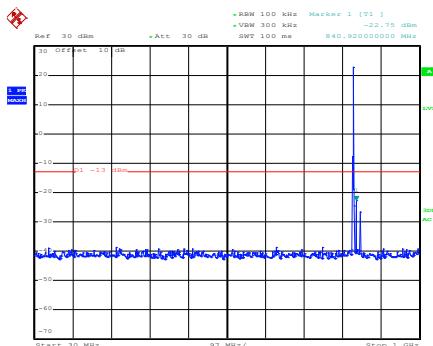
30MHz~1GHz



Date: 20.OCT.2017 17:08:51

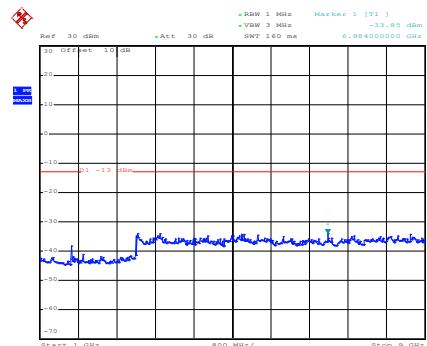
1GHz~9GHz

#### Middle channel



Date: 18.OCT.2017 10:14:29

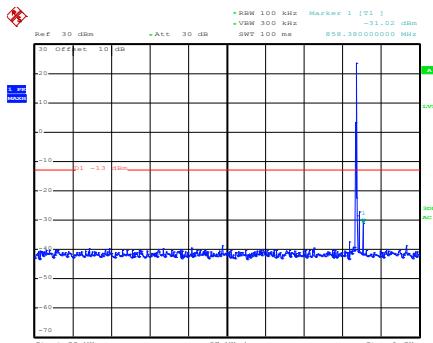
30MHz~1GHz



Date: 20.OCT.2017 17:09:49

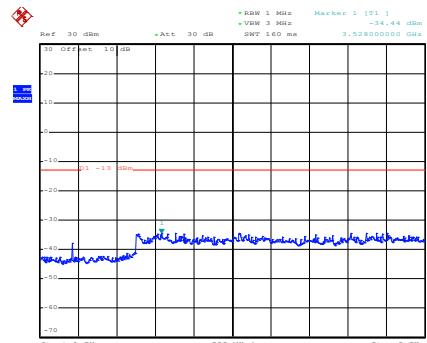
1GHz~9GHz

#### High channel



Date: 18.OCT.2017 10:19:10

30MHz~1GHz

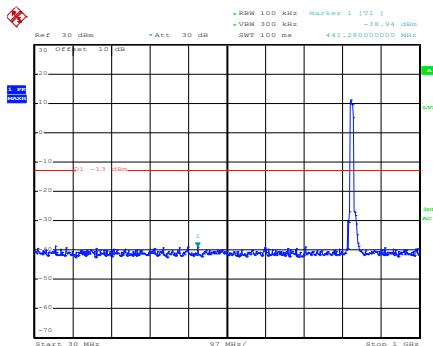


Date: 20.OCT.2017 17:10:44

1GHz~9GHz

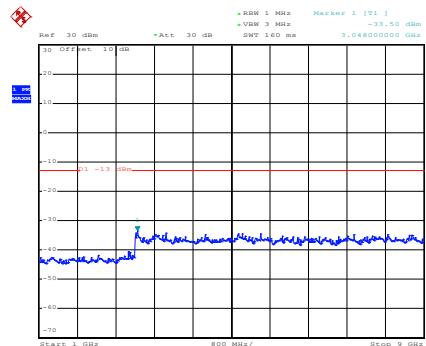
## 16 QAM & RB Size 50

### Lowest channel



Date: 18.OCT.2017 10:14:03

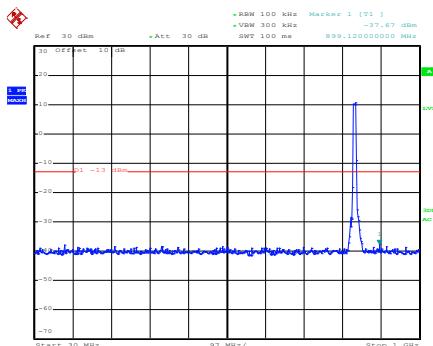
30MHz~1GHz



Date: 20.OCT.2017 17:09:25

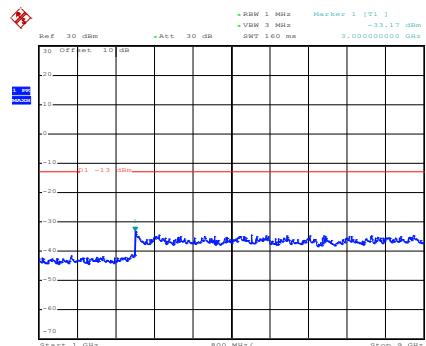
1GHz~9GHz

### Middle channel



Date: 18.OCT.2017 10:18:39

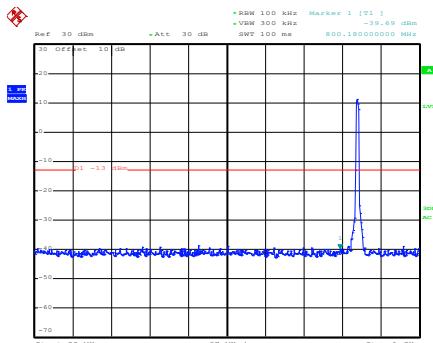
30MHz~1GHz



Date: 20.OCT.2017 17:10:21

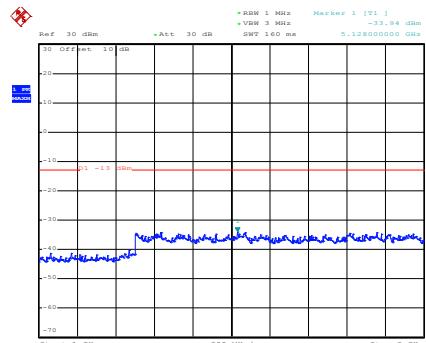
1GHz~9GHz

### High channel



Date: 18.OCT.2017 10:19:52

30MHz~1GHz



Date: 20.OCT.2017 17:11:17

1GHz~9GHz