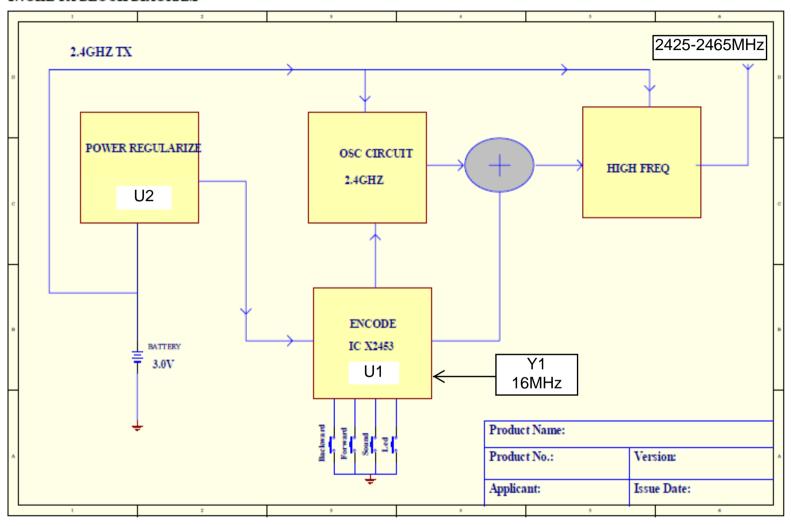
2.4GHZ TX BLOCK DIAGRAM



16MHz 4. System block diagram **RX** Memory RX **GFSK** Low noise RX FIFO Filter Demodulator Amplifie SPI Packet Processing & IRQ ANTP State Control TX CSN SCK ANTN MOS TX D/A **GFSK** TX FIFO **Amplifier** MISC Convertor Modulator PB6 XC1 IO Port Power Managment Oscillation Circuit XC2

Figure 1 X2453 System Block Diagram