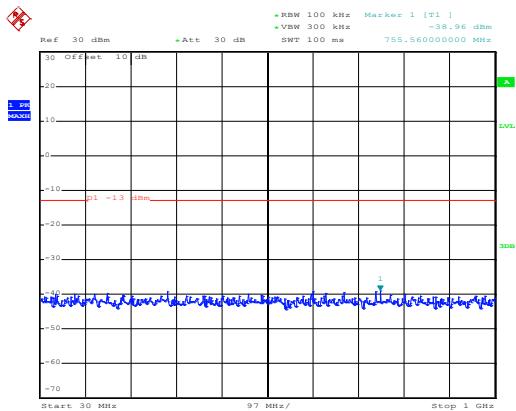
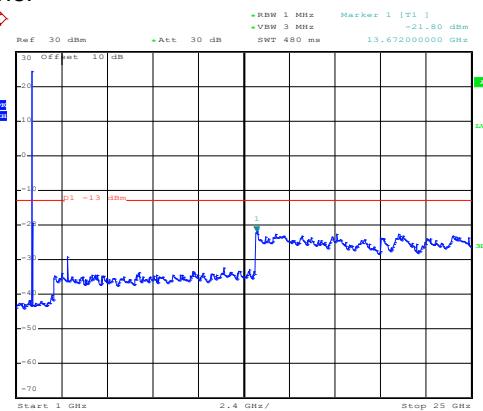


## 10MHz

### 16 QAM & RB Size 1 Lowest channel



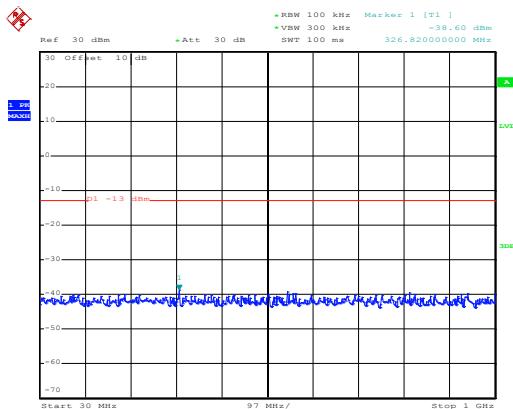
Date: 4.DEC.2017 14:43:35



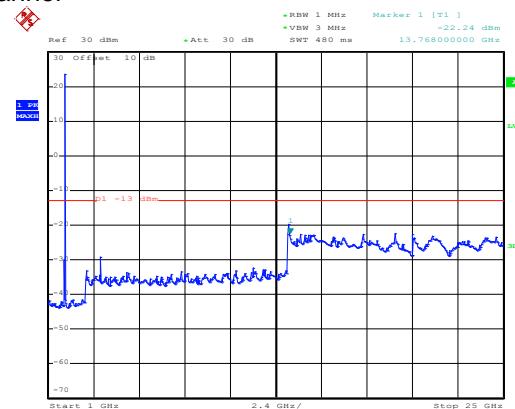
Date: 1.DEC.2017 15:20:49

### 30MHz~1GHz

### Middle channel



Date: 4.DEC.2017 14:43:58

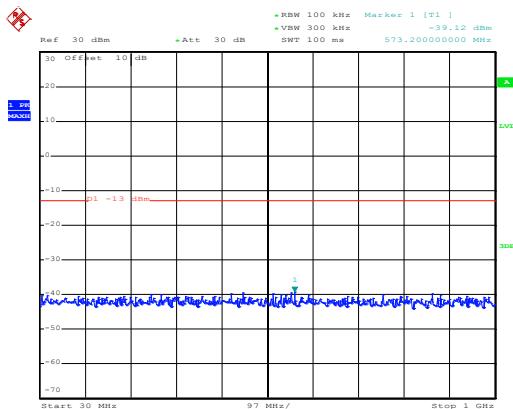


Date: 1.DEC.2017 15:21:34

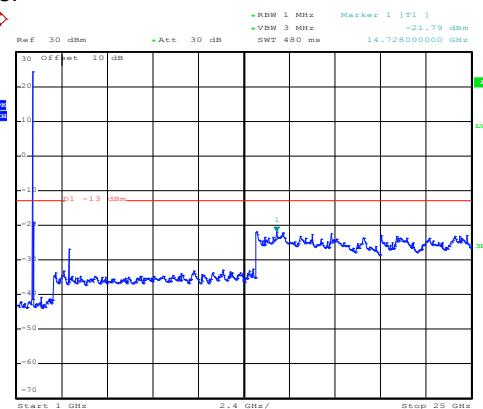
### 30MHz~1GHz

### 1GHz~25GHz

### High channel



Date: 4.DEC.2017 14:44:21

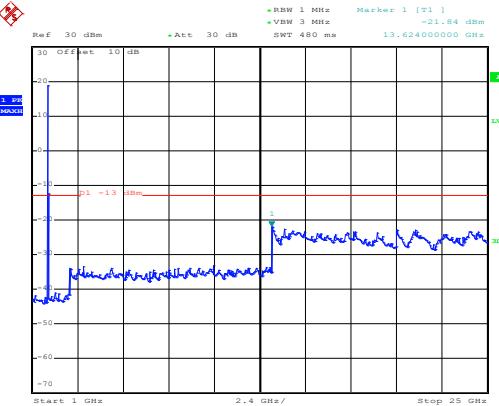
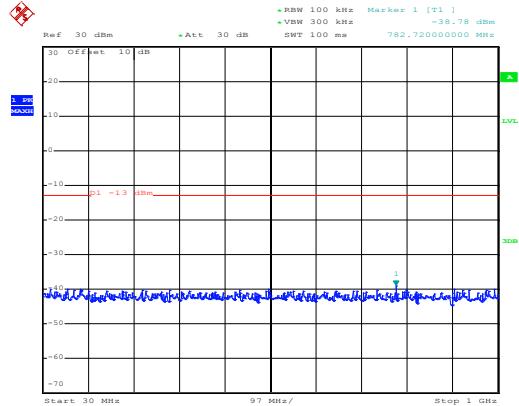


Date: 1.DEC.2017 15:22:19

### 30MHz~1GHz

### 1GHz~25GHz

## 16 QAM & RB Size 50 Lowest channel



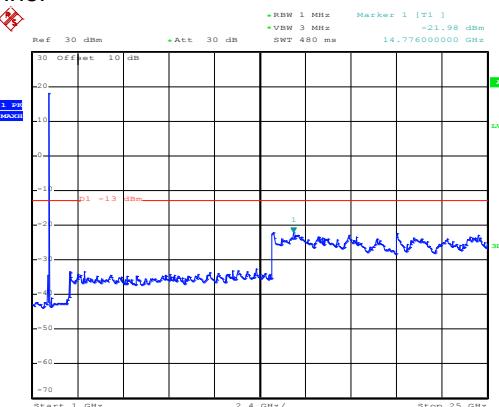
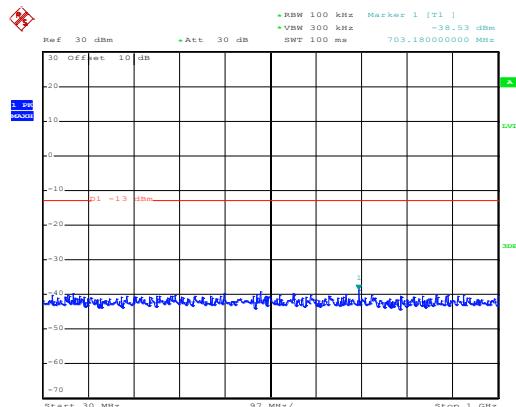
Date: 4.DEC.2017 14:43:46

Date: 1.DEC.2017 15:21:08

## 30MHz~1GHz

## 1GHz~25GHz

## Middle channel



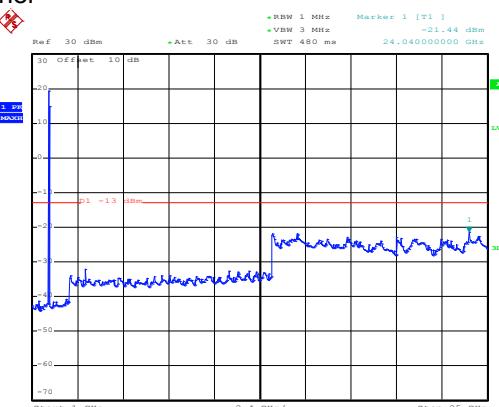
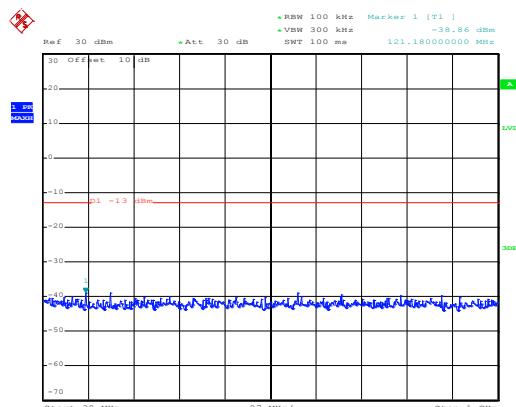
Date: 4.DEC.2017 14:44:09

Date: 1.DEC.2017 15:21:53

## 30MHz~1GHz

## 1GHz~25GHz

## High channel



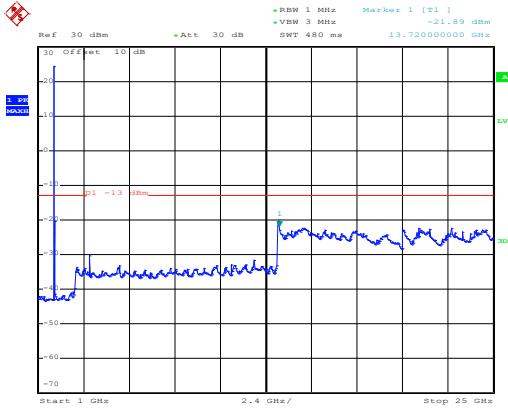
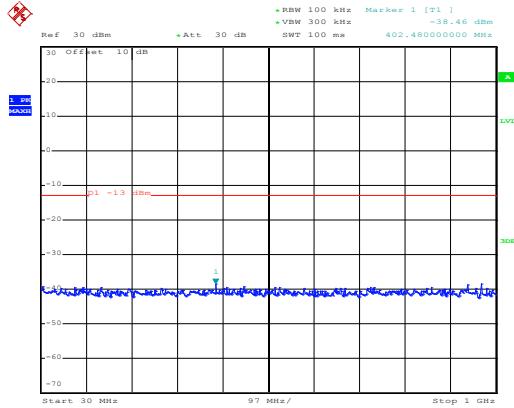
Date: 4.DEC.2017 14:44:42

Date: 1.DEC.2017 15:22:42

## 30MHz~1GHz

## 1GHz~25GHz

**QPSK & RB Size 1  
Lowest channel**



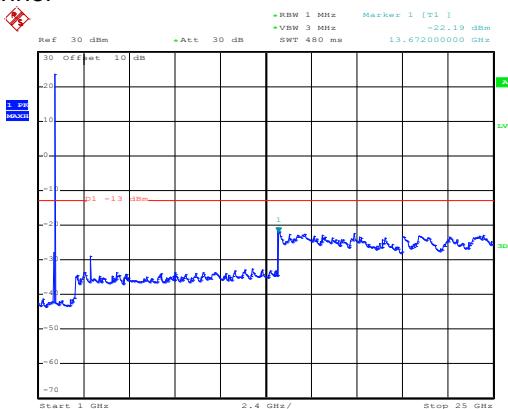
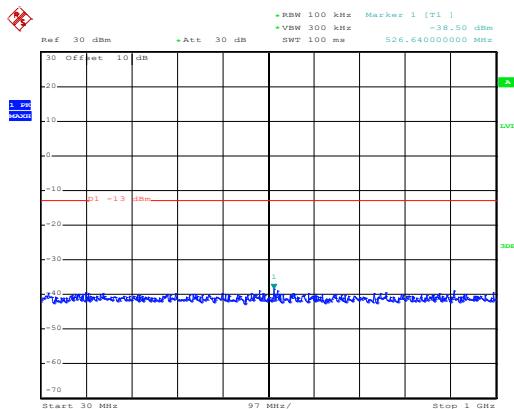
Date: 4.DEC.2017 14:43:32

**30MHz~1GHz**

Date: 1.DEC.2017 15:20:40

**1GHz~25GHz**

**Middle channel**



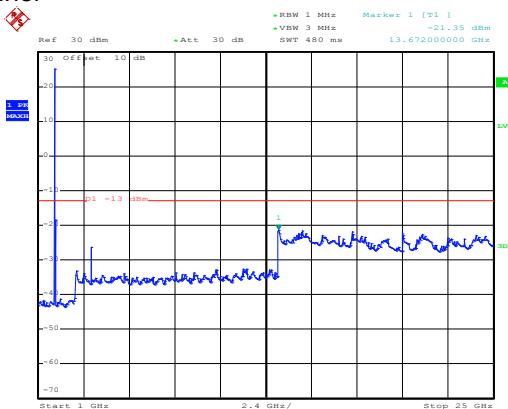
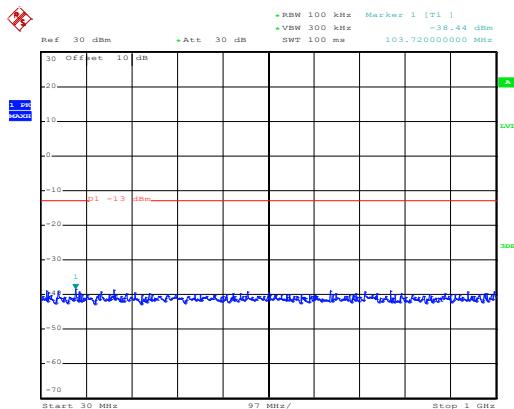
Date: 4.DEC.2017 14:43:54

**30MHz~1GHz**

Date: 1.DEC.2017 15:21:27

**1GHz~25GHz**

**High channel**



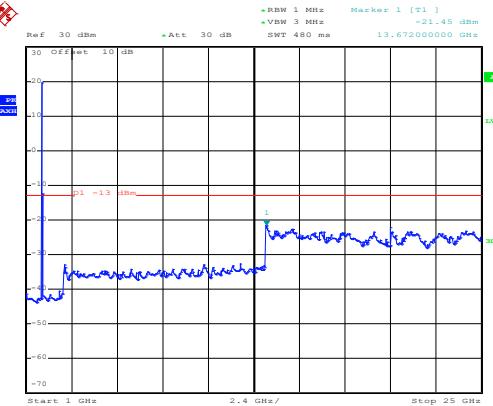
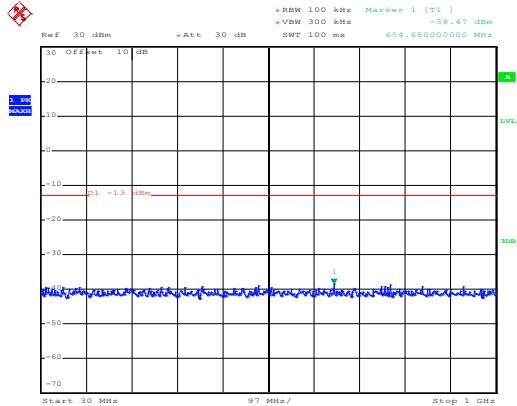
Date: 4.DEC.2017 14:44:17

**30MHz~1GHz**

Date: 1.DEC.2017 15:22:09

**1GHz~25GHz**

**QPSK & RB Size 50  
Lowest channel**



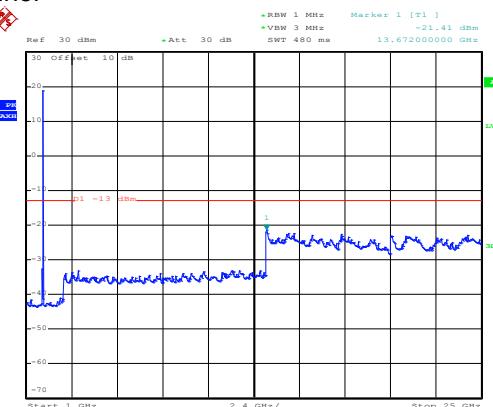
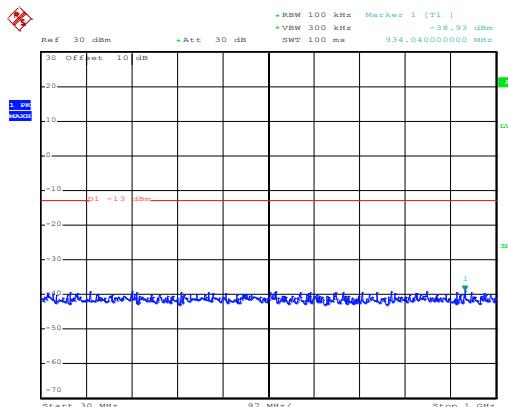
Date: 4.DEC.2017 14:43:43

**30MHz~1GHz**

Date: 1.DEC.2017 15:21:02

**1GHz~25GHz**

**Middle channel**



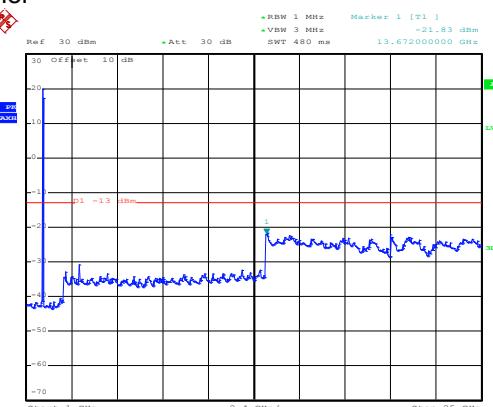
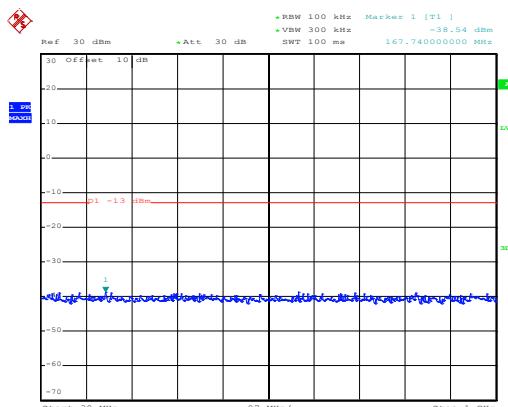
Date: 4.DEC.2017 14:44:04

**30MHz~1GHz**

Date: 1.DEC.2017 15:21:44

**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 14:44:38

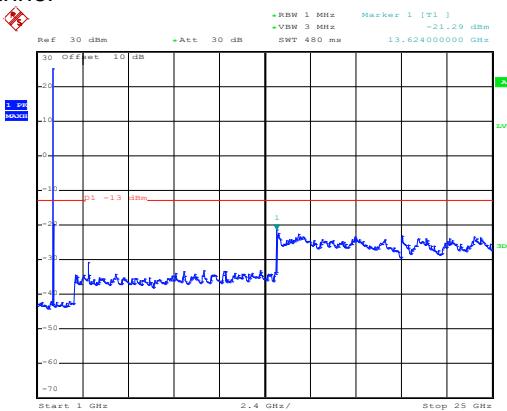
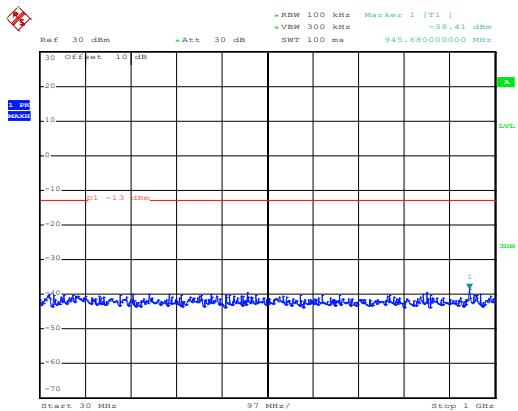
**30MHz~1GHz**

Date: 1.DEC.2017 15:22:32

**1GHz~25GHz**

## 15MHz

### 16 QAM & RB Size 1 Lowest channel

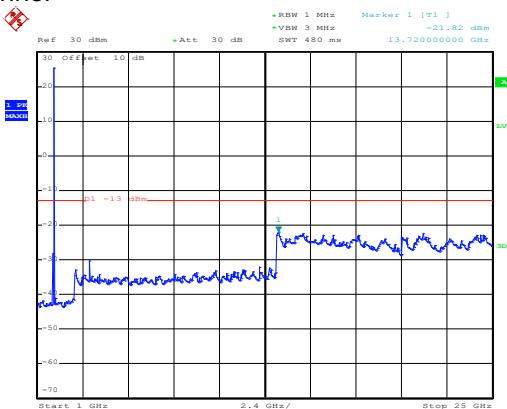
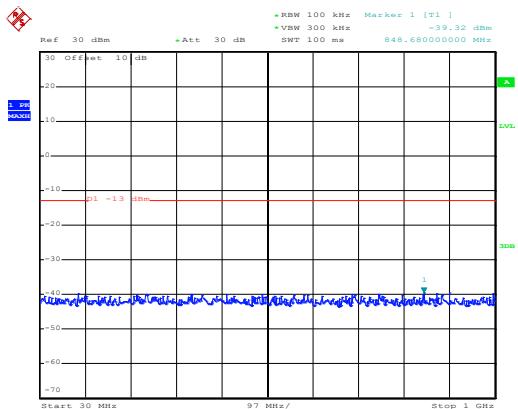


Date: 4.DEC.2017 14:44:56

Date: 1.DEC.2017 15:28:16

### 30MHz~1GHz

### Middle channel



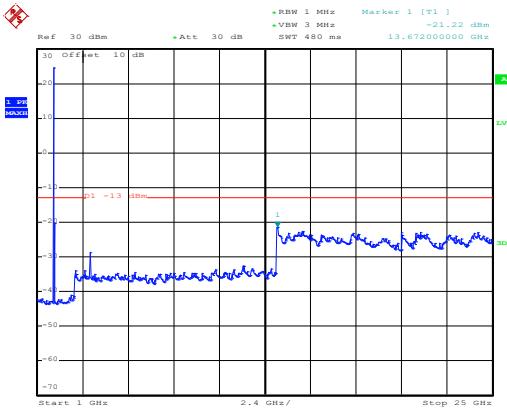
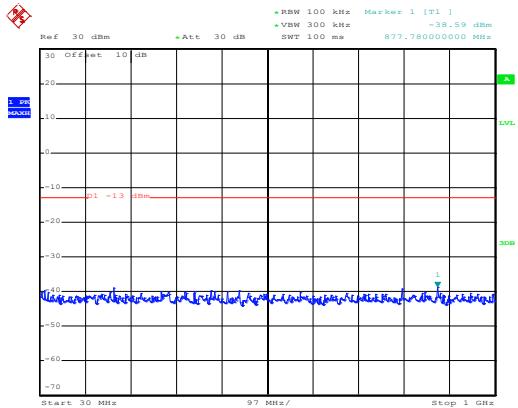
Date: 4.DEC.2017 14:45:19

Date: 1.DEC.2017 15:29:01

### 30MHz~1GHz

### 1GHz~25GHz

### High channel



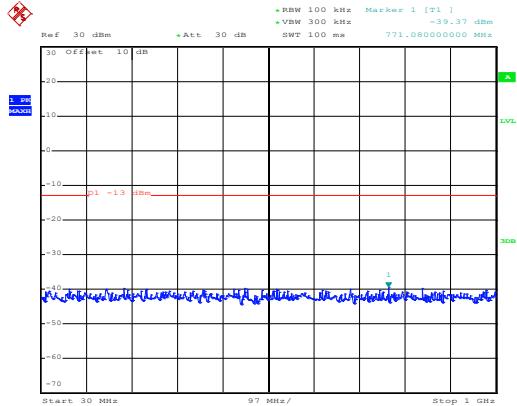
Date: 4.DEC.2017 14:46:54

Date: 1.DEC.2017 15:29:44

### 30MHz~1GHz

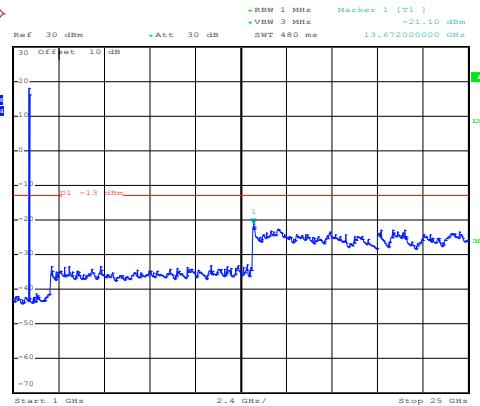
### 1GHz~25GHz

**16 QAM & RB Size 75  
Lowest channel**



Date: 4.DEC.2017 14:45:07

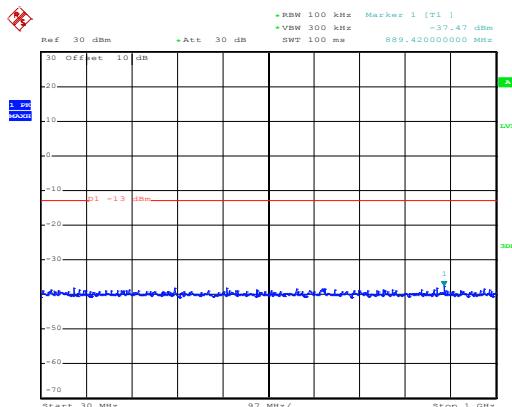
**30MHz~1GHz**



Date: 1.DEC.2017 15:28:33

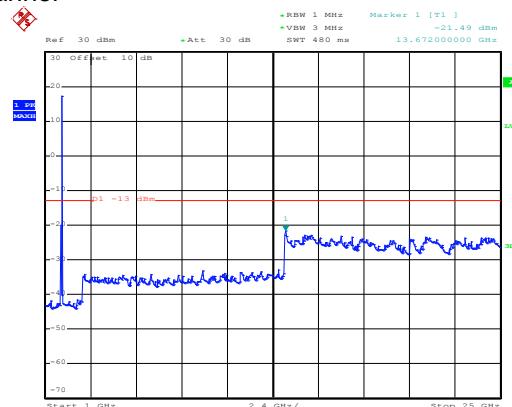
**1GHz~25GHz**

**Middle channel**



Date: 4.DEC.2017 14:46:38

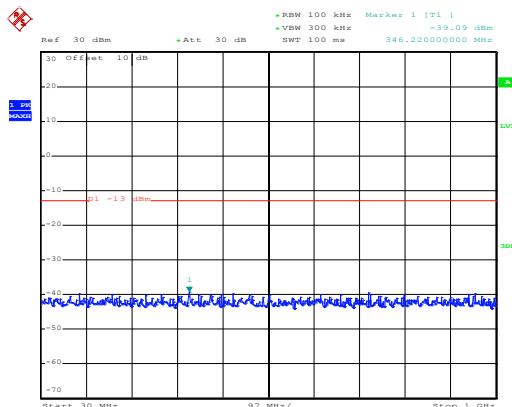
**30MHz~1GHz**



Date: 1.DEC.2017 15:29:17

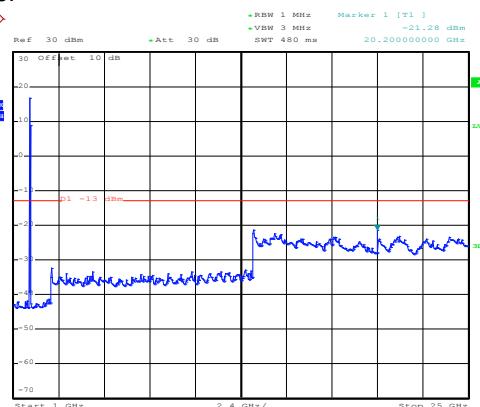
**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 14:47:05

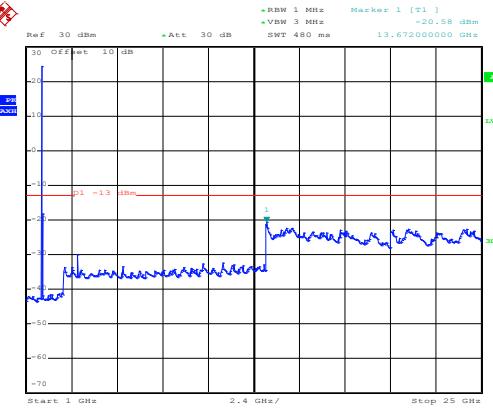
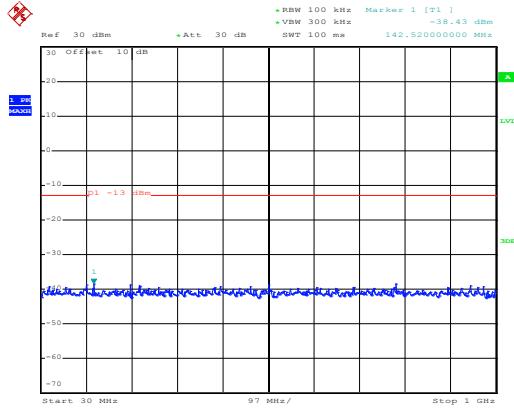
**30MHz~1GHz**



Date: 1.DEC.2017 15:30:04

**1GHz~25GHz**

**QPSK & RB Size 1  
Lowest channel**



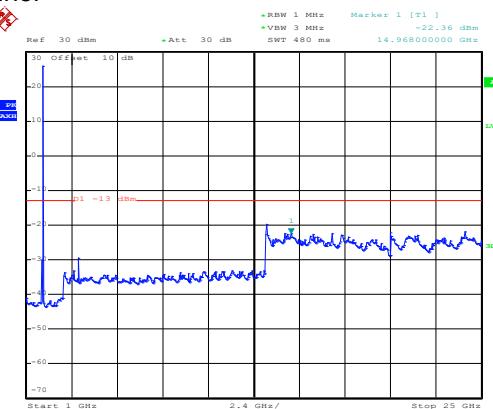
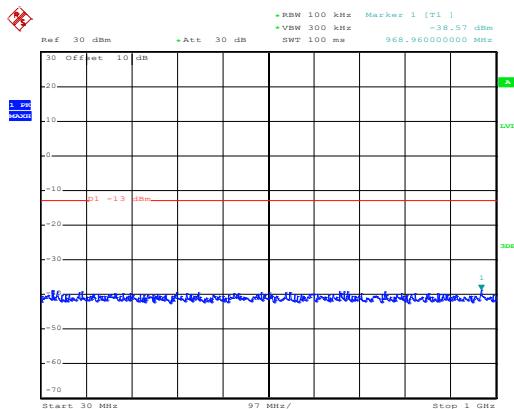
Date: 4.DEC.2017 14:44:52

**30MHz~1GHz**

Date: 1.DEC.2017 15:28:09

**1GHz~25GHz**

**Middle channel**



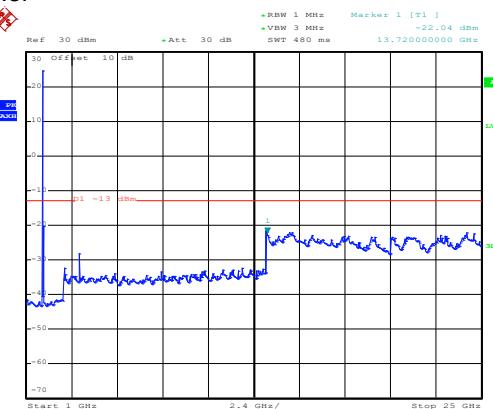
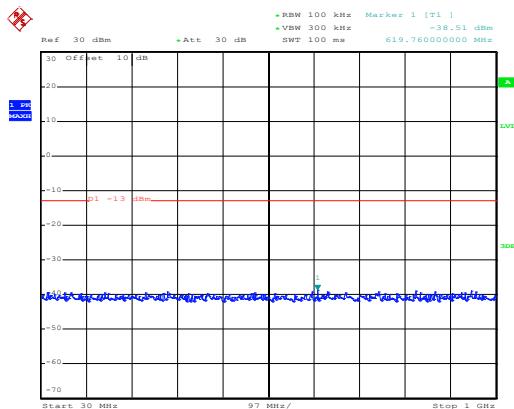
Date: 4.DEC.2017 14:45:15

**30MHz~1GHz**

Date: 1.DEC.2017 15:28:51

**1GHz~25GHz**

**High channel**



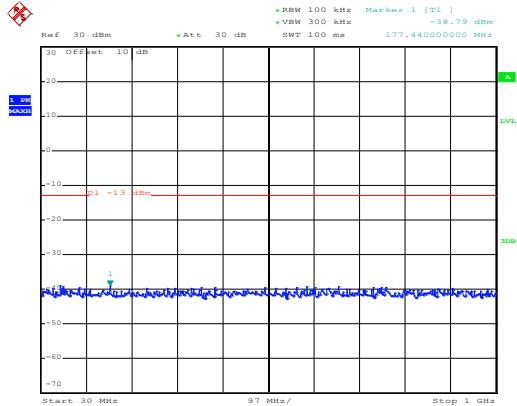
Date: 4.DEC.2017 14:46:50

**30MHz~1GHz**

Date: 1.DEC.2017 15:29:36

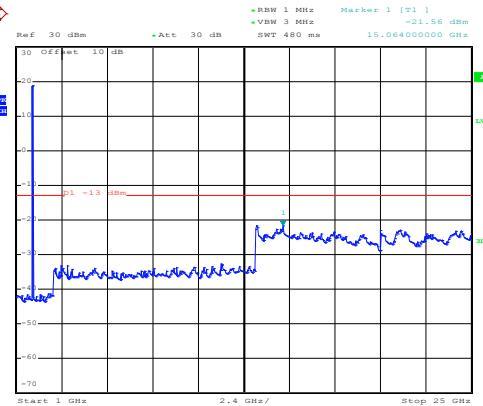
**1GHz~25GHz**

**QPSK & RB Size 75  
Lowest channel**



Date: 4.DEC.2017 14:45:03

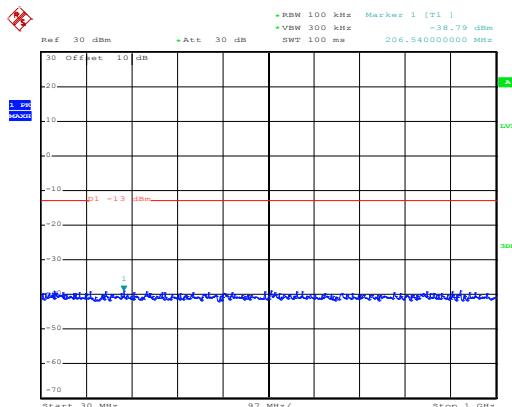
**30MHz~1GHz**



Date: 1.DEC.2017 15:28:26

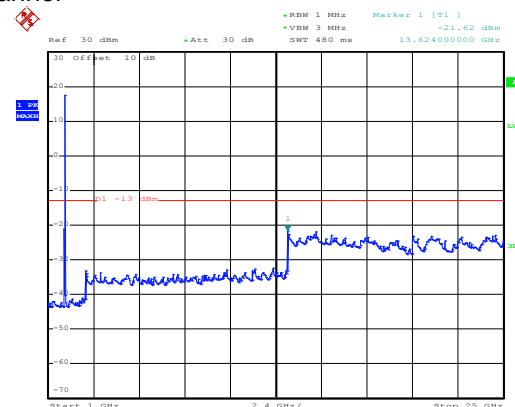
**1GHz~25GHz**

**Middle channel**



Date: 4.DEC.2017 14:45:35

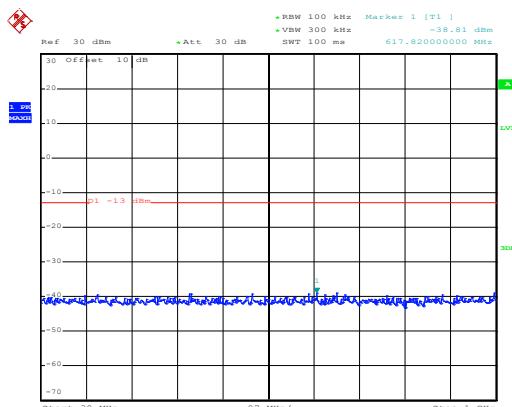
**30MHz~1GHz**



Date: 1.DEC.2017 15:29:11

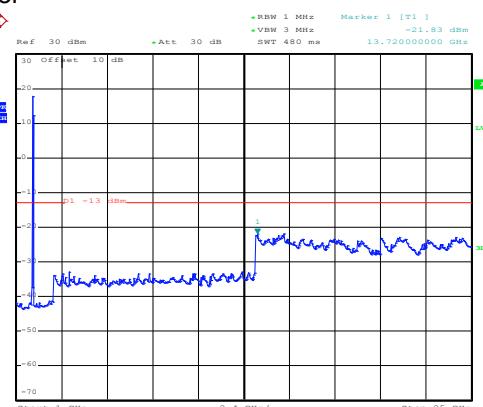
**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 14:47:01

**30MHz~1GHz**

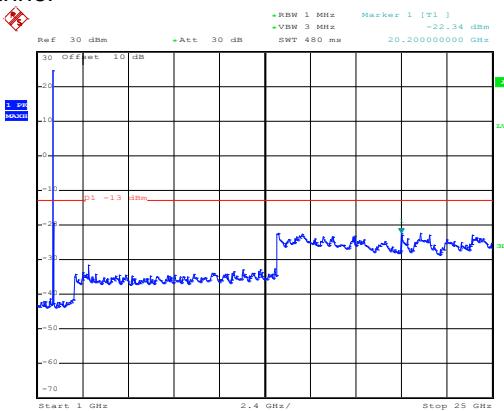
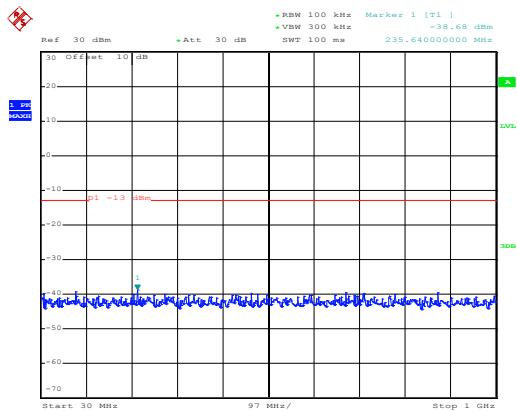


Date: 1.DEC.2017 15:29:58

**1GHz~25GHz**

## 20MHz

### 16 QAM & RB Size 1 Lowest channel



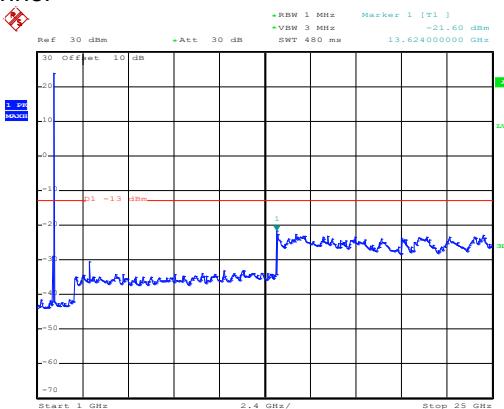
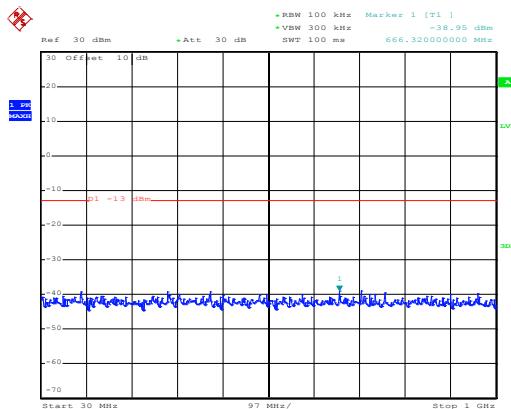
Date: 4.DEC.2017 14:47:18

Date: 1.DEC.2017 15:30:58

### 30MHz~1GHz

### 1GHz~25GHz

### Middle channel



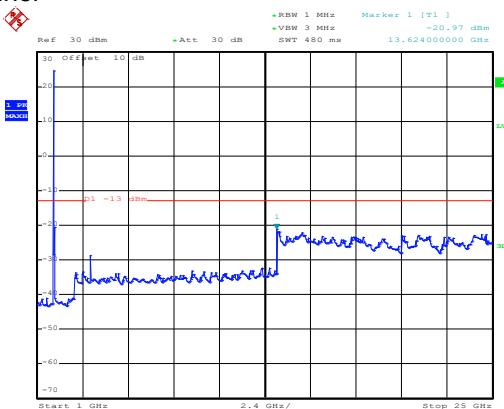
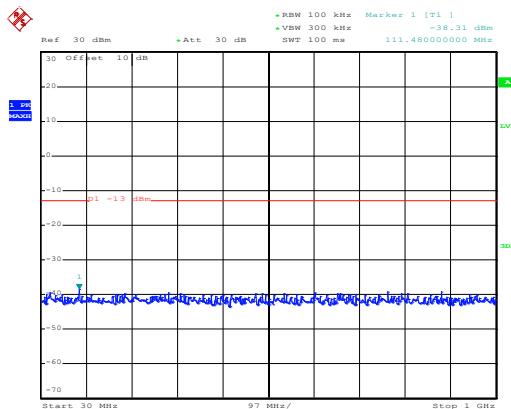
Date: 4.DEC.2017 14:47:41

Date: 1.DEC.2017 15:31:42

### 30MHz~1GHz

### 1GHz~25GHz

### High channel



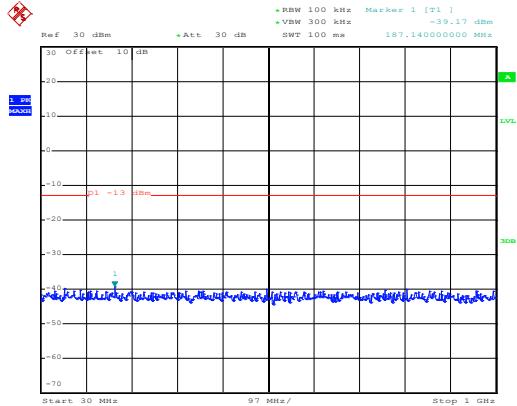
Date: 4.DEC.2017 14:48:05

Date: 1.DEC.2017 15:32:40

### 30MHz~1GHz

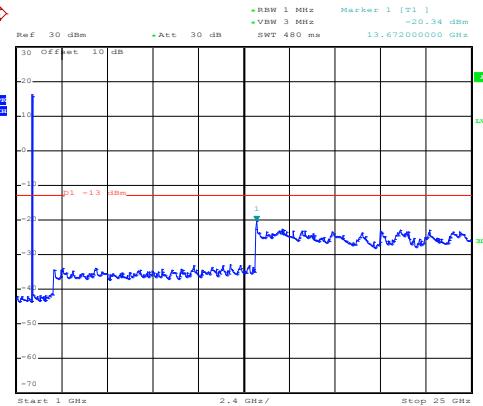
### 1GHz~25GHz

## 16 QAM & RB Size 100 Lowest channel



Date: 4.DEC.2017 14:47:29

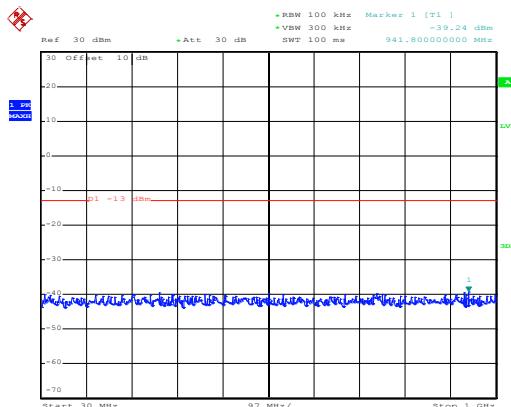
30MHz~1GHz



Date: 1.DEC.2017 15:31:17

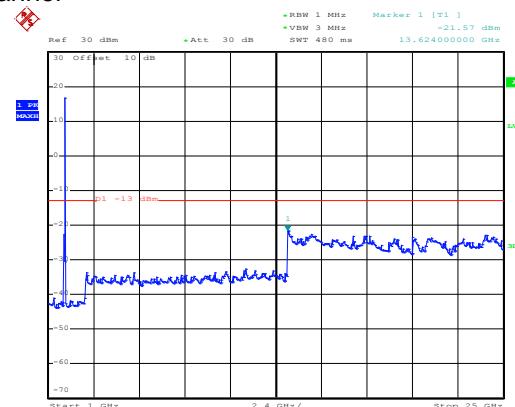
1GHz~25GHz

## Middle channel



Date: 4.DEC.2017 14:47:52

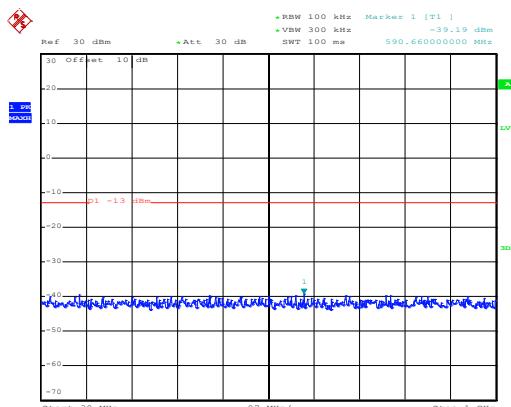
30MHz~1GHz



Date: 1.DEC.2017 15:32:10

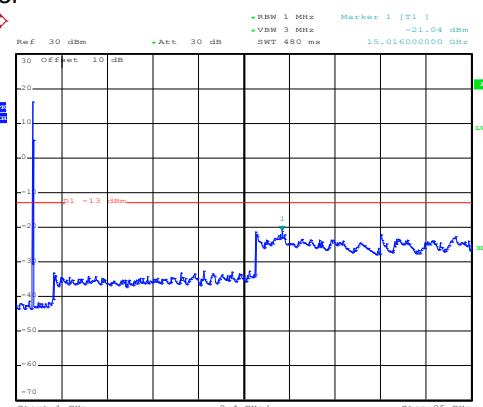
1GHz~25GHz

## High channel



Date: 4.DEC.2017 14:48:15

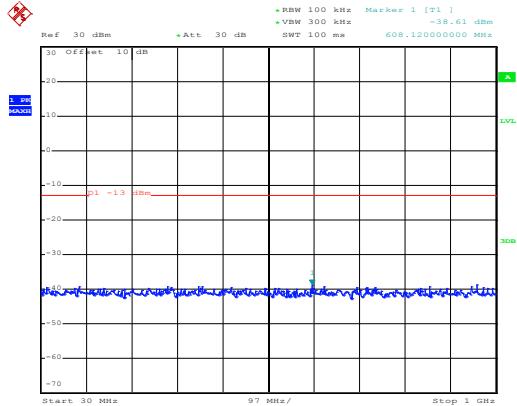
30MHz~1GHz



Date: 1.DEC.2017 15:33:03

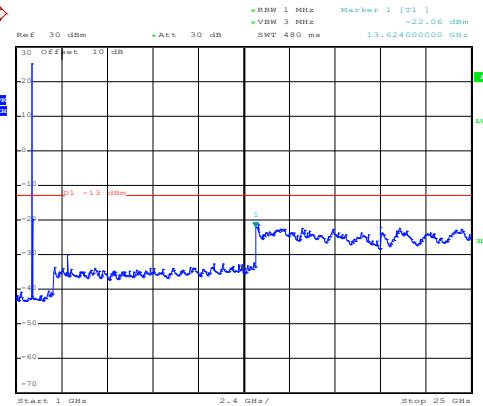
1GHz~25GHz

**QPSK & RB Size 1  
Lowest channel**



Date: 4.DEC.2017 14:47:15

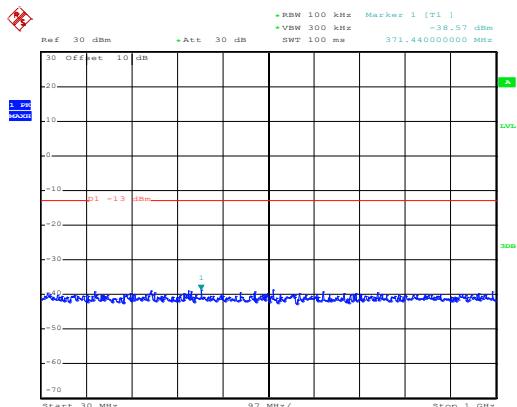
**30MHz~1GHz**



Date: 1.DEC.2017 15:30:43

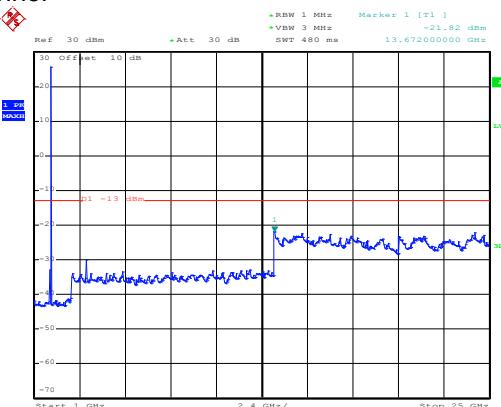
**1GHz~25GHz**

**Middle channel**



Date: 4.DEC.2017 14:47:38

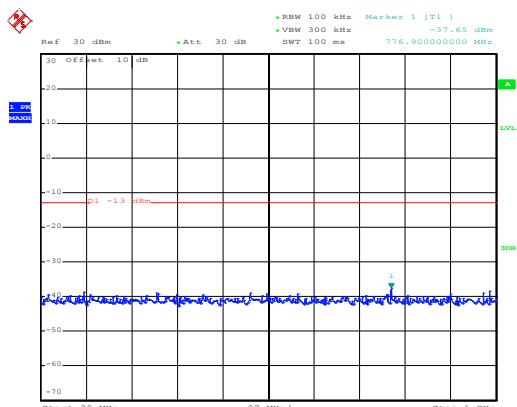
**30MHz~1GHz**



Date: 1.DEC.2017 15:31:33

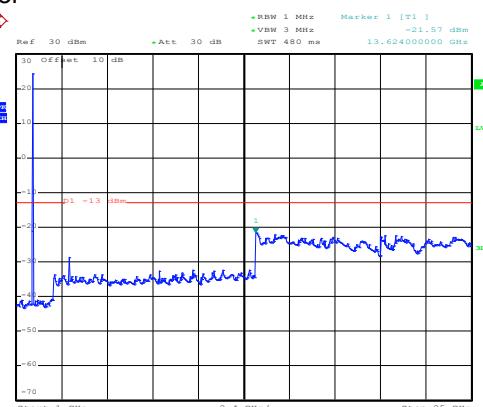
**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 14:48:01

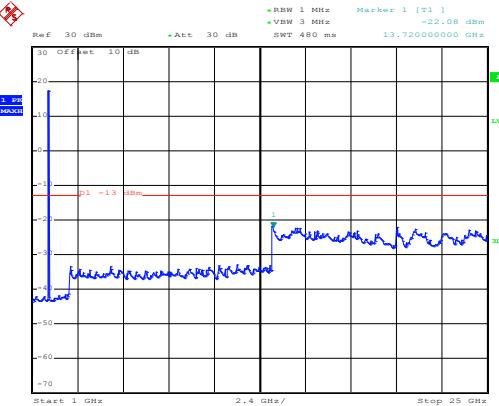
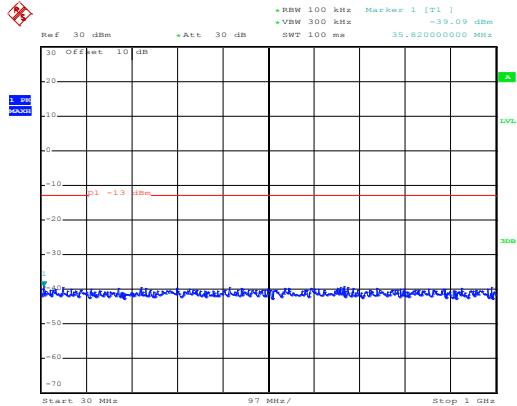
**30MHz~1GHz**



Date: 1.DEC.2017 15:32:26

**1GHz~25GHz**

**QPSK & RB Size 100  
Lowest channel**



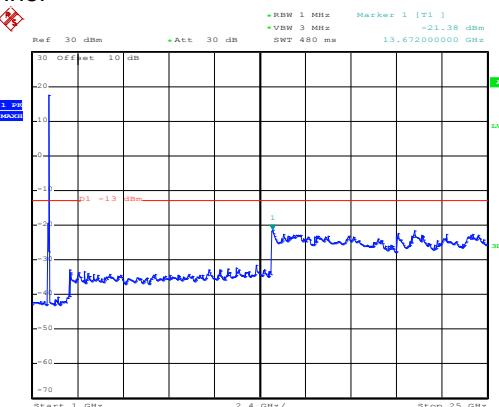
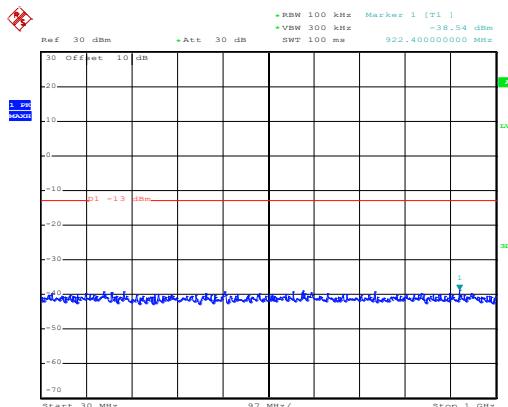
Date: 4.DEC.2017 14:47:25

**30MHz~1GHz**

Date: 1.DEC.2017 15:31:08

**1GHz~25GHz**

**Middle channel**



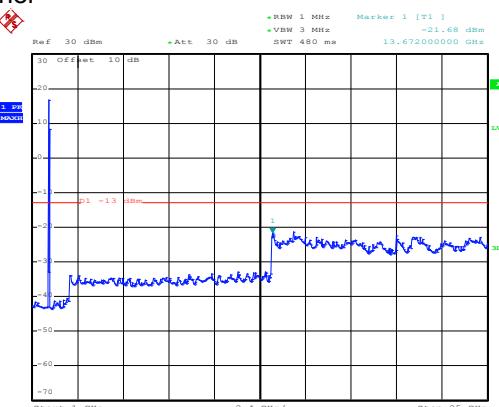
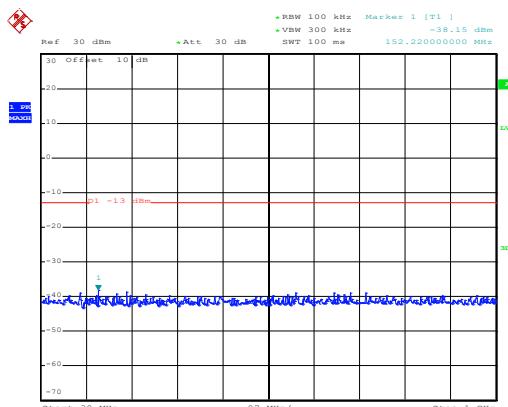
Date: 4.DEC.2017 14:47:48

**30MHz~1GHz**

Date: 1.DEC.2017 15:32:02

**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 14:48:11

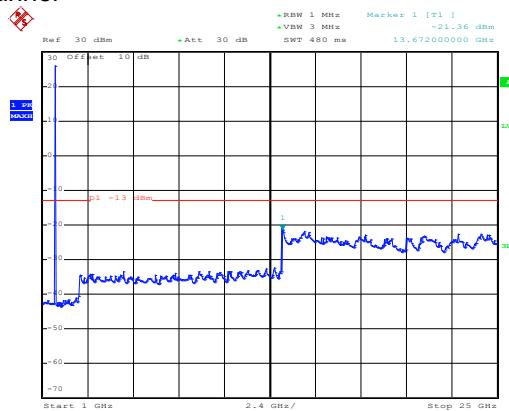
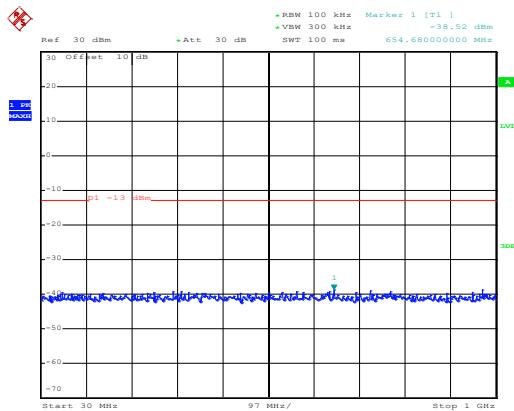
**30MHz~1GHz**

Date: 1.DEC.2017 15:32:52

**1GHz~25GHz**

## LTE band 4, 1.4MHz

### 16 QAM & RB Size 1 Lowest channel

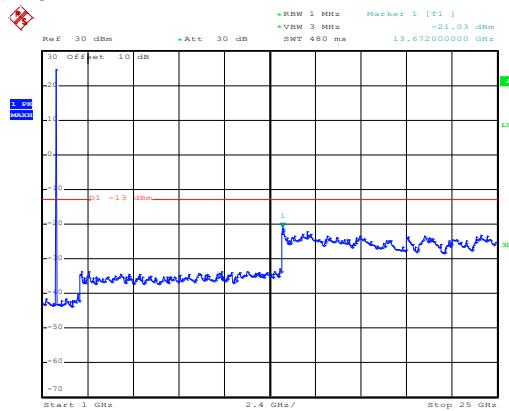
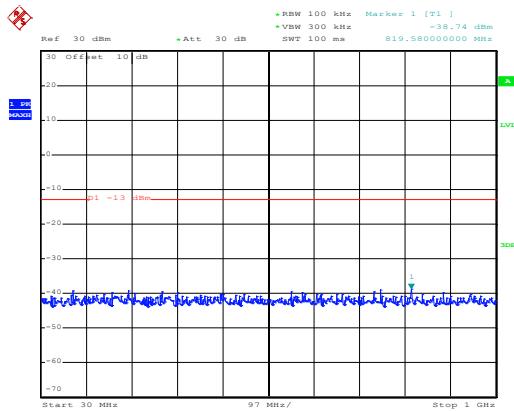


Date: 4.DEC.2017 15:00:25

Date: 1.DEC.2017 15:37:59

### 30MHz~1GHz

#### Middle channel

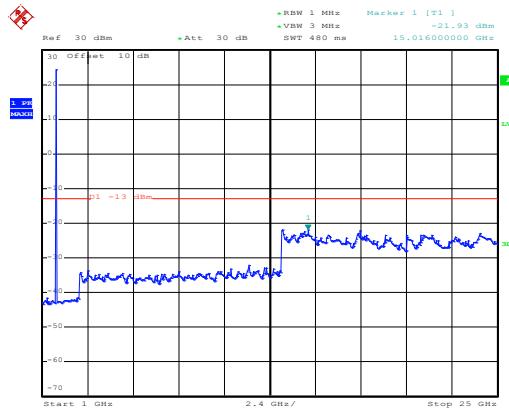
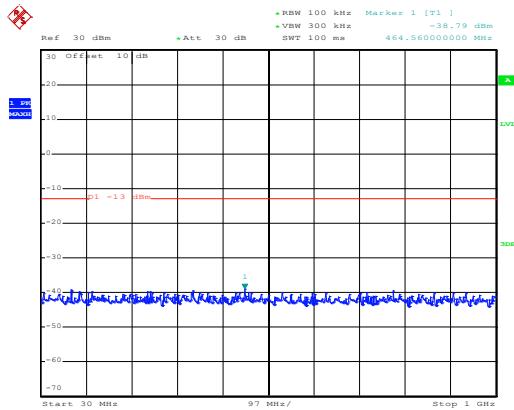


Date: 4.DEC.2017 15:00:58

Date: 1.DEC.2017 15:38:48

### 30MHz~1GHz

#### High channel



Date: 4.DEC.2017 15:01:21

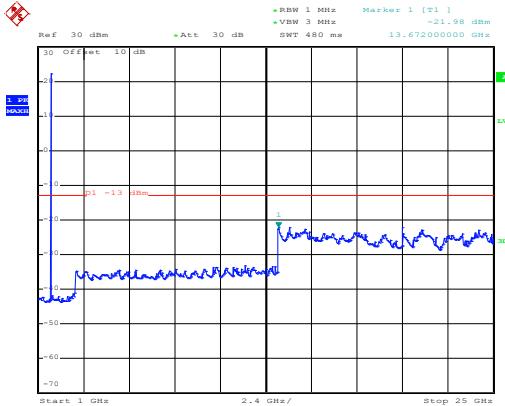
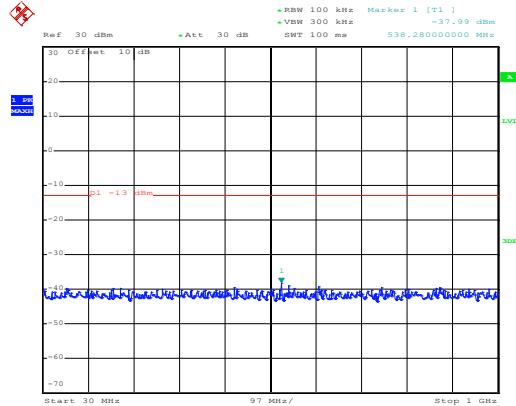
Date: 1.DEC.2017 15:39:42

### 30MHz~1GHz

### 1GHz~25GHz

### 1GHz~25GHz

## 16 QAM & RB Size 6 Lowest channel



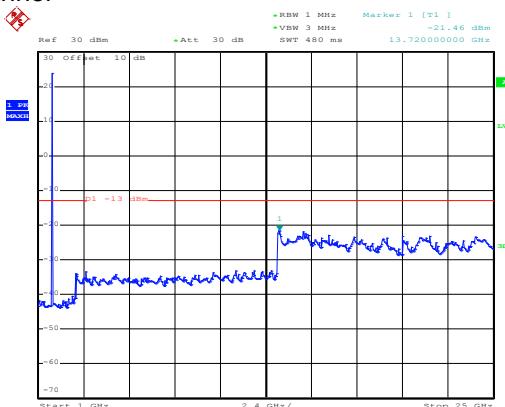
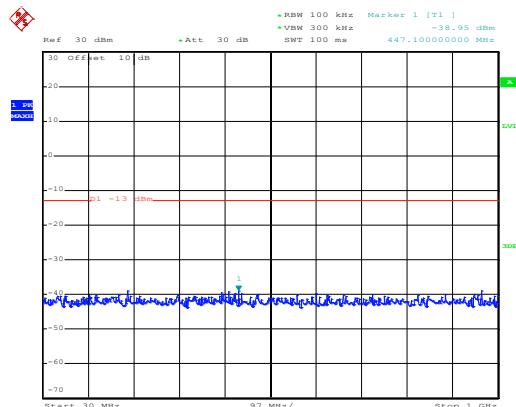
Date: 4.DEC.2017 15:00:40

### 30MHz~1GHz

Date: 1.DEC.2017 15:38:16

### 1GHz~25GHz

## Middle channel



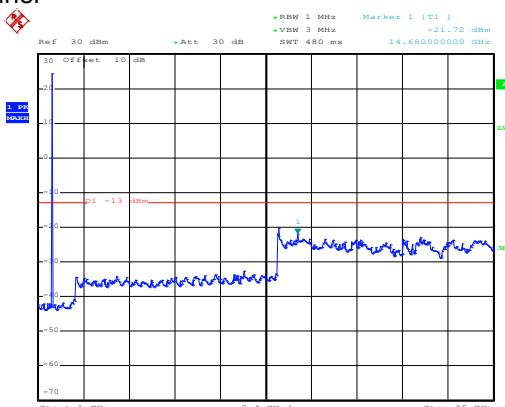
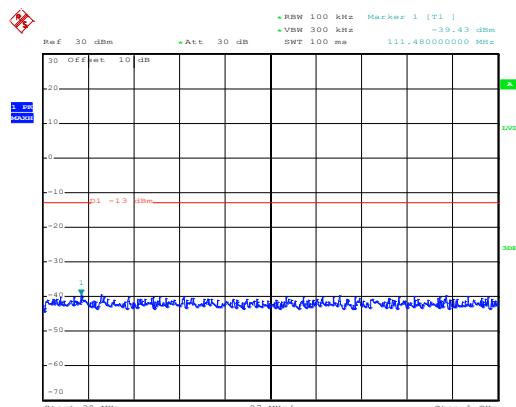
Date: 4.DEC.2017 15:01:09

### 30MHz~1GHz

Date: 1.DEC.2017 15:39:08

### 1GHz~25GHz

## High channel



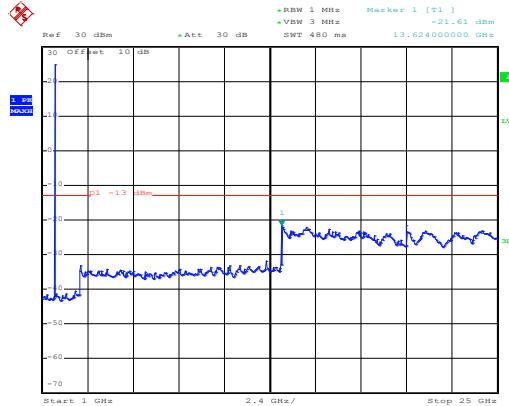
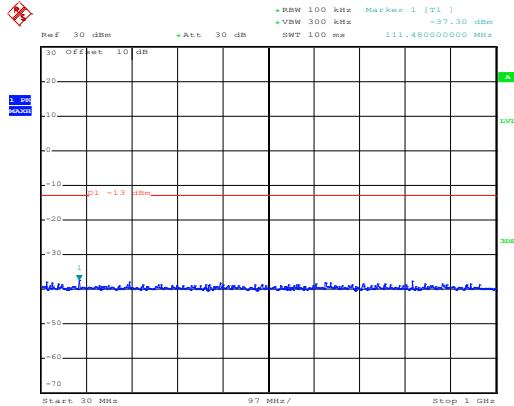
Date: 4.DEC.2017 15:01:30

### 30MHz~1GHz

Date: 1.DEC.2017 15:40:12

### 1GHz~25GHz

**QPSK & RB Size 1  
Lowest channel**



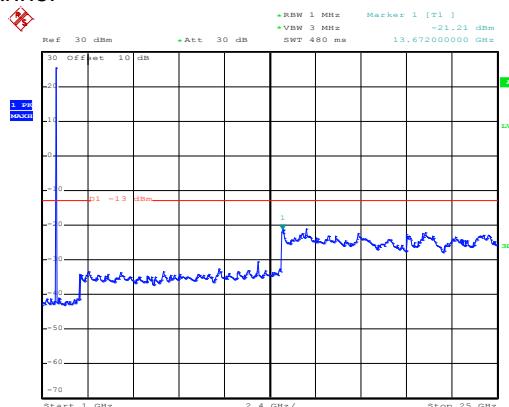
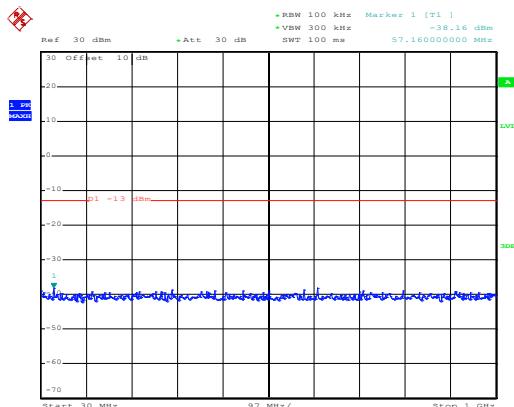
Date: 4.DEC.2017 15:00:00

**30MHz~1GHz**

Date: 1.DEC.2017 15:37:44

**1GHz~25GHz**

**Middle channel**



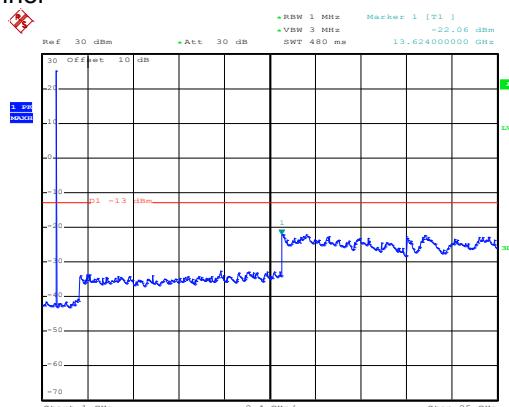
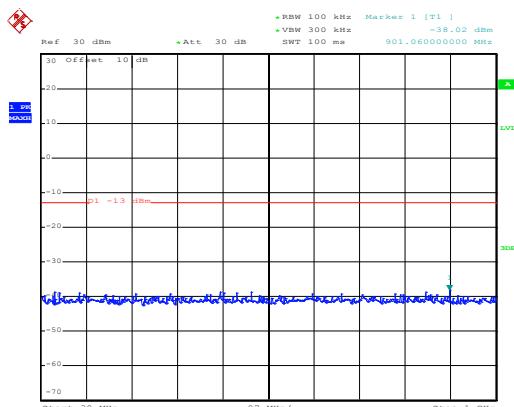
Date: 4.DEC.2017 15:00:55

**30MHz~1GHz**

Date: 1.DEC.2017 15:38:40

**1GHz~25GHz**

**High channel**



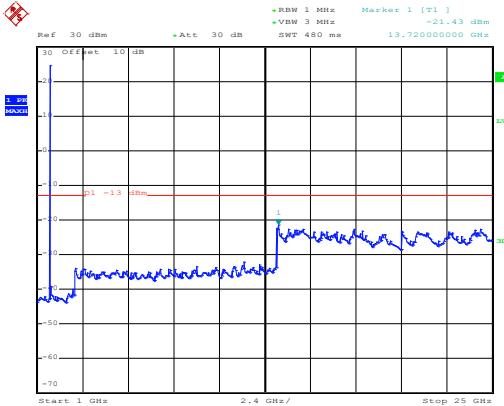
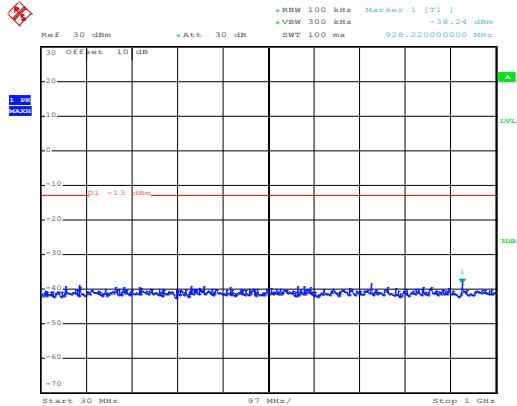
Date: 4.DEC.2017 15:01:18

**30MHz~1GHz**

Date: 1.DEC.2017 15:39:30

**1GHz~25GHz**

**QPSK & RB Size 6  
Lowest channel**



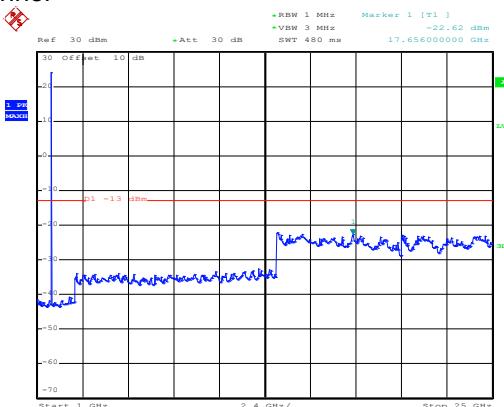
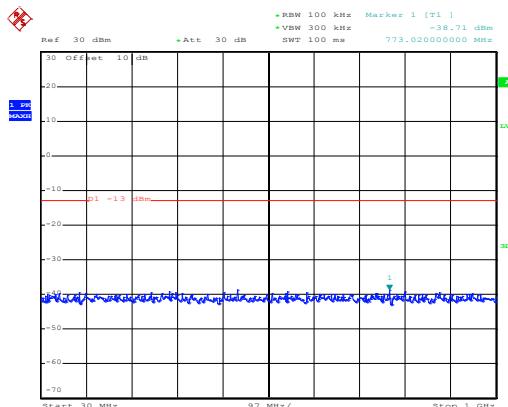
Date: 4.DEC.2017 15:00:36

**30MHz~1GHz**

Date: 1.DEC.2017 15:38:09

**1GHz~25GHz**

**Middle channel**



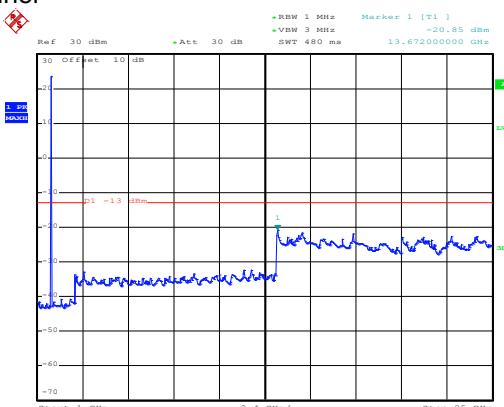
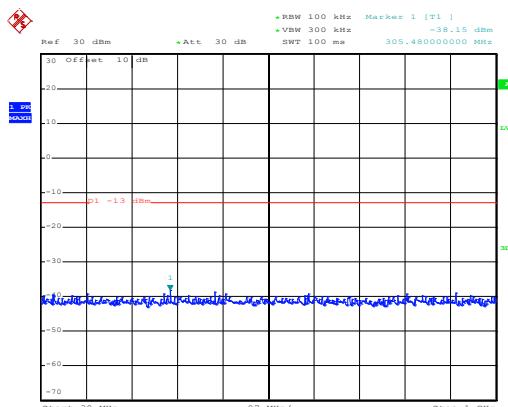
Date: 4.DEC.2017 15:01:05

**30MHz~1GHz**

Date: 1.DEC.2017 15:39:01

**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 15:01:27

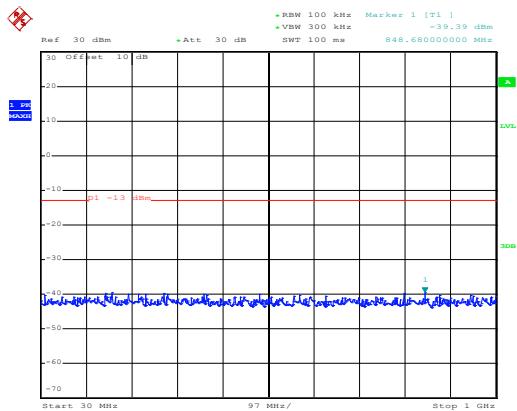
**30MHz~1GHz**

Date: 1.DEC.2017 15:40:04

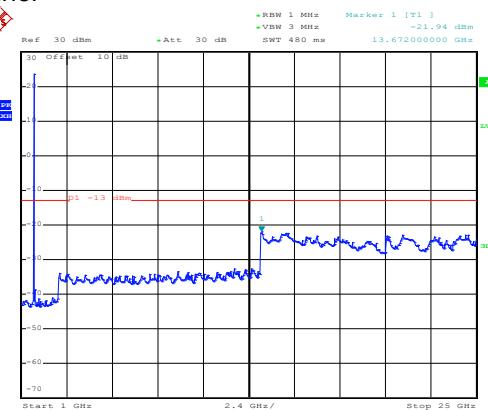
**1GHz~25GHz**

## 3MHz

### 16 QAM & RB Size 1 Lowest channel



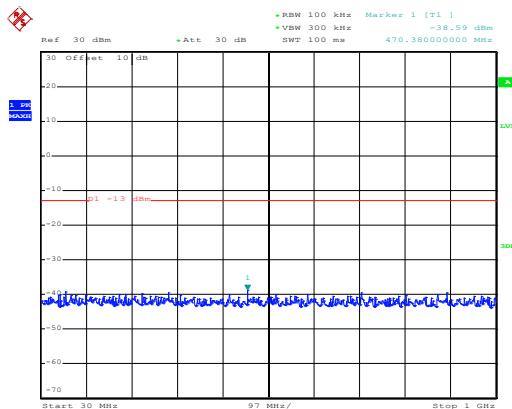
Date: 4.DEC.2017 15:01:43



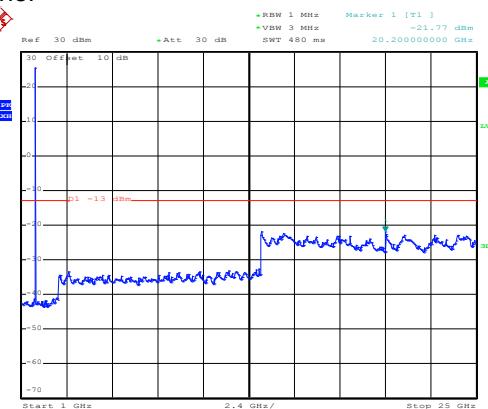
Date: 1.DEC.2017 15:41:02

### 30MHz~1GHz

### Middle channel



Date: 4.DEC.2017 15:02:10

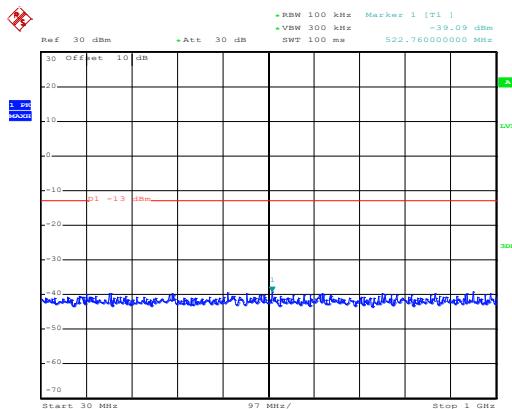


Date: 1.DEC.2017 15:41:50

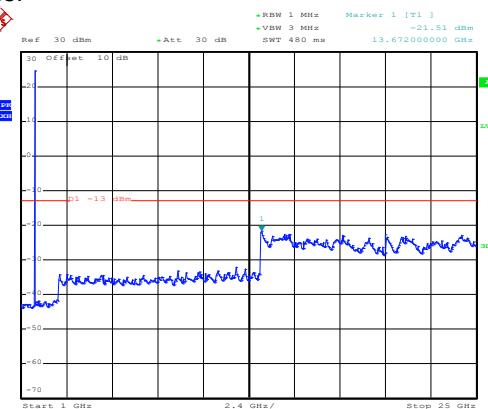
### 30MHz~1GHz

### 1GHz~25GHz

### High channel



Date: 4.DEC.2017 15:02:41

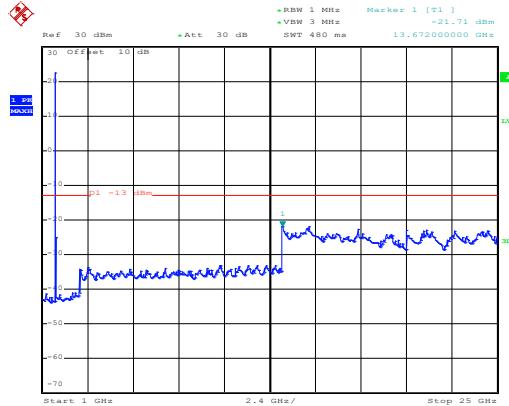
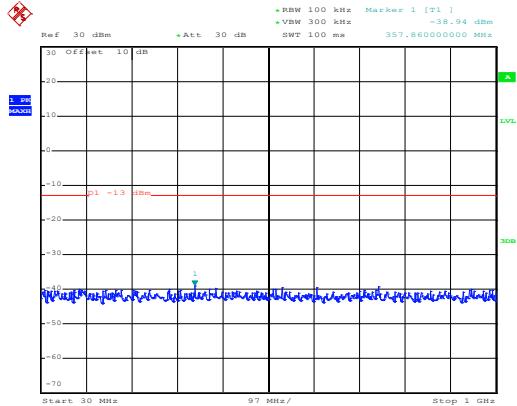


Date: 1.DEC.2017 15:42:47

### 30MHz~1GHz

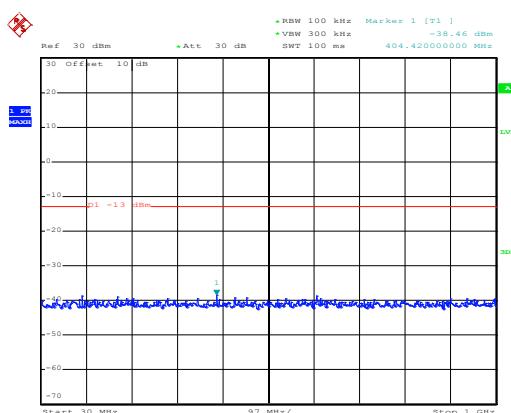
### 1GHz~25GHz

## 16 QAM & RB Size 15 Lowest channel

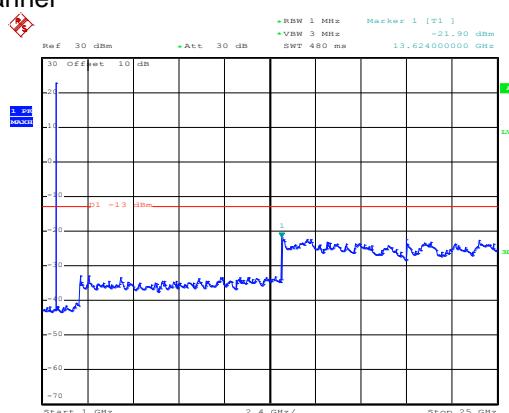


Date: 4.DEC.2017 15:01:57

## 30MHz~1GHz

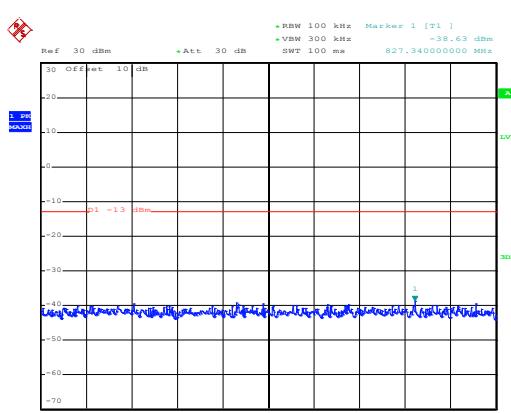


## Middle channel

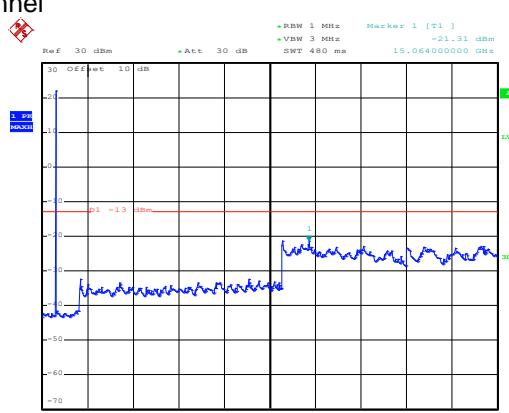


Date: 4.DEC.2017 15:02:25

## 30MHz~1GHz



## High channel



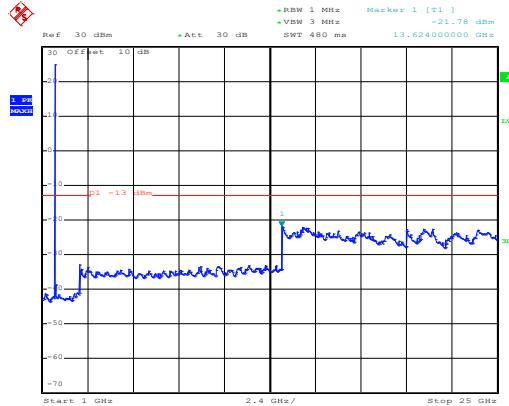
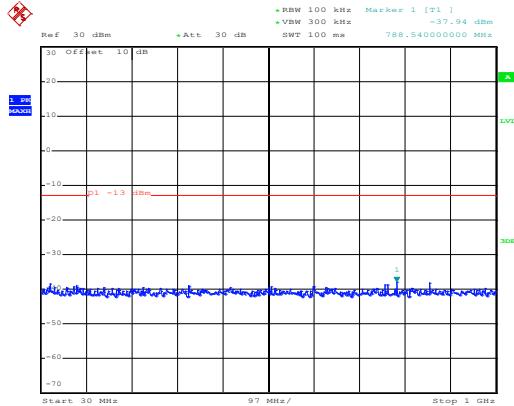
Date: 4.DEC.2017 15:03:03

## 30MHz~1GHz

Date: 1.DEC.2017 15:43:08

## 1GHz~25GHz

**QPSK & RB Size 1  
Lowest channel**



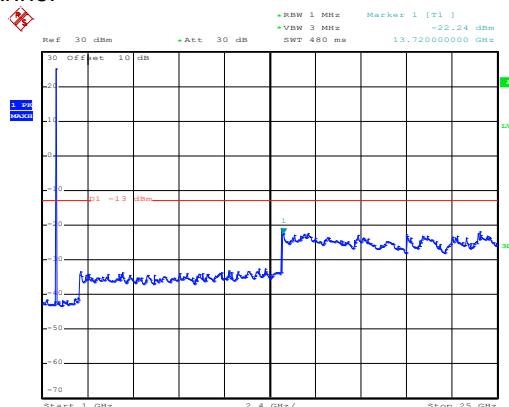
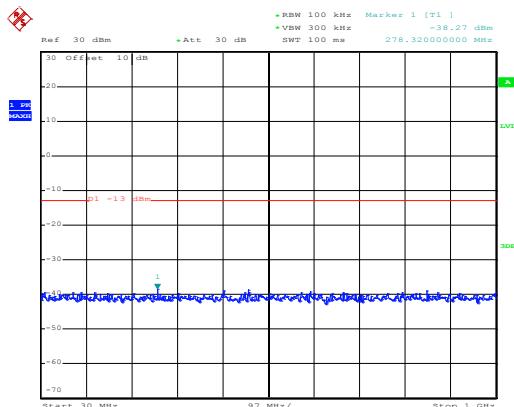
Date: 4.DEC.2017 15:01:40

**30MHz~1GHz**

Date: 1.DEC.2017 15:40:50

**1GHz~25GHz**

**Middle channel**



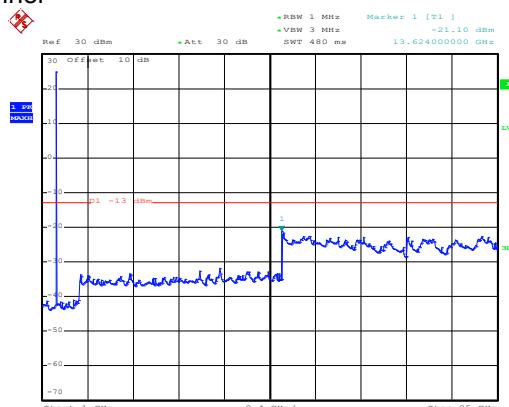
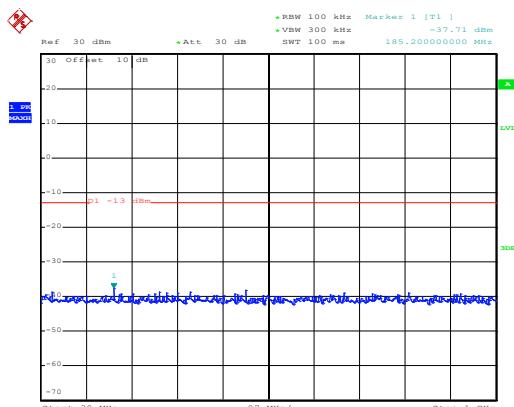
Date: 4.DEC.2017 15:02:07

**30MHz~1GHz**

Date: 1.DEC.2017 15:41:40

**1GHz~25GHz**

**High channel**



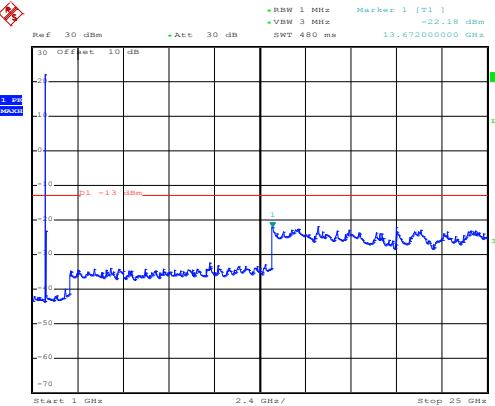
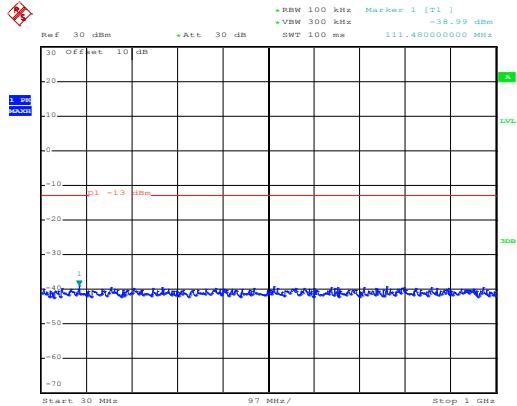
Date: 4.DEC.2017 15:02:37

**30MHz~1GHz**

Date: 1.DEC.2017 15:42:39

**1GHz~25GHz**

**QPSK & RB Size 15  
Lowest channel**



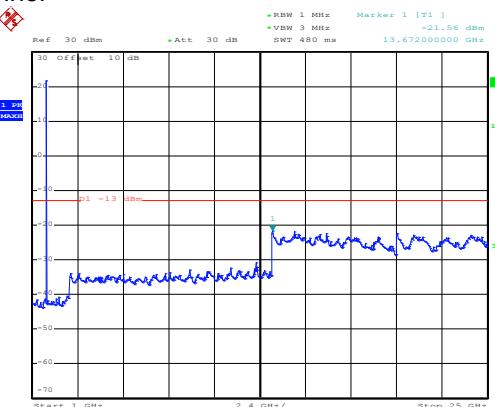
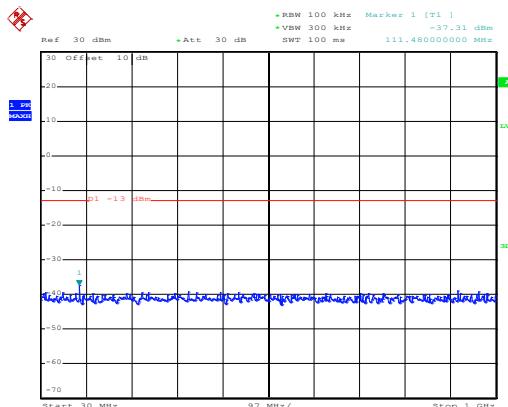
Date: 4.DEC.2017 15:01:53

**30MHz~1GHz**

Date: 1.DEC.2017 15:41:14

**1GHz~25GHz**

**Middle channel**



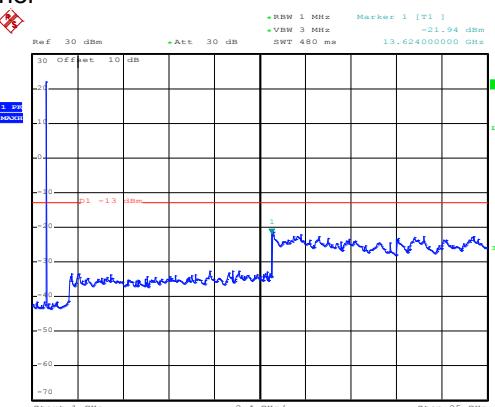
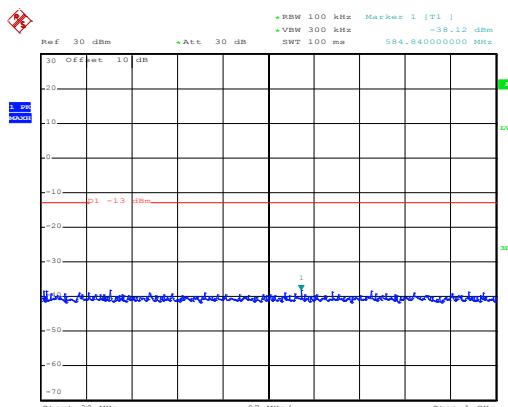
Date: 4.DEC.2017 15:02:17

**30MHz~1GHz**

Date: 1.DEC.2017 15:42:04

**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 15:02:59

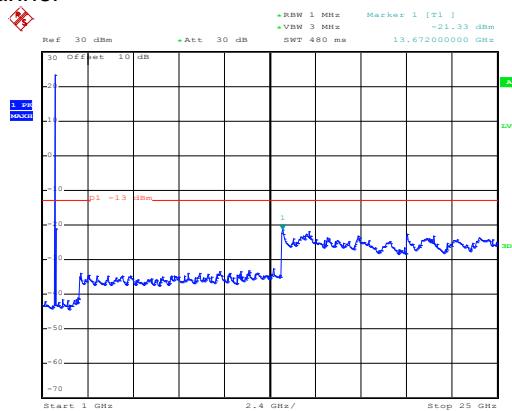
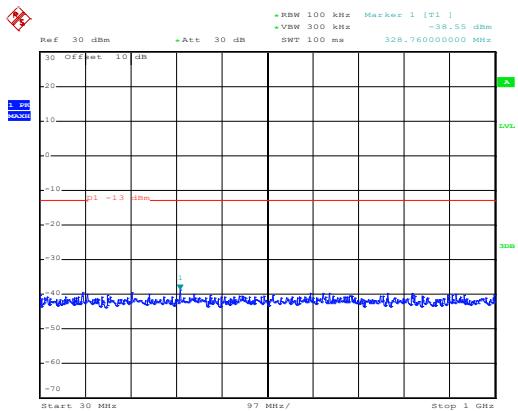
**30MHz~1GHz**

Date: 1.DEC.2017 15:42:57

**1GHz~25GHz**

## 5MHz

### 16 QAM & RB Size 1 Lowest channel



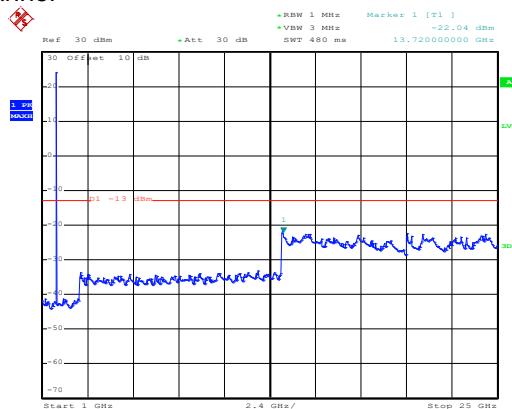
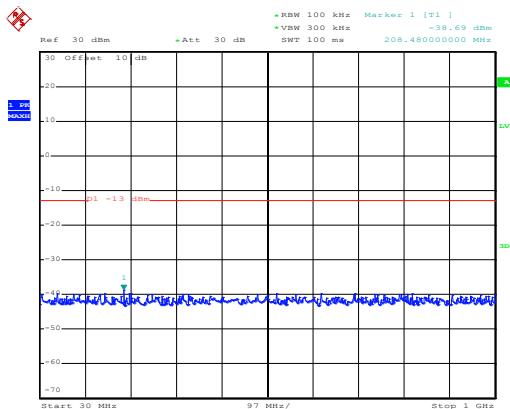
Date: 4.DEC.2017 15:03:20

Date: 1.DEC.2017 15:44:09

### 30MHz~1GHz

### 1GHz~25GHz

### Middle channel



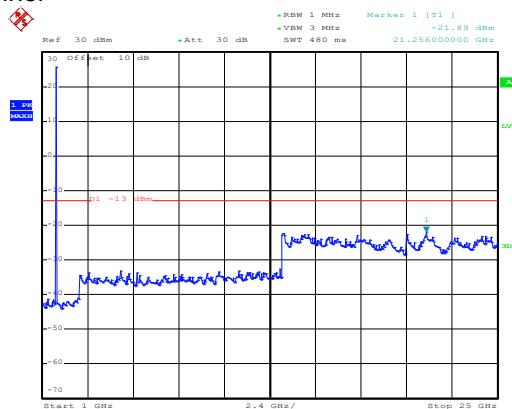
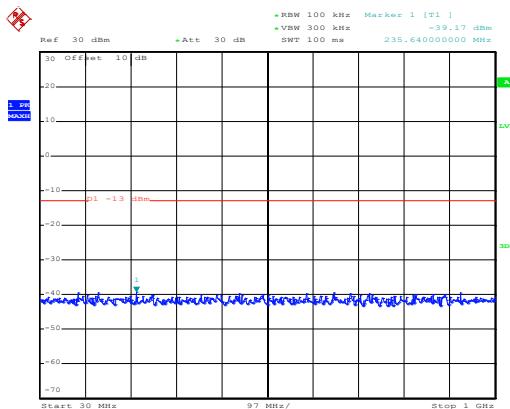
Date: 4.DEC.2017 15:03:48

Date: 1.DEC.2017 15:44:57

### 30MHz~1GHz

### 1GHz~25GHz

### High channel



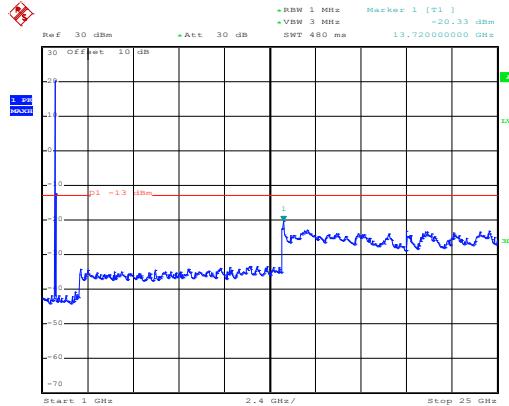
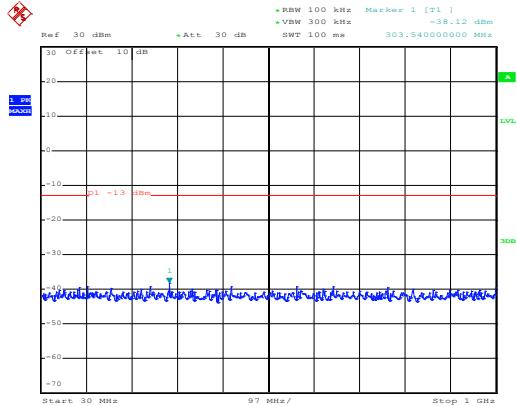
Date: 4.DEC.2017 15:04:31

Date: 1.DEC.2017 15:45:45

### 30MHz~1GHz

### 1GHz~25GHz

## 16 QAM & RB Size 25 Lowest channel



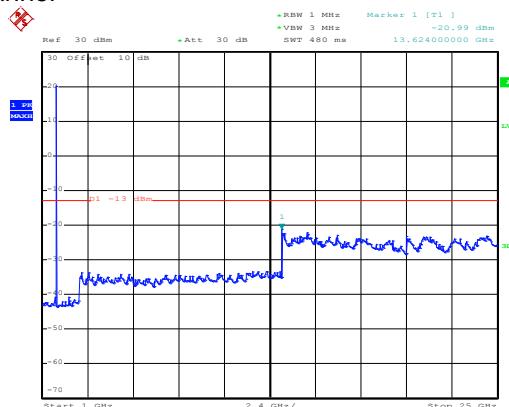
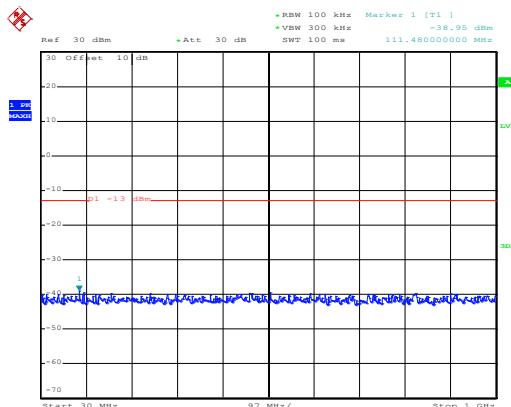
Date: 4.DEC.2017 15:03:33

Date: 1.DEC.2017 15:44:29

## 30MHz~1GHz

## 1GHz~25GHz

## Middle channel



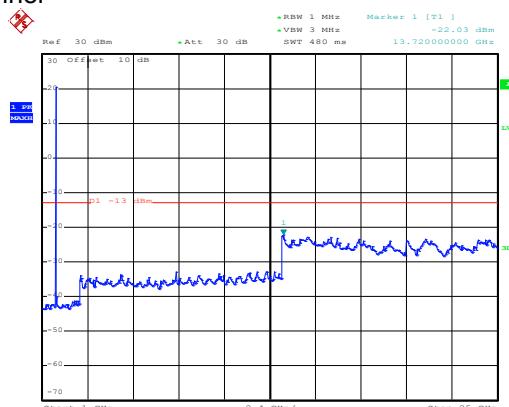
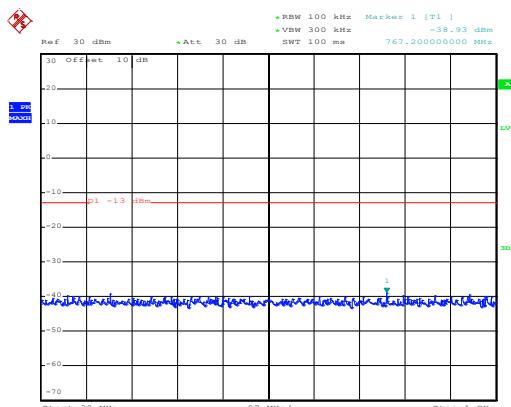
Date: 4.DEC.2017 15:04:01

Date: 1.DEC.2017 15:45:17

## 30MHz~1GHz

## 1GHz~25GHz

## High channel



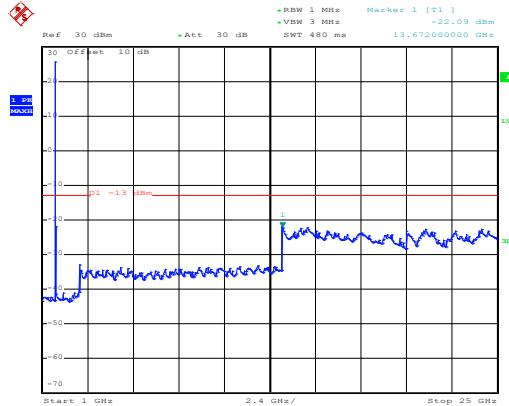
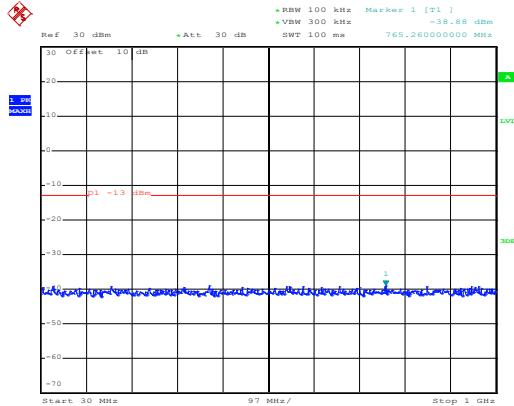
Date: 4.DEC.2017 15:05:02

Date: 1.DEC.2017 15:46:02

## 30MHz~1GHz

## 1GHz~25GHz

**QPSK & RB Size 1  
Lowest channel**



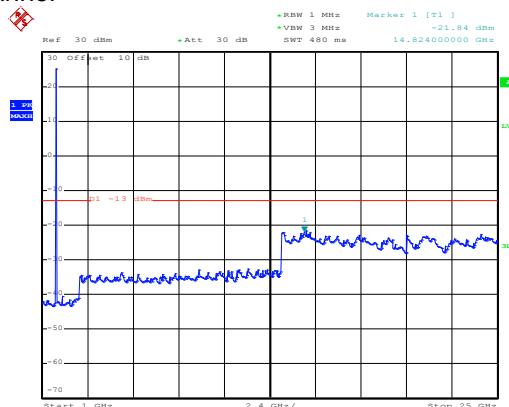
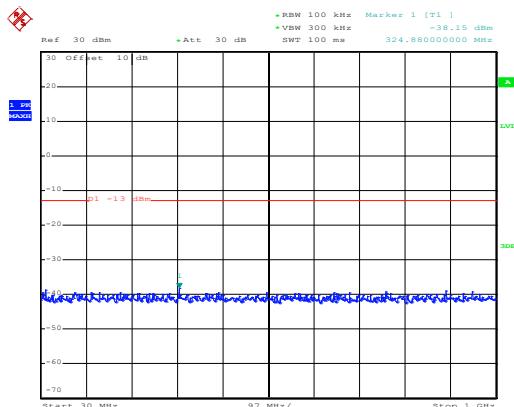
Date: 4.DEC.2017 15:03:16

**30MHz~1GHz**

Date: 1.DEC.2017 15:44:03

**1GHz~25GHz**

**Middle channel**



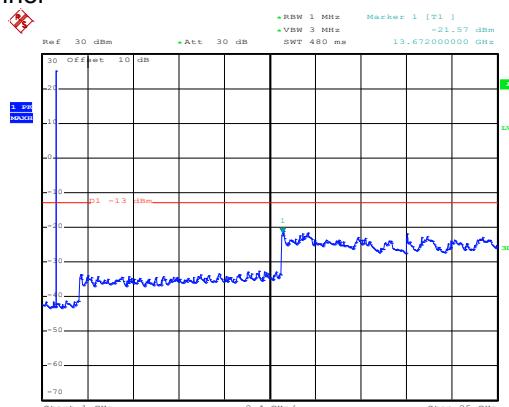
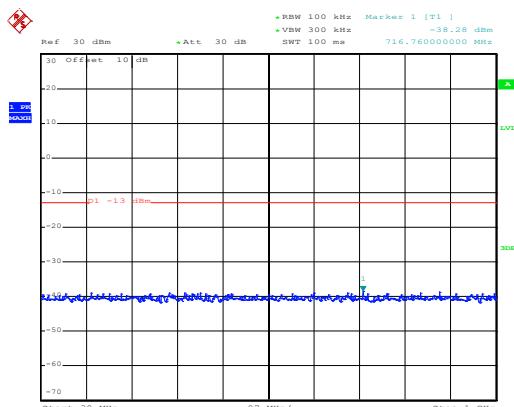
Date: 4.DEC.2017 15:03:42

**30MHz~1GHz**

Date: 1.DEC.2017 15:44:49

**1GHz~25GHz**

**High channel**



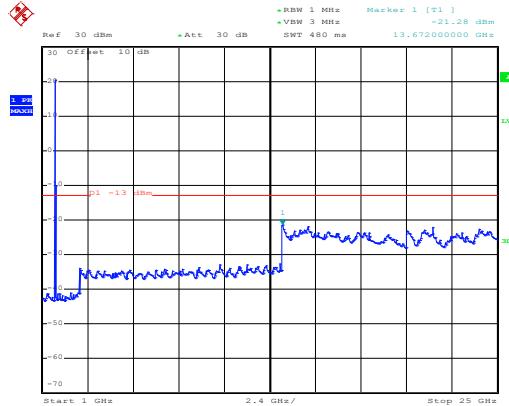
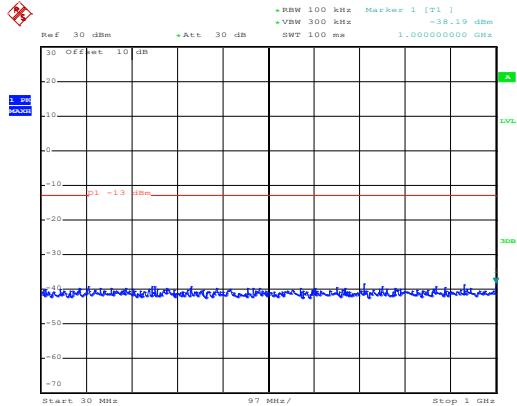
Date: 4.DEC.2017 15:04:25

**30MHz~1GHz**

Date: 1.DEC.2017 15:45:37

**1GHz~25GHz**

**QPSK & RB Size 25  
Lowest channel**



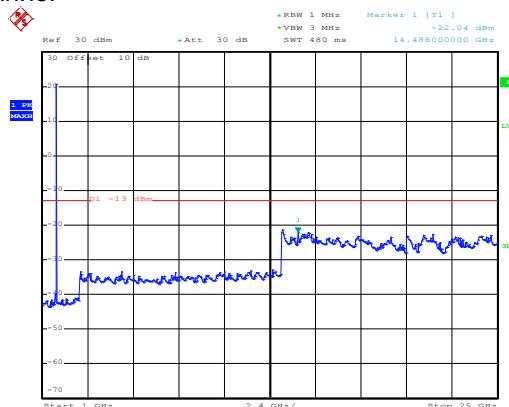
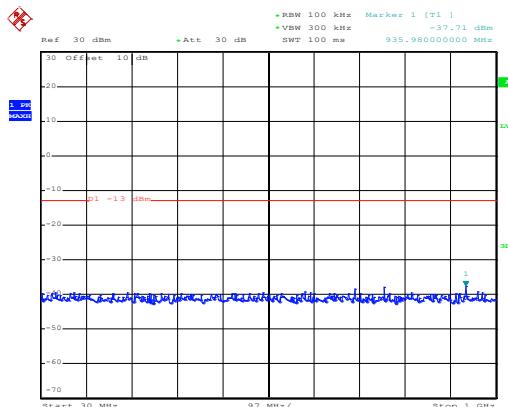
Date: 4.DEC.2017 15:03:29

**30MHz~1GHz**

Date: 1.DEC.2017 15:44:23

**1GHz~25GHz**

**Middle channel**



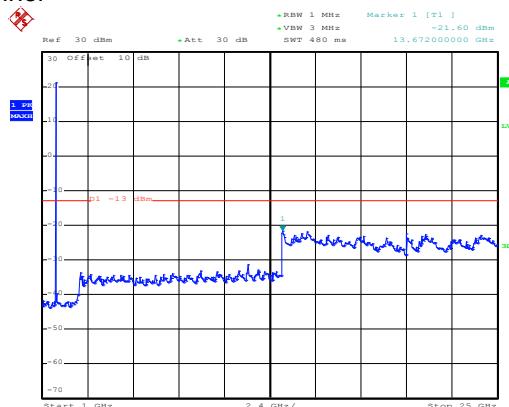
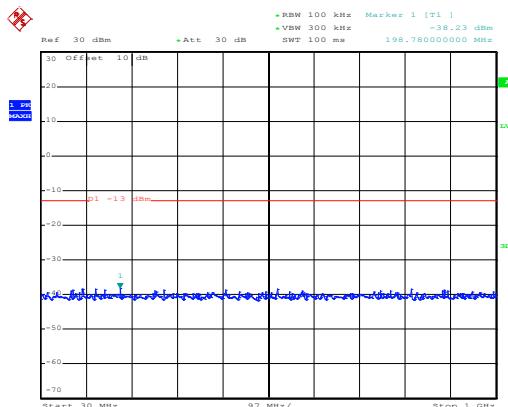
Date: 4.DEC.2017 15:03:55

**30MHz~1GHz**

Date: 1.DEC.2017 15:45:09

**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 15:04:57

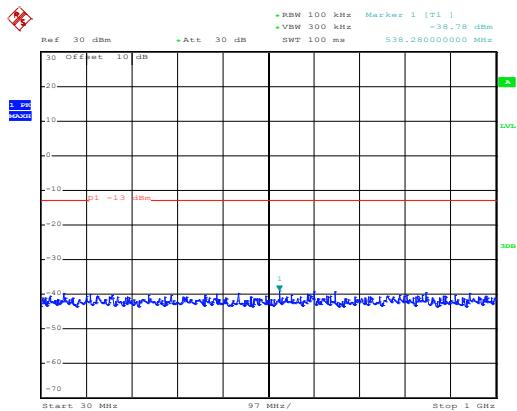
**30MHz~1GHz**

Date: 1.DEC.2017 15:45:54

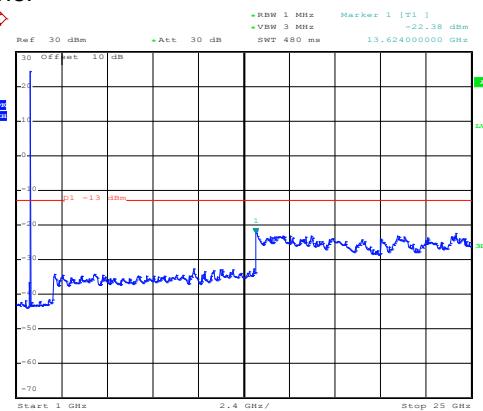
**1GHz~25GHz**

## 10MHz

### 16 QAM & RB Size 1 Lowest channel



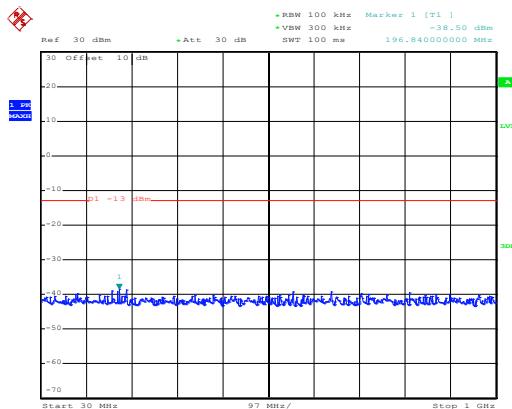
Date: 4.DEC.2017 15:05:20



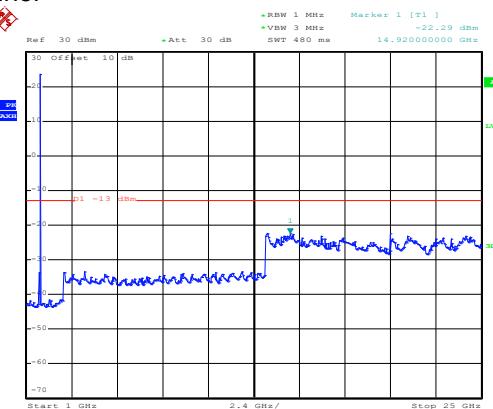
Date: 1.DEC.2017 15:46:43

### 30MHz~1GHz

### Middle channel



Date: 4.DEC.2017 15:05:55

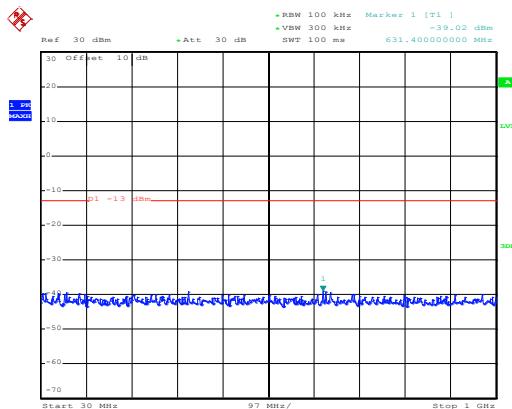


Date: 1.DEC.2017 15:47:36

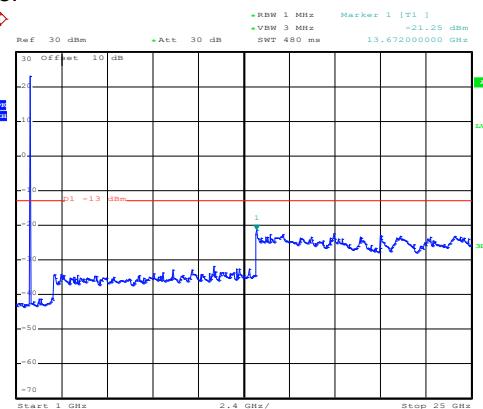
### 30MHz~1GHz

### 1GHz~25GHz

### High channel



Date: 4.DEC.2017 15:06:22

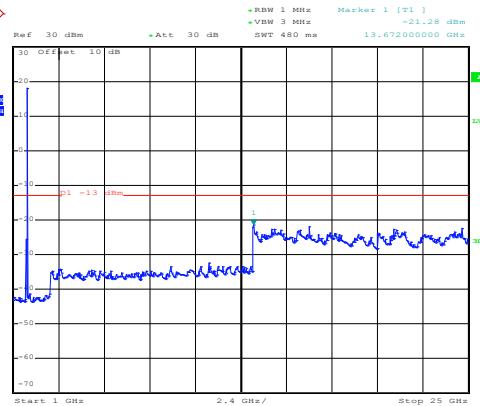
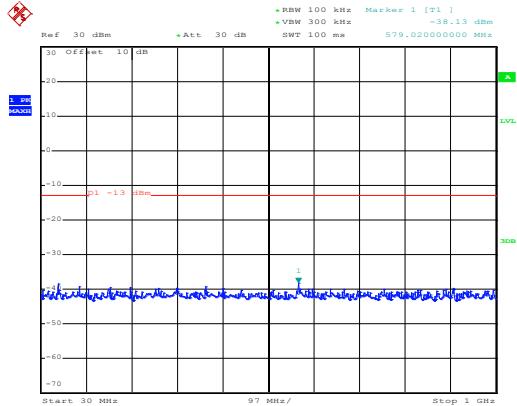


Date: 1.DEC.2017 15:48:20

### 30MHz~1GHz

### 1GHz~25GHz

## 16 QAM & RB Size 50 Lowest channel



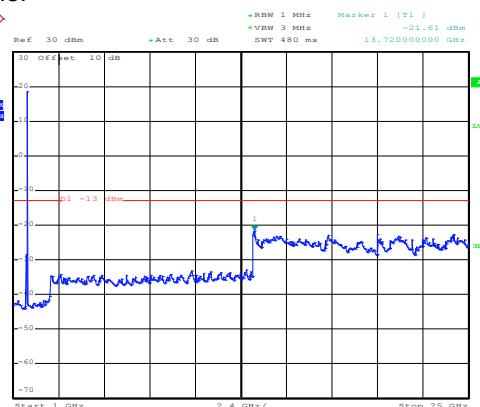
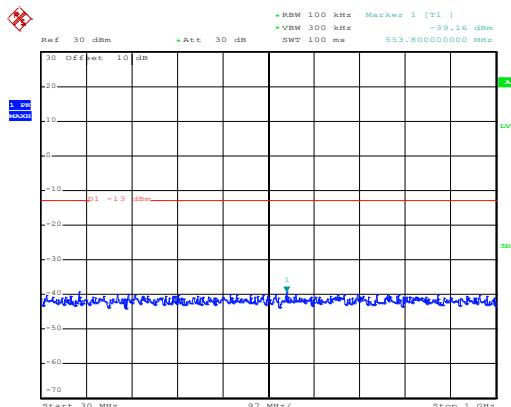
Date: 4.DEC.2017 15:05:38

Date: 1.DEC.2017 15:47:00

## 30MHz~1GHz

## 1GHz~25GHz

## Middle channel



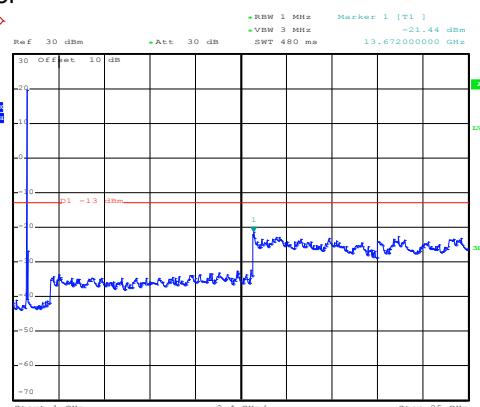
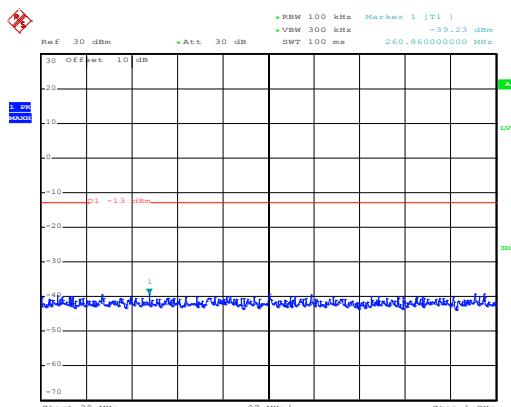
Date: 4.DEC.2017 15:06:06

Date: 1.DEC.2017 15:47:53

## 30MHz~1GHz

## 1GHz~25GHz

## High channel



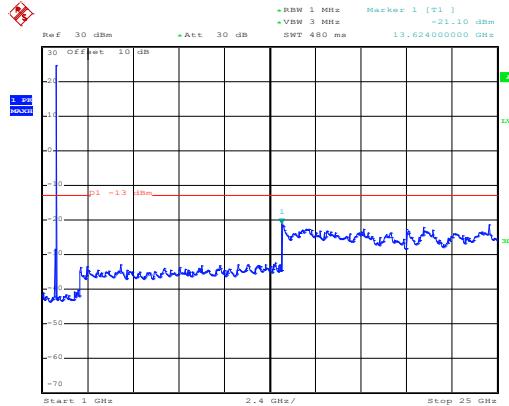
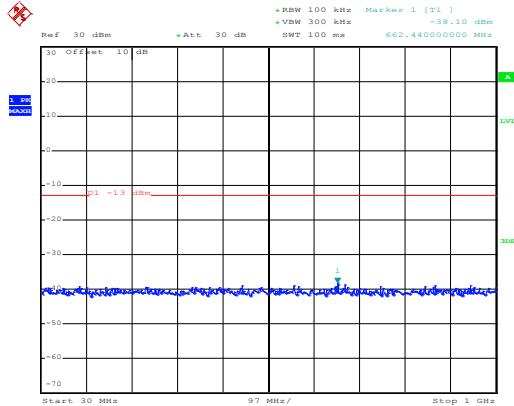
Date: 4.DEC.2017 15:06:38

Date: 1.DEC.2017 15:48:42

## 30MHz~1GHz

## 1GHz~25GHz

**QPSK & RB Size 1  
Lowest channel**



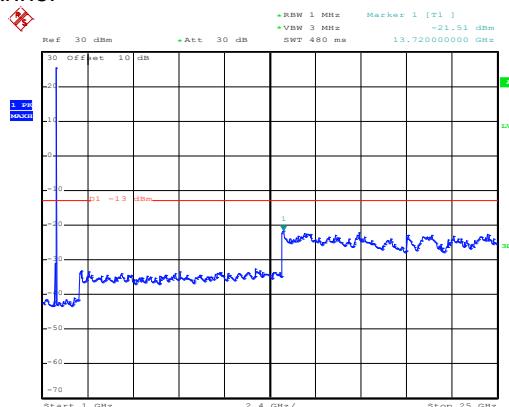
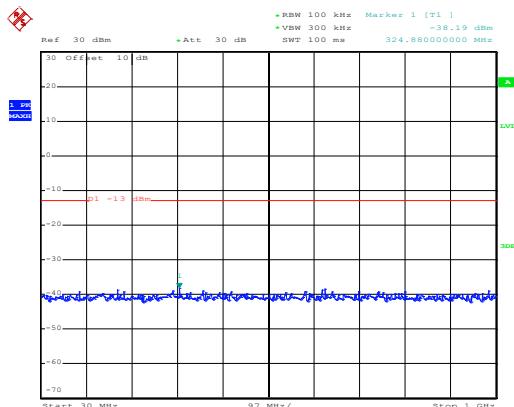
Date: 4.DEC.2017 15:05:16

**30MHz~1GHz**

Date: 1.DEC.2017 15:46:35

**1GHz~25GHz**

**Middle channel**



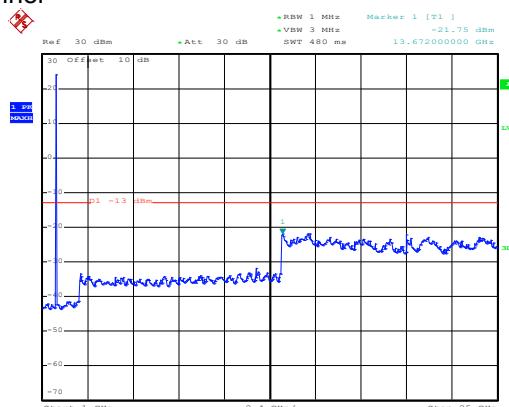
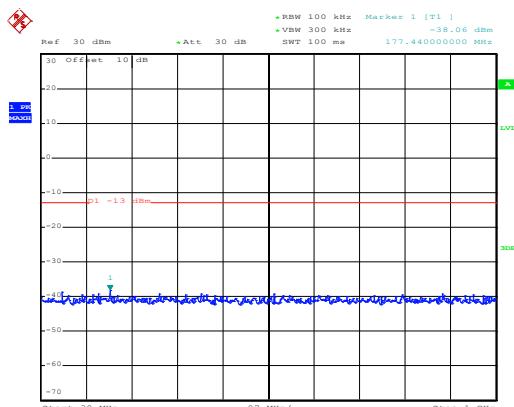
Date: 4.DEC.2017 15:05:51

**30MHz~1GHz**

Date: 1.DEC.2017 15:47:28

**1GHz~25GHz**

**High channel**



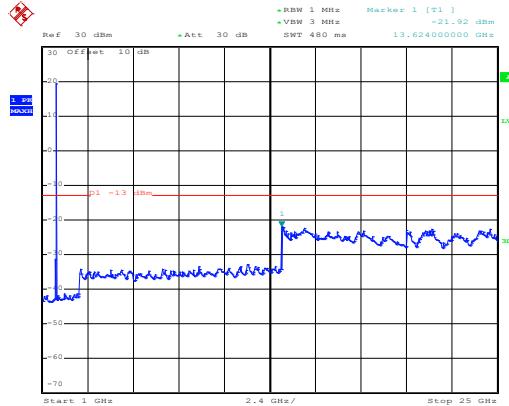
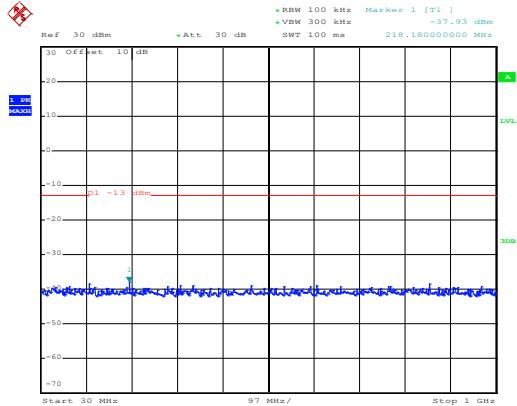
Date: 4.DEC.2017 15:06:18

**30MHz~1GHz**

Date: 1.DEC.2017 15:48:09

**1GHz~25GHz**

**QPSK & RB Size 50  
Lowest channel**



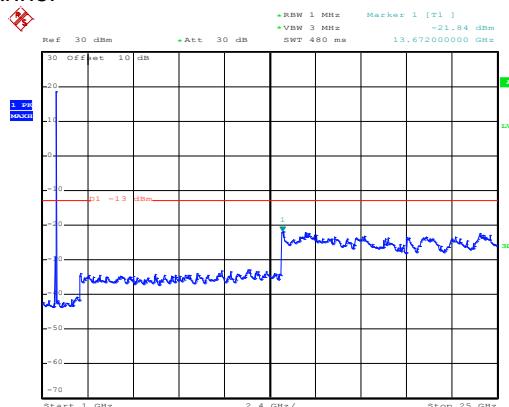
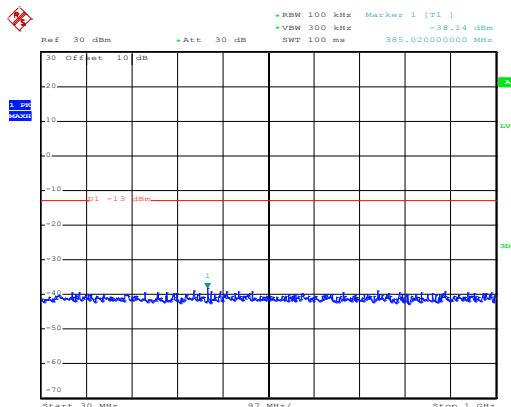
Date: 4.DEC.2017 15:05:33

**30MHz~1GHz**

Date: 1.DEC.2017 15:46:53

**1GHz~25GHz**

**Middle channel**



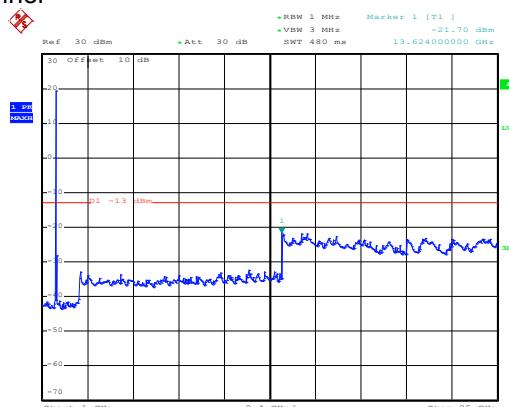
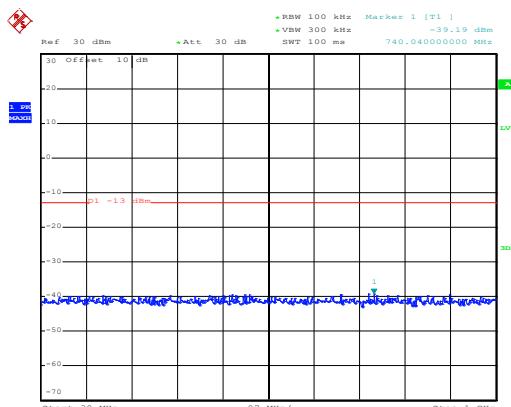
Date: 4.DEC.2017 15:06:02

**30MHz~1GHz**

Date: 1.DEC.2017 15:47:46

**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 15:06:30

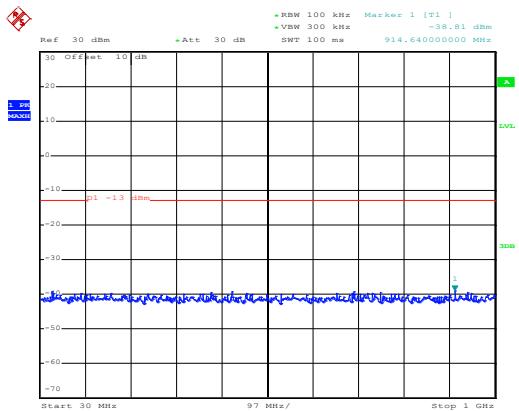
**30MHz~1GHz**

Date: 1.DEC.2017 15:48:35

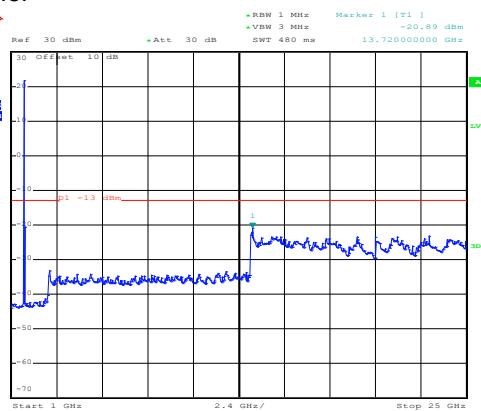
**1GHz~25GHz**

## 15MHz

### 16 QAM & RB Size 1 Lowest channel



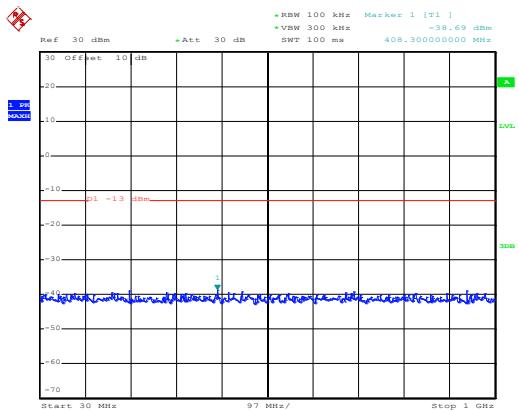
Date: 4.DEC.2017 15:06:57



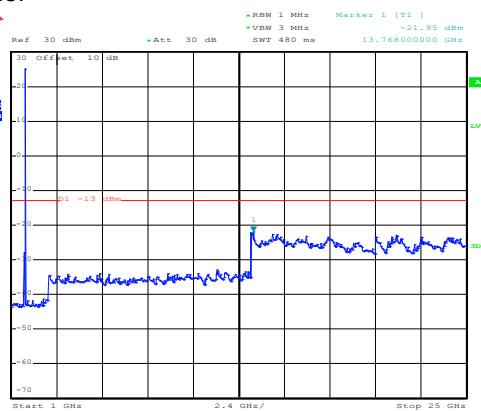
Date: 1.DEC.2017 15:49:26

### 30MHz~1GHz

### Middle channel



Date: 4.DEC.2017 15:09:16

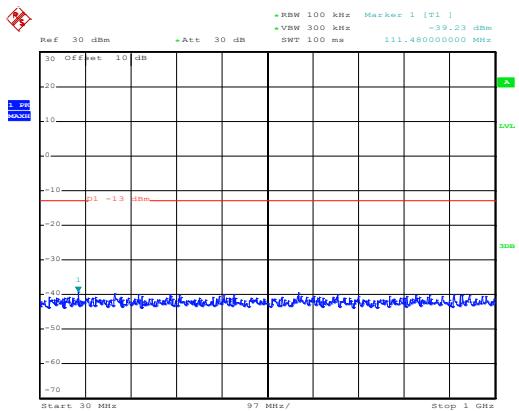


Date: 1.DEC.2017 15:50:06

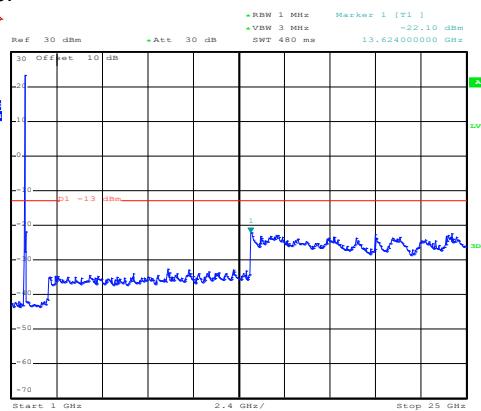
### 30MHz~1GHz

### 1GHz~25GHz

### High channel



Date: 4.DEC.2017 15:09:41

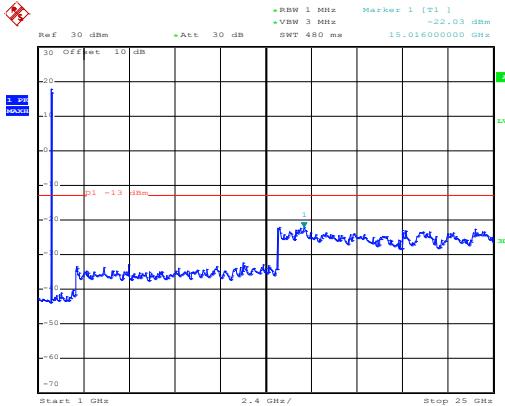
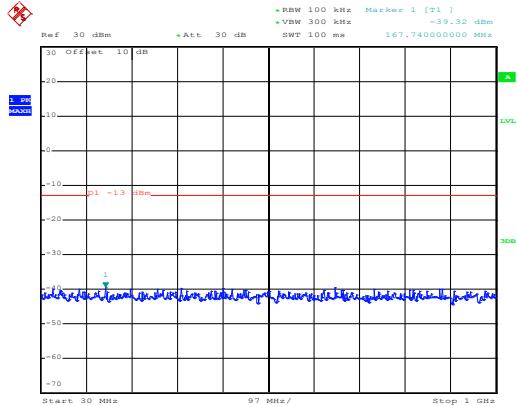


Date: 1.DEC.2017 15:50:50

### 30MHz~1GHz

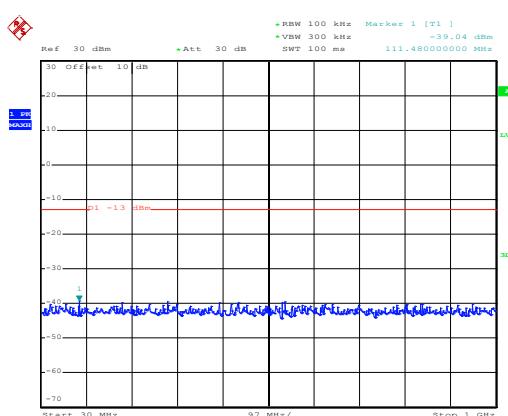
### 1GHz~25GHz

## 16 QAM & RB Size 75 Lowest channel

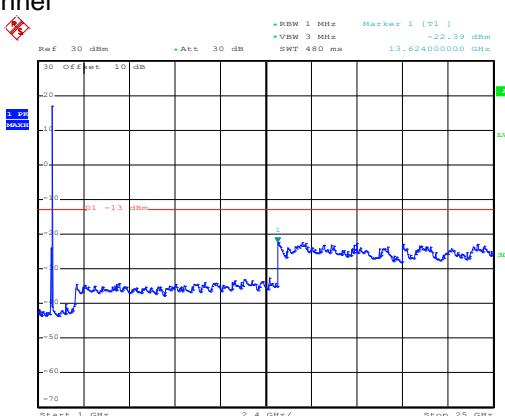


Date: 4.DEC.2017 15:07:15

## 30MHz~1GHz

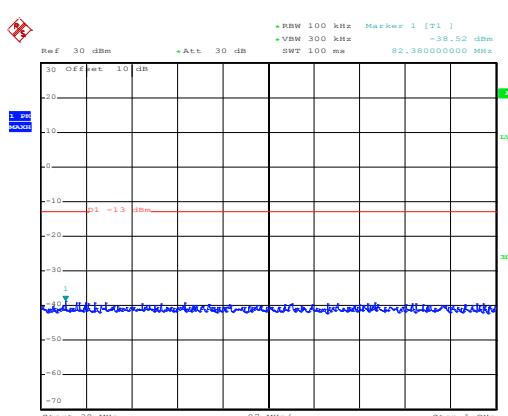


## Middle channel

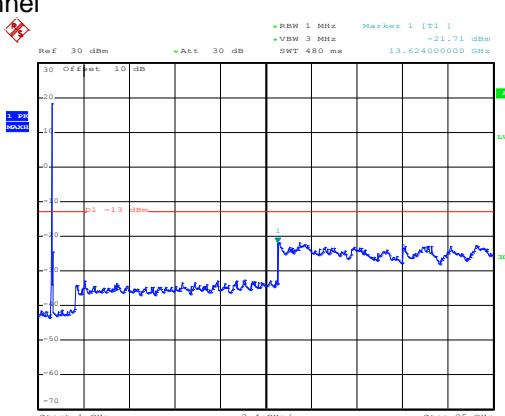


Date: 4.DEC.2017 15:09:29

## 30MHz~1GHz



## High channel



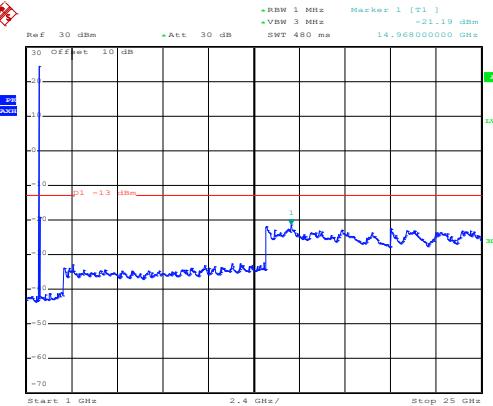
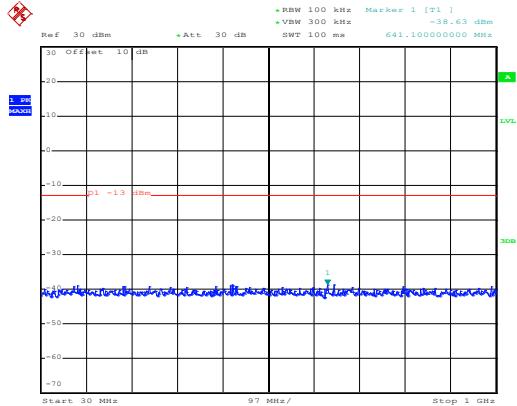
Date: 4.DEC.2017 15:13:36

## 30MHz~1GHz

Date: 1.DEC.2017 15:51:08

## 1GHz~25GHz

**QPSK & RB Size 1  
Lowest channel**



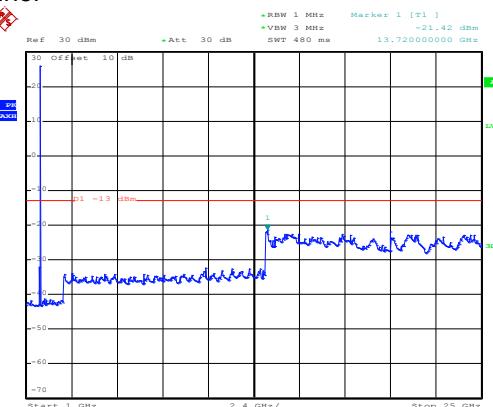
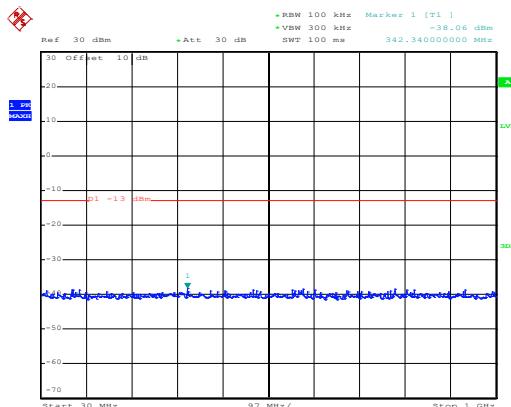
Date: 4.DEC.2017 15:06:49

**30MHz~1GHz**

Date: 1.DEC.2017 15:49:19

**1GHz~25GHz**

**Middle channel**



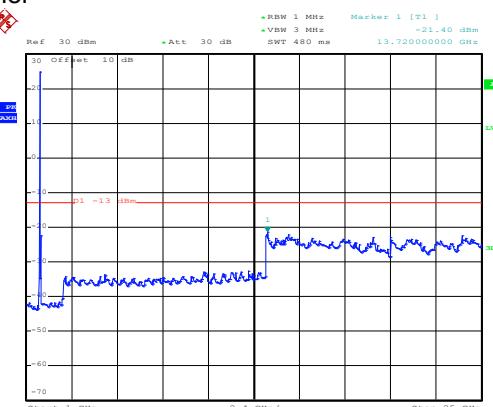
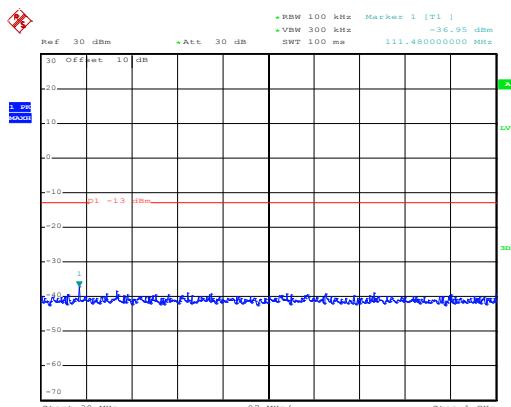
Date: 4.DEC.2017 15:09:09

**30MHz~1GHz**

Date: 1.DEC.2017 15:49:58

**1GHz~25GHz**

**High channel**



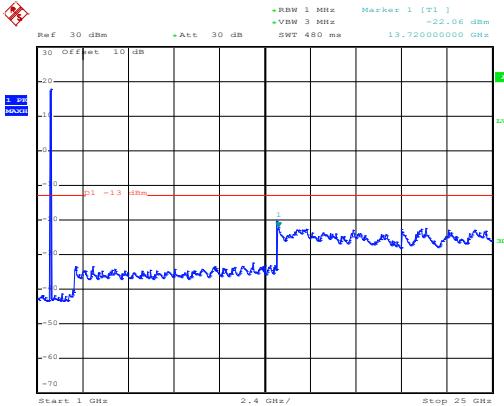
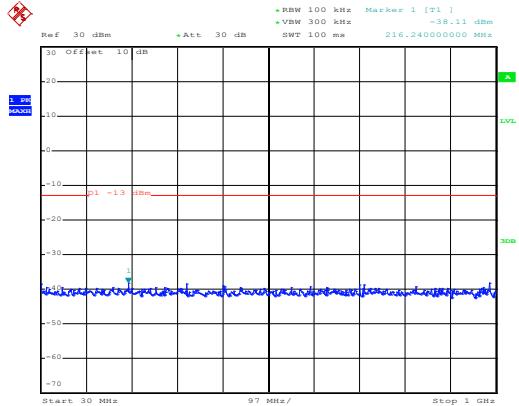
Date: 4.DEC.2017 15:13:52

**30MHz~1GHz**

Date: 1.DEC.2017 15:50:42

**1GHz~25GHz**

**QPSK & RB Size 75  
Lowest channel**



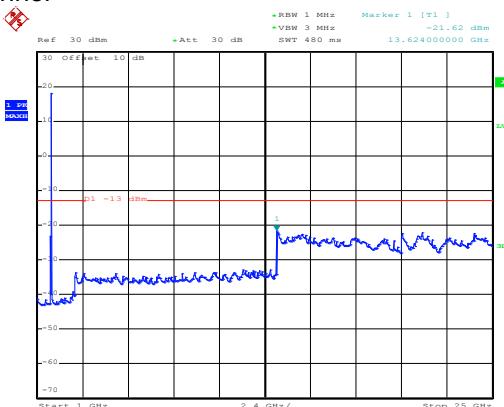
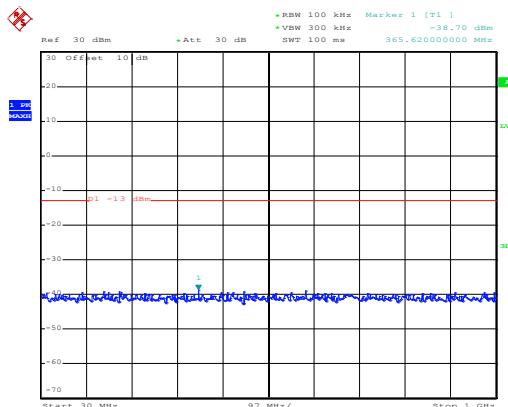
Date: 4.DEC.2017 15:07:08

**30MHz~1GHz**

Date: 1.DEC.2017 15:49:36

**1GHz~25GHz**

**Middle channel**



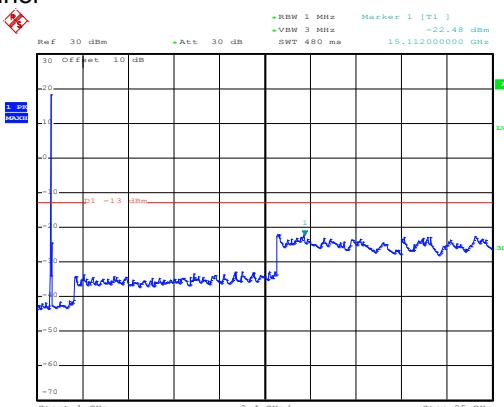
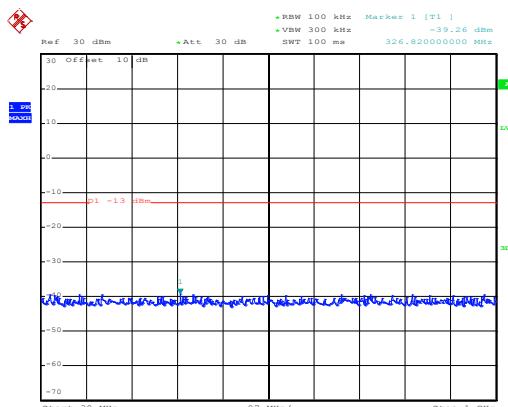
Date: 4.DEC.2017 15:09:25

**30MHz~1GHz**

Date: 1.DEC.2017 15:50:18

**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 15:13:43

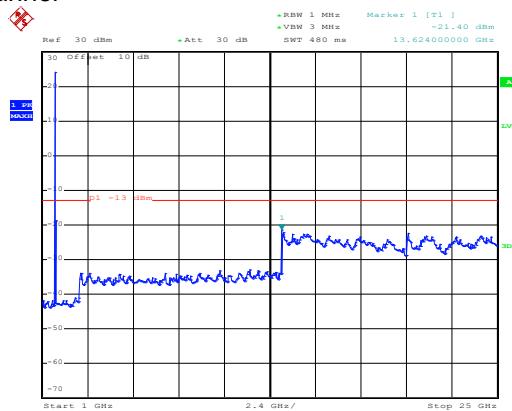
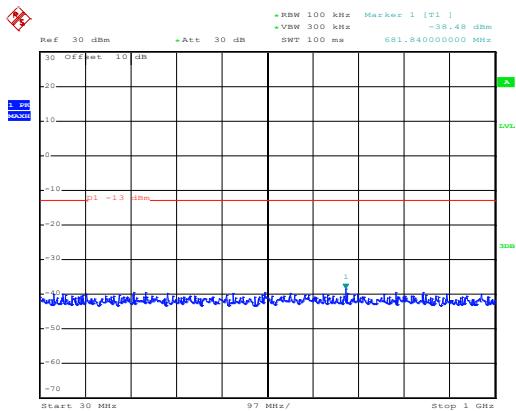
**30MHz~1GHz**

Date: 1.DEC.2017 15:51:00

**1GHz~25GHz**

## 20MHz

### 16 QAM & RB Size 1 Lowest channel



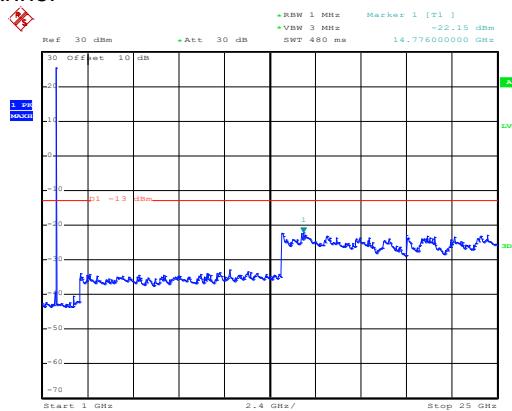
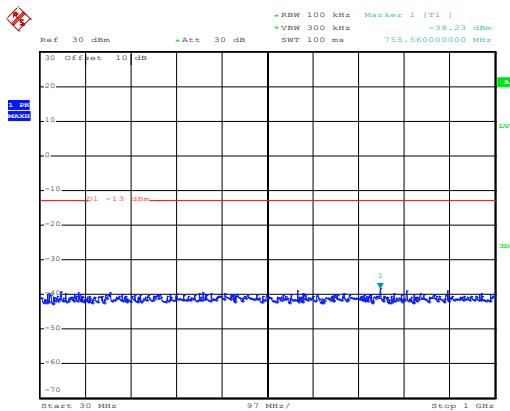
Date: 4.DEC.2017 15:10:12

Date: 1.DEC.2017 15:51:53

## 30MHz~1GHz

## 1GHz~25GHz

### Middle channel



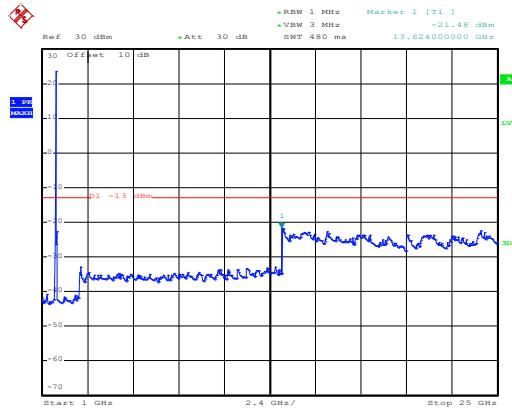
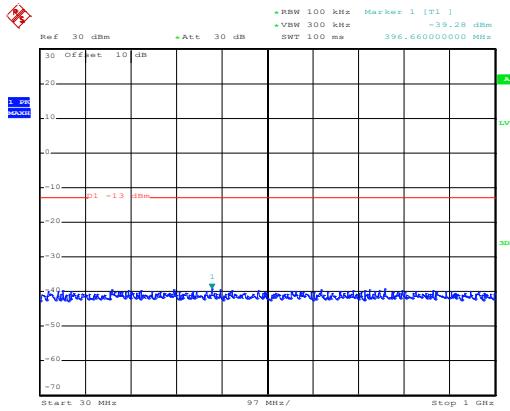
Date: 4.DEC.2017 15:12:29

Date: 1.DEC.2017 15:52:40

## 30MHz~1GHz

## 1GHz~25GHz

### High channel



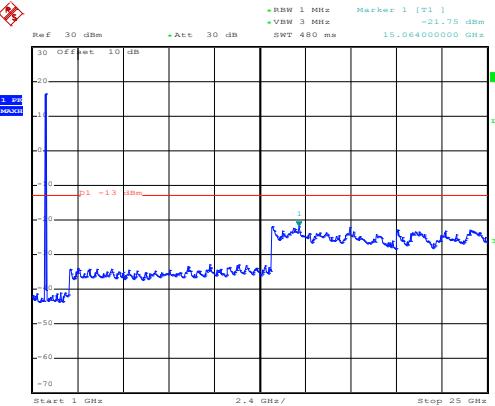
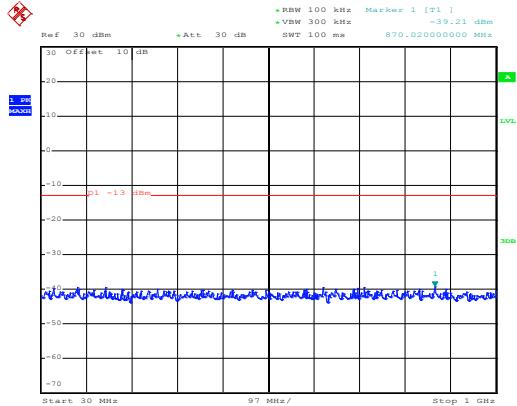
Date: 4.DEC.2017 15:13:05

Date: 1.DEC.2017 15:53:34

## 30MHz~1GHz

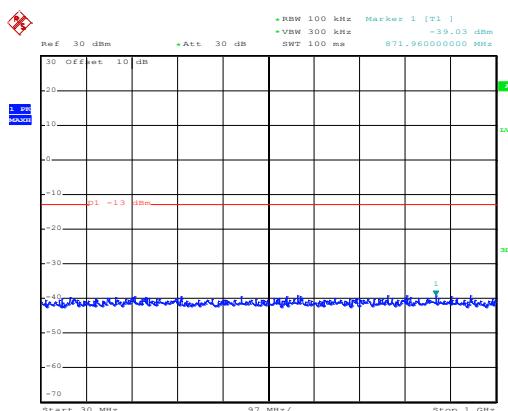
## 1GHz~25GHz

## 16 QAM & RB Size 100 Lowest channel

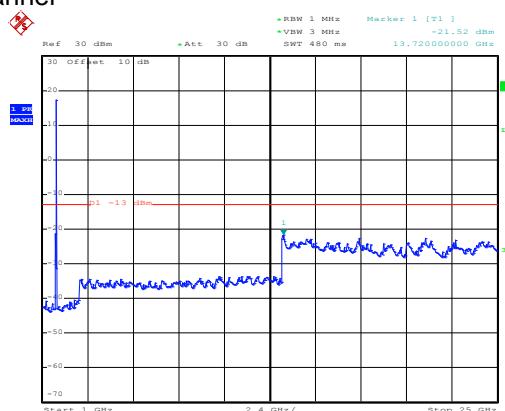


Date: 4.DEC.2017 15:12:09

## 30MHz~1GHz

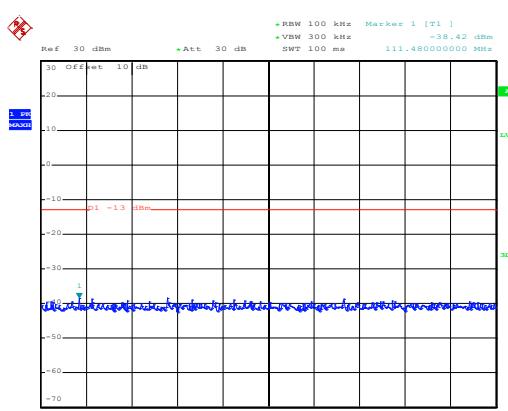


## Middle channel

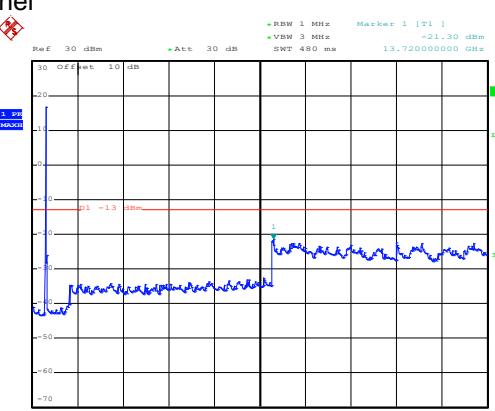


Date: 4.DEC.2017 15:12:48

## 30MHz~1GHz



## High channel



Date: 4.DEC.2017 15:13:26

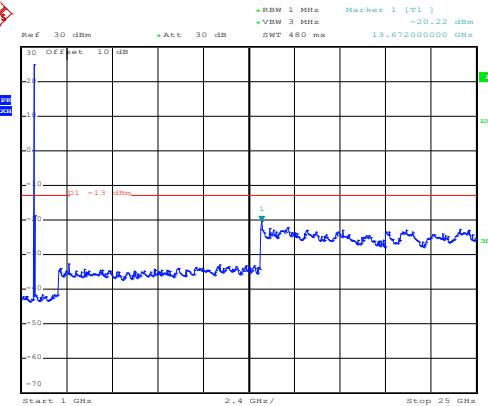
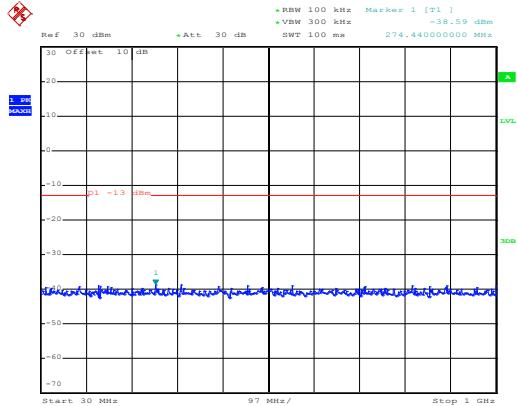
## 30MHz~1GHz

Date: 1.DEC.2017 15:52:57

## 1GHz~25GHz

## 1GHz~25GHz

## QPSK & RB Size 1 Lowest channel



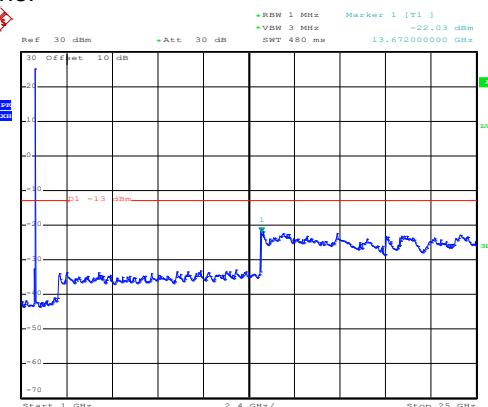
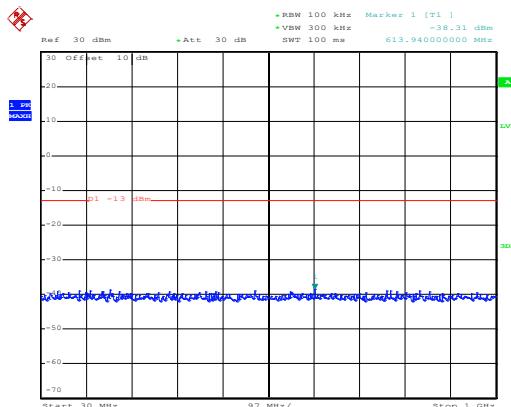
Date: 4.DEC.2017 15:10:07

30MHz~1GHz

Date: 1.DEC.2017 15:51:44

1GHz~25GHz

## Middle channel



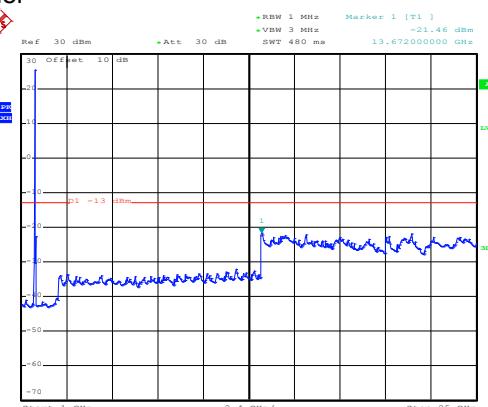
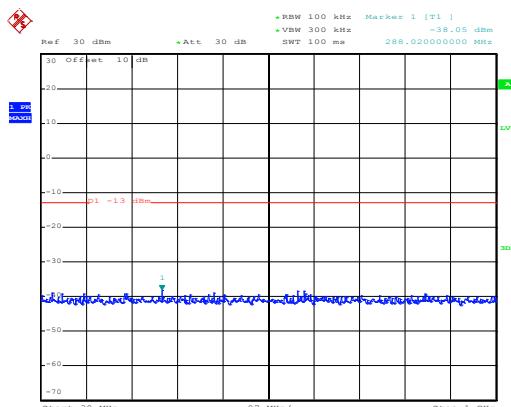
Date: 4.DEC.2017 15:12:22

30MHz~1GHz

Date: 1.DEC.2017 15:52:32

1GHz~25GHz

## High channel



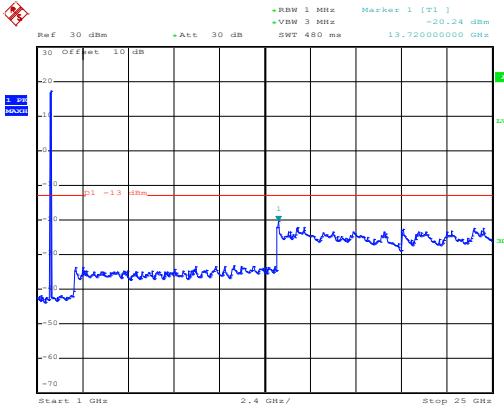
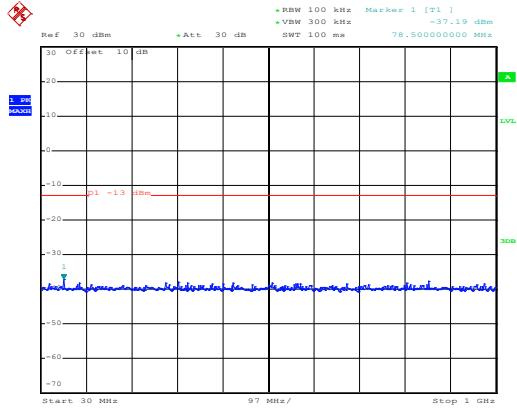
Date: 4.DEC.2017 15:12:58

30MHz~1GHz

Date: 1.DEC.2017 15:53:24

1GHz~25GHz

**QPSK & RB Size 100  
Lowest channel**



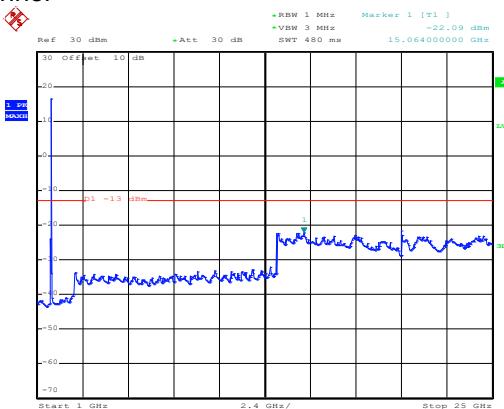
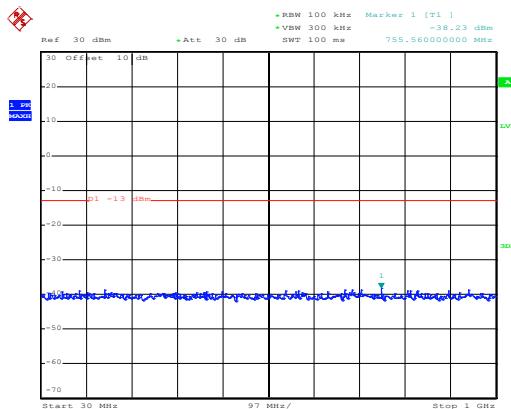
Date: 4.DEC.2017 15:12:05

**30MHz~1GHz**

Date: 1.DEC.2017 15:52:05

**1GHz~25GHz**

**Middle channel**



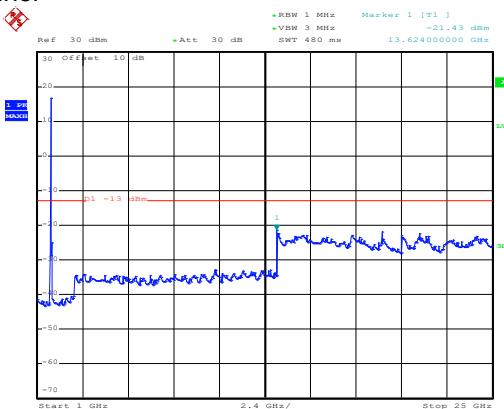
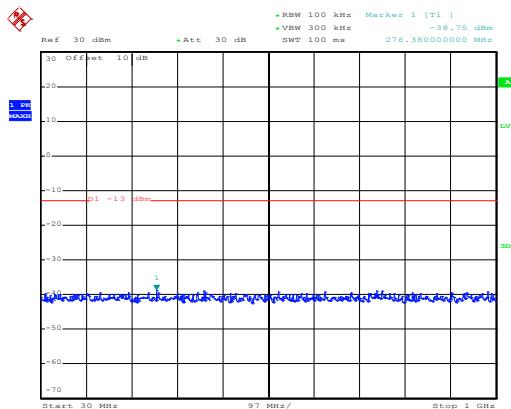
Date: 4.DEC.2017 15:12:41

**30MHz~1GHz**

Date: 1.DEC.2017 15:52:50

**1GHz~25GHz**

**High channel**



Date: 4.DEC.2017 15:13:14

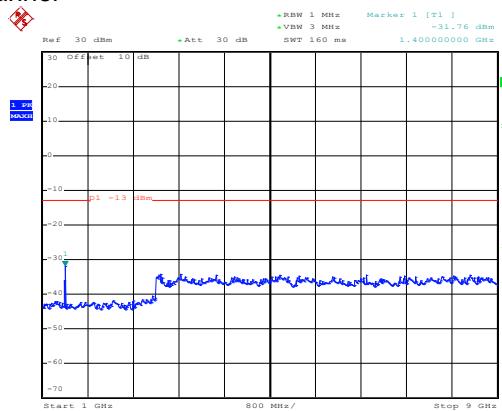
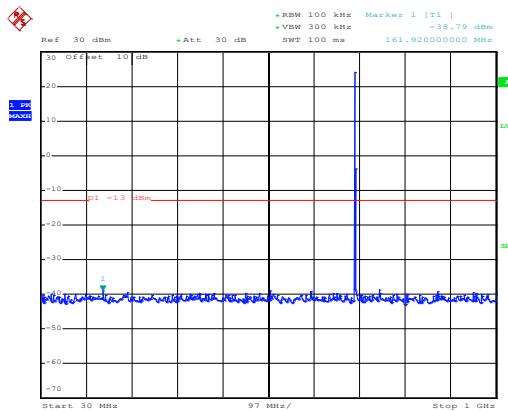
**30MHz~1GHz**

Date: 1.DEC.2017 15:53:46

**1GHz~25GHz**

## LTE band 12, 1.4MHz:

### 16 QAM & RB Size 1 Lowest channel

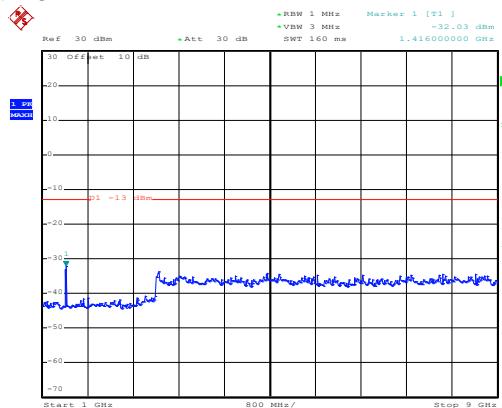
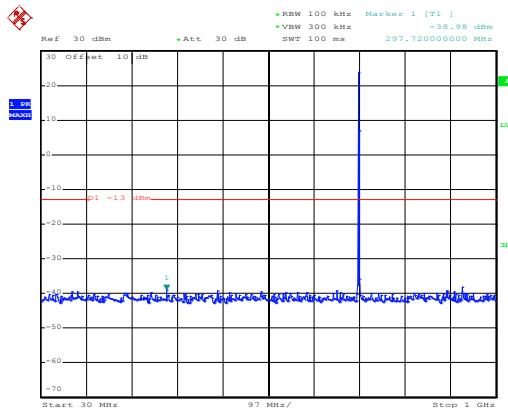


Date: 4.DEC.2017 14:10:37

Date: 1.DEC.2017 16:54:00

### 30MHz~1GHz

### Middle channel

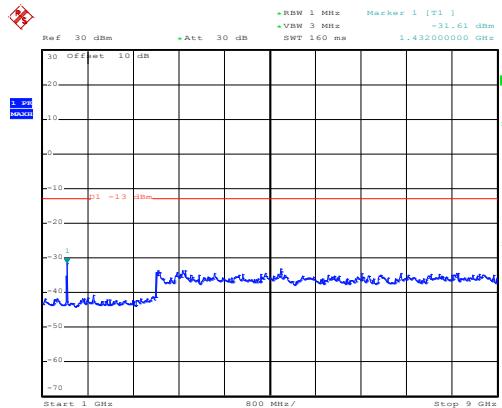
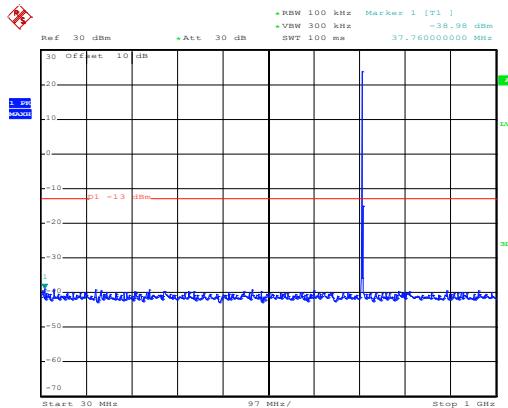


Date: 4.DEC.2017 14:11:30

Date: 1.DEC.2017 16:52:55

### 30MHz~1GHz

### High channel



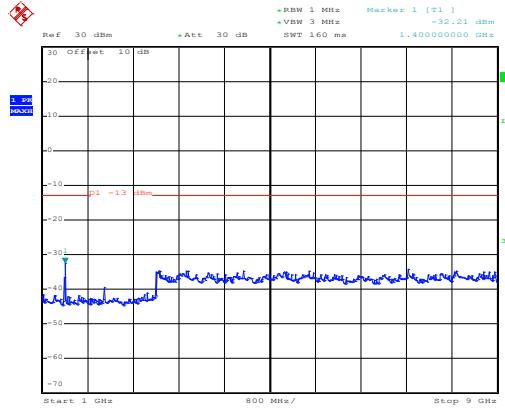
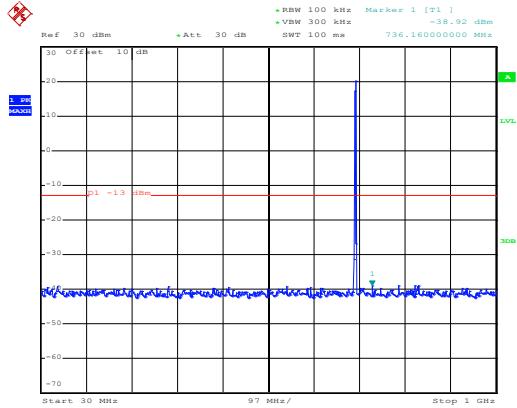
Date: 4.DEC.2017 14:12:31

Date: 1.DEC.2017 16:55:30

### 30MHz~1GHz

### 1GHz~8GHz

## 16 QAM & RB Size 6 Lowest channel



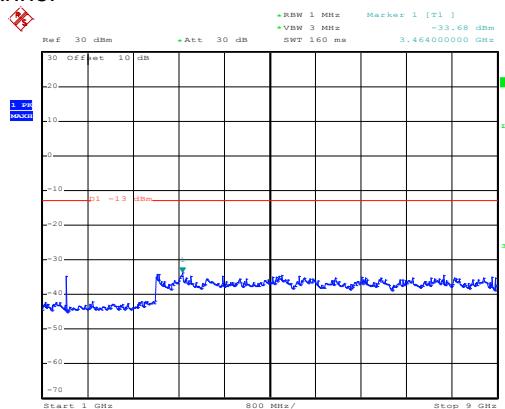
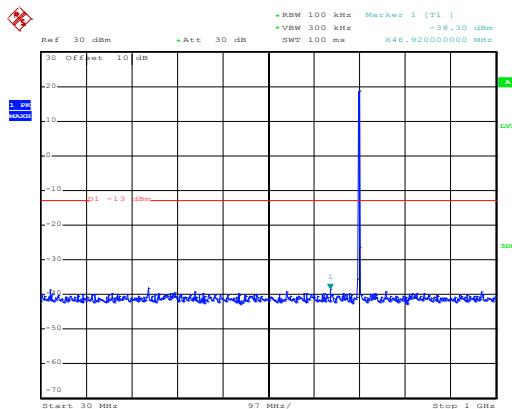
Date: 4.DEC.2017 14:11:02

Date: 1.DEC.2017 16:54:46

30MHz~1GHz

1GHz~8GHz

## Middle channel



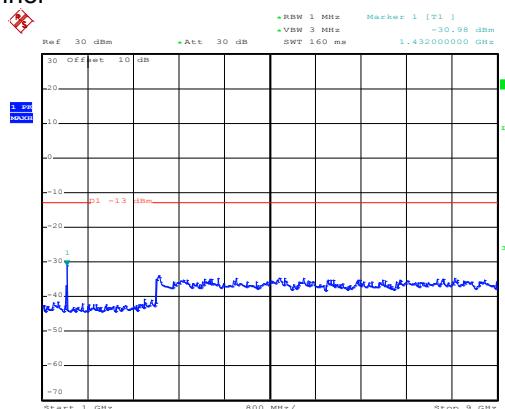
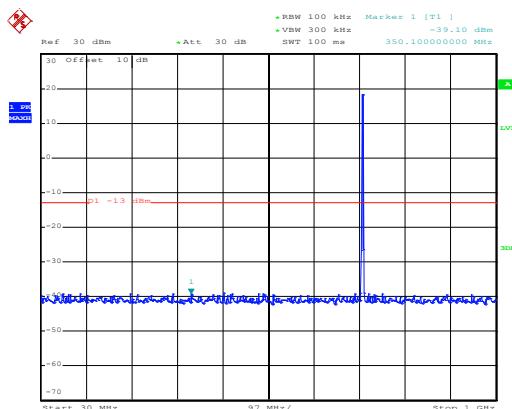
Date: 4.DEC.2017 14:11:54

Date: 1.DEC.2017 16:53:08

30MHz~1GHz

1GHz~8GHz

## High channel



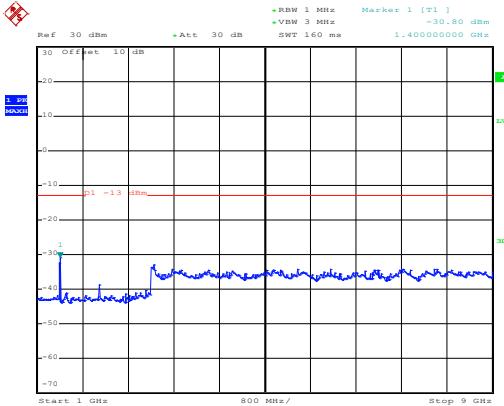
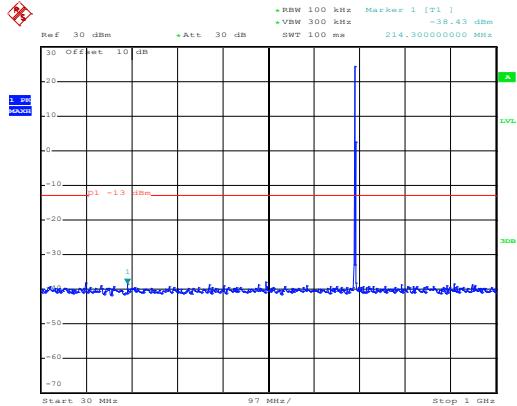
Date: 4.DEC.2017 14:12:55

Date: 1.DEC.2017 16:55:53

30MHz~1GHz

1GHz~8GHz

**QPSK & RB Size 1  
Lowest channel**



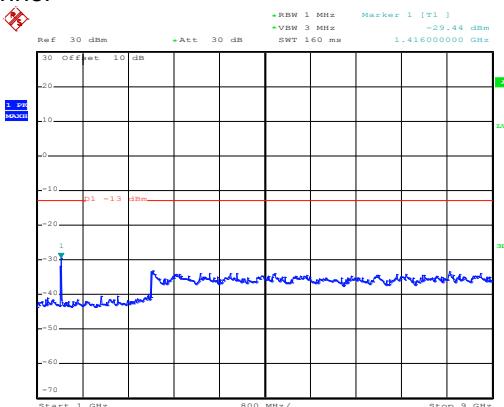
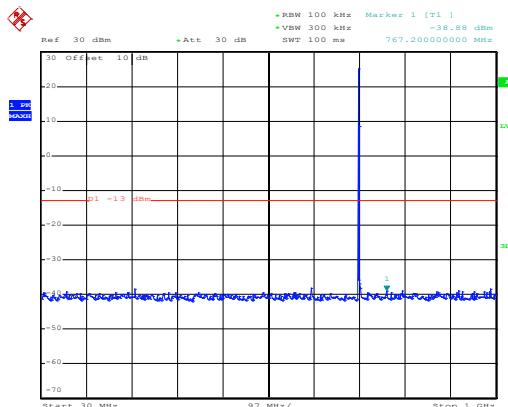
Date: 4.DEC.2017 14:10:21

**30MHz~1GHz**

Date: 1.DEC.2017 16:53:43

**1GHz~8GHz**

**Middle channel**



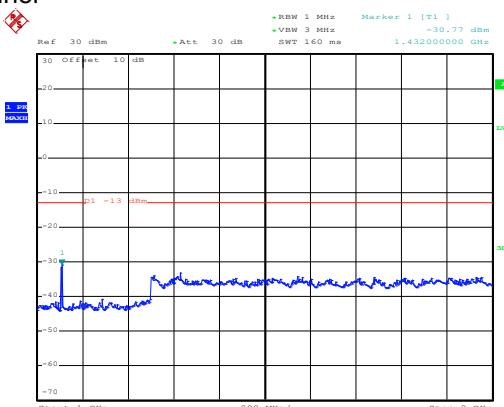
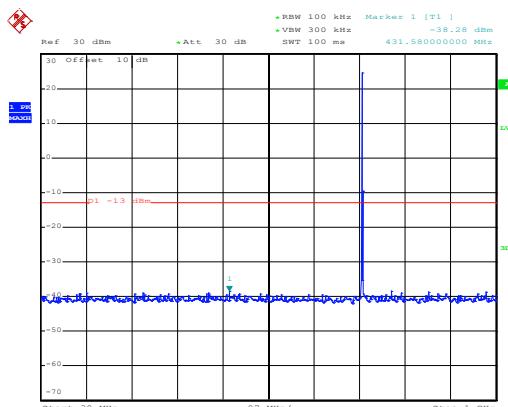
Date: 4.DEC.2017 14:11:22

**30MHz~1GHz**

Date: 1.DEC.2017 16:52:49

**1GHz~8GHz**

**High channel**



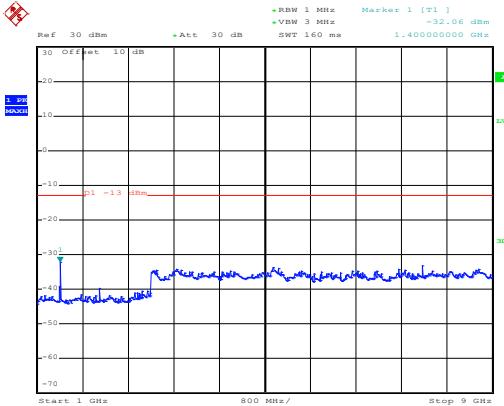
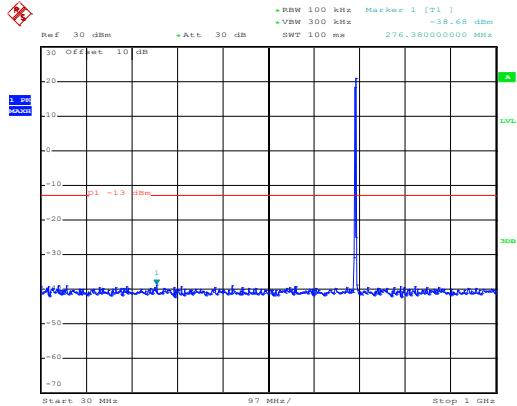
Date: 4.DEC.2017 14:12:21

**30MHz~1GHz**

Date: 1.DEC.2017 16:55:12

**1GHz~8GHz**

**QPSK & RB Size 6  
Lowest channel**



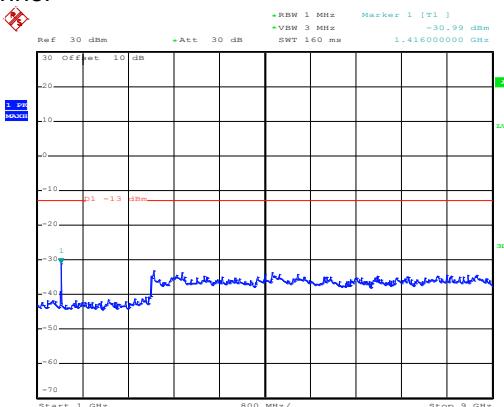
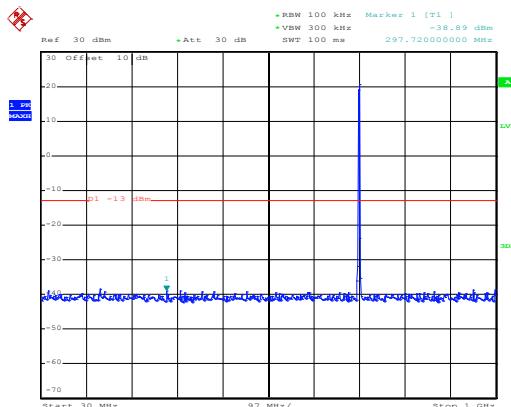
Date: 4.DEC.2017 14:10:53

**30MHz~1GHz**

Date: 1.DEC.2017 16:54:40

**1GHz~8GHz**

**Middle channel**



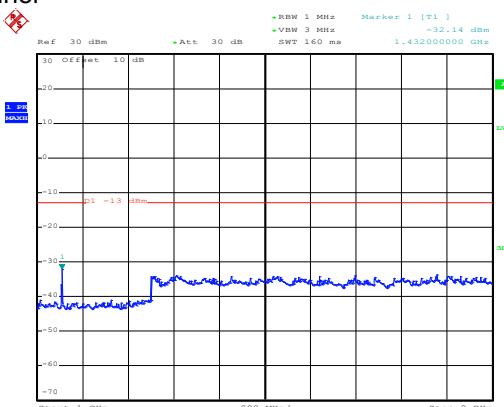
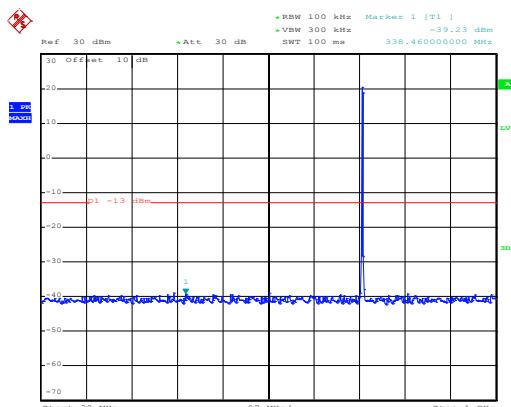
Date: 4.DEC.2017 14:11:41

**30MHz~1GHz**

Date: 1.DEC.2017 16:53:04

**1GHz~8GHz**

**High channel**



Date: 4.DEC.2017 14:12:44

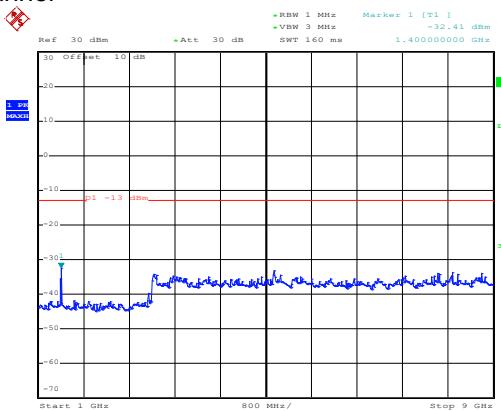
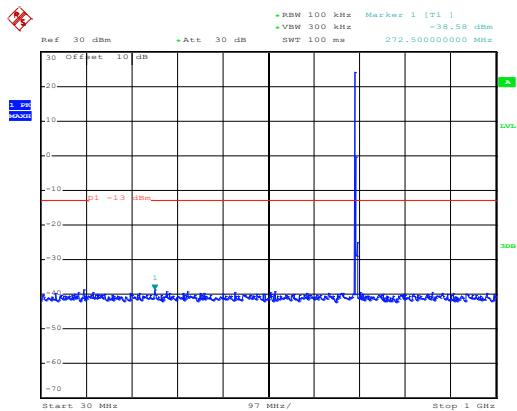
**30MHz~1GHz**

Date: 1.DEC.2017 16:55:47

**1GHz~8GHz**

## 3MHz

### 16 QAM & RB Size 1 Lowest channel

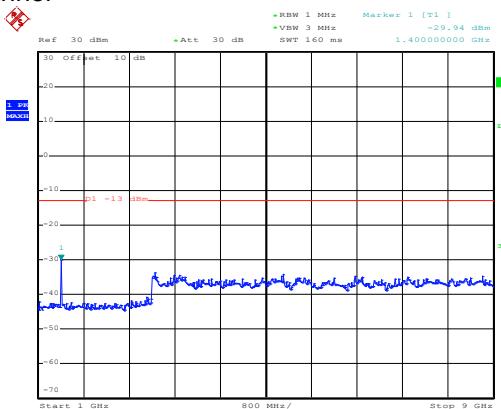
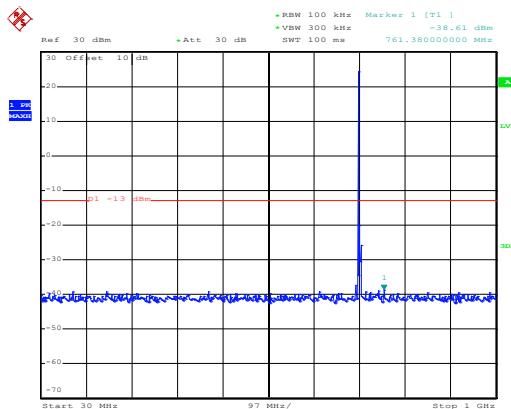


Date: 4.DEC.2017 14:13:52

Date: 1.DEC.2017 16:57:29

### 30MHz~1GHz

### Middle channel



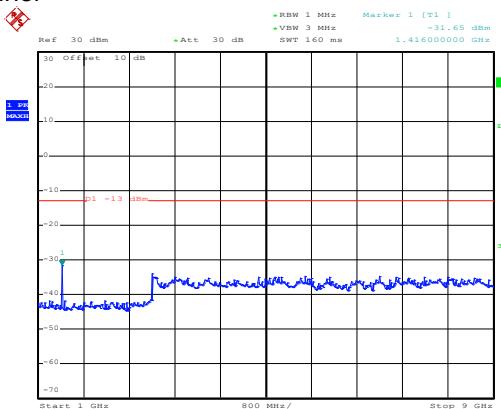
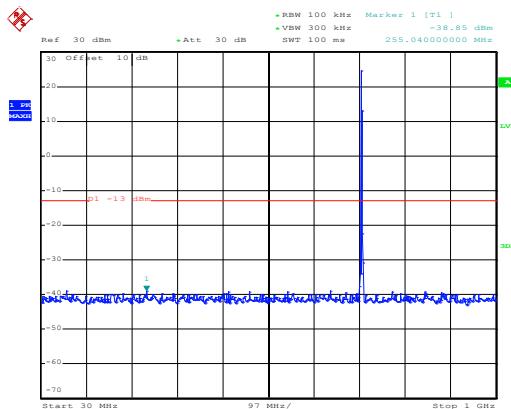
Date: 4.DEC.2017 14:16:01

Date: 1.DEC.2017 17:00:28

### 30MHz~1GHz

### 1GHz~8GHz

### High channel



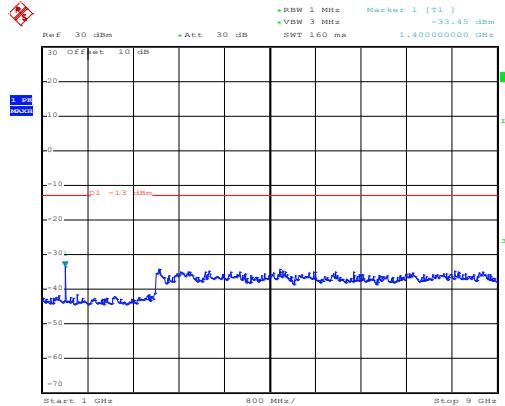
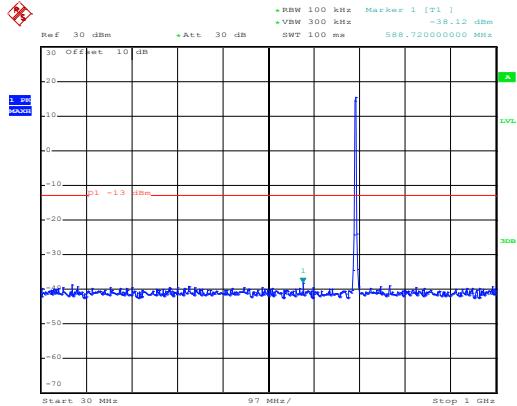
Date: 4.DEC.2017 14:16:52

Date: 1.DEC.2017 17:01:16

### 30MHz~1GHz

### 1GHz~8GHz

## 16 QAM & RB Size 15 Lowest channel



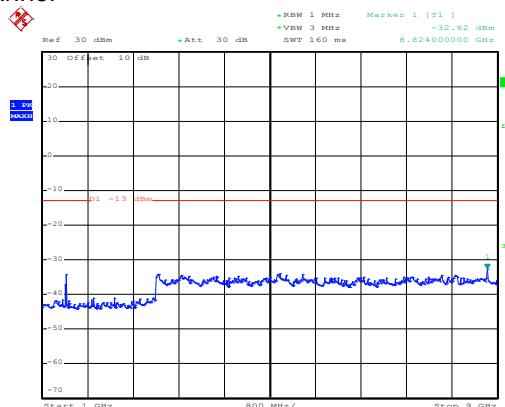
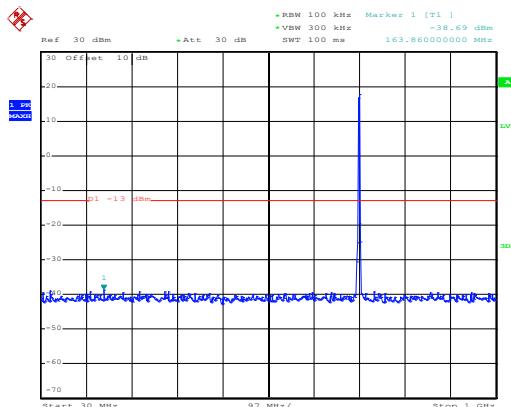
Date: 4.DEC.2017 14:14:15

## 30MHz~1GHz

Date: 1.DEC.2017 16:57:48

## 1GHz~8GHz

## Middle channel



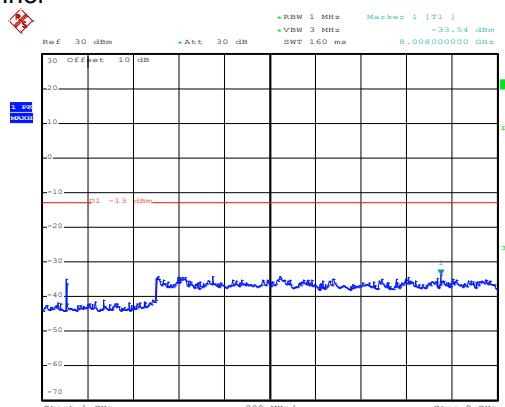
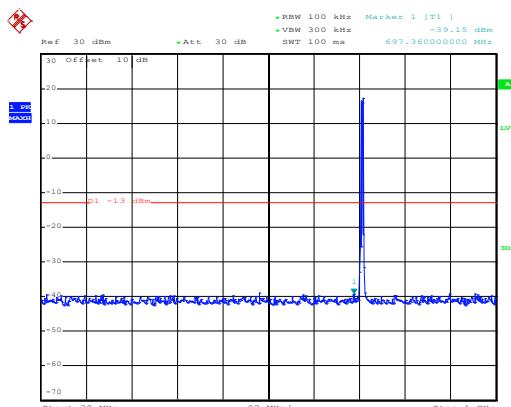
Date: 4.DEC.2017 14:16:22

## 30MHz~1GHz

Date: 1.DEC.2017 17:00:46

## 1GHz~8GHz

## High channel



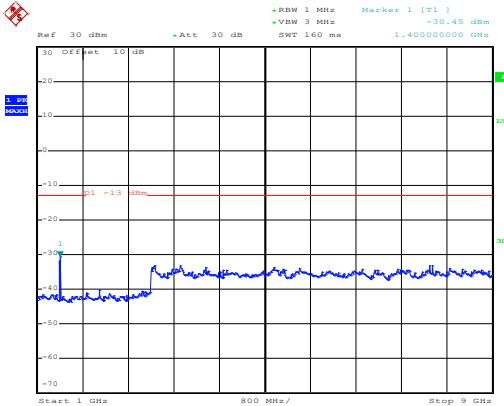
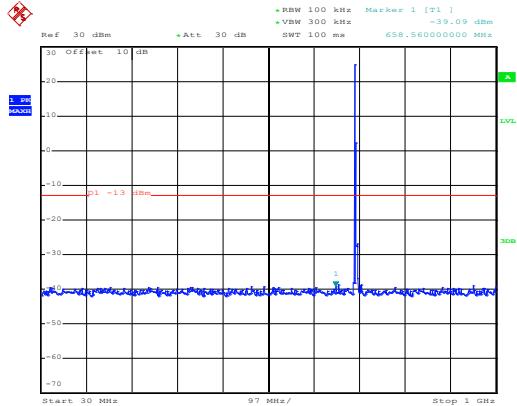
Date: 4.DEC.2017 14:17:12

## 30MHz~1GHz

Date: 1.DEC.2017 17:01:41

## 1GHz~8GHz

**QPSK & RB Size 1  
Lowest channel**



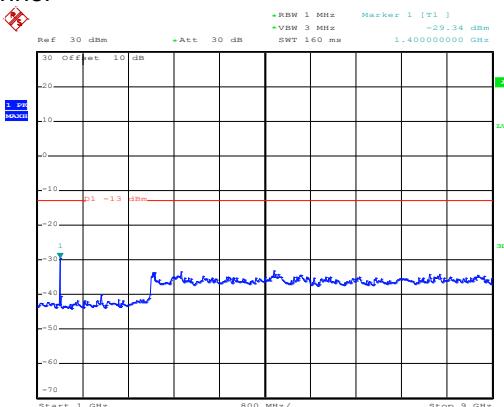
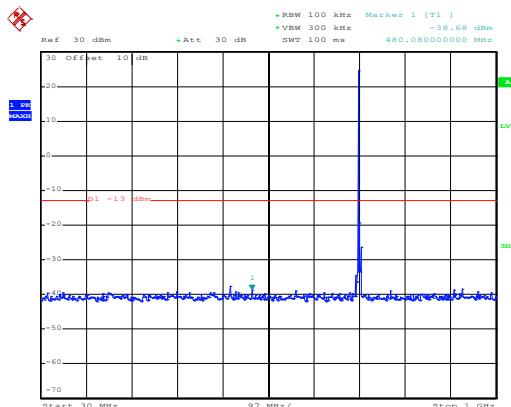
Date: 4.DEC.2017 14:13:39

**30MHz~1GHz**

Date: 1.DEC.2017 16:57:11

**1GHz~8GHz**

**Middle channel**



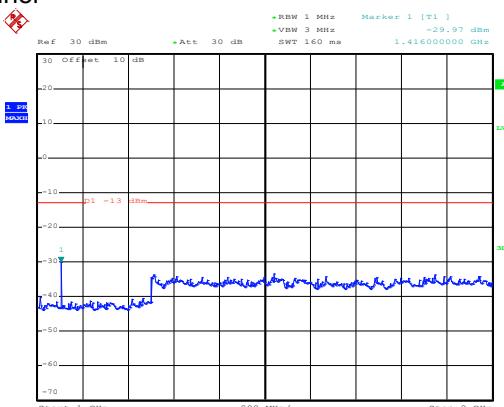
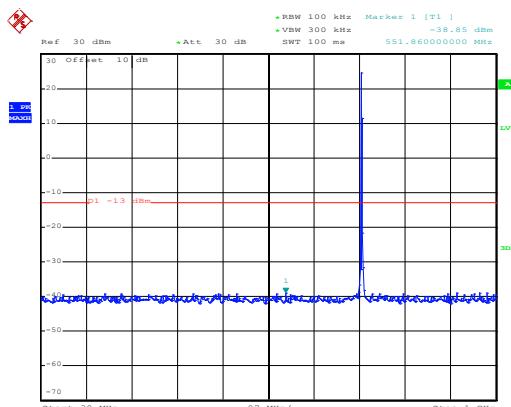
Date: 4.DEC.2017 14:15:50

**30MHz~1GHz**

Date: 1.DEC.2017 17:00:21

**1GHz~8GHz**

**High channel**



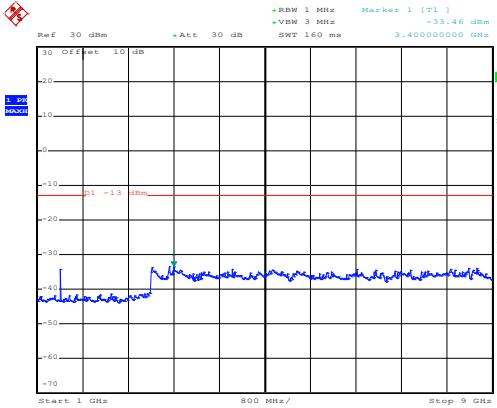
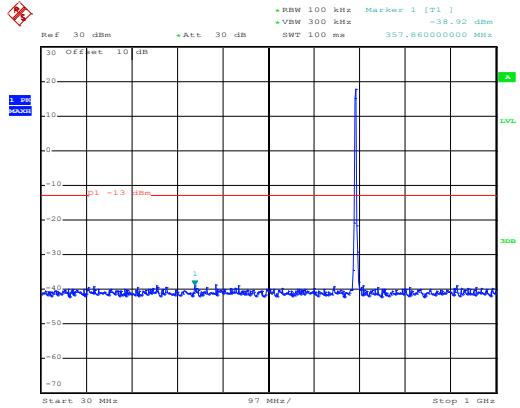
Date: 4.DEC.2017 14:16:42

**30MHz~1GHz**

Date: 1.DEC.2017 17:01:08

**1GHz~8GHz**

**QPSK & RB Size 15  
Lowest channel**



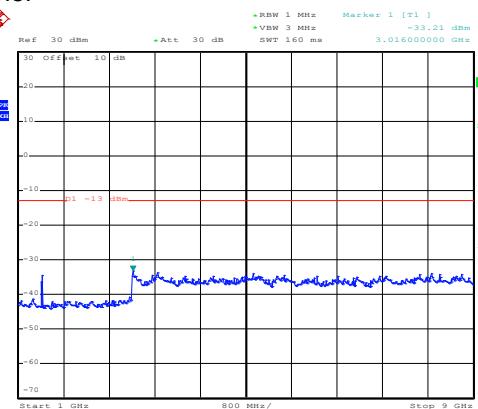
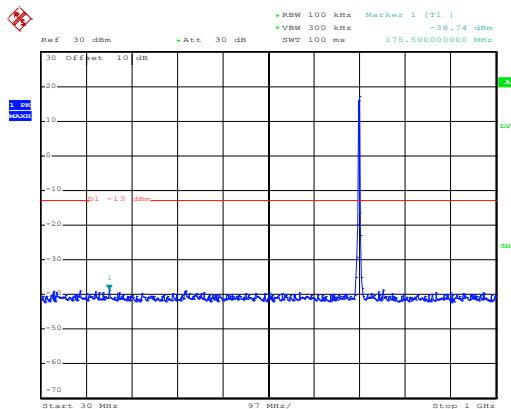
Date: 4.DEC.2017 14:14:03

Date: 1.DEC.2017 16:57:43

**30MHz~1GHz**

**1GHz~8GHz**

**Middle channel**



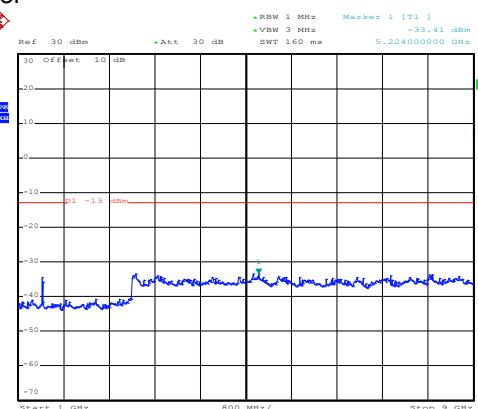
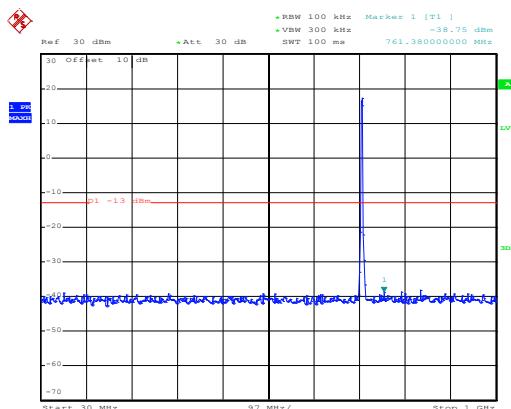
Date: 4.DEC.2017 14:16:13

Date: 1.DEC.2017 17:00:38

**30MHz~1GHz**

**1GHz~8GHz**

**High channel**



Date: 4.DEC.2017 14:17:04

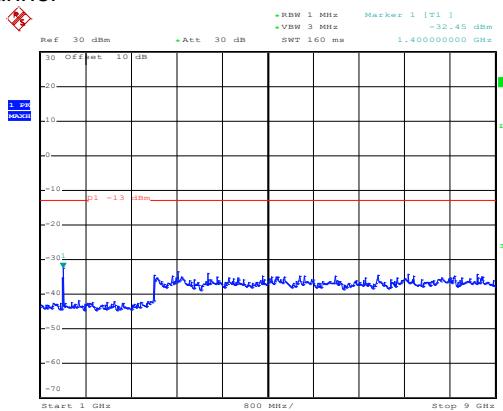
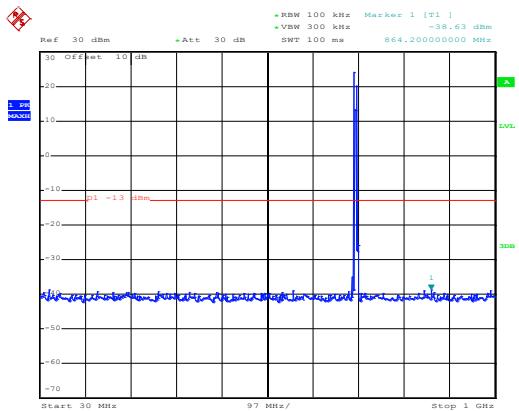
Date: 1.DEC.2017 17:01:32

**30MHz~1GHz**

**1GHz~8GHz**

## 5MHz

### 16 QAM & RB Size 1 Lowest channel

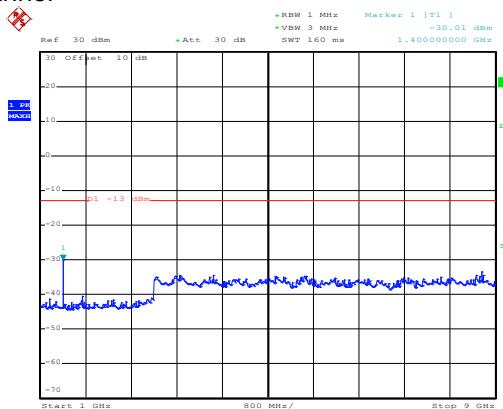
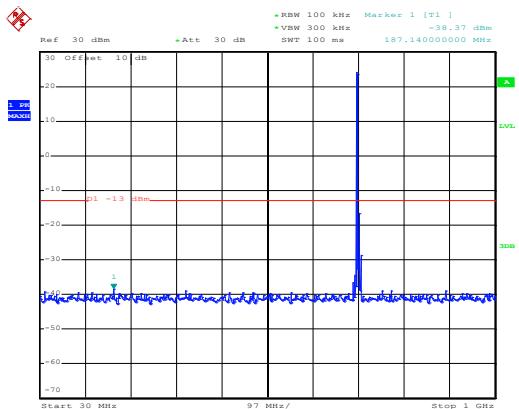


Date: 4.DEC.2017 14:18:08

Date: 1.DEC.2017 17:04:57

### 30MHz~1GHz

### Middle channel



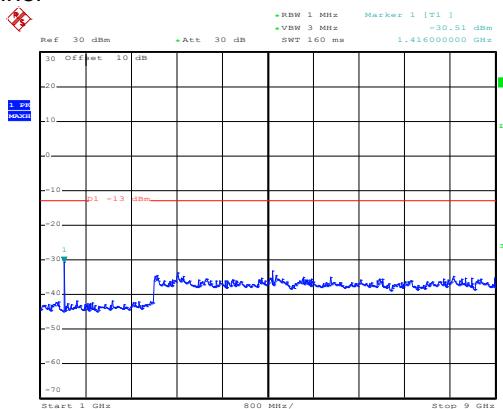
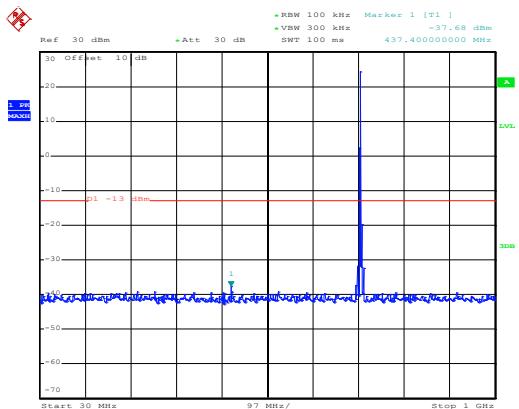
Date: 4.DEC.2017 14:19:09

Date: 1.DEC.2017 17:07:00

### 30MHz~1GHz

### 1GHz~8GHz

### High channel



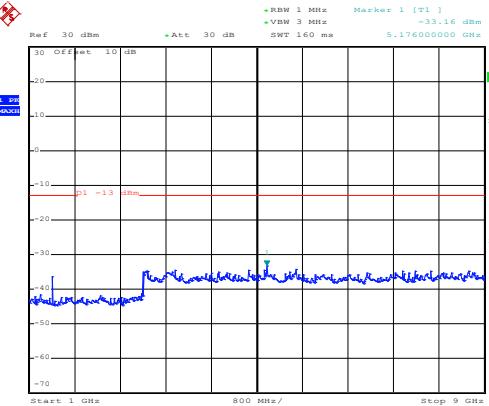
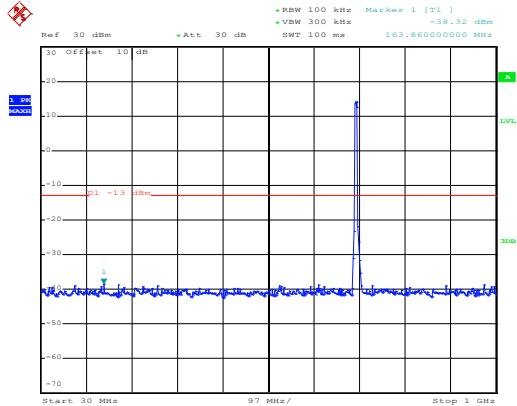
Date: 4.DEC.2017 14:20:03

Date: 1.DEC.2017 17:07:32

### 30MHz~1GHz

### 1GHz~8GHz

16 QAM & RB Size 25  
Lowest channel



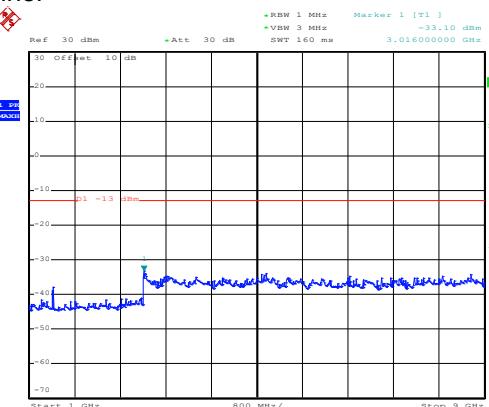
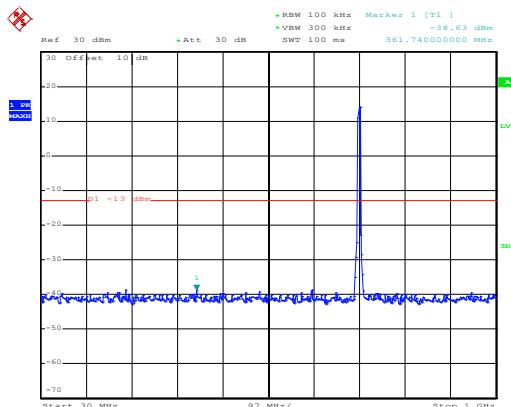
Date: 4.DEC.2017 14:18:34

Date: 1.DEC.2017 17:05:10

30MHz~1GHz

1GHz~8GHz

Middle channel



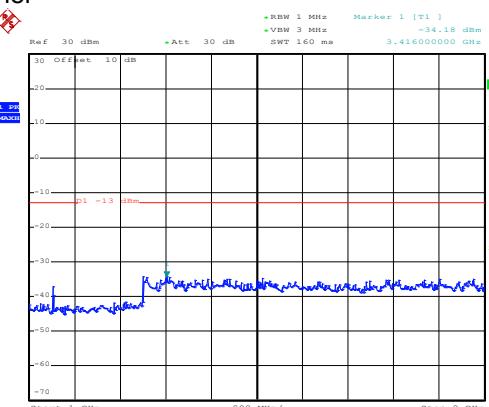
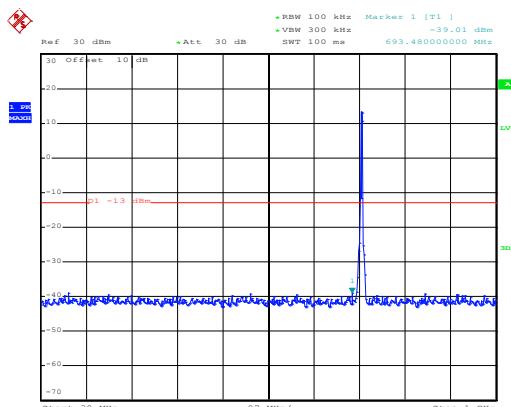
Date: 4.DEC.2017 14:19:35

Date: 1.DEC.2017 17:07:14

30MHz~1GHz

1GHz~8GHz

High channel



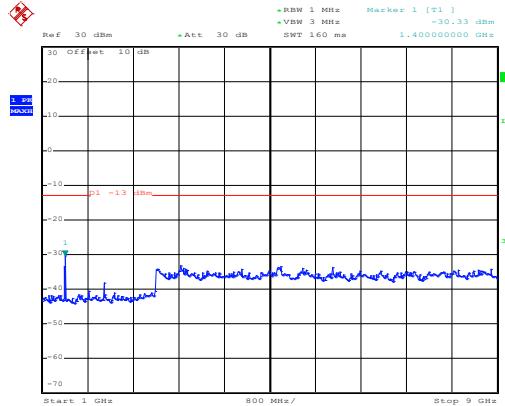
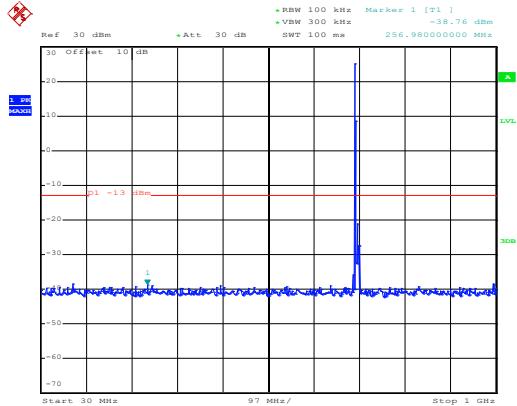
Date: 4.DEC.2017 14:20:22

Date: 1.DEC.2017 17:07:43

30MHz~1GHz

1GHz~8GHz

**QPSK & RB Size 1  
Lowest channel**



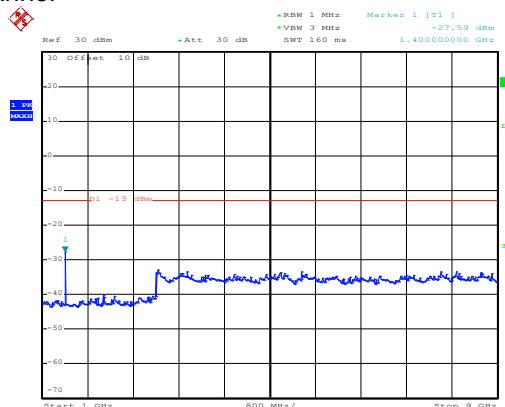
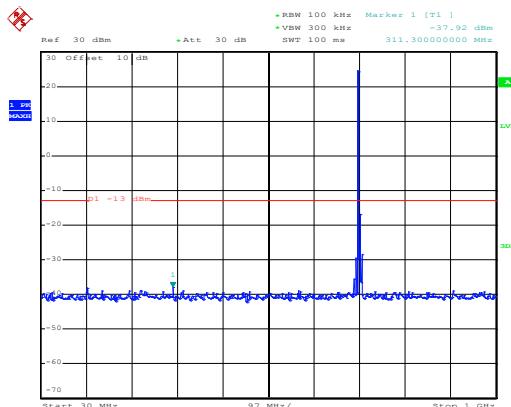
Date: 4.DEC.2017 14:17:55

**30MHz~1GHz**

Date: 1.DEC.2017 17:04:52

**1GHz~8GHz**

**Middle channel**



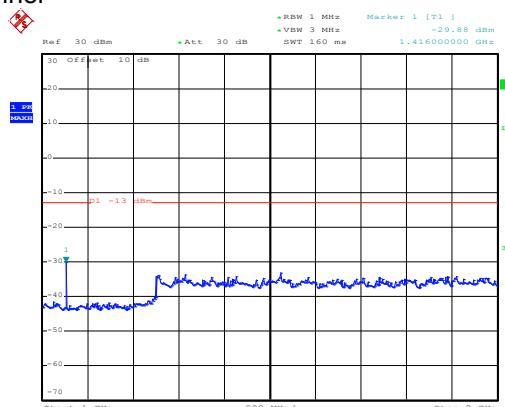
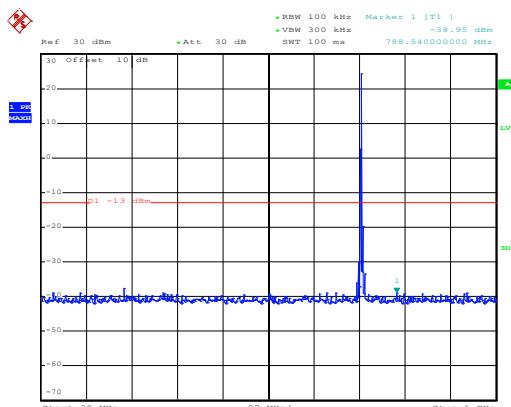
Date: 4.DEC.2017 14:18:59

**30MHz~1GHz**

Date: 1.DEC.2017 17:06:54

**1GHz~8GHz**

**High channel**



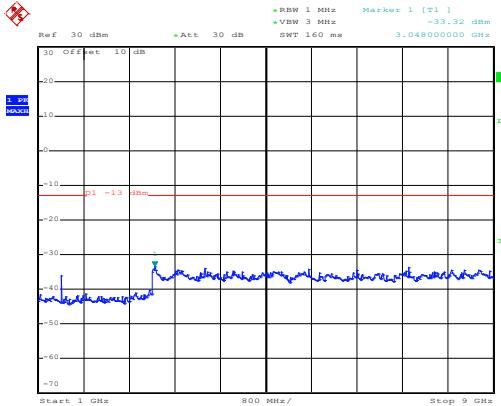
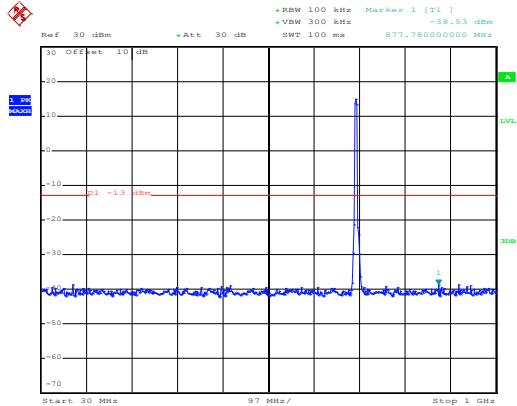
Date: 4.DEC.2017 14:19:53

**30MHz~1GHz**

Date: 1.DEC.2017 17:07:27

**1GHz~8GHz**

**QPSK & RB Size 25  
Lowest channel**



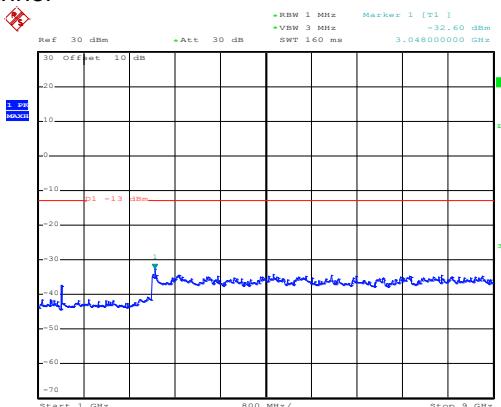
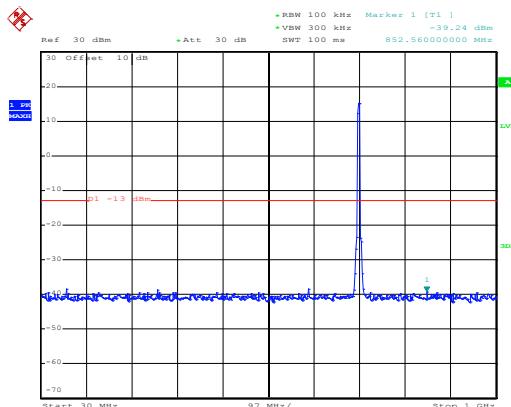
Date: 4.DEC.2017 14:18:21

**30MHz~1GHz**

Date: 1.DEC.2017 17:05:04

**1GHz~8GHz**

**Middle channel**



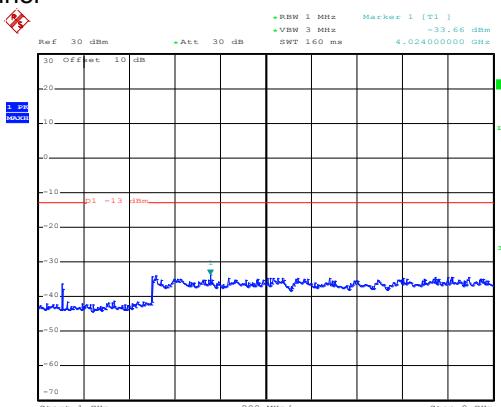
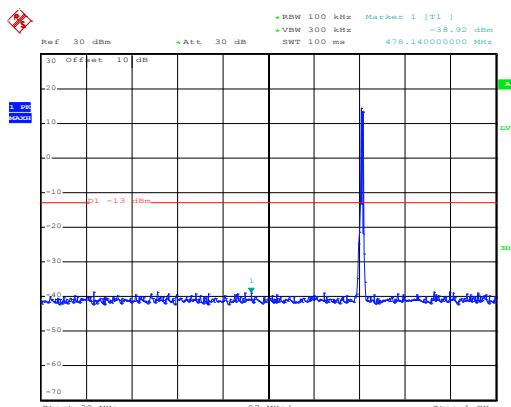
Date: 4.DEC.2017 14:19:25

**30MHz~1GHz**

Date: 1.DEC.2017 17:07:09

**1GHz~8GHz**

**High channel**



Date: 4.DEC.2017 14:20:15

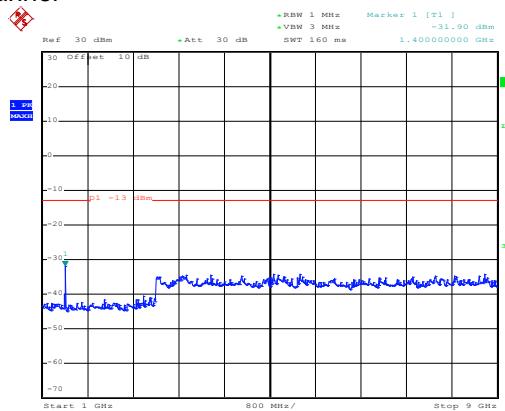
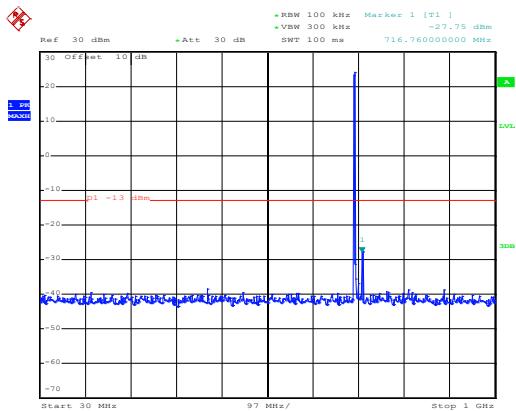
**30MHz~1GHz**

Date: 1.DEC.2017 17:07:39

**1GHz~8GHz**

## 10MHz

### 16 QAM & RB Size 1 Lowest channel

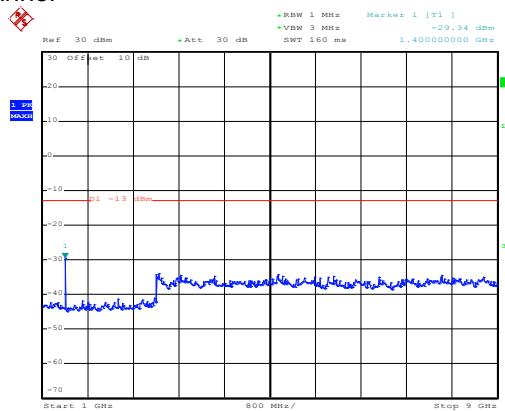
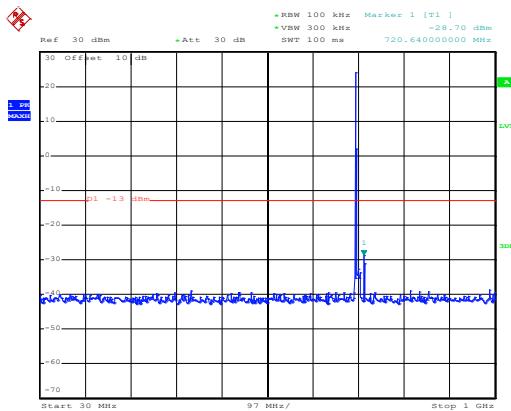


Date: 4.DEC.2017 14:21:12

Date: 1.DEC.2017 17:08:26

### 30MHz~1GHz

### Middle channel



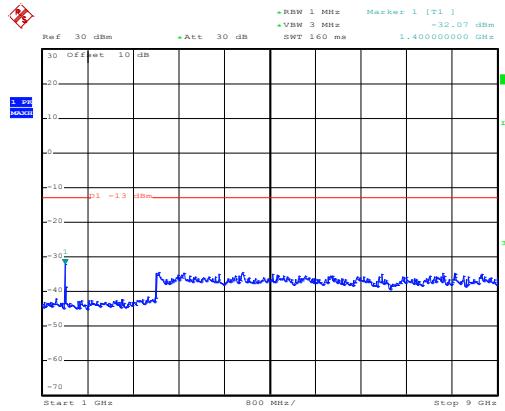
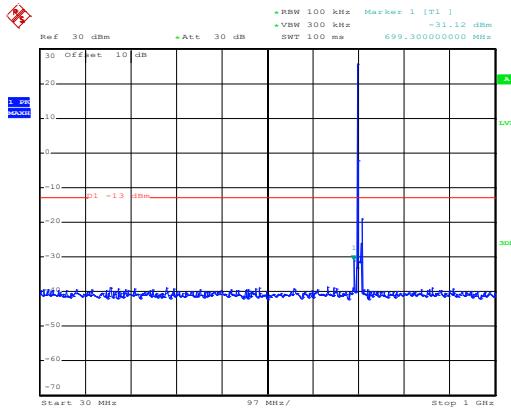
Date: 4.DEC.2017 14:22:06

Date: 1.DEC.2017 17:08:57

### 30MHz~1GHz

### 1GHz~8GHz

### High channel



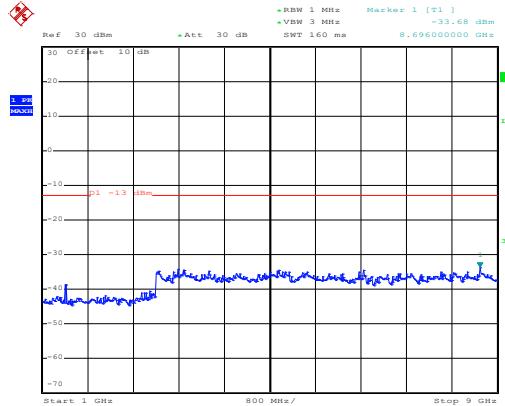
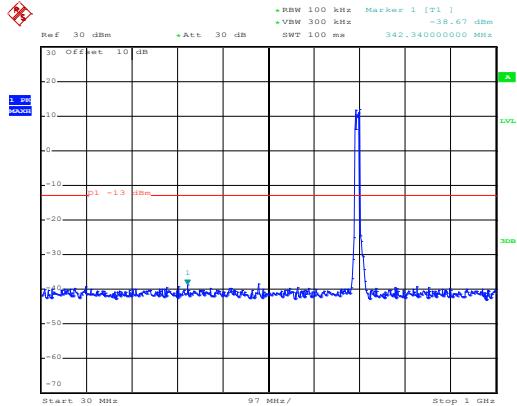
Date: 4.DEC.2017 14:23:03

Date: 1.DEC.2017 17:09:48

### 30MHz~1GHz

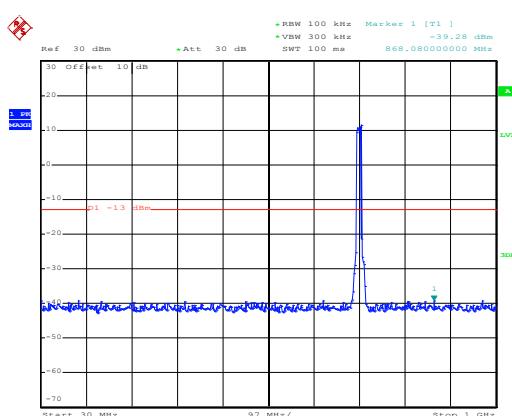
### 1GHz~8GHz

## 16 QAM & RB Size 50 Lowest channel

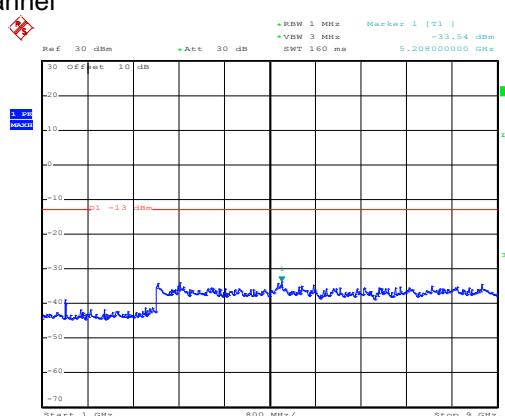


Date: 4.DEC.2017 14:21:41

## 30MHz~1GHz

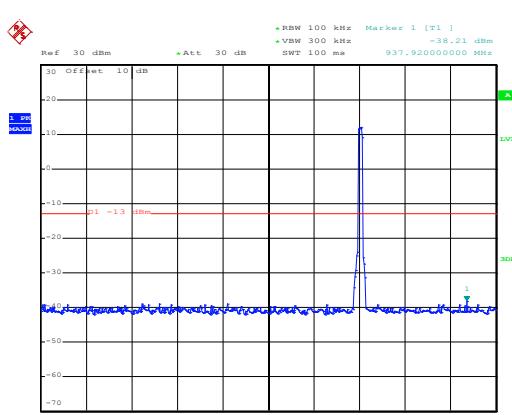


## Middle channel

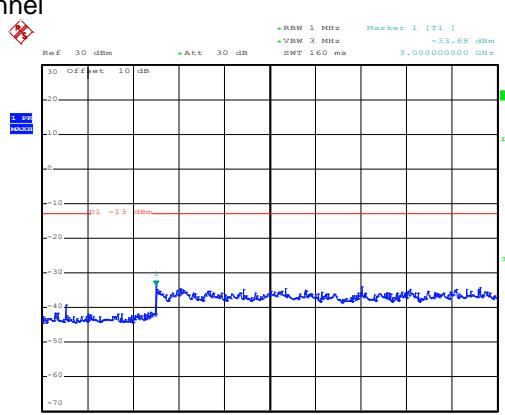


Date: 4.DEC.2017 14:22:30

## 30MHz~1GHz



## High channel



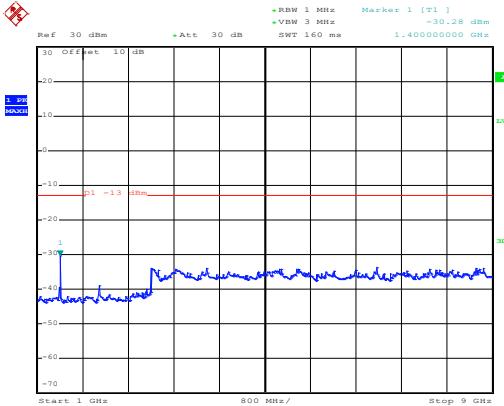
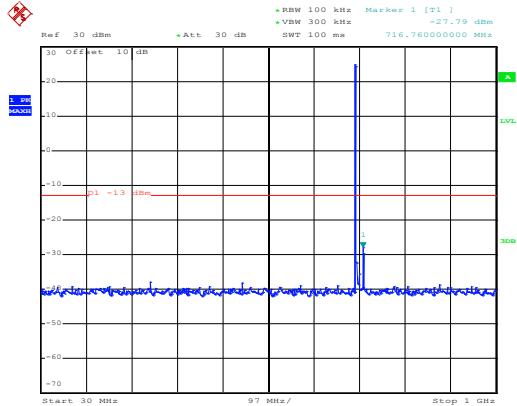
Date: 4.DEC.2017 14:23:38

## 30MHz~1GHz

Date: 1.DEC.2017 17:10:01

## 1GHz~8GHz

## QPSK & RB Size 1 Lowest channel



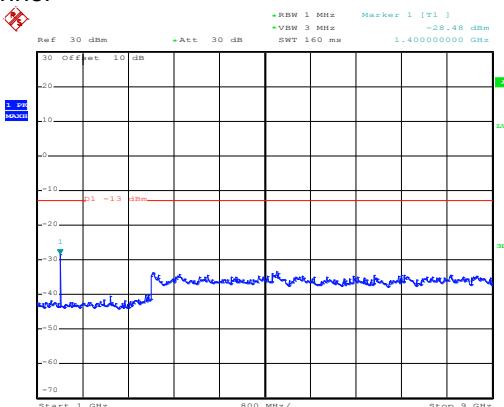
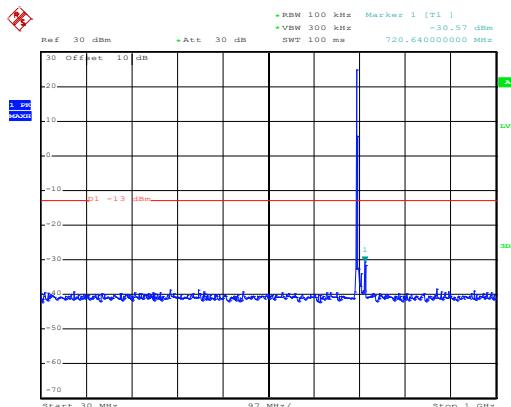
Date: 4.DEC.2017 14:21:06

30MHz~1GHz

Date: 1.DEC.2017 17:08:21

1GHz~8GHz

## Middle channel



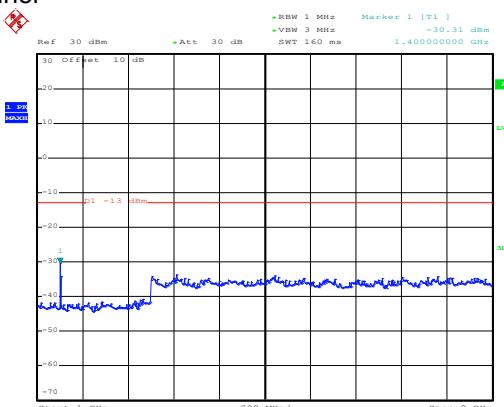
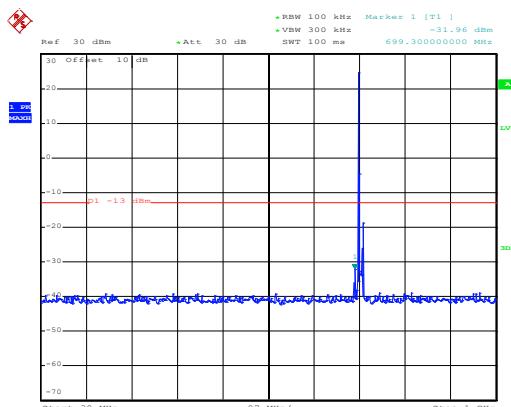
Date: 4.DEC.2017 14:21:59

30MHz~1GHz

Date: 1.DEC.2017 17:08:52

1GHz~8GHz

## High channel



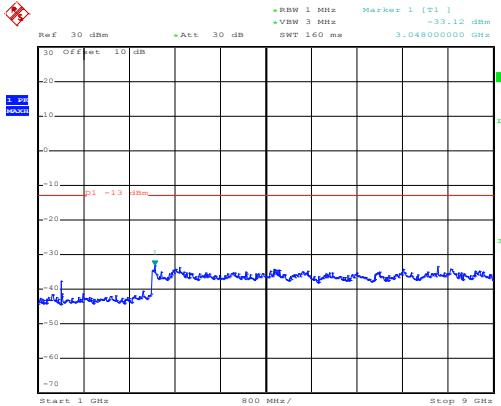
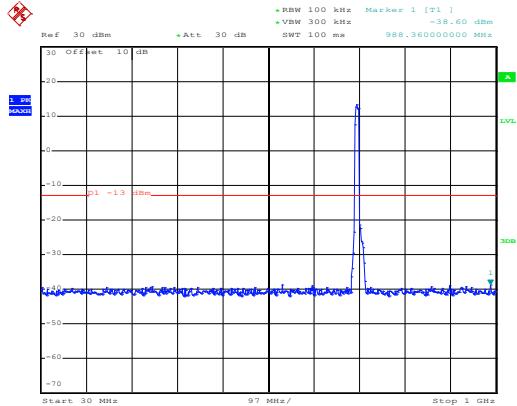
Date: 4.DEC.2017 14:22:48

30MHz~1GHz

Date: 1.DEC.2017 17:09:42

1GHz~8GHz

**QPSK & RB Size 50  
Lowest channel**



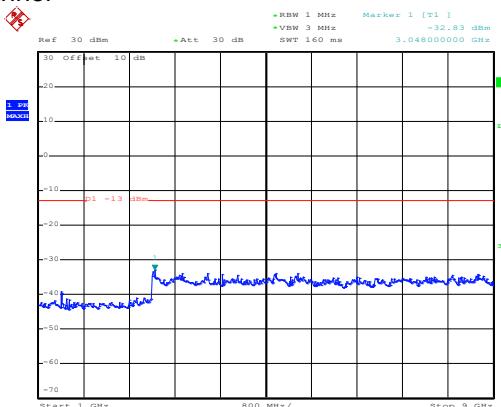
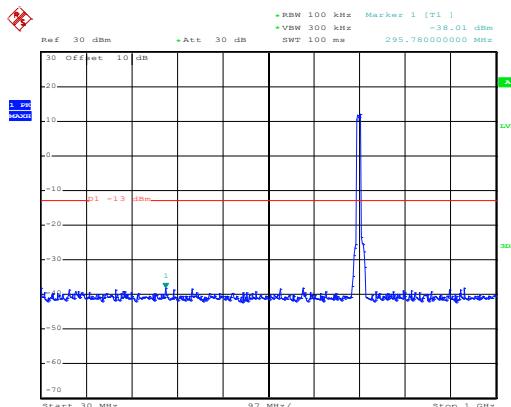
Date: 4.DEC.2017 14:21:31

**30MHz~1GHz**

Date: 1.DEC.2017 17:08:33

**1GHz~8GHz**

**Middle channel**



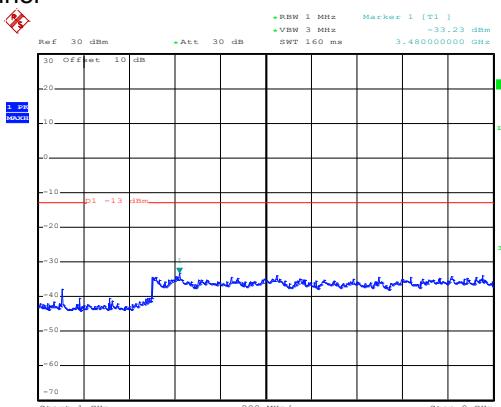
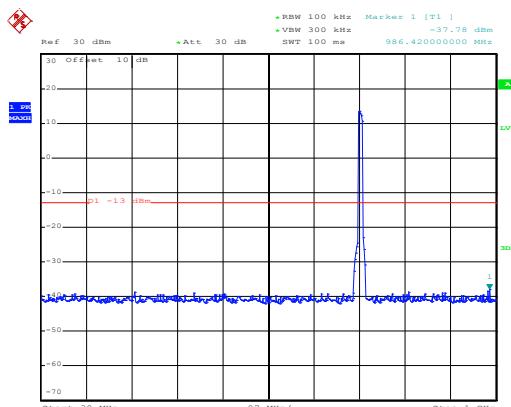
Date: 4.DEC.2017 14:22:20

**30MHz~1GHz**

Date: 1.DEC.2017 17:09:25

**1GHz~8GHz**

**High channel**



Date: 4.DEC.2017 14:23:21

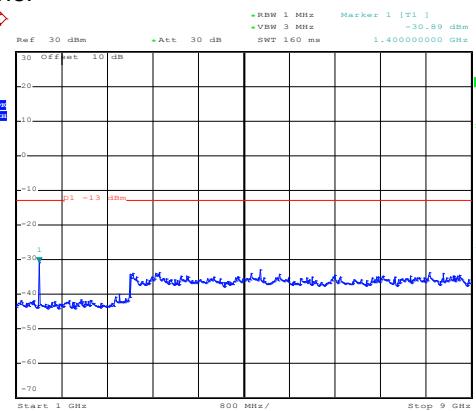
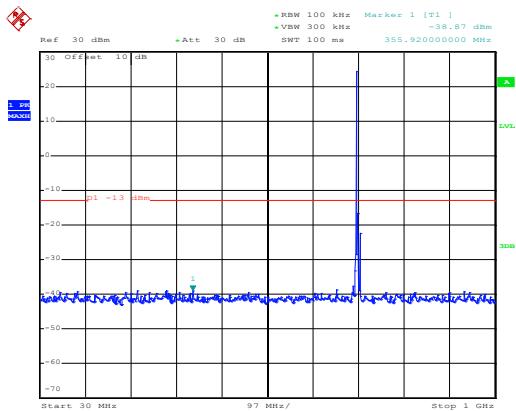
**30MHz~1GHz**

Date: 1.DEC.2017 17:09:56

**1GHz~8GHz**

LTE band 17, 5MHz:

16 QAM & RB Size 1  
Lowest channel



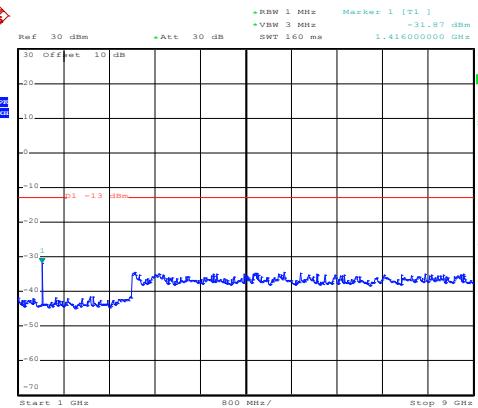
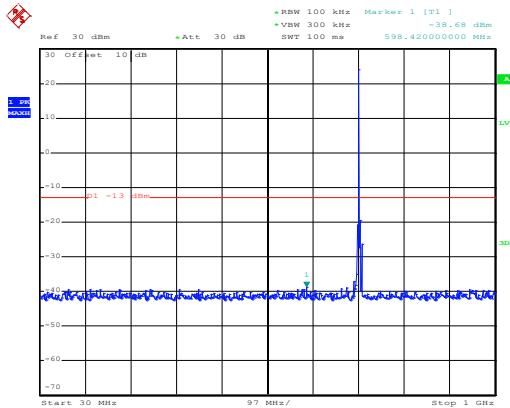
Date: 4.DEC.2017 14:24:46

30MHz~1GHz

Date: 1.DEC.2017 17:10:54

1GHz~8GHz

Middle channel



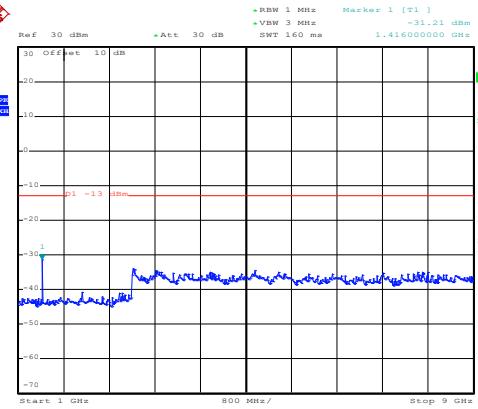
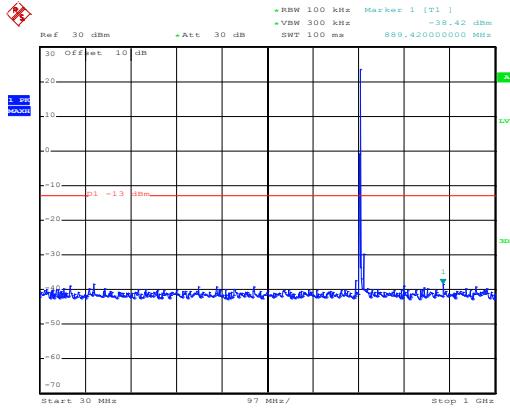
Date: 4.DEC.2017 14:25:38

30MHz~1GHz

Date: 1.DEC.2017 17:11:39

1GHz~8GHz

High channel



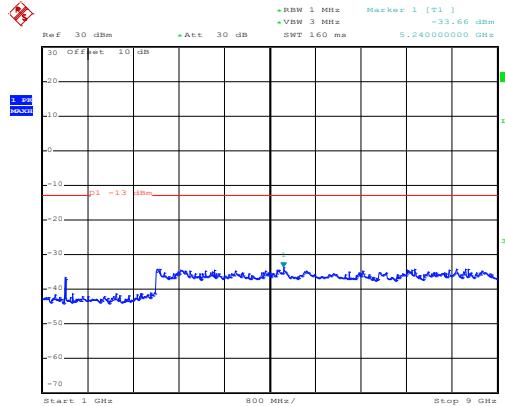
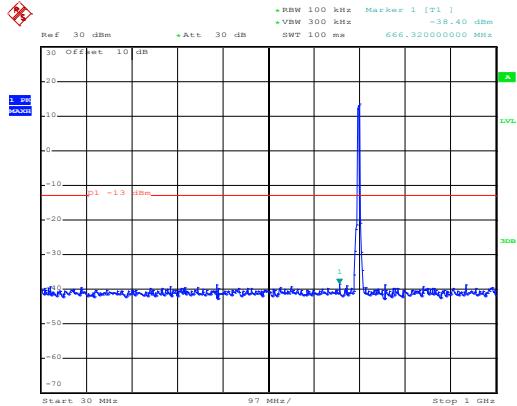
Date: 4.DEC.2017 14:26:21

30MHz~1GHz

Date: 1.DEC.2017 17:12:19

1GHz~8GHz

16 QAM & RB Size 25  
Lowest channel



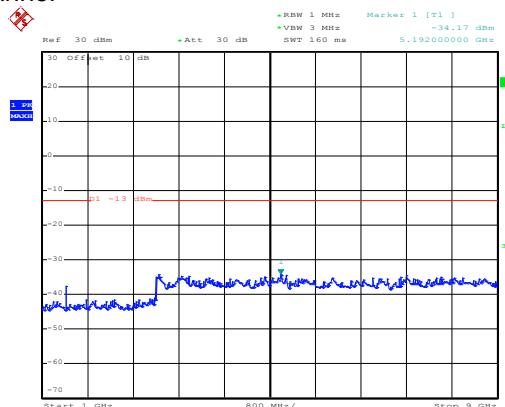
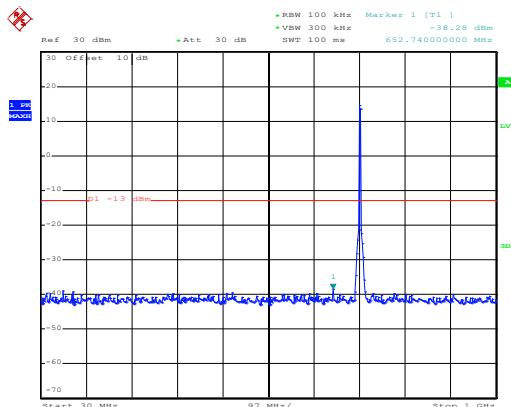
Date: 4.DEC.2017 14:25:08

30MHz~1GHz

Date: 1.DEC.2017 17:11:12

1GHz~8GHz

Middle channel



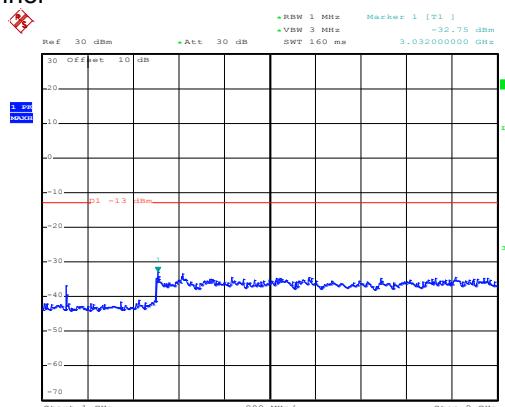
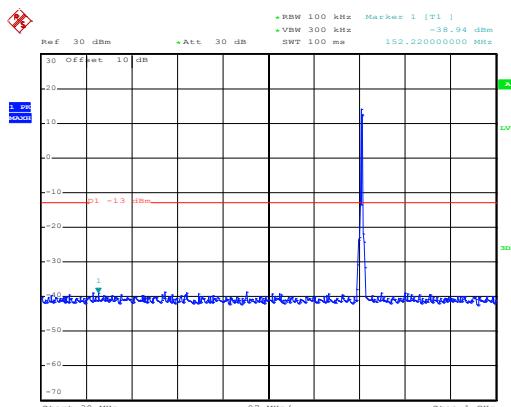
Date: 4.DEC.2017 14:25:56

30MHz~1GHz

Date: 1.DEC.2017 17:11:57

1GHz~8GHz

High channel



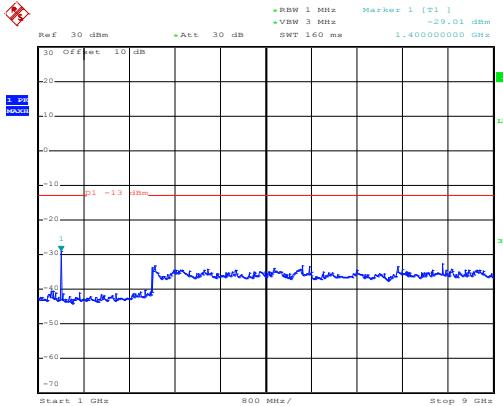
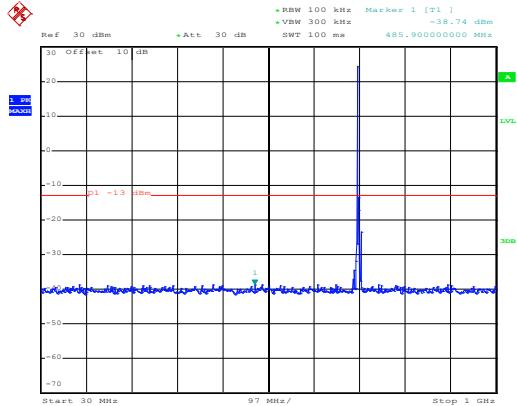
Date: 4.DEC.2017 14:26:50

30MHz~1GHz

Date: 1.DEC.2017 17:12:40

1GHz~8GHz

**QPSK & RB Size 1  
Lowest channel**



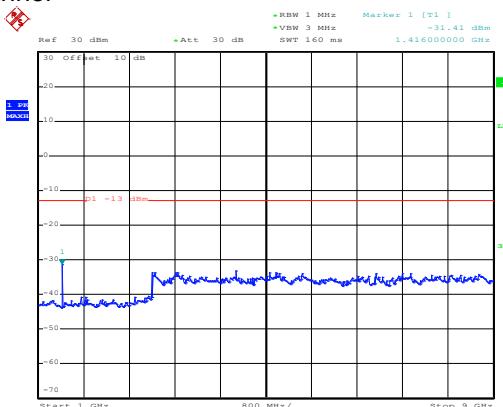
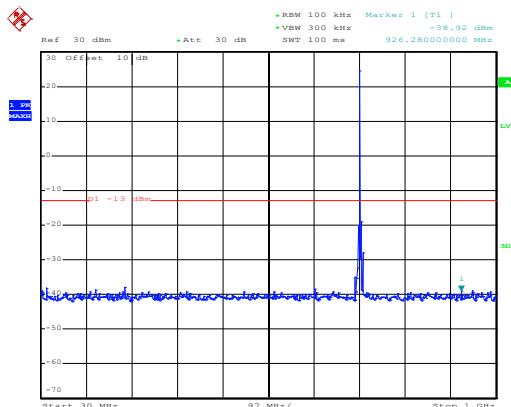
Date: 4.DEC.2017 14:24:37

**30MHz~1GHz**

Date: 1.DEC.2017 17:10:43

**1GHz~8GHz**

**Middle channel**



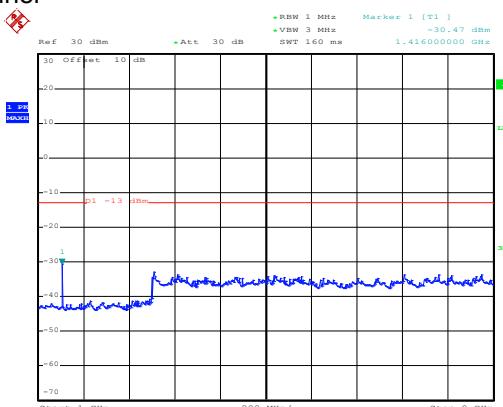
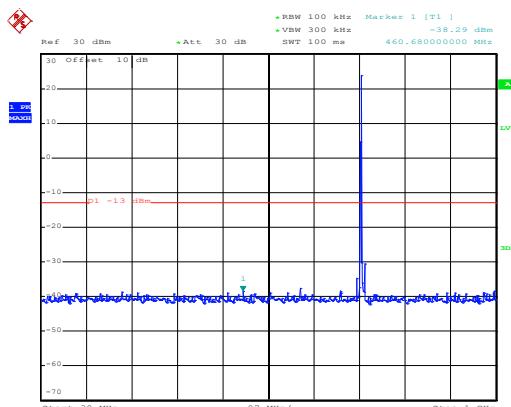
Date: 4.DEC.2017 14:25:30

**30MHz~1GHz**

Date: 1.DEC.2017 17:11:34

**1GHz~8GHz**

**High channel**



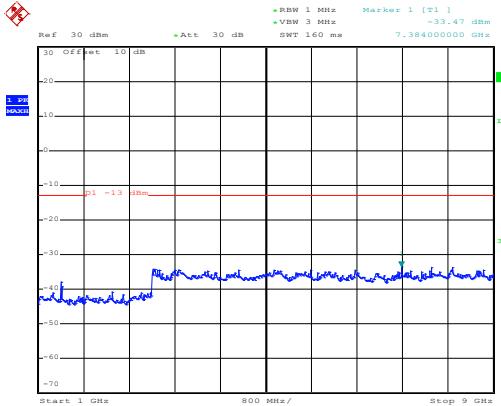
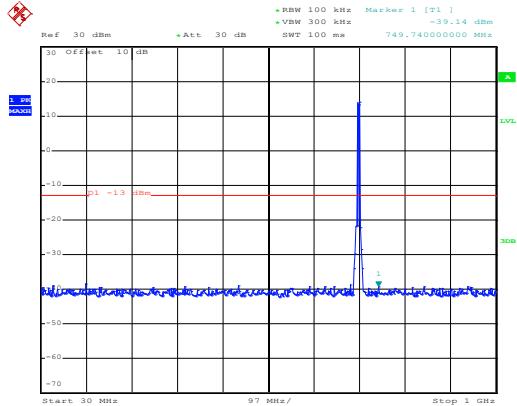
Date: 4.DEC.2017 14:26:13

**30MHz~1GHz**

Date: 1.DEC.2017 17:12:13

**1GHz~8GHz**

**QPSK & RB Size 25  
Lowest channel**



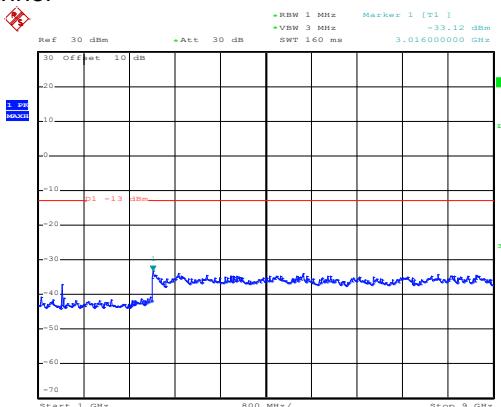
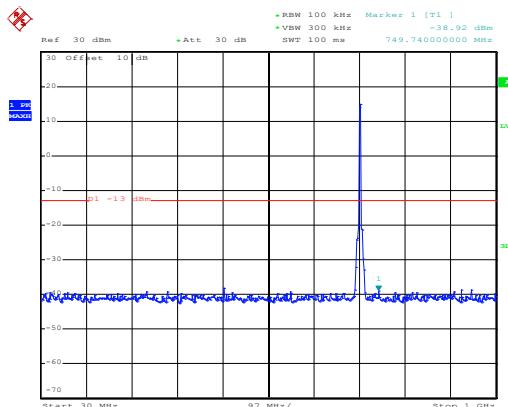
Date: 4.DEC.2017 14:24:57

**30MHz~1GHz**

Date: 1.DEC.2017 17:11:02

**1GHz~8GHz**

**Middle channel**



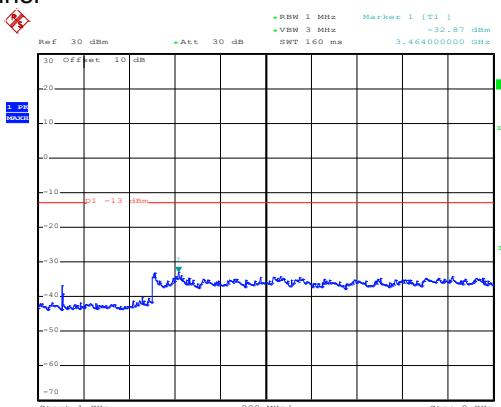
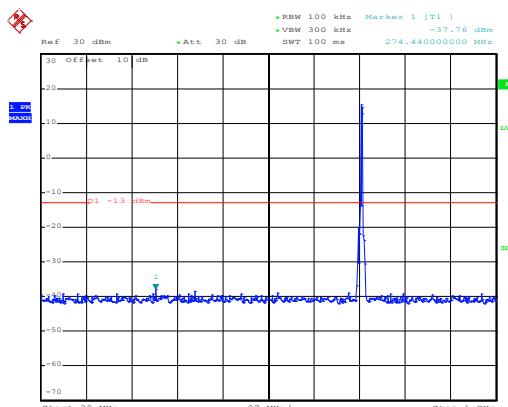
Date: 4.DEC.2017 14:25:49

**30MHz~1GHz**

Date: 1.DEC.2017 17:11:52

**1GHz~8GHz**

**High channel**



Date: 4.DEC.2017 14:26:37

**30MHz~1GHz**

Date: 1.DEC.2017 17:12:32

**1GHz~8GHz**