



Revision History			
Rev	Date	By	Description
V1	10Jul09	BC	Initial Prototype
V2	17Au09	BC	Expand program header
B	5Dec09	BC	Add C1, Chg C6,7,8 to UL App.
C	12May10	BC	Chg Power Relays and Drv
D	29May10	BC	Chg U2, Del R3,4
E	16Jun10	BC	Swap Pin 7,9 at U3, add D2, C7, Del X2
F	26Jun10	BC	Add D7,8,Q4, move Buzzer IO
G	2Jul10	BC	Del D7,8,Q4
H	6Aug10	BC	CHG R2 TO IND
J	22May11	BC	CHG R14 TO 680
K	29Jul13	BC	CHG RLY, PSU, SCRW TERM
L	27Mar2014	BC	CHG F1 TO FUSE RESISTOR
M	21Aug2014	BC	ADD R1 AT Q2

All circuitry housed within 240v electrical box

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Checked QA			
Approved		Title: PCB,SCH,FR1, INTERBOX	Rev. M
Client: Marsco Technologies	Drawing Number:	C3.7	
Scale: N/A	Size A	Sheet: 1 of 1	