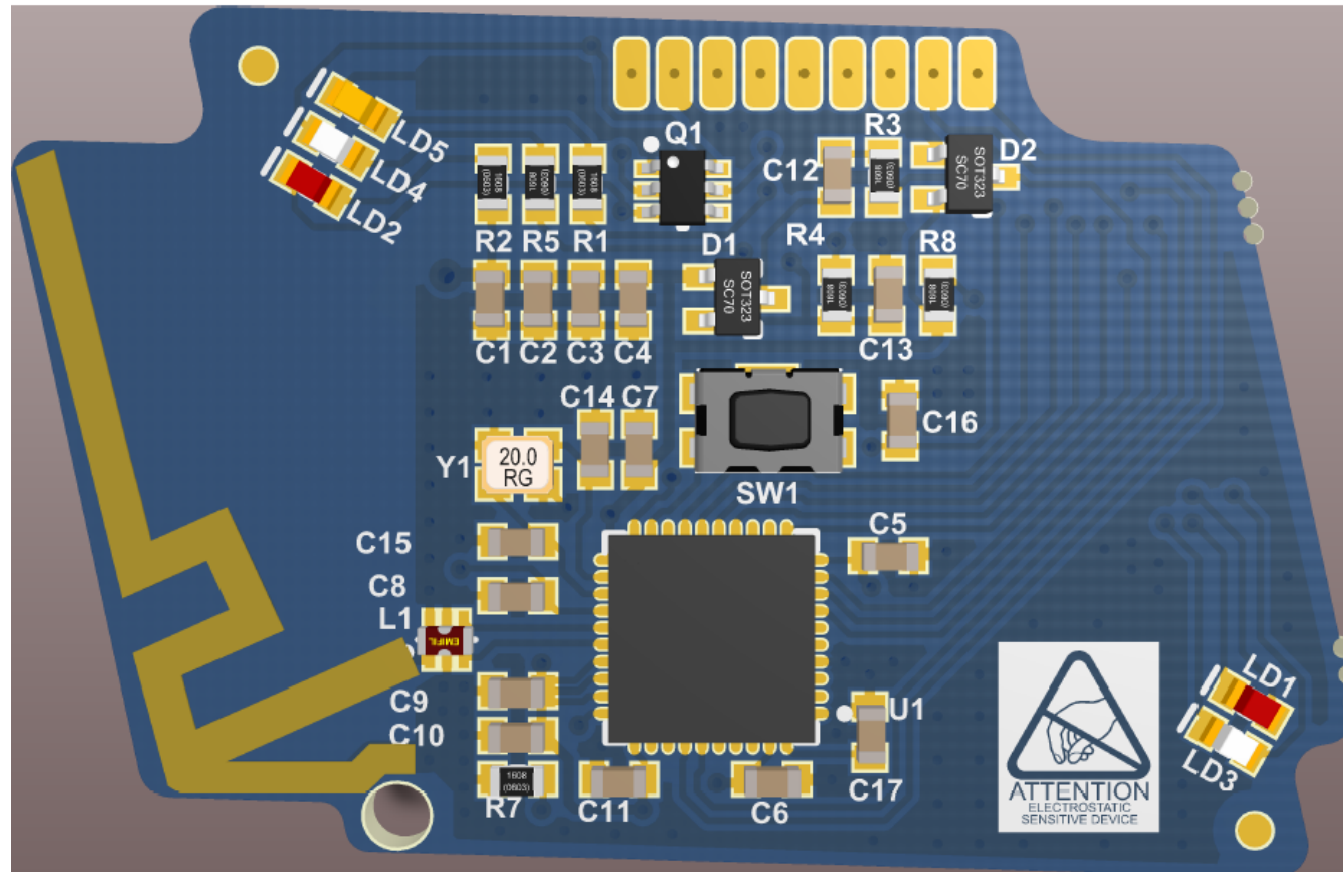


Tehnickal Map:

WK2.0 TX

8 429 000



Drawn By: Elvis Stanic
Materija, 25.03.2014

1

2

3

4

DESIGN INFORMATION/SPECIFICATION

BOARD TECHNOLOGY

BOARD SIZE

39.55 mm x 25.74 mm

(MIN. TRACK / PADS / CLEARANCE 0.2mm)

MINIMUM ANNULAR RING 0.05mm (0.002") EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C AND IS THE MANUFACTURING REGISTRATION ALLOWANCE

CIRCUIT TYPE: ☐ SS ☒ DS ☐ PTH MULTILAYER ☐ Number of Layers : _____MATERIAL: ☒ FR4 ☐ CEM1 ☐ FR2 ☐ POLYIMIDE ☐ ARLON ☐ TUC-722THICKNESS: ☐ 0.8mm ☐ 1.2mm ☒ 1.6mm ☐ 2.2mm ☐ 3.2mmTOLERANCE: ☒ IN A/W ANSI IPC-6012 TYPE 2 CLASS 2 ☐ OTHER +/- _____BOW & TWIST: ☒ IN A/W ANSI IPC-6012 TYPE 2 CLASS 2 ☐ AS SHOWN

COPPER THICKNESS

OUTER: ☐ 18um (1/2oz) ☒ 35um (1oz) ☐ 70um (2oz)INNER SIGNAL: ☐ 18um (1/2oz) ☒ 35um (1oz) ☐ 70um (2oz)INNER PWR: ☐ 18um (1/2oz) ☒ 35um (1oz) ☐ 70um (2oz)

DRILLING:

VIEWED FROM: ☒ COMPONENT SIDE ☐ SOLDER SIDEREFERENCE: ☐ AS SHOWN ☐ PATTERN MASTER LIST ☒ NC_ DRILL FILESPTH MINIMUM COPPER THICKNESS: ☒ 20um ☐ OTHER _____VIAS: ☐ NONE ☒ FEED THRU' ☐ TENTED ☐ BURIED LAYER SEQUENCE _____☒ UNIQUE DRILL ☐ 2 SIZES ☐ BLIND LAYER SEQUENCE _____

SILKSCREEN:

LEGEND / SCREEN PRINT: ☐ NONE ☒ TOP OVERLAY (GTO) ☒ BOT. OVERLAY (GBO)COLOUR: ☒ WHITE ☐ BLACK ☐ OTHER _____SOLDER RESIST: ☒ TOP SOLDER (GTS) ☒ BOTTOM SOLDER (GBS)COLOUR: ☐ GREEN ☒ BLUE ☐ OTHER _____

FINISH:

☐ OSP Preservative☐ HAL (RoHS)☒ IMMERSION TIN (RoHS) ☐ OTHER _____☐ IMMERSION GOLD (RoHS)☐ HARD GOLD ☐ OTHER _____☐ AS SHOWN ☐ REFER _____PANELIZATION: ☒ YES ☐ NO

PANEL SIZE: _____

PEEL ABLE MASK: ☐ YES ☒ NO

LAYER: _____

PROFILING:

☒ CUT AND TRIM ACCORDING TO LAYER MECHANICAL 1 (GM1)☐ REFER PCB BLANK DWG No. _____☐ USE PROFILE/ ROUTE TAPE SUPPLIED (REFER PATTERN MASTER LIST)☐ SQUARE CUT ☒ N.C. ROUTE ☐ V. SCORE ☐ BLANK

PHOTOPLOTTER:

DATA TYPE: RS274X, UNIT: MILLIMETERS, FORMAT: 2:4

EXTENSIONS: .GTL .GBL .GTO .GBO .GTS .GBS .GTP .GBP .Gx .GPx .GMx .GM1

NC DRILL:

SUPPRESS TRAILING ZEROES, UNIT: MILLIMETERS, FORMAT: 2:4

EXTENSION: .TXT

ACCEPTABILITY:

MATERIALS AND WORKMANSHIP FOR ALL PRINTED WIRING BOARDS TO MEET OR EXCEED THE REQUIREMENTS OF:

☒ IN A/W ANSI IPC-A-600F ☐ 1 ☒ 2 ☐ 3☐ UL CERTIFICATION ☐ UL LOGO ON PCB☐ MIL-P-55110E GRP ☐ A ☐ B ☐ AS2546 ☐ OTHER _____

ADDITIONAL REQUIREMENTS:

MICROSECTION: ☒ NONE ☐ SAMPLE PLAN ☐ MIL-P-55110E SERIAL Nos. PER ORDERCERTIFICATION: ☐ NONE ☐ MIL-P-55110E GRP ☐ A ☐ B IPC CLASS: ☐ 1 ☒ 2 ☐ 3☐ QUALITY RELEASE REPORT ☒ OTHER PER ORDERELECTRICAL TEST: Bare Board ☐ NONE ☒ REQUIRED ☐ PER ORDERSURFACE MOUNT: ☐ NONE ☒ 1 SIDE ☐ 2 SIDES ☐ MIN. PITCH 0.50mmMANUFACTURER'S ID/LOGO: ☒ NONE ☐ FOIL ☐ LEGEND ☐ REFER _____MANUFACTURER'S ACCREDITATION: ☒ ISO9001 ☐ NONEPACKAGING AND HANDLING: ☒ PER ORDER ☐ REFER _____

ABBREVIATIONS:

GTL=Top Layer

GTO=Top Overlay

GTS=Top Solder

GTP=Top Paste

Gx=Internal Layer x

AST=Assembly top

PMT=Peel Able Mask Top

GBL=Bottom Layer

GBO=Bottom Overlay

GBS=Bottom Solder

GBP=Bottom Paste

GPx=Negative Plane x

ASB=Assembly Bottom

PMB=Peel Able Mask Bottom

GM1=Board outline (milling line)

GMx=Mechanical Layer x

SCH=Schematic drawing

PNL=Panelization drawing

PL=Part List

ME=Mechanical, critical parts

BDG=Block diagram

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6242 Materija
www.atech.si

Drawn By:

Elvis Stanic

Approved By:

Martin Zerjal

Date:

25.03.2014

File:

WK2_0 TX verD.PcbDoc

Title:

WK2.0 TX

Atech ID:

1409990

Version:

D

Layer:

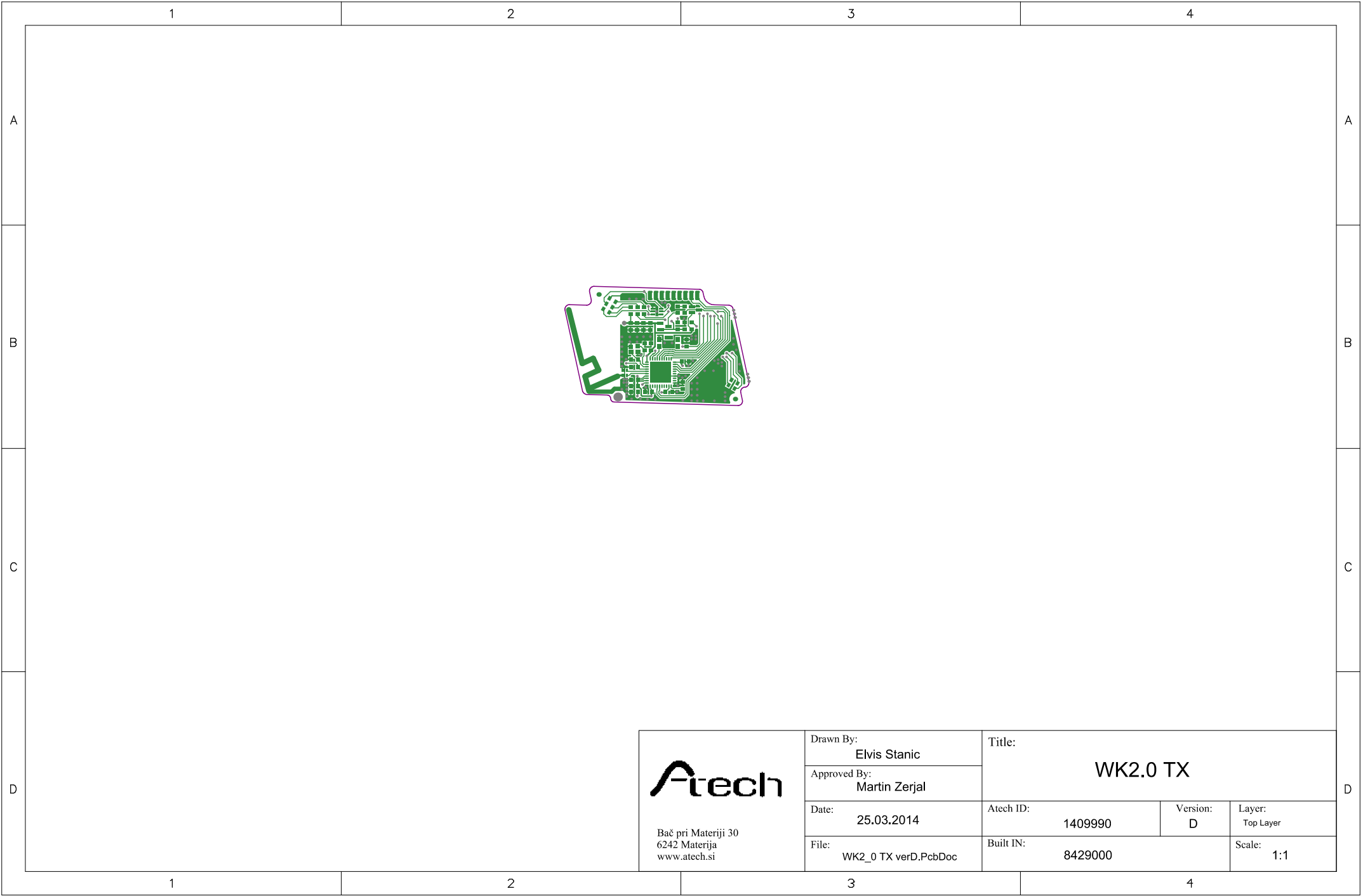
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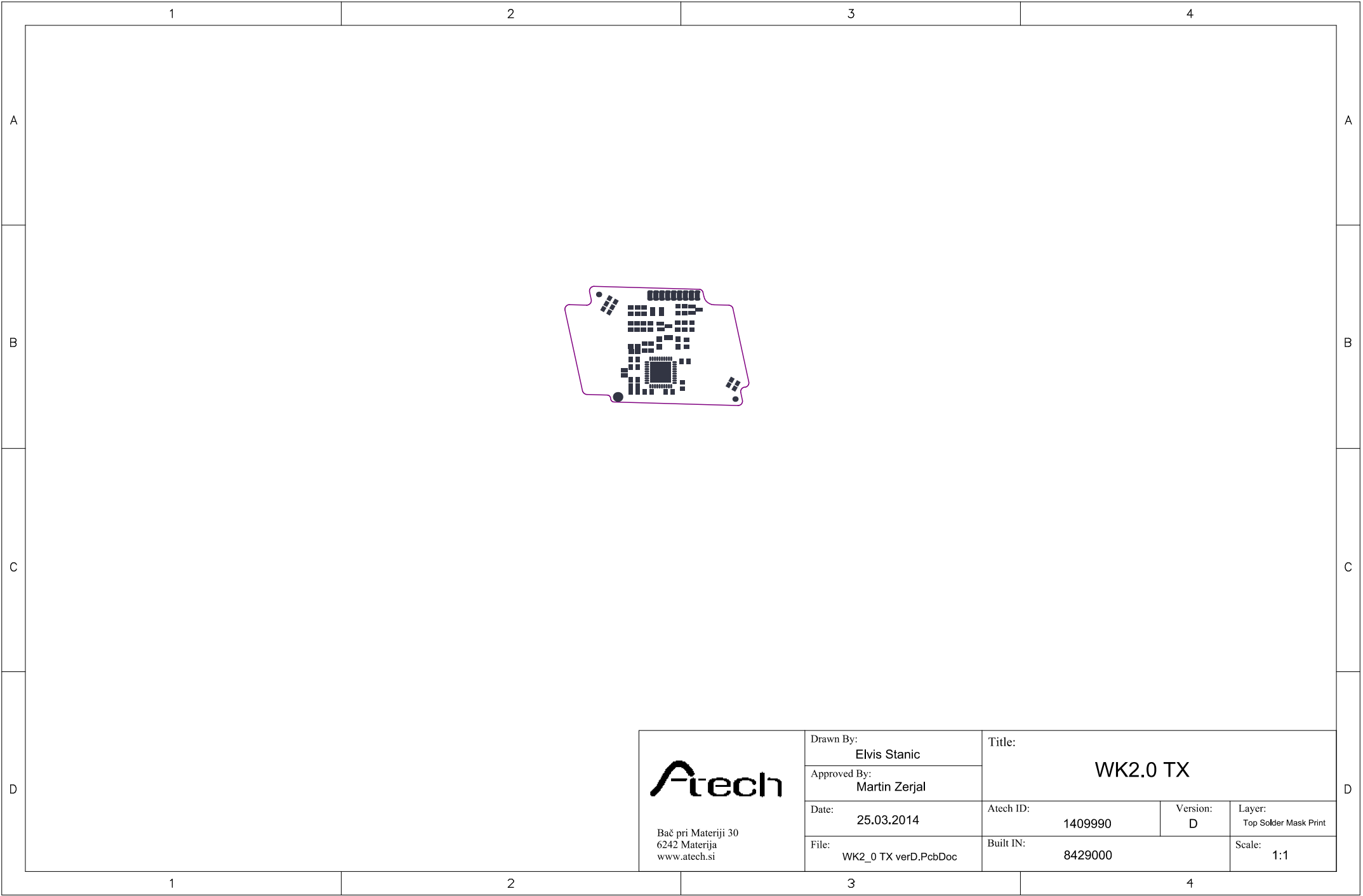
Built IN:

8429000

Scale:

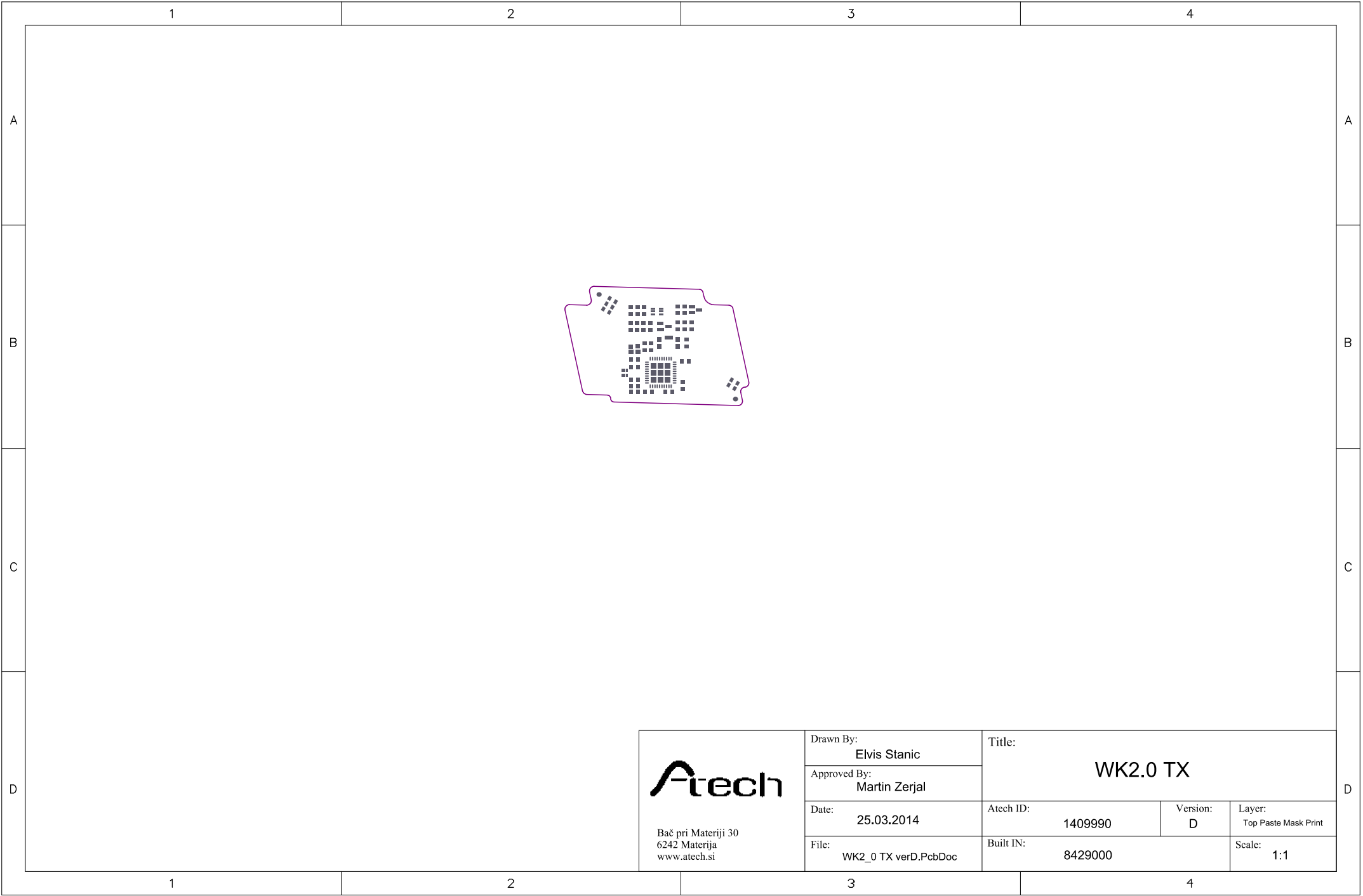
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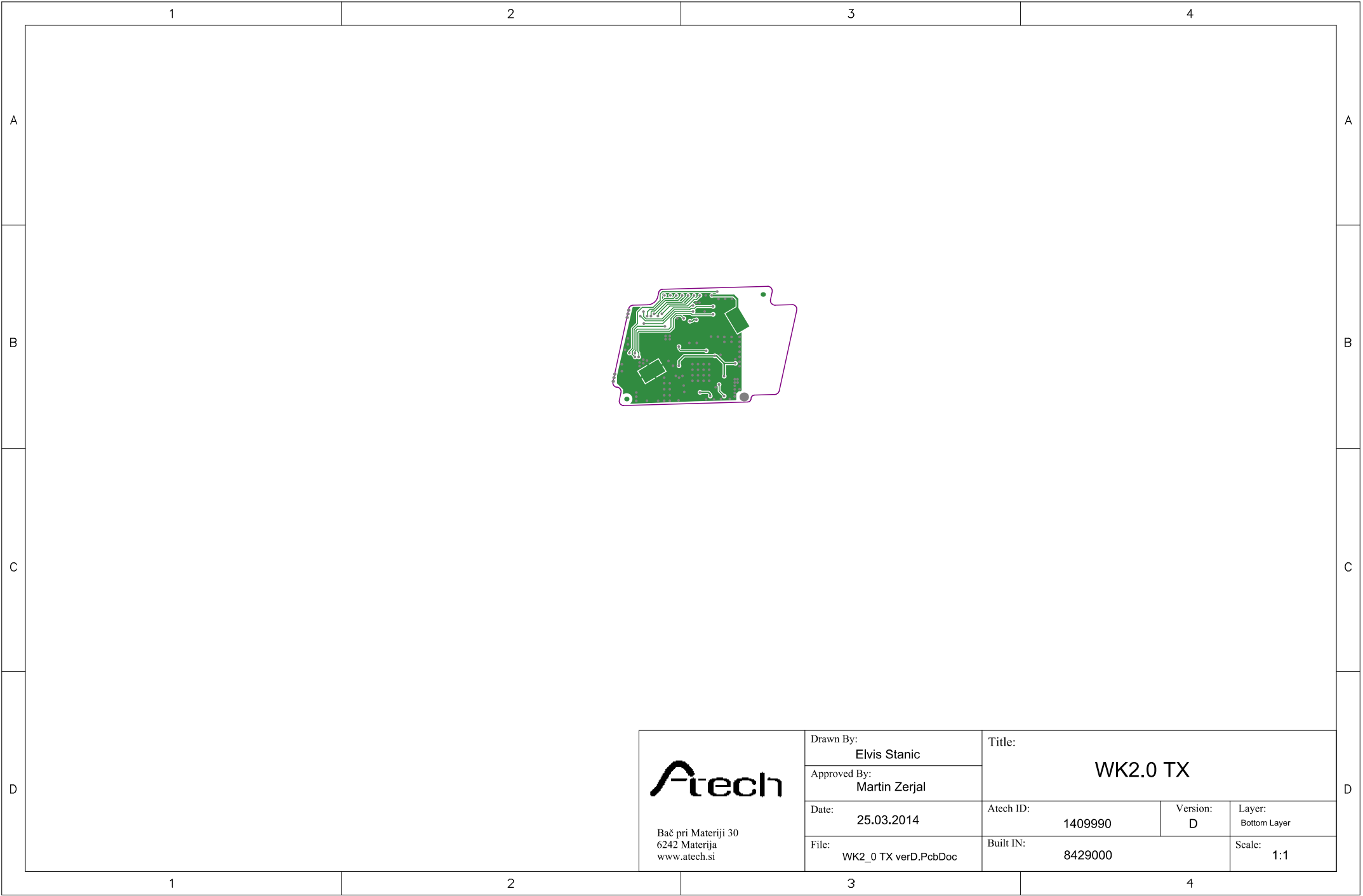
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Drawn By: Elvis Stanic		Title: WK2.0 TX		
Approved By: Martin Zerjal				
Date: 25.03.2014	Atech ID: 1409990	Version: D	Layer: Top Solder Mask Print	
File: WK2_0 TX verD.PcbDoc	Built IN: 8429000		Scale: 1:1	



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Drawn By: Elvis Stanic	WK2.0 TX		
Approved By: Martin Zerjal			
Date: 25.03.2014	Atech ID: 1409990	Version: D	Layer: Top Paste Mask Print
File: WK2_0 TX verD.PcbDoc	Built IN: 8429000	Scale: 1:1	



1

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4

A

A

B

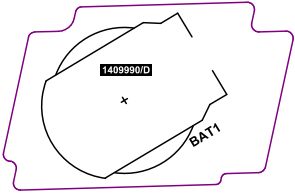
B

C

C

D

D



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Drawn By:
Elvis Stanic

Approved By:
Martin Zerjal

Date:
25.03.2014

File:
WK2_0 TX verD.PcbDoc

Title:
WK2.0 TX

Atech ID:
1409990

Built IN:
8429000

Version:
D

Layer:
Bottom Silkscreen Overlay

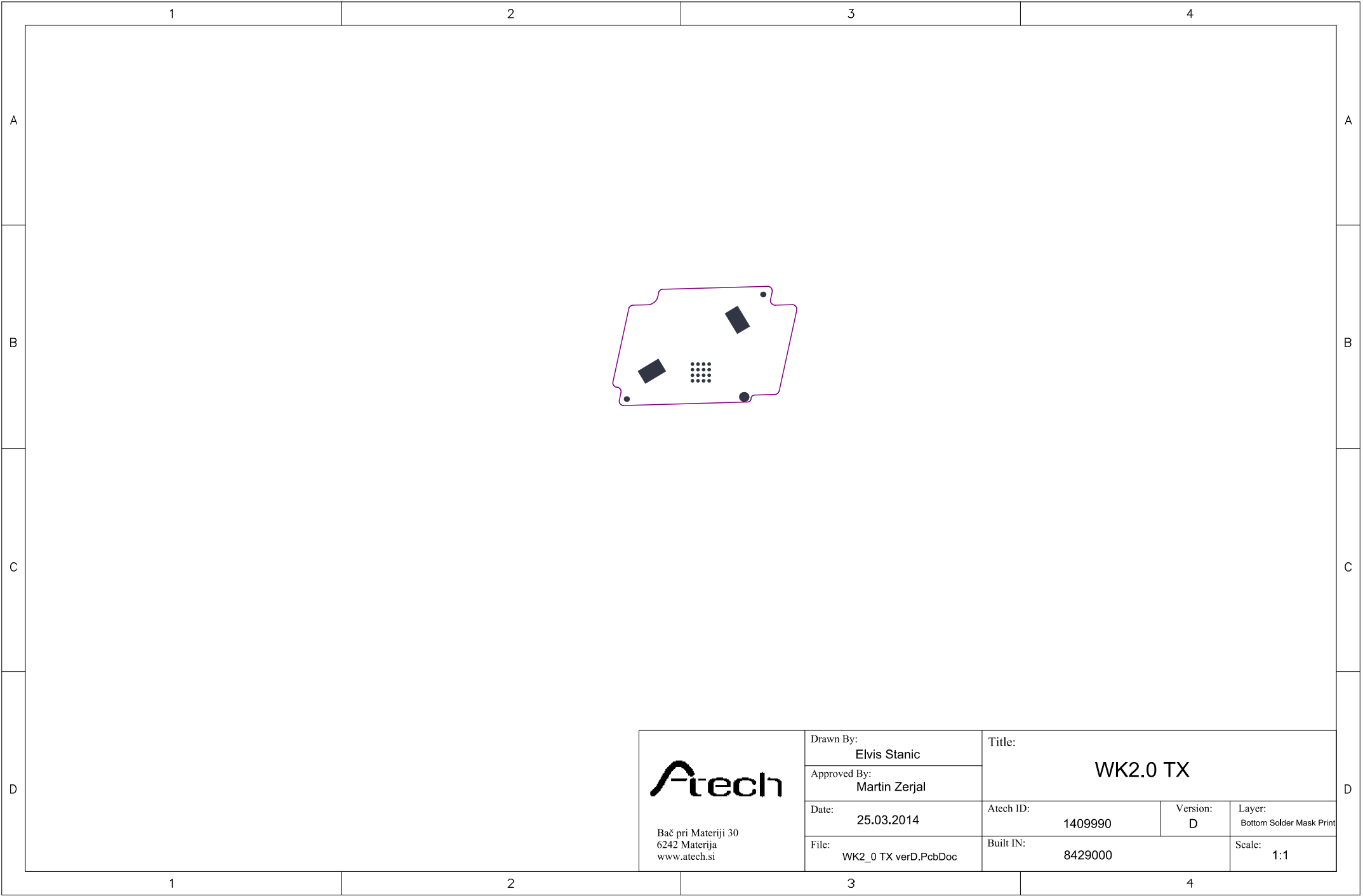
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1

2

3

4



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Drawn By: Elvis Stanic	Title: WK2.0 TX		
Approved By: Martin Zerjal			
Date: 25.03.2014	Atech ID: 1409990	Version: D	Layer: Bottom Solder Mask Print
File: WK2_0 TX verD.PcbDoc	Built IN: 8429000	Scale: 1:1	

1

2

3

4

A

A

B

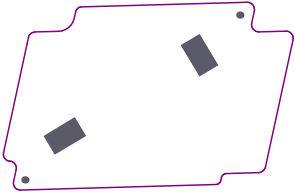
B

C

C

D

D



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Drawn By:
Elvis Stanic

Approved By:
Martin Zerjal

Date:
25.03.2014

File:
WK2_0 TX verD.PcbDoc

Title:
WK2.0 TX

Atech ID:
1409990

Built IN:
8429000

Version:
D

Layer:
Bottom Paste Mask Print

Scale:
1:1

1

2

3

4

1

2

3

4

A

A

B

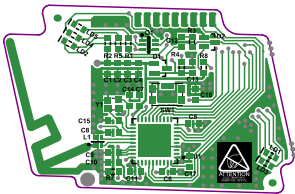
B

C

C

D

D



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Drawn By:
Elvis Stanic

Approved By:
Martin Zerjal

Date:
25.03.2014

File:
WK2_0 TX verD.PcbDoc

Title:

WK2.0 TX

Atech ID:
1409990

Built IN:
8429000

Version:
D

Layer:
Assembly Top

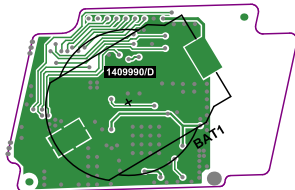



Scale:
1:1

1

2

3

4

1	2	3	4														
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B				B													
C				C													
D	<table><tr><td rowspan="4"><div data-bbox="1035 1258 1249 1319"></div><div data-bbox="1043 1365 1179 1419">Bač pri Materiji 30 6242 Materija www.atech.si</div></td><td>Drawn By: Elvis Stanic</td><td colspan="2" rowspan="2">Title: WK2.0 TX</td></tr><tr><td>Approved By: Martin Zerjal</td></tr><tr><td>Date: 25.03.2014</td><td>Atech ID: 1409990</td><td>Version: D</td><td>Layer: Assamby Bottom</td></tr><tr><td>File: WK2_0 TX verD.PcbDoc</td><td>Built IN: 8429000</td><td colspan="2">Scale: 1:1</td></tr></table>			<div data-bbox="1035 1258 1249 1319"></div> <div data-bbox="1043 1365 1179 1419">Bač pri Materiji 30 6242 Materija www.atech.si</div>	Drawn By: Elvis Stanic	Title: WK2.0 TX		Approved By: Martin Zerjal	Date: 25.03.2014	Atech ID: 1409990	Version: D	Layer: Assamby Bottom	File: WK2_0 TX verD.PcbDoc	Built IN: 8429000	Scale: 1:1		D
<div data-bbox="1035 1258 1249 1319"></div> <div data-bbox="1043 1365 1179 1419">Bač pri Materiji 30 6242 Materija www.atech.si</div>	Drawn By: Elvis Stanic	Title: WK2.0 TX															
	Approved By: Martin Zerjal																
	Date: 25.03.2014	Atech ID: 1409990	Version: D		Layer: Assamby Bottom												
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1	2	3	4														

