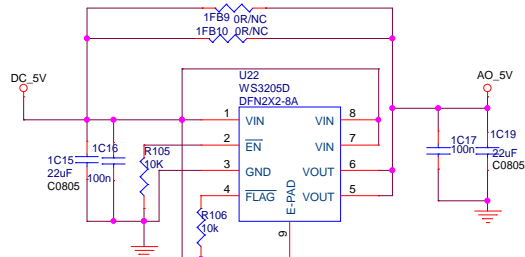
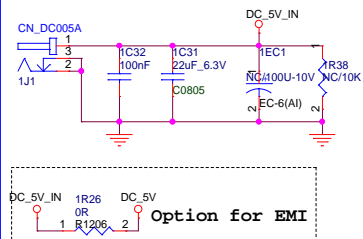
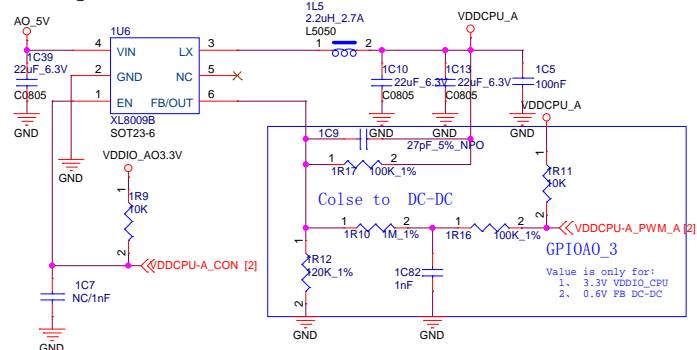


DC IN 5V/2A



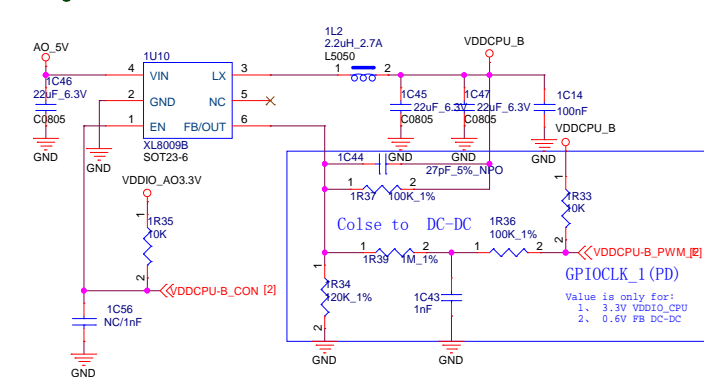
Design Max: 2A

VDDCPU_A



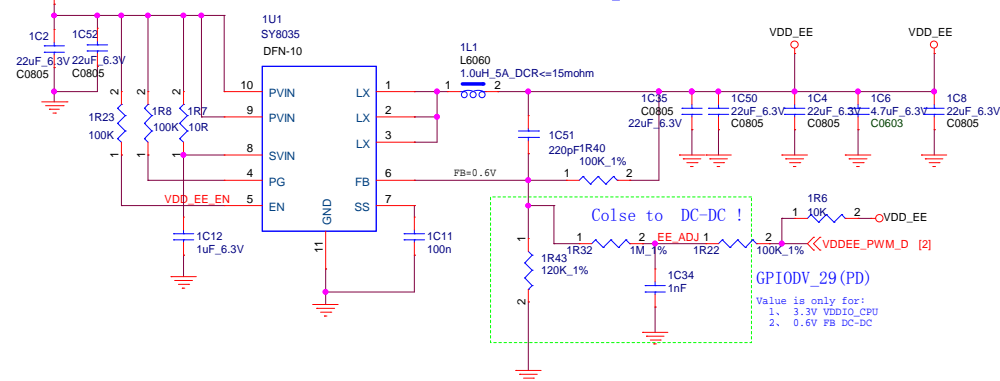
Design Max: 2A

VDDCPU_B



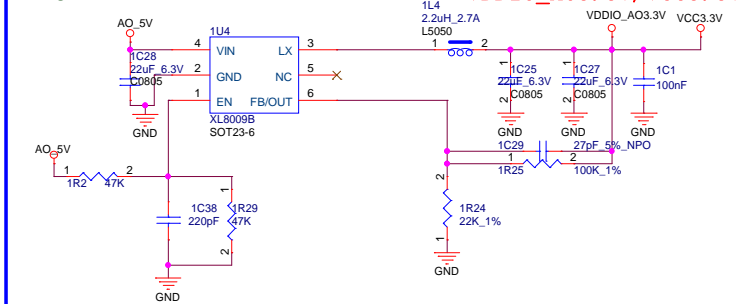
5V to VDD_EE Design Max: 5A

VDD_EE Default



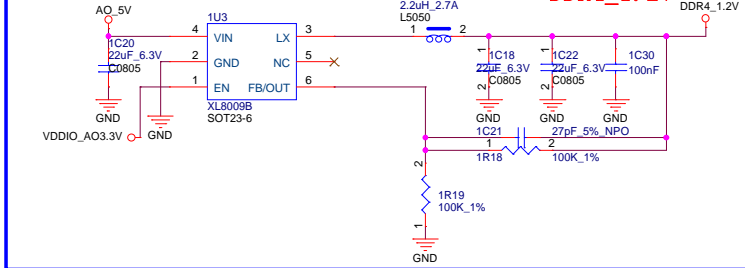
Design Max: 2A

VDDIO_A03.3V/VCC3.3V



Design Max: 2A

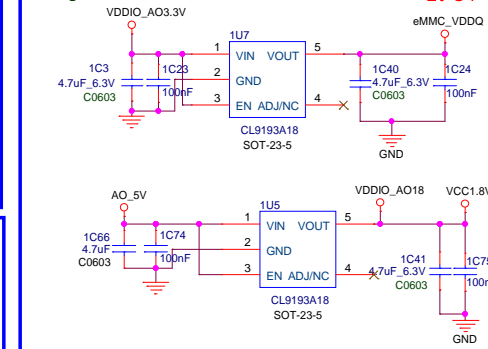
DDR4_1.2V



5V Control

Design Max: 300mA

1.8V

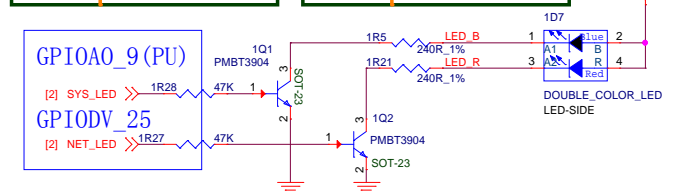


System LED

NET LED

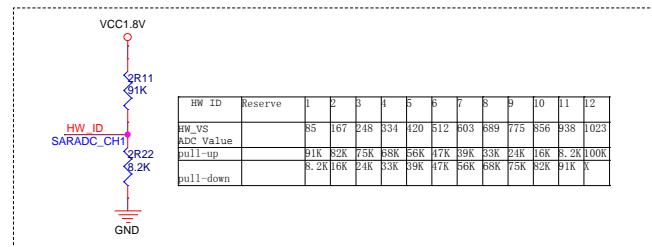
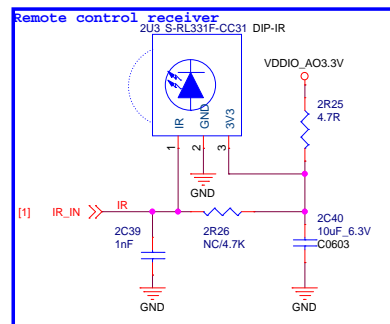
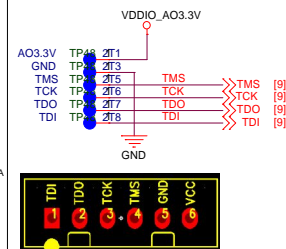
Logic	LED function
SYS_LED=1	System running: green LED
SYS_LED=0	LED off

Logic	LED function
NET_LED=1	NET running: Blue LED
NET_LED=0	NET off



GIEC

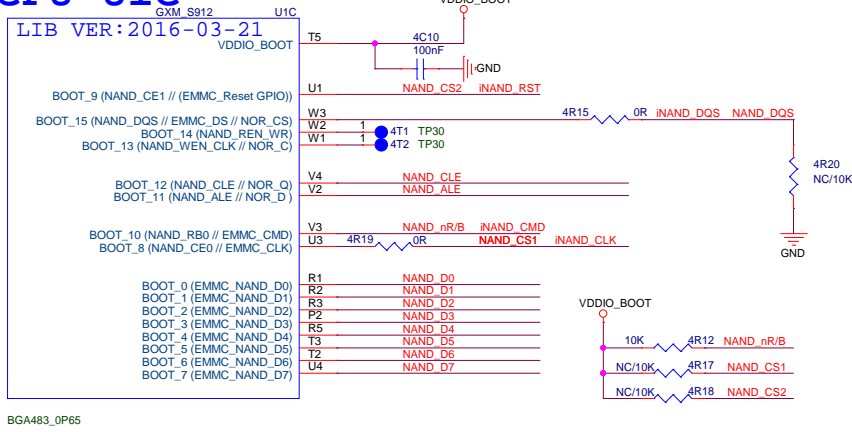
File	P01 POWER	Rev
Size	Document Number	
A3	RM-MPEG-172G VER0.1	
Date:	Thursday, July 20, 2016	Sheet 3 of 12



HW ID	Reserve	1	2	3	4	5	6	7	8	9	10	11	12
HW_VS ADC Value		85	167	248	334	420	512	603	689	775	856	938	1023
pull-up		91K	82K	75K	68K	56K	47K	39K	33K	24K	16K	8.2K	100K
pull-down		8.2K	16K	24K	33K	39K	47K	56K	68K	75K	82K	91K	X

GIEC				
Title P02 CPU				
Size A3	Document Number RM-MPEG-172G VER0.1			Rev
Date:	Thursday, July 20, 2016		Sheet	4 of 12

CPU U1C



Power ON Config

NAND_CLE

1 boot from NAND/emmc

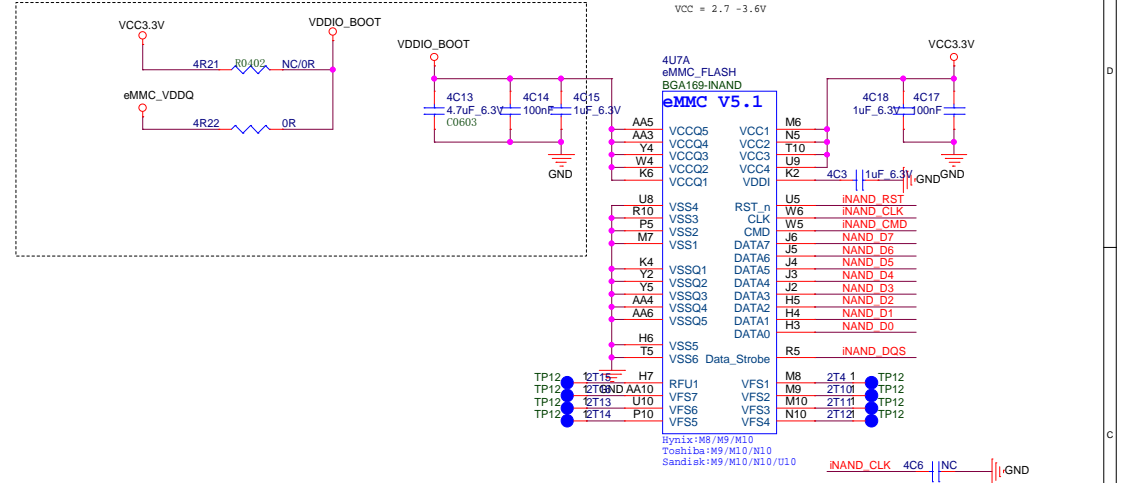
0 boot from SPI



iNAND

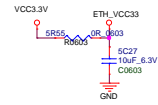
PCB Decal:169pin balls Type(BGA)

VCC = 2.7 ~3.6V



GIEC

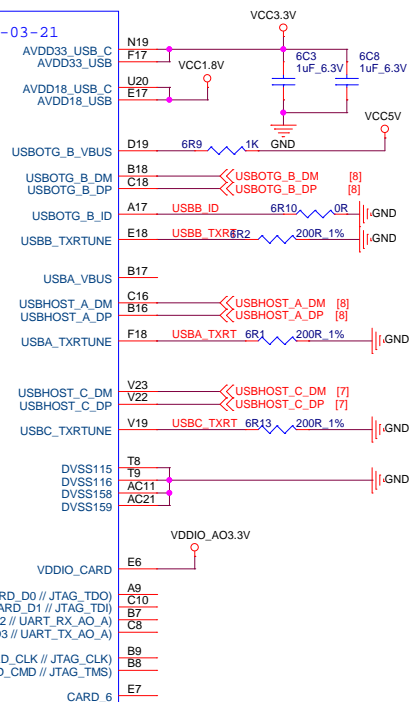
Title		
P04 FLASH		
Size	Document Number	Rev
A3	RM-MPEG-172G VERO.1	
Date:	Thursday, July 20, 2016	Sheet 6 of 12



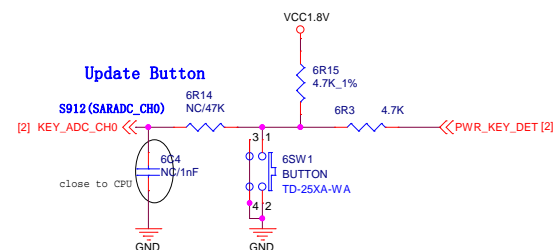
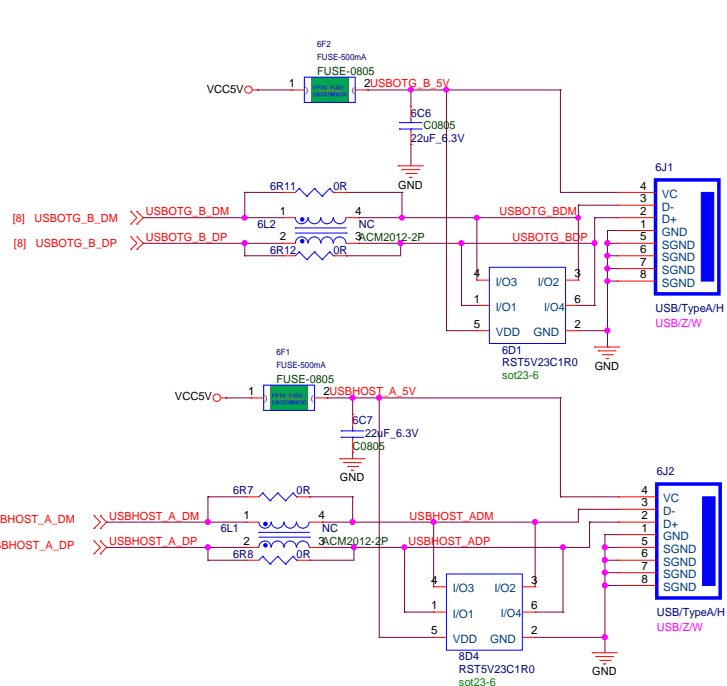
GIEC			
Title	P05 Ethernet		
Size A2	Document Number RM-MPEG-172G VER0.1	Rev	
Date	Thursday, July 20, 2018	Sheet	7 of 12

Card Power

LJB VER:2016-03-21



BGA483_0P65

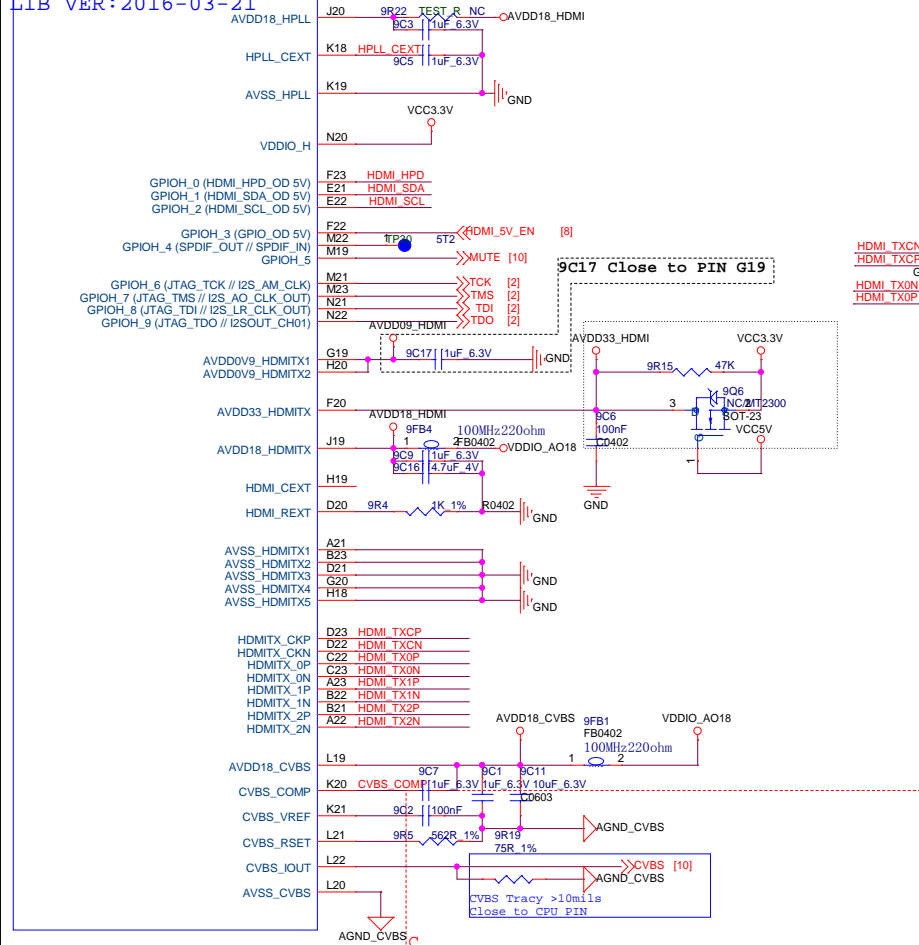


GIEC

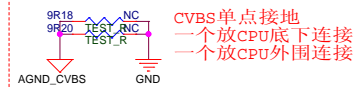
Title				
P06 USB&CARD&KEY				
Size	Document Number			Rev
A3	RM-MPEG-172G VER0.1			
Date:	Thursday, July 20, 2016		Sheet	8 of 12

CPU U1G

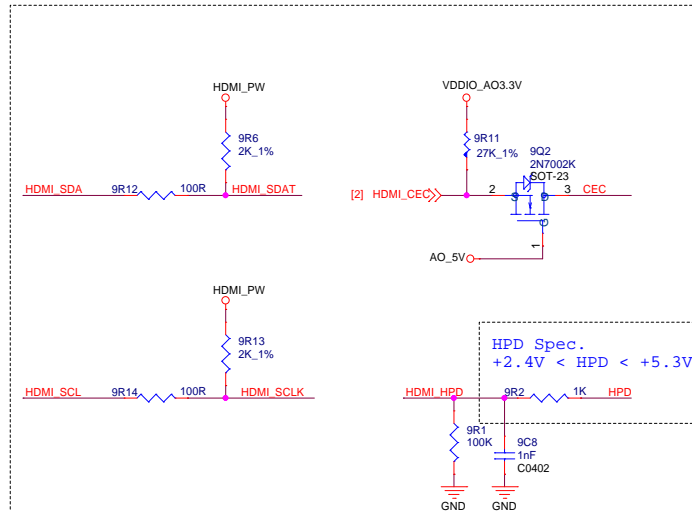
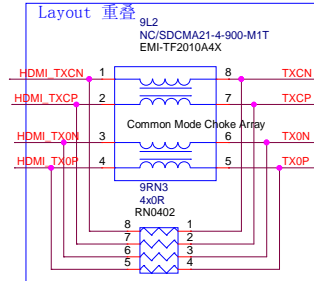
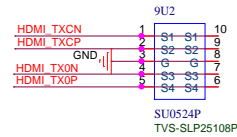
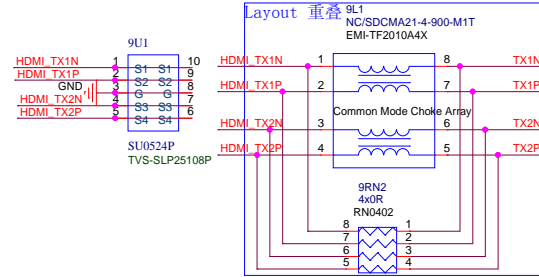
U1G GXM_S912
LIB VER:2016-03-21



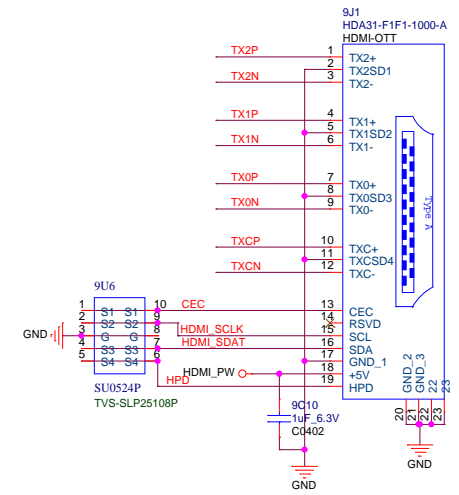
BGA483_OP65



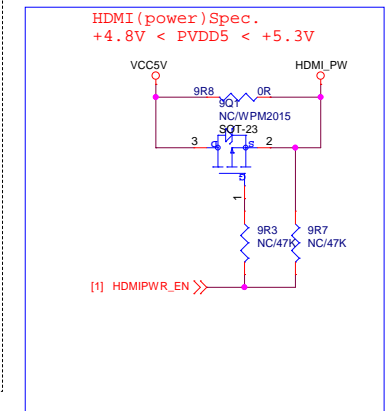
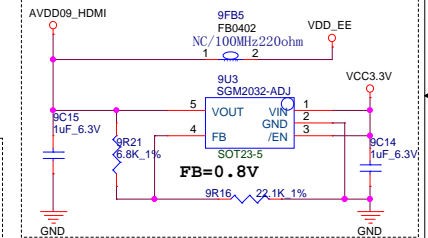
HDMI output



Type A

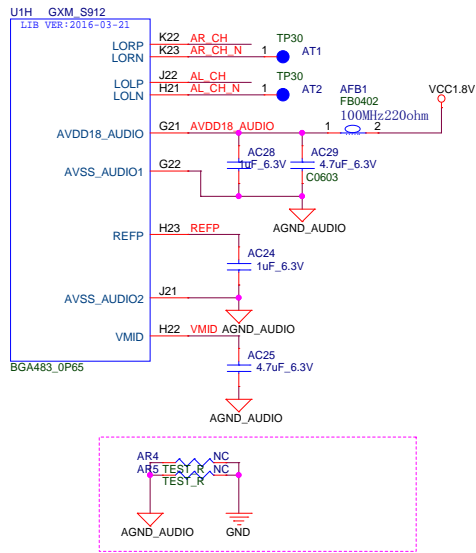


Used LDO for HDMI 4K compliance TEST
Used FB for low cost solution

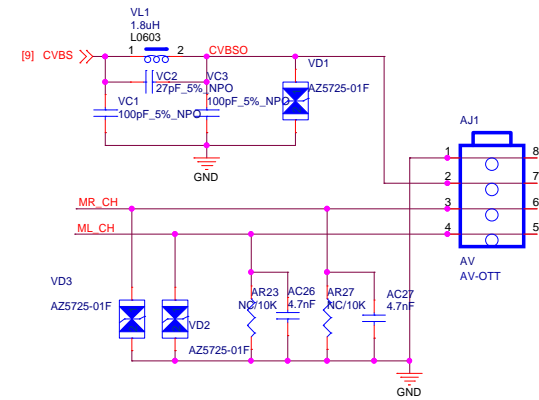
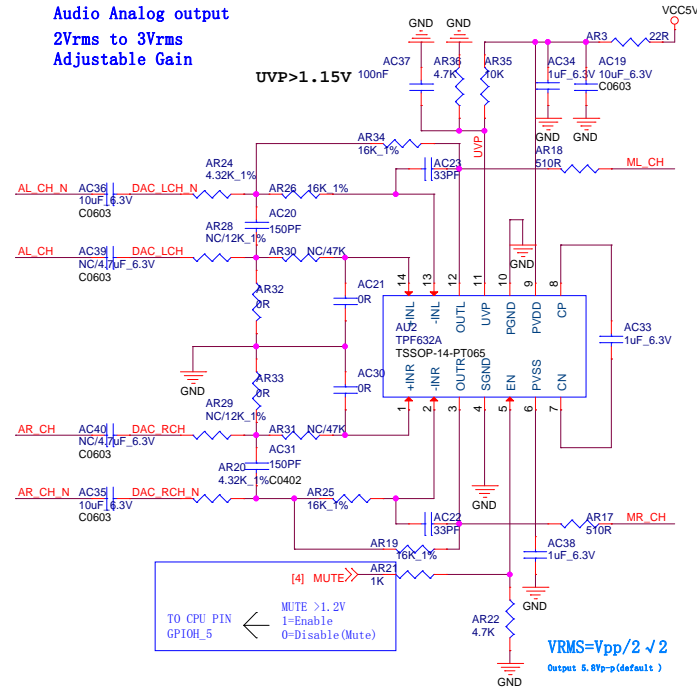


GIEC

Title			
P08 HDMI&CVBS			
Size	Document Number		Rev
A3	RM-MPEG-172G VER0.1		
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Audio Analog output
2Vrms to 3Vrms
Adjustable Gain



GIEC			
Title	P09 Audio		
Size	Document Number	Rev	
A3	RM-MPEG-172G VER0.1		
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