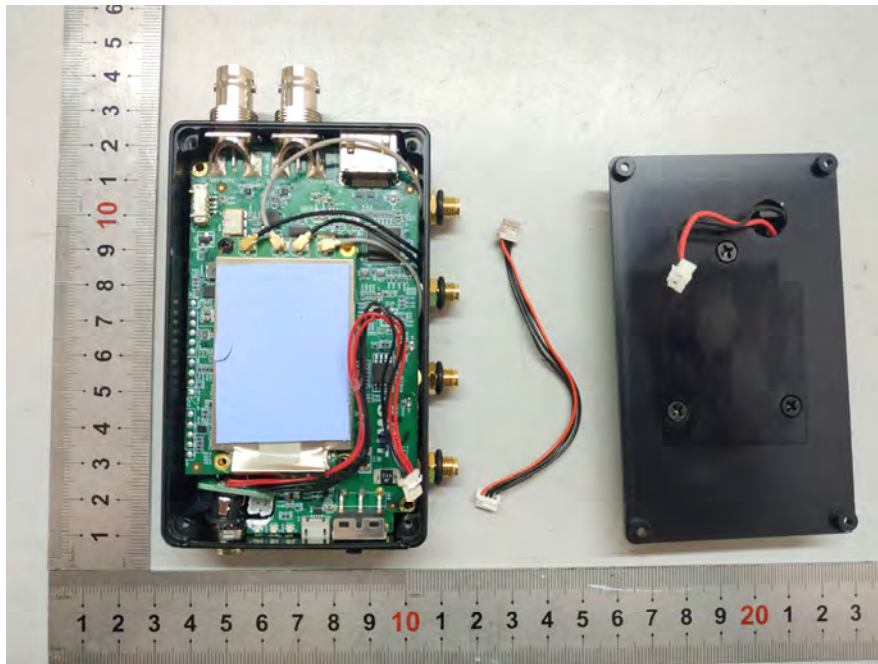
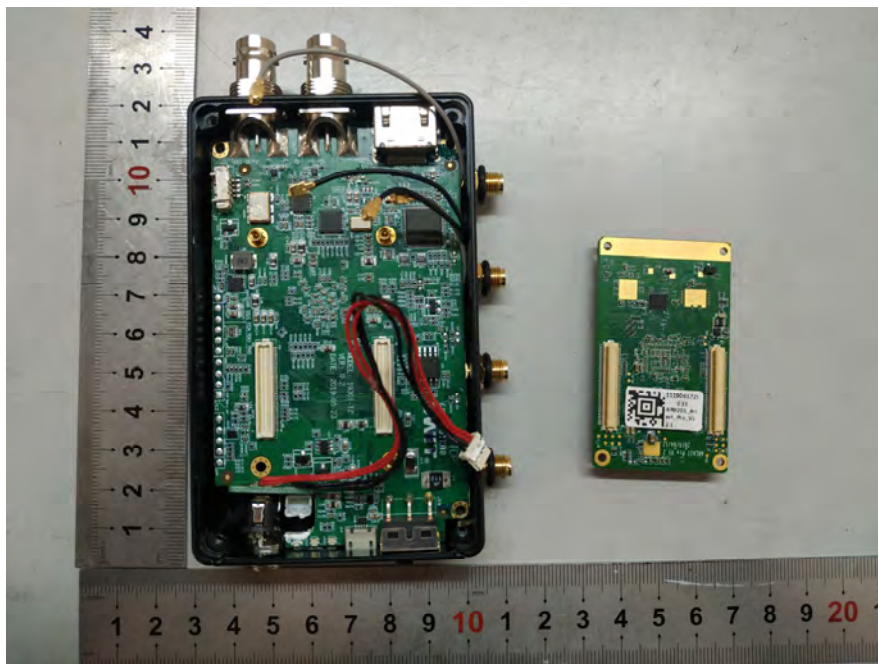


## **EXHIBIT B - EUT INTERNAL PHOTOGRAPHS**

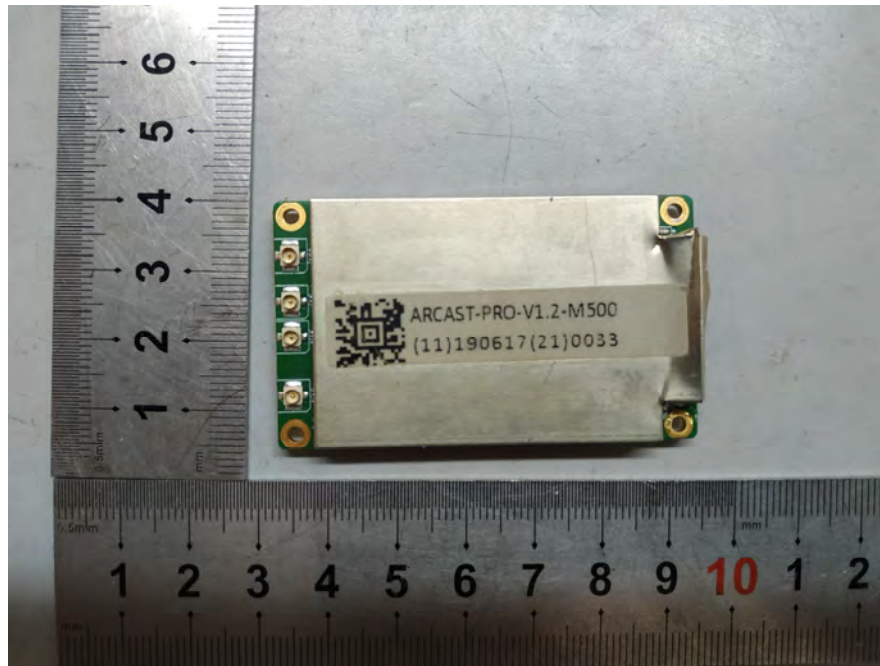
**EUT – Cover off View-1**



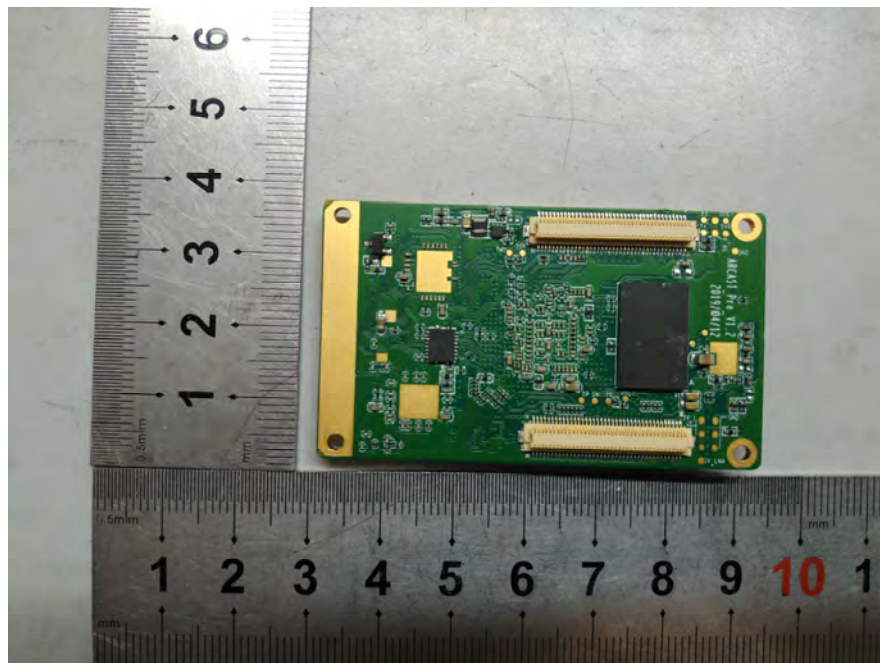
**EUT – Cover off View-2**



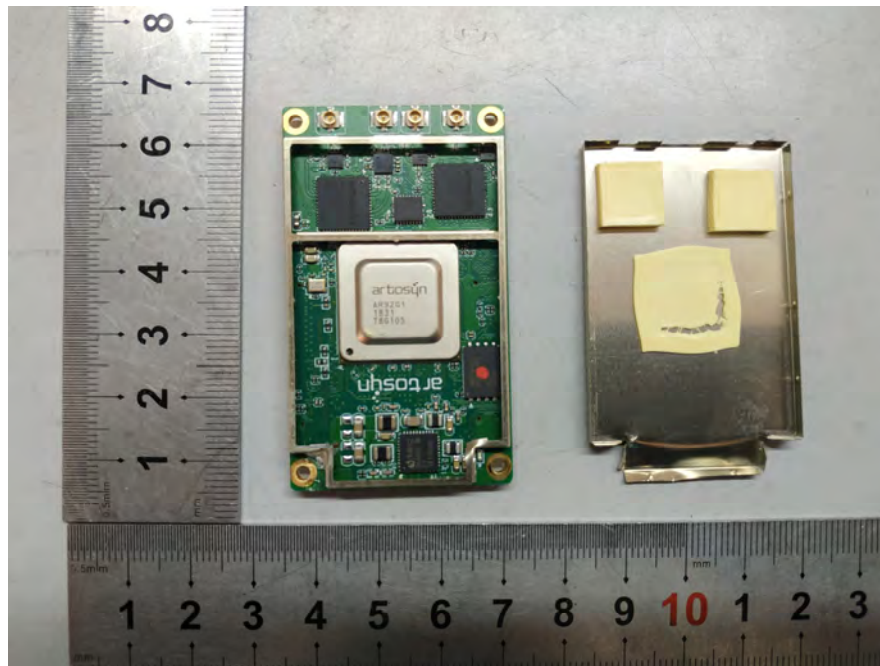
**EUT – PCB-1 Top View**



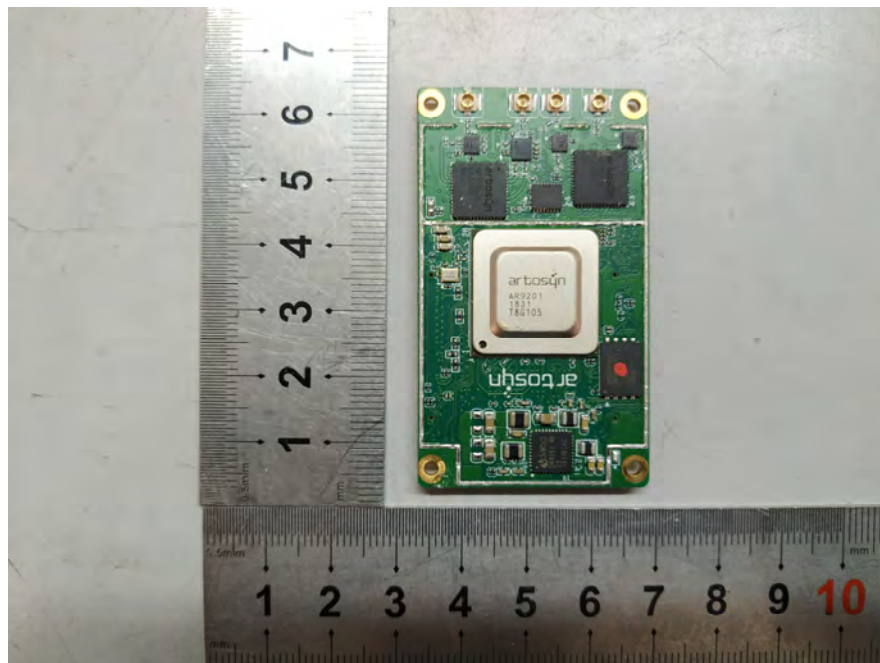
**EUT – PCB-1 Bottom View**



**EUT – Cover off View-3**

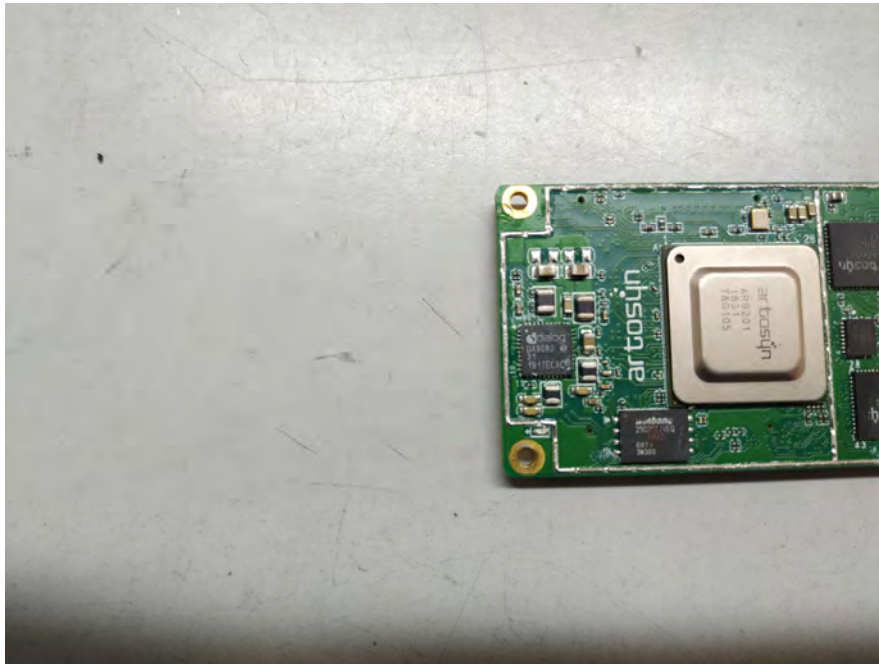


**EUT – PCB-1 Top (Shielding off) View**

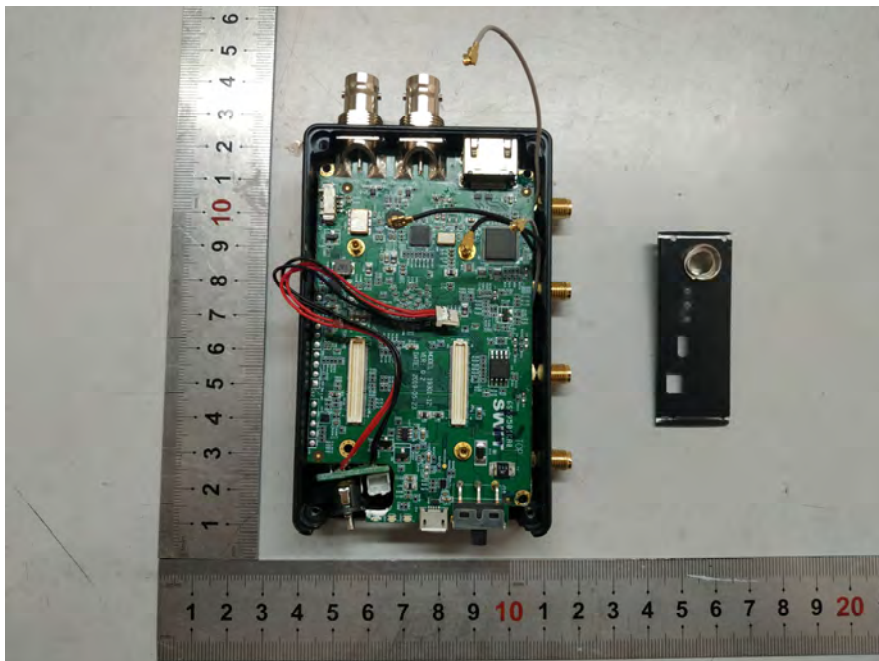




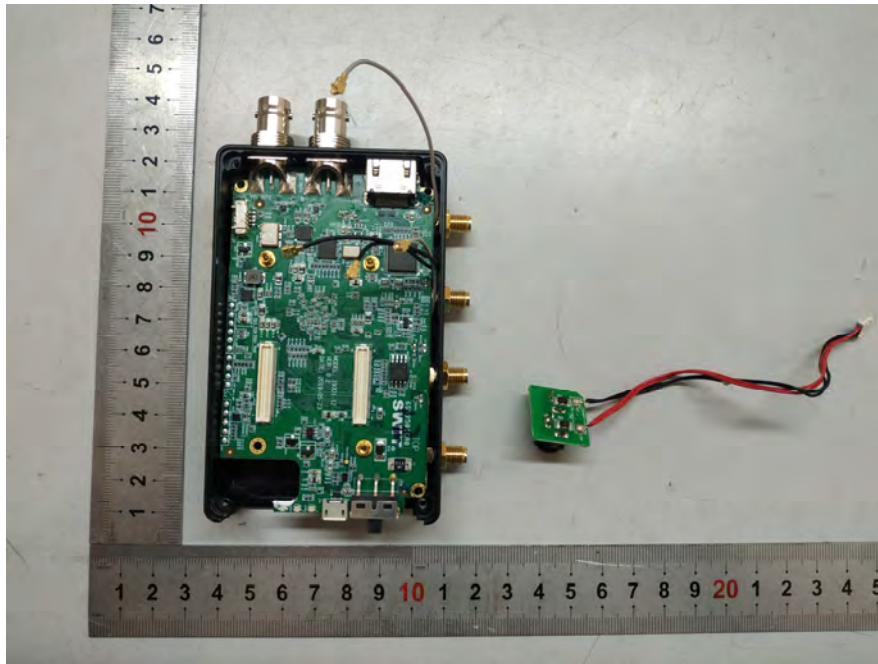
**EUT – PCB-1 Top IC Chip View**



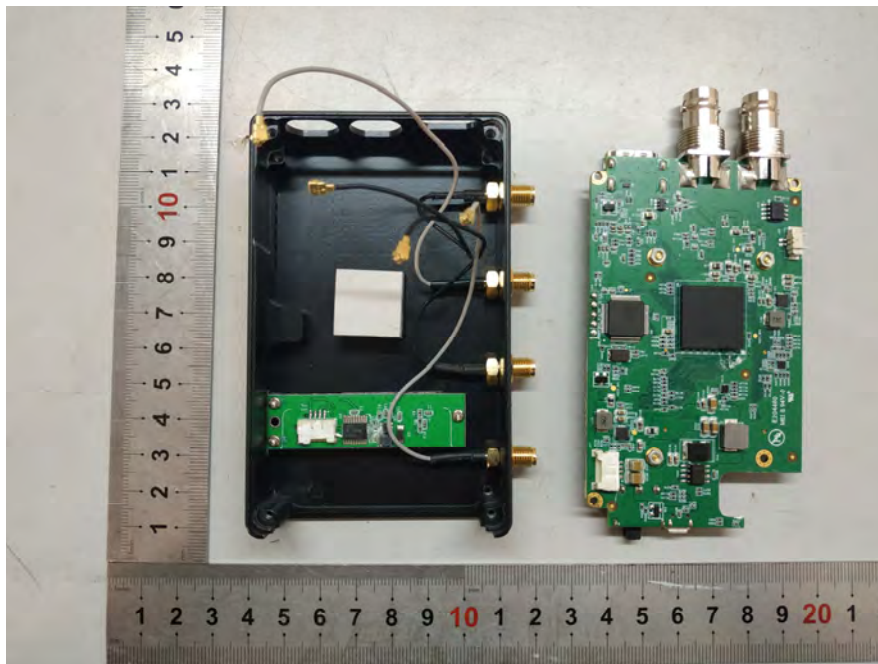
**EUT – Cover off View-4**



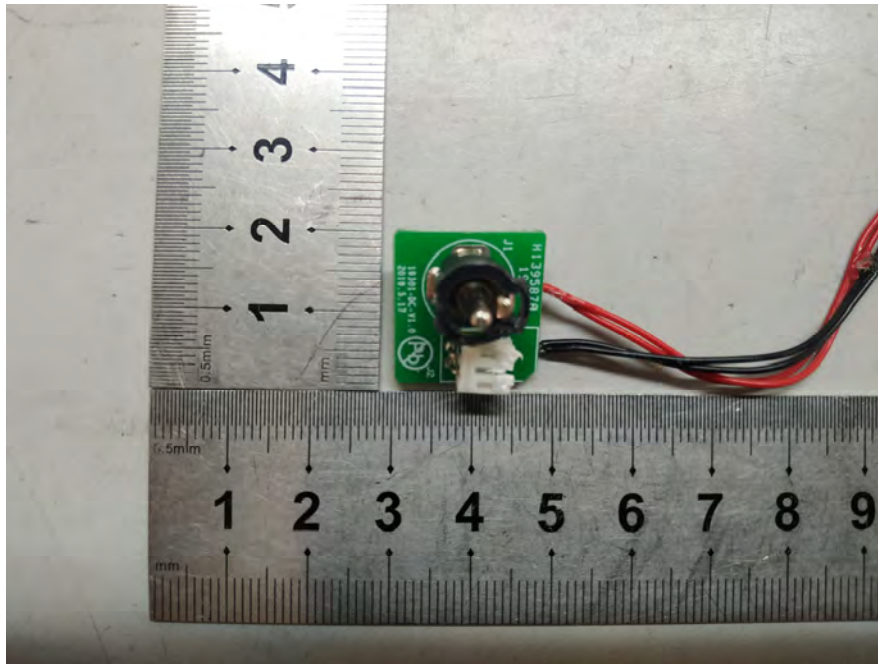
**EUT – Cover off View-5**



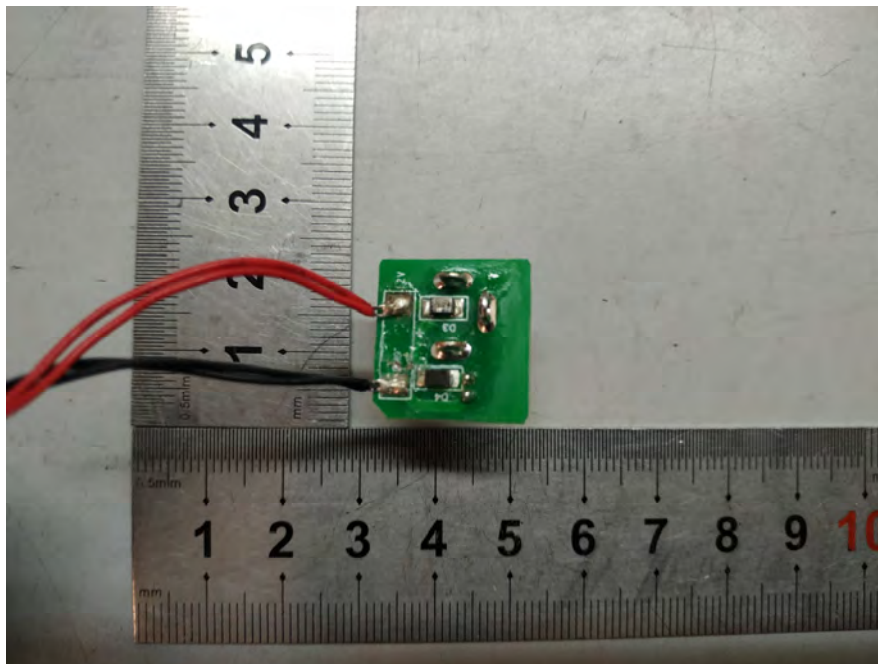
**EUT – Cover off View-6**



**EUT – PCB-2 Top View**

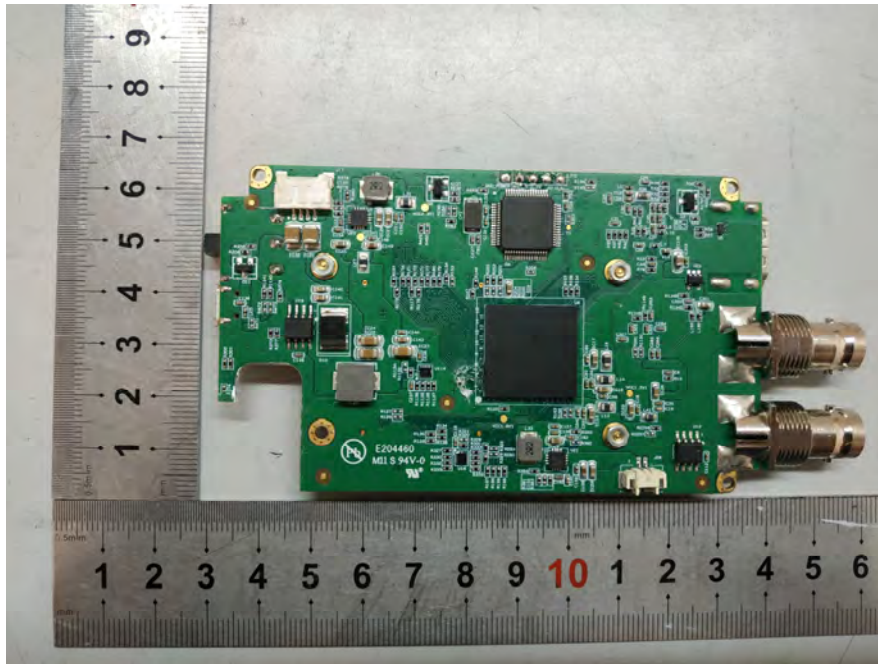


**EUT – PCB-2 Bottom View**

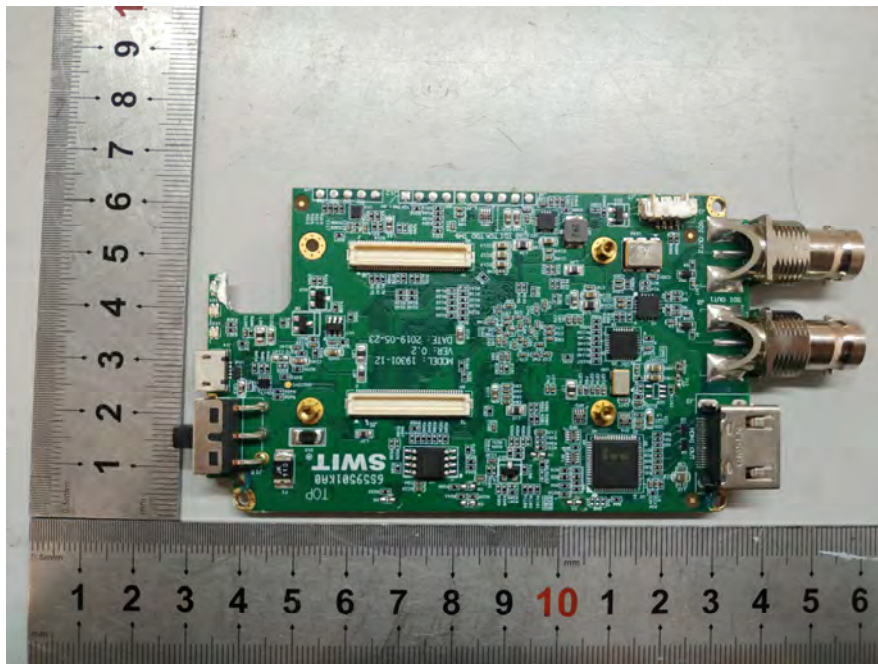




**EUT – PCB-3 Top View**



**EUT – PCB-3 Bottom View**



**EUT – PCB-3 IC Chip View-1**

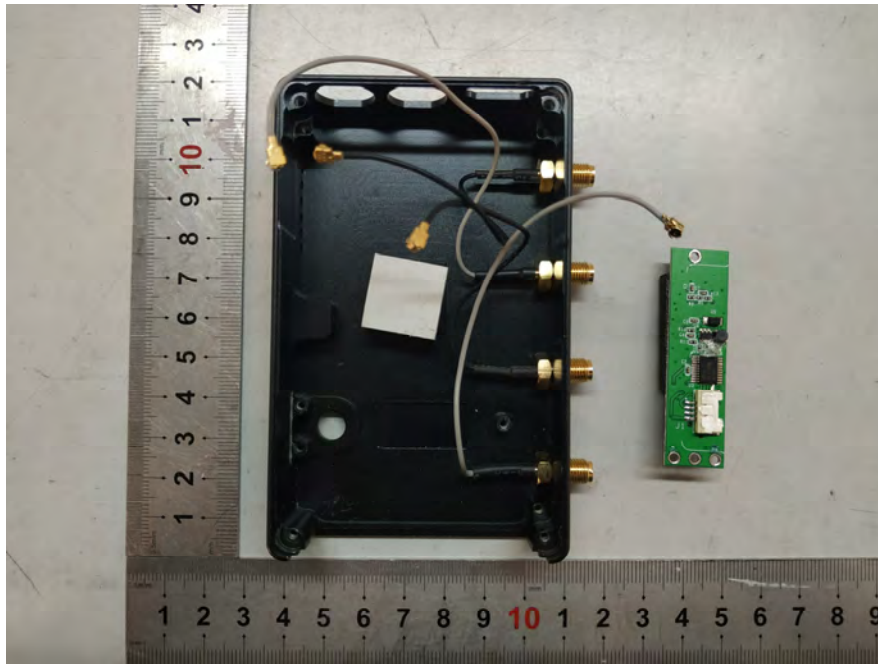


**EUT – PCB-3 IC Chip View-2**

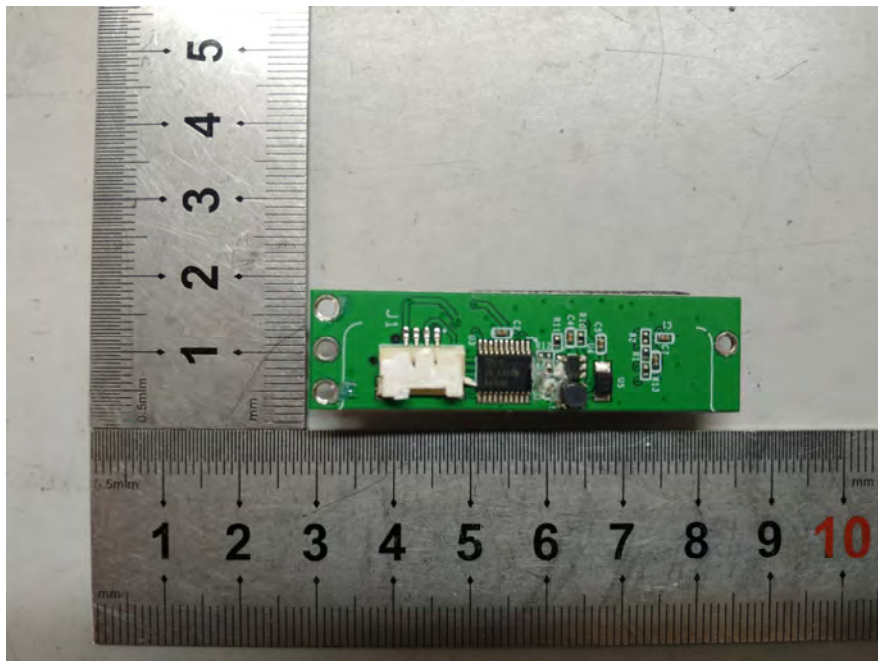




**EUT – Cover off View-7**



**EUT – PCB-4 Top View**



**EUT – PCB-4 Bottom View**

