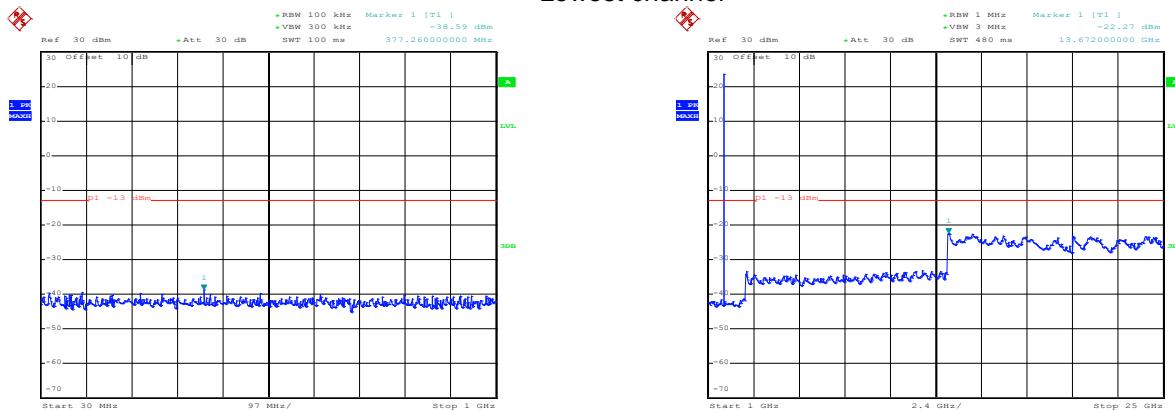


5MHz

16 QAM & RB Size 1 Lowest channel



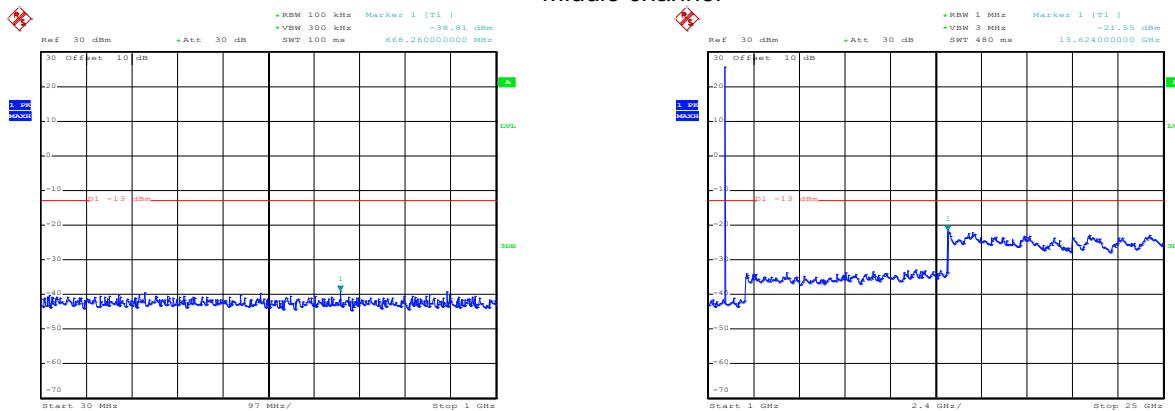
Date: 17.NOV.2017 18:34:56

30MHz~1GHz

Date: 17.NOV.2017 18:50:43

1GHz~25GHz

Middle channel



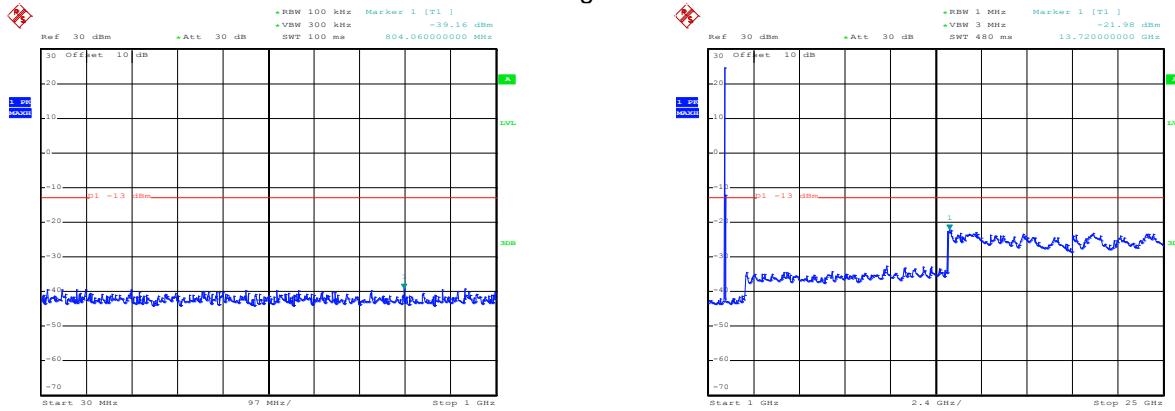
Date: 17.NOV.2017 18:35:26

30MHz~1GHz

Date: 17.NOV.2017 18:53:59

1GHz~25GHz

High channel



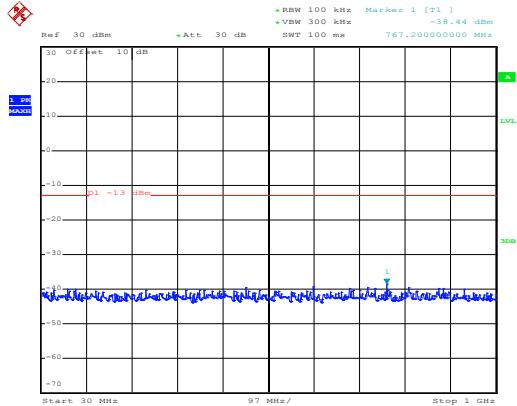
Date: 17.NOV.2017 18:35:49

30MHz~1GHz

Date: 17.NOV.2017 18:54:54

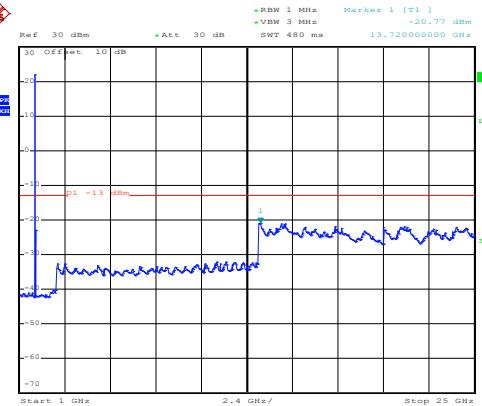
1GHz~25GHz

**16 QAM & RB Size 25
Lowest channel**



Date: 17.NOV.2017 18:35:07

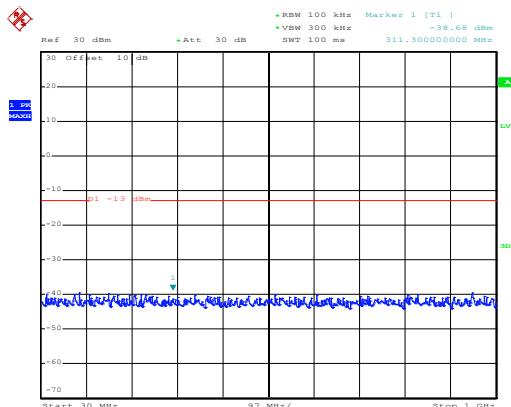
30MHz~1GHz



Date: 17.NOV.2017 18:53:19

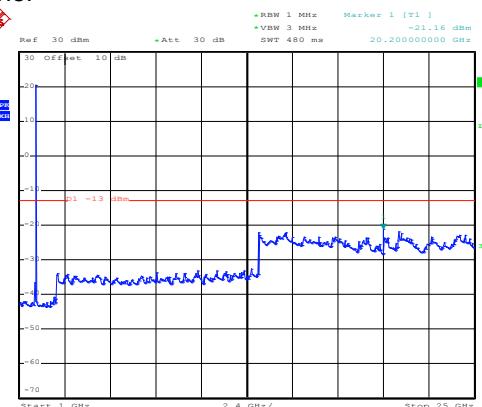
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 18:35:37

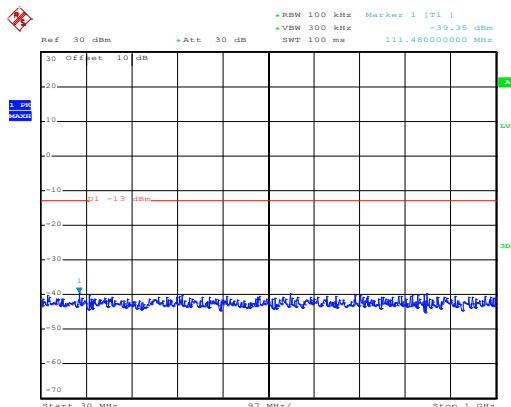
30MHz~1GHz



Date: 17.NOV.2017 18:54:25

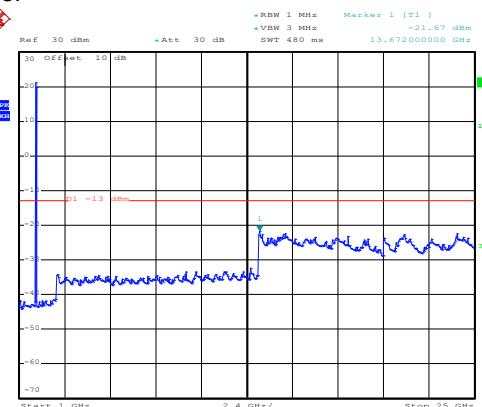
1GHz~25GHz

High channel



Date: 17.NOV.2017 18:35:57

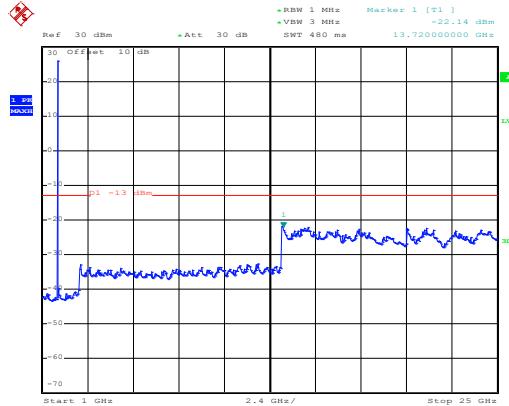
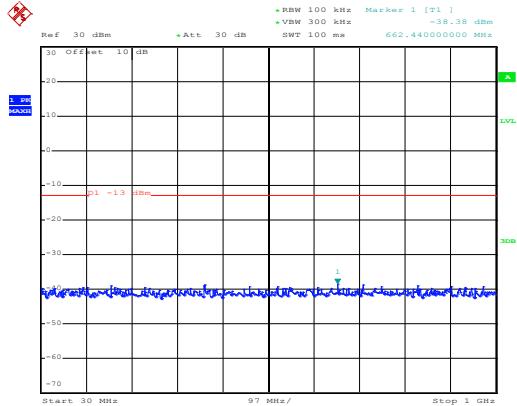
30MHz~1GHz



Date: 17.NOV.2017 18:55:14

1GHz~25GHz

**QPSK & RB Size 1
Lowest channel**



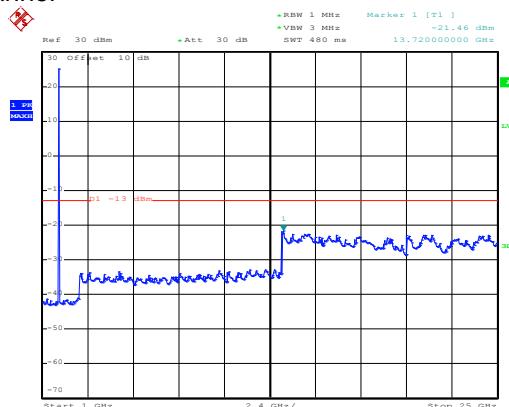
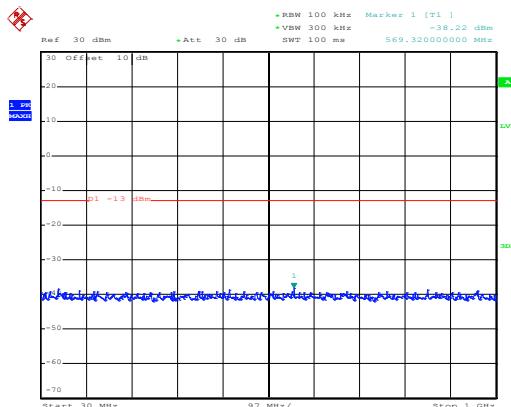
Date: 17.NOV.2017 18:34:53

30MHz~1GHz

Date: 17.NOV.2017 18:50:23

1GHz~25GHz

Middle channel



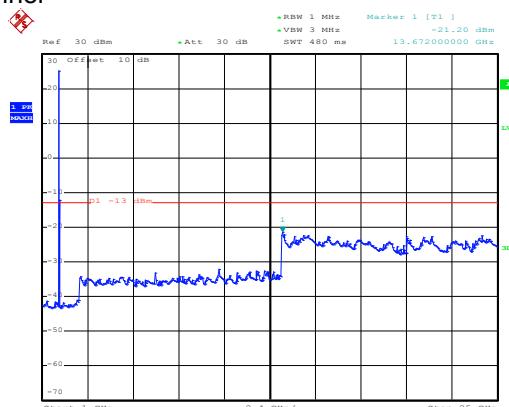
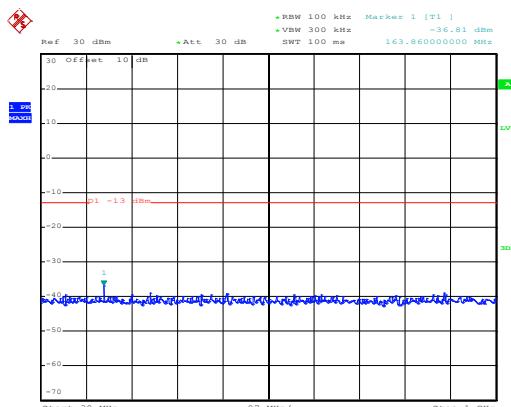
Date: 17.NOV.2017 18:35:23

30MHz~1GHz

Date: 17.NOV.2017 18:53:42

1GHz~25GHz

High channel



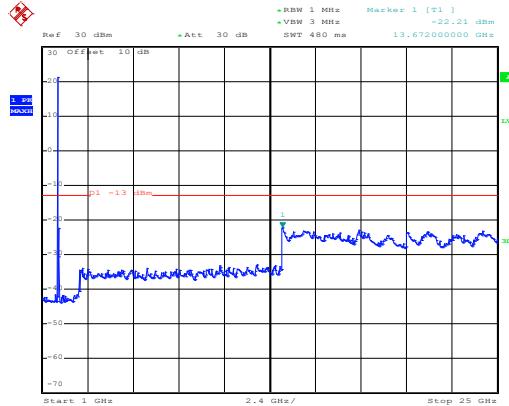
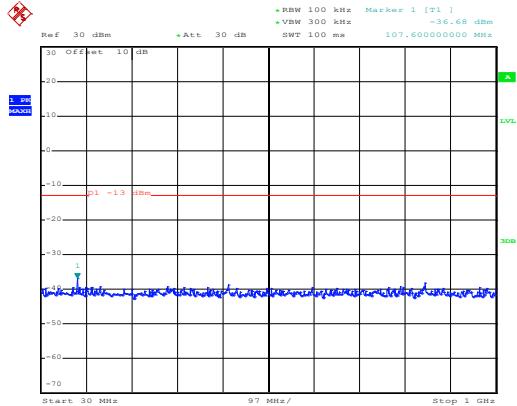
Date: 17.NOV.2017 18:35:45

30MHz~1GHz

Date: 17.NOV.2017 18:54:45

1GHz~25GHz

**QPSK & RB Size 25
Lowest channel**



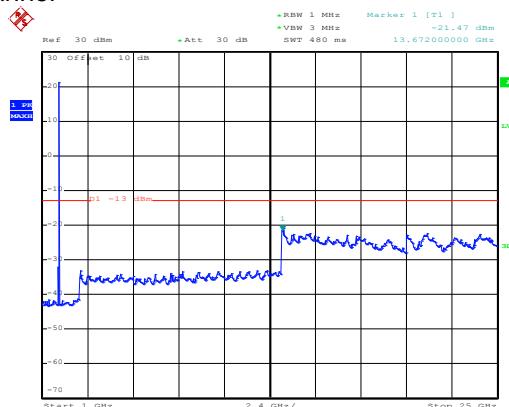
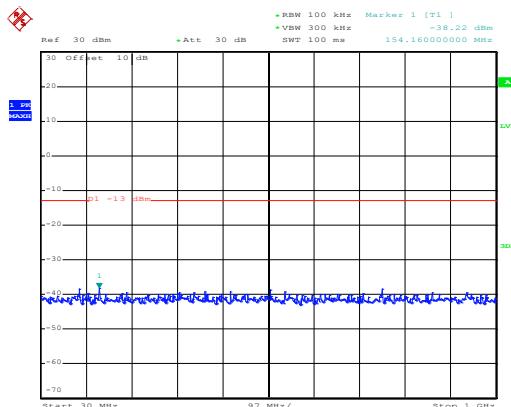
Date: 17.NOV.2017 18:35:04

30MHz~1GHz

Date: 17.NOV.2017 18:50:55

1GHz~25GHz

Middle channel



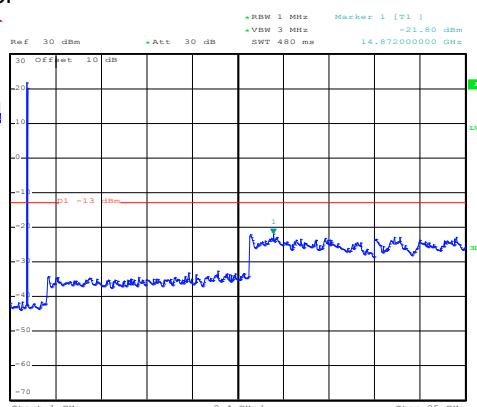
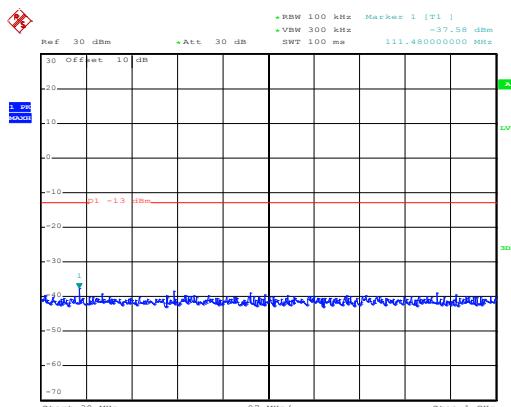
Date: 17.NOV.2017 18:35:32

30MHz~1GHz

Date: 17.NOV.2017 18:54:15

1GHz~25GHz

High channel



Date: 17.NOV.2017 18:35:54

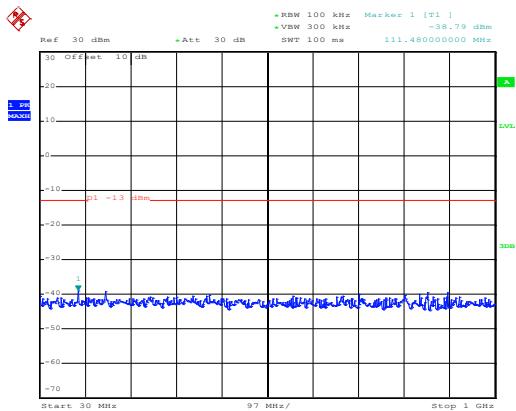
30MHz~1GHz

Date: 17.NOV.2017 18:55:04

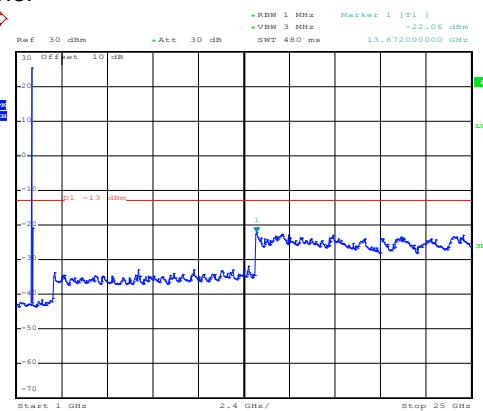
1GHz~25GHz

10MHz

16 QAM & RB Size 1 Lowest channel



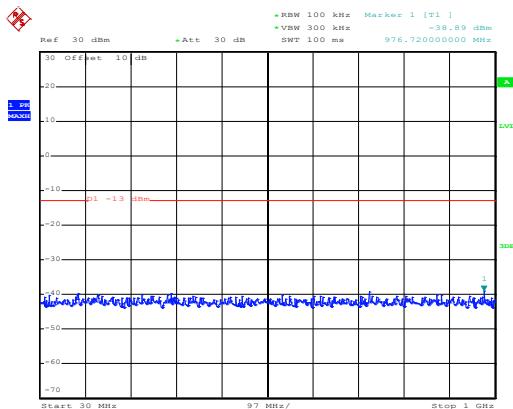
Date: 17.NOV.2017 18:36:11



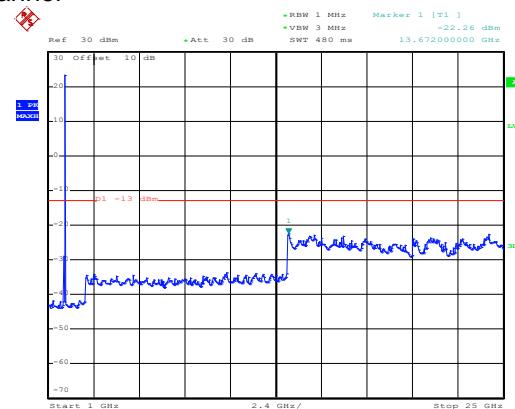
Date: 17.NOV.2017 18:56:10

30MHz~1GHz

Middle channel



Date: 17.NOV.2017 18:36:41

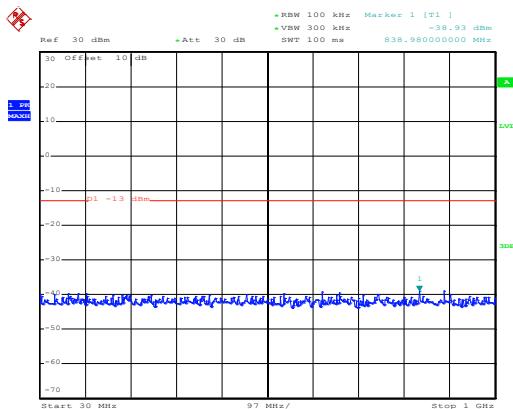


Date: 17.NOV.2017 18:57:00

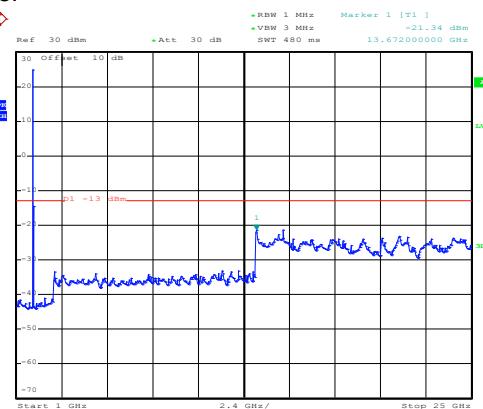
30MHz~1GHz

1GHz~25GHz

High channel



Date: 17.NOV.2017 18:37:10

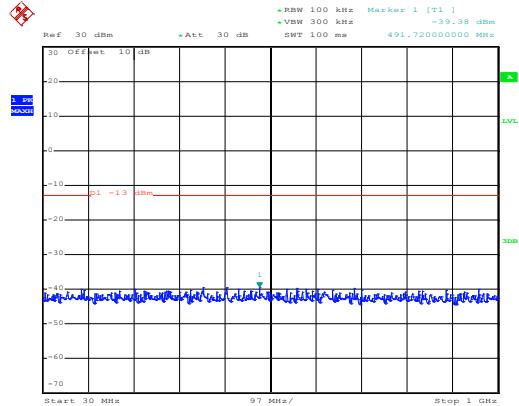


Date: 17.NOV.2017 18:57:48

30MHz~1GHz

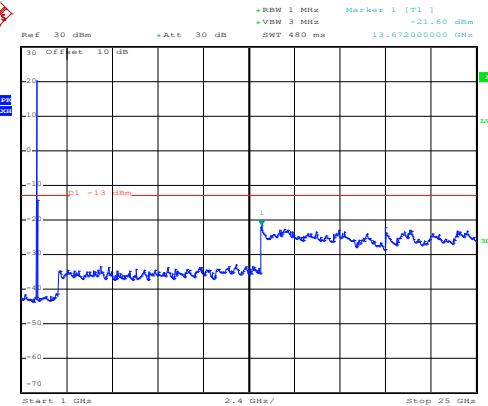
1GHz~25GHz

16 QAM & RB Size 50 Lowest channel



Date: 17.NOV.2017 18:36:29

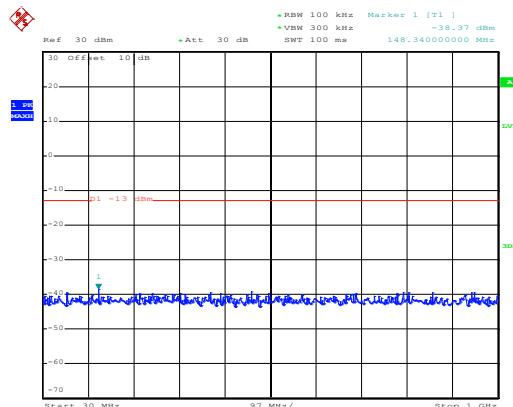
30MHz~1GHz



Date: 17.NOV.2017 18:56:33

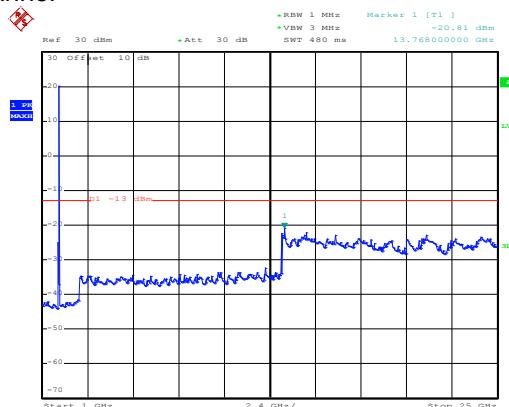
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 18:36:55

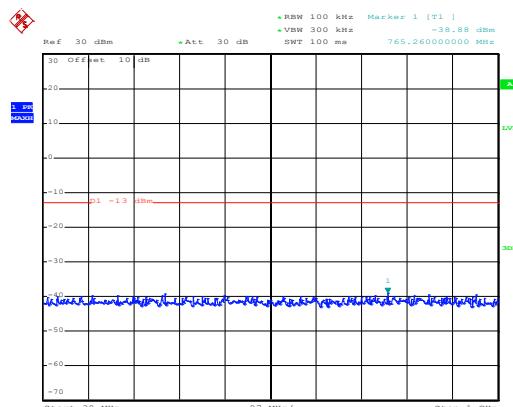
30MHz~1GHz



Date: 17.NOV.2017 18:57:19

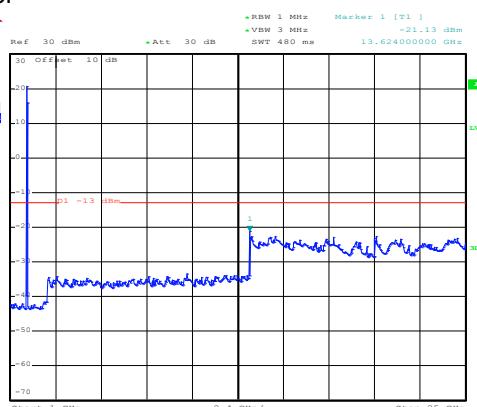
1GHz~25GHz

High channel



Date: 17.NOV.2017 18:37:21

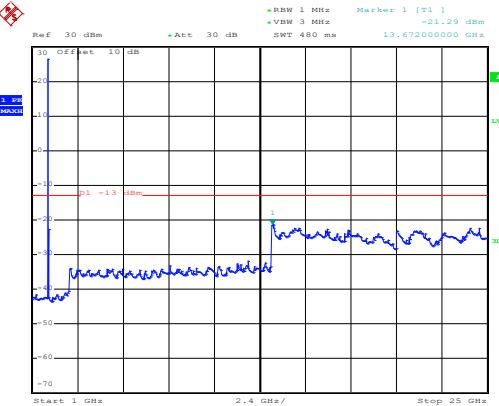
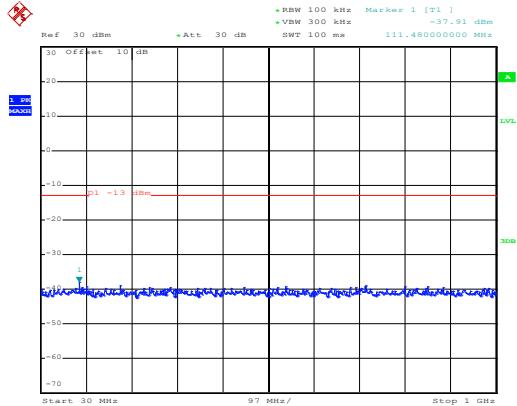
30MHz~1GHz



Date: 17.NOV.2017 18:58:08

1GHz~25GHz

**QPSK & RB Size 1
Lowest channel**



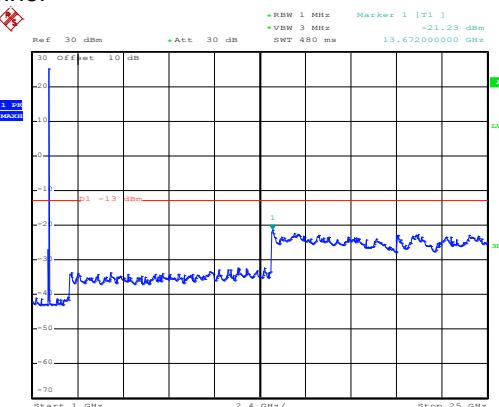
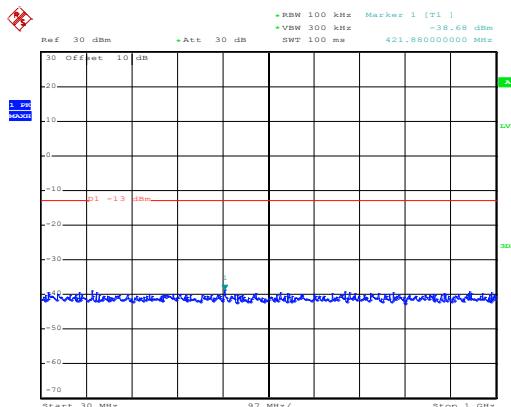
Date: 17.NOV.2017 18:36:08

30MHz~1GHz

Date: 17.NOV.2017 18:55:58

1GHz~25GHz

Middle channel



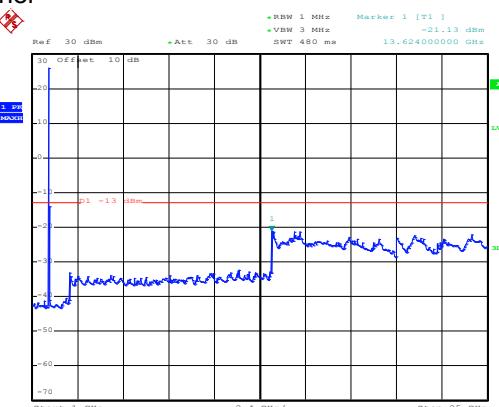
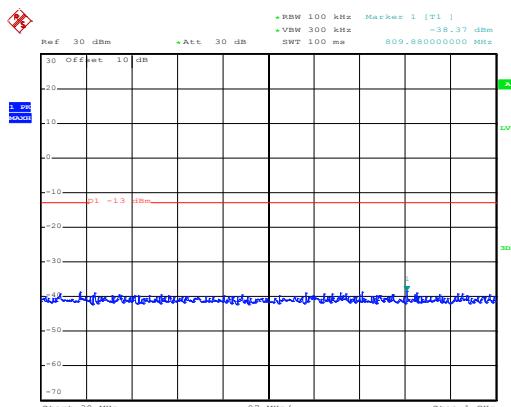
Date: 17.NOV.2017 18:36:37

30MHz~1GHz

Date: 17.NOV.2017 18:56:55

1GHz~25GHz

High channel



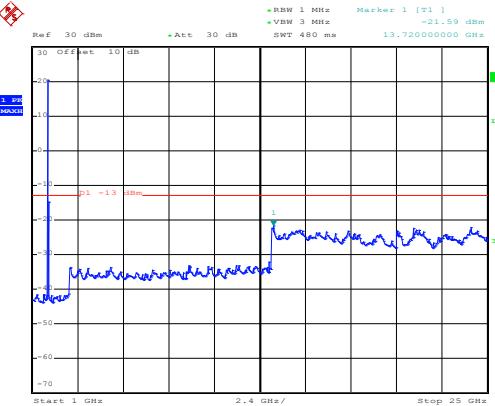
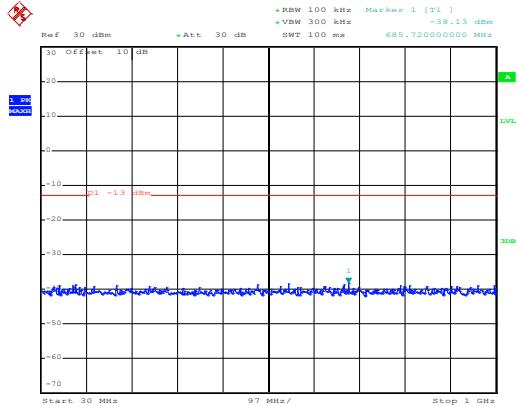
Date: 17.NOV.2017 18:37:06

30MHz~1GHz

Date: 17.NOV.2017 18:57:43

1GHz~25GHz

**QPSK & RB Size 50
Lowest channel**



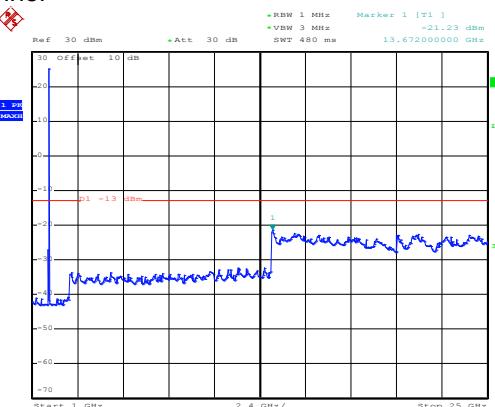
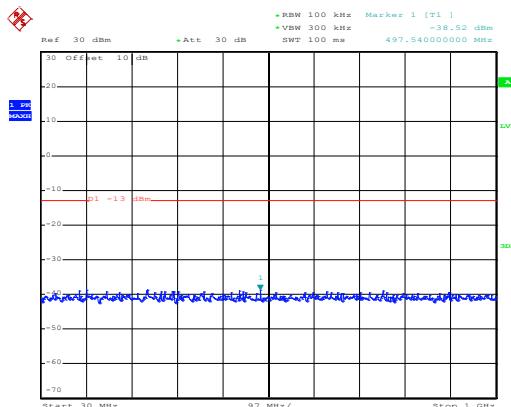
Date: 17.NOV.2017 18:36:26

30MHz~1GHz

Date: 17.NOV.2017 18:56:21

1GHz~25GHz

Middle channel



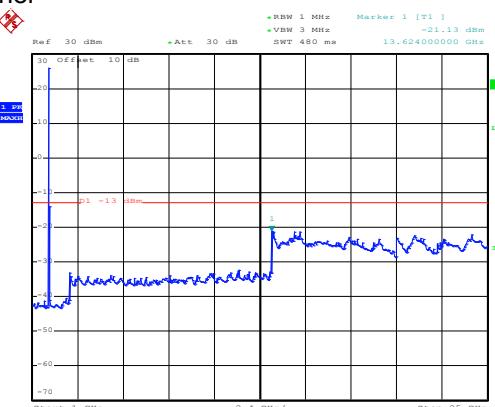
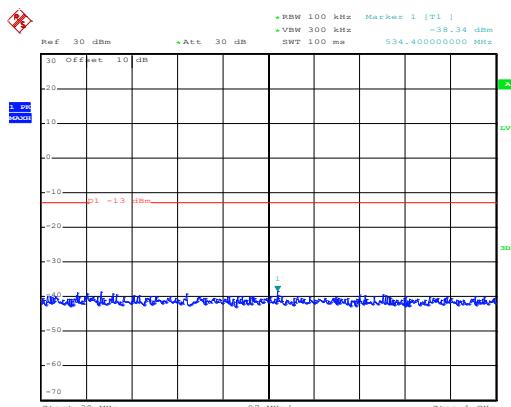
Date: 17.NOV.2017 18:36:51

30MHz~1GHz

Date: 17.NOV.2017 18:56:55

1GHz~25GHz

High channel



Date: 17.NOV.2017 18:37:16

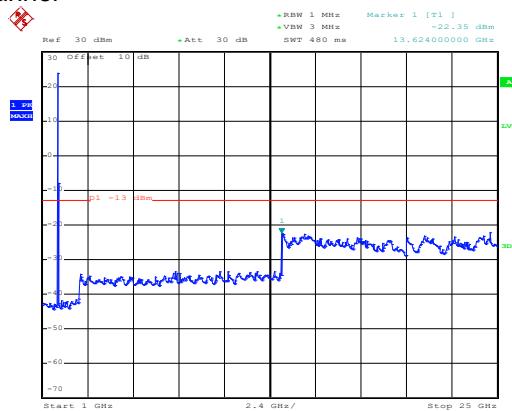
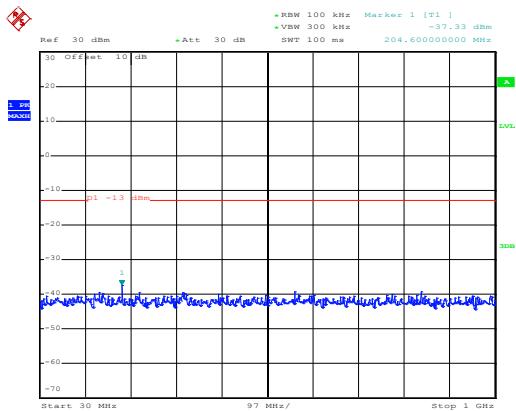
30MHz~1GHz

Date: 17.NOV.2017 18:57:43

1GHz~25GHz

15MHz

16 QAM & RB Size 1 Lowest channel

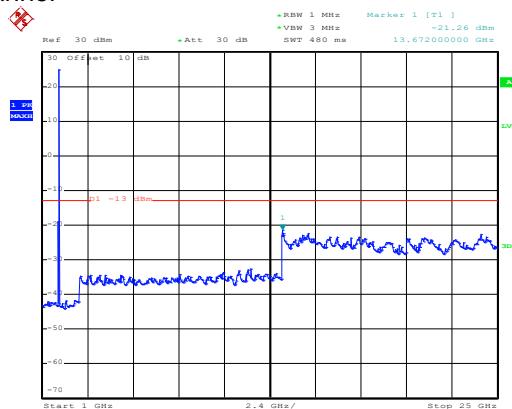
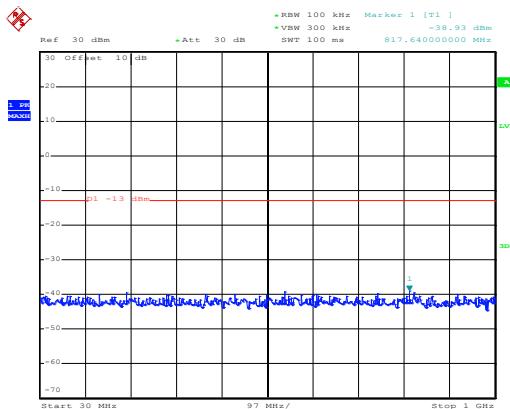


Date: 17.NOV.2017 18:37:37

Date: 17.NOV.2017 18:59:11

30MHz~1GHz

Middle channel



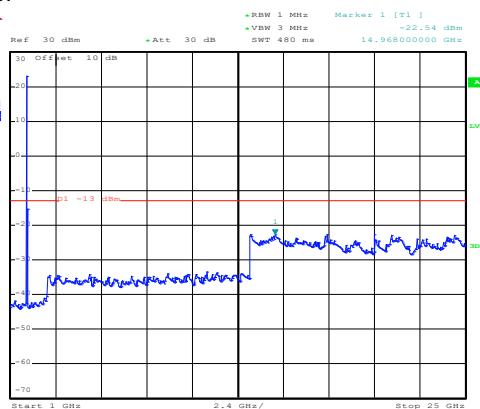
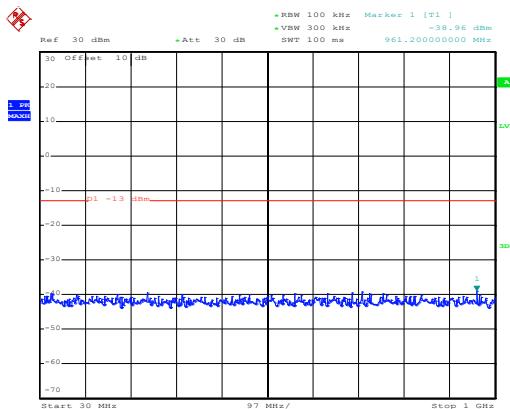
Date: 17.NOV.2017 18:38:01

Date: 17.NOV.2017 18:59:54

30MHz~1GHz

1GHz~25GHz

High channel



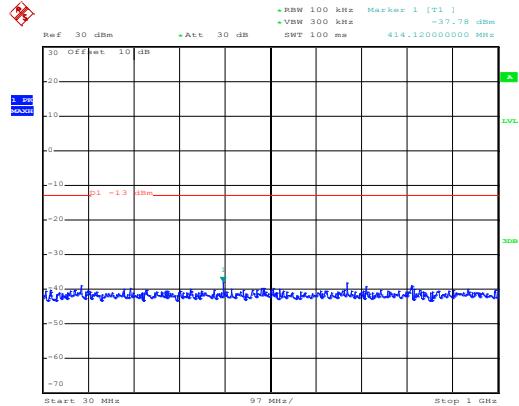
Date: 17.NOV.2017 18:38:25

Date: 17.NOV.2017 19:00:35

30MHz~1GHz

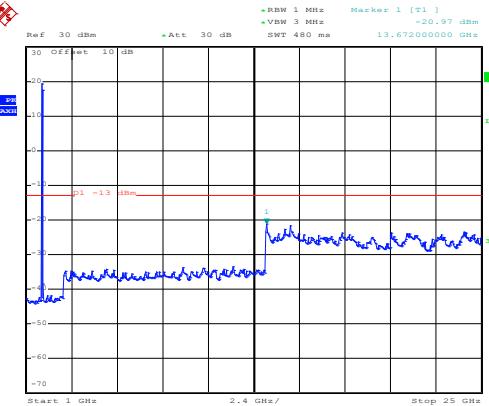
1GHz~25GHz

16 QAM & RB Size 75 Lowest channel



Date: 17.NOV.2017 18:37:47

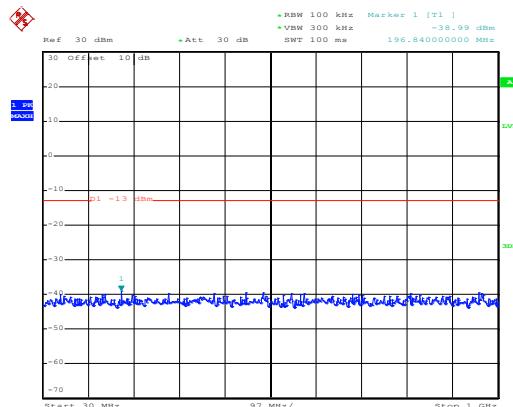
30MHz~1GHz



Date: 17.NOV.2017 18:59:32

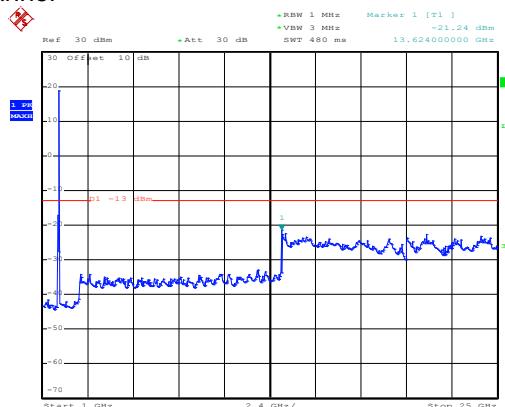
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 18:38:10

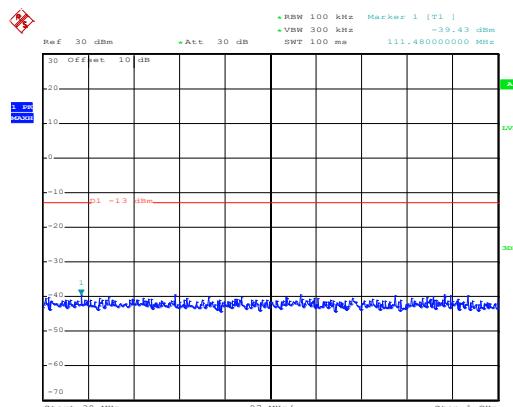
30MHz~1GHz



Date: 17.NOV.2017 19:00:11

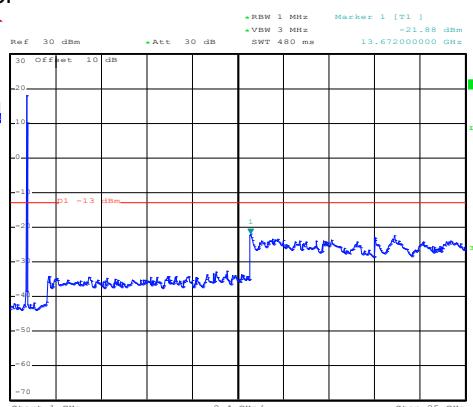
1GHz~25GHz

High channel



Date: 17.NOV.2017 18:38:35

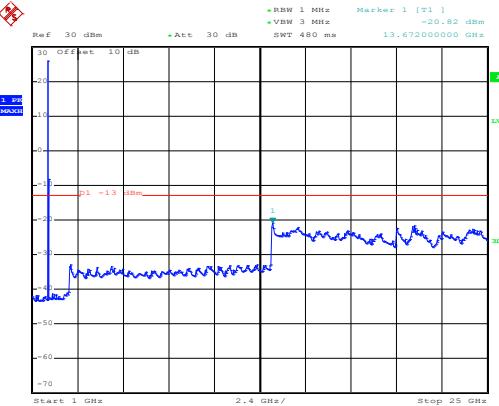
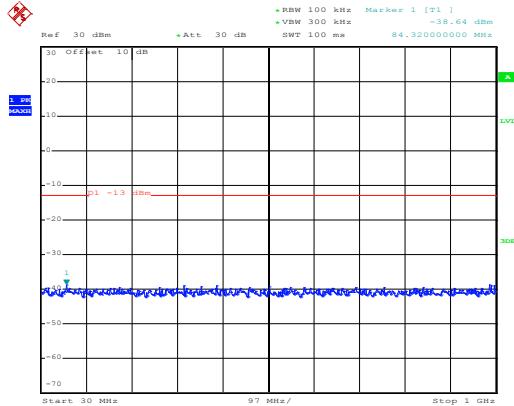
30MHz~1GHz



Date: 17.NOV.2017 19:00:53

1GHz~25GHz

**QPSK & RB Size 1
Lowest channel**



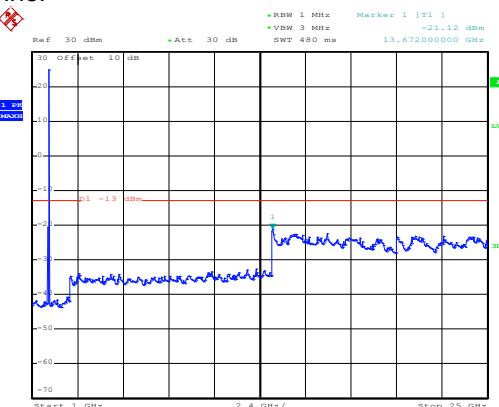
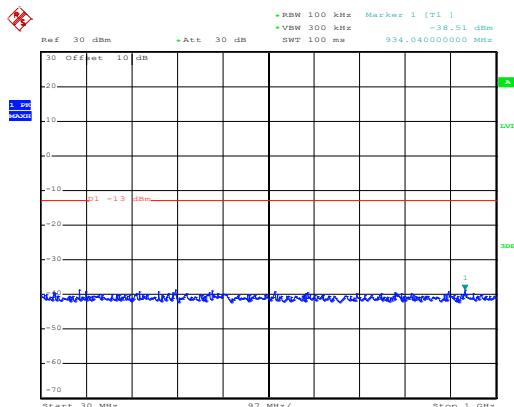
Date: 17.NOV.2017 18:37:33

30MHz~1GHz

Date: 17.NOV.2017 18:59:01

1GHz~25GHz

Middle channel



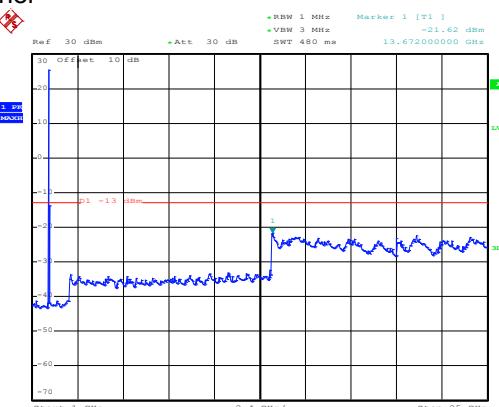
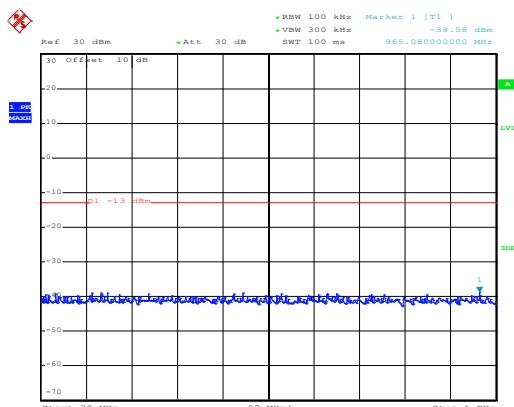
Date: 17.NOV.2017 18:37:57

30MHz~1GHz

Date: 17.NOV.2017 18:59:47

1GHz~25GHz

High channel



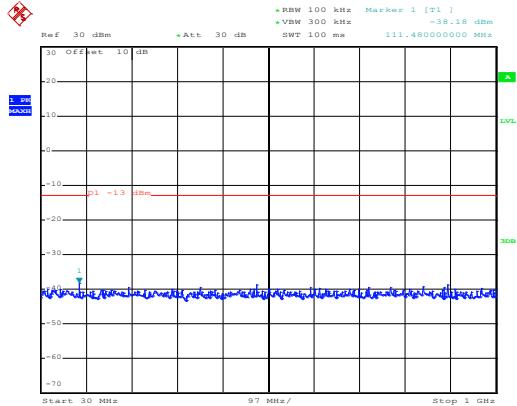
Date: 17.NOV.2017 18:38:21

30MHz~1GHz

Date: 17.NOV.2017 19:00:28

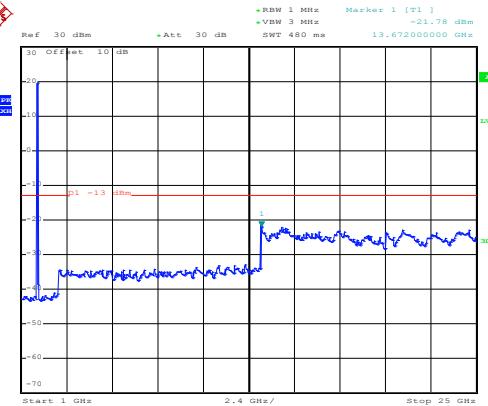
1GHz~25GHz

**QPSK & RB Size 75
Lowest channel**



Date: 17.NOV.2017 18:37:42

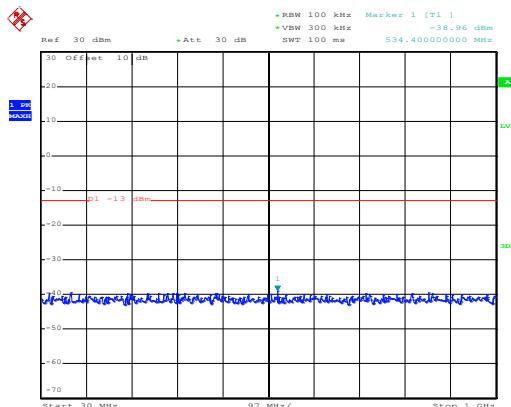
30MHz~1GHz



Date: 17.NOV.2017 18:59:26

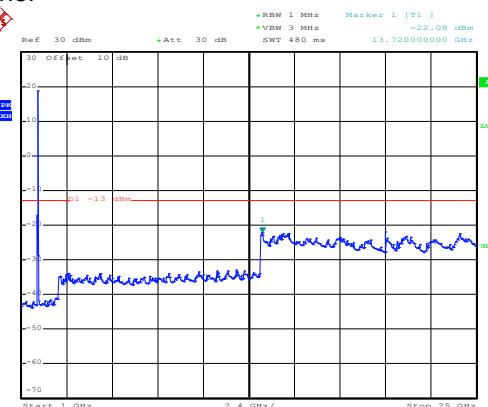
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 18:38:07

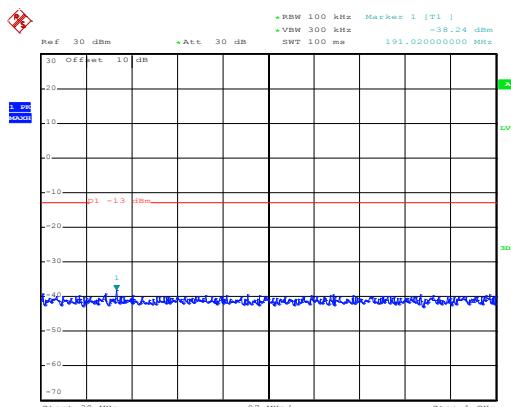
30MHz~1GHz



Date: 17.NOV.2017 19:00:06

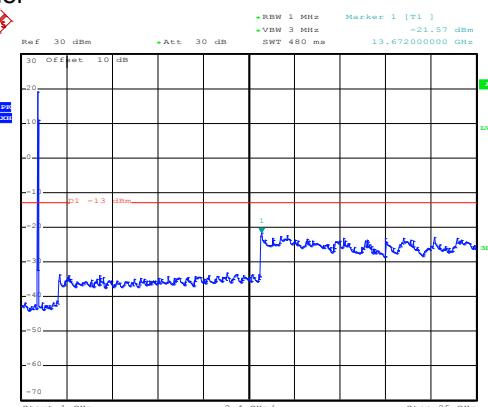
1GHz~25GHz

High channel



Date: 17.NOV.2017 18:38:32

30MHz~1GHz

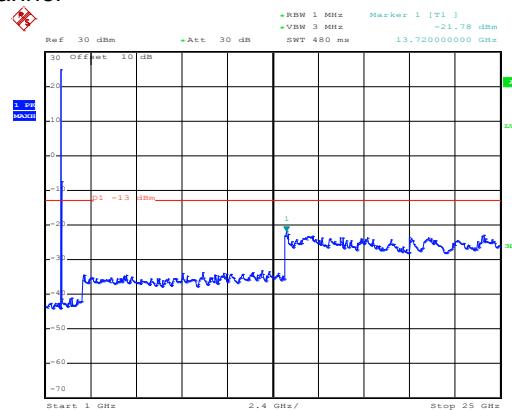
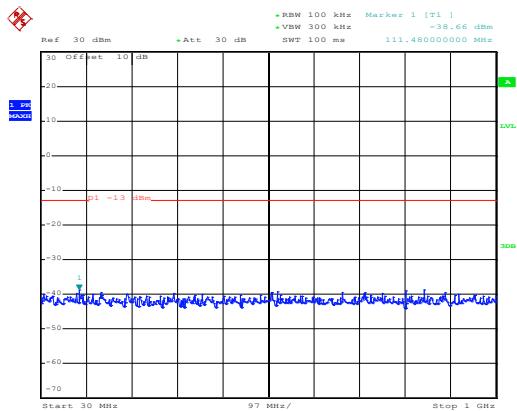


Date: 17.NOV.2017 19:00:45

1GHz~25GHz

20MHz

16 QAM & RB Size 1 Lowest channel



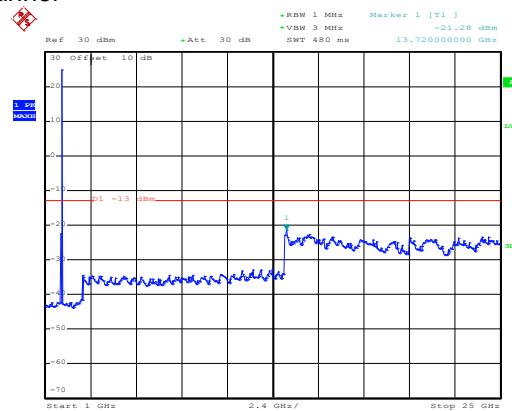
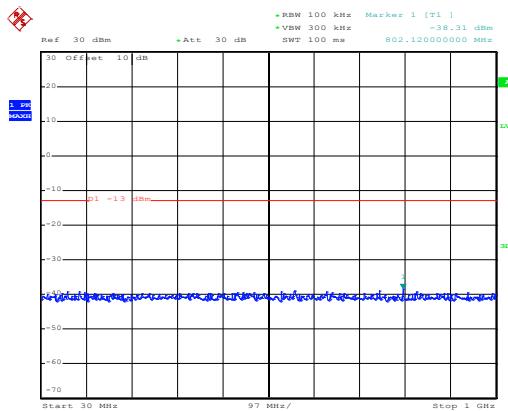
Date: 17.NOV.2017 18:38:52

Date: 17.NOV.2017 19:01:45

30MHz~1GHz

1GHz~25GHz

Middle channel



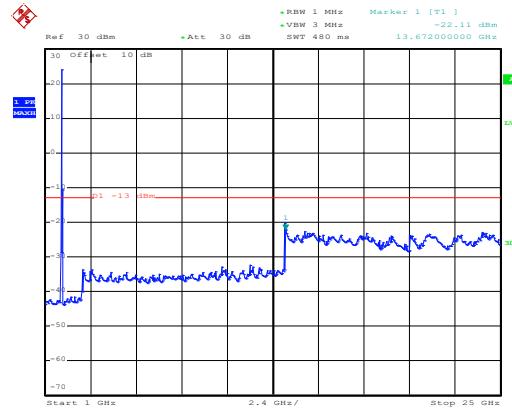
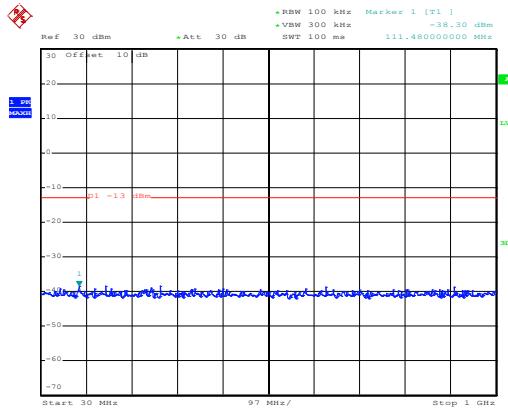
Date: 17.NOV.2017 18:39:52

Date: 17.NOV.2017 19:02:32

30MHz~1GHz

1GHz~25GHz

High channel



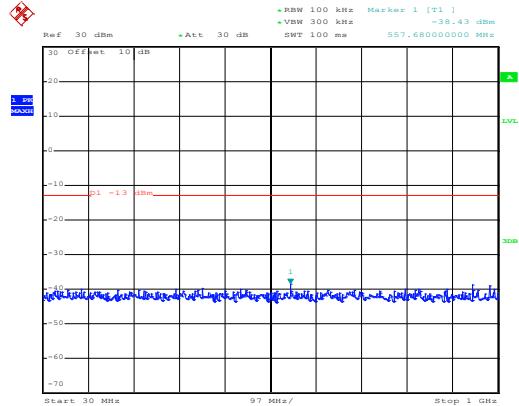
Date: 17.NOV.2017 18:40:59

Date: 17.NOV.2017 19:03:27

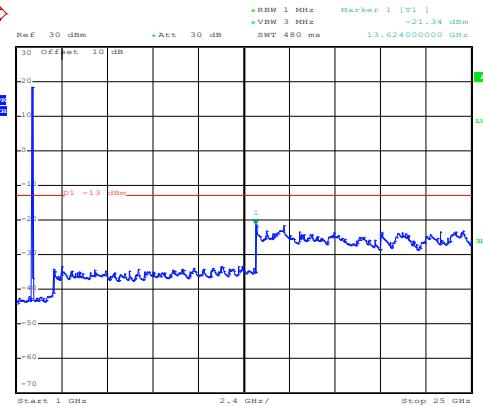
30MHz~1GHz

1GHz~25GHz

16 QAM & RB Size 100 Lowest channel



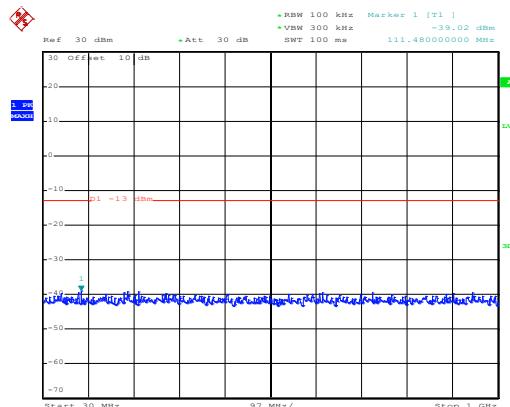
Date: 17.NOV.2017 18:39:14



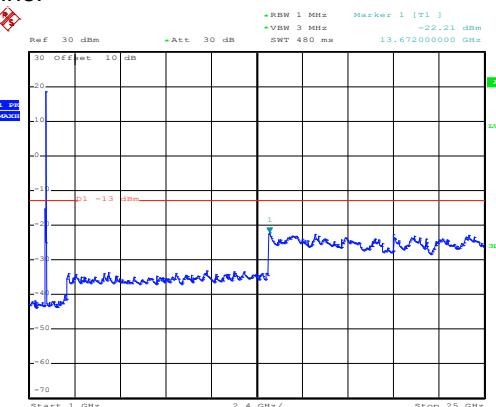
Date: 17.NOV.2017 19:02:05

30MHz~1GHz

Middle channel



Date: 17.NOV.2017 18:40:33

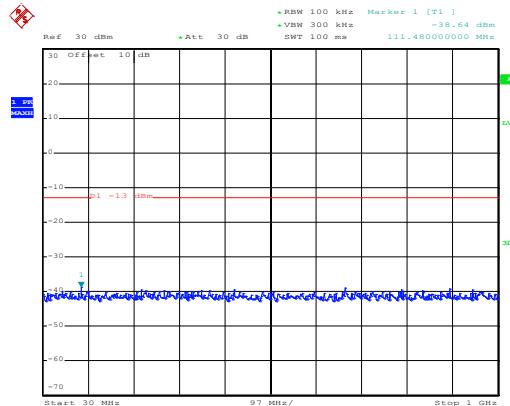


Date: 17.NOV.2017 19:02:58

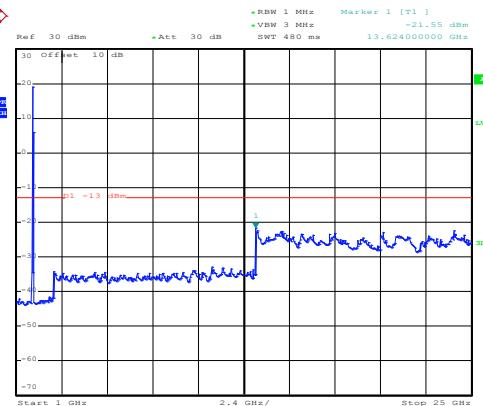
30MHz~1GHz

1GHz~25GHz

High channel



Date: 17.NOV.2017 18:41:18

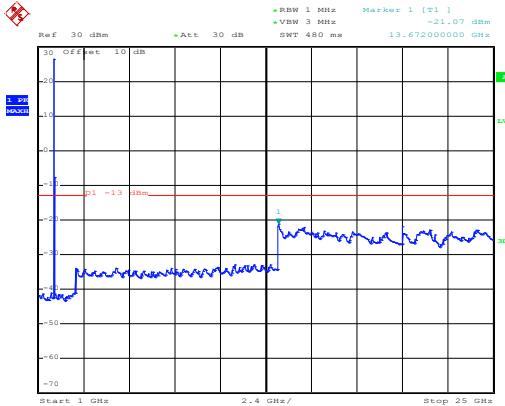
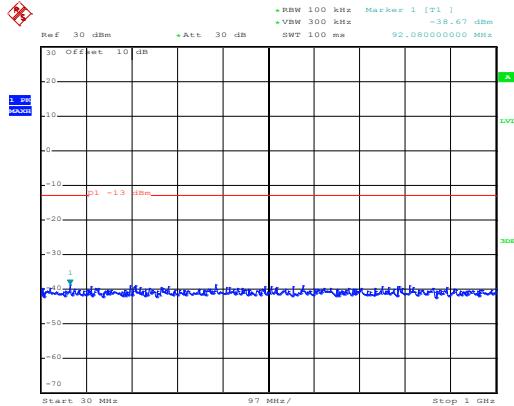


Date: 17.NOV.2017 19:03:49

30MHz~1GHz

1GHz~25GHz

**QPSK & RB Size 1
Lowest channel**



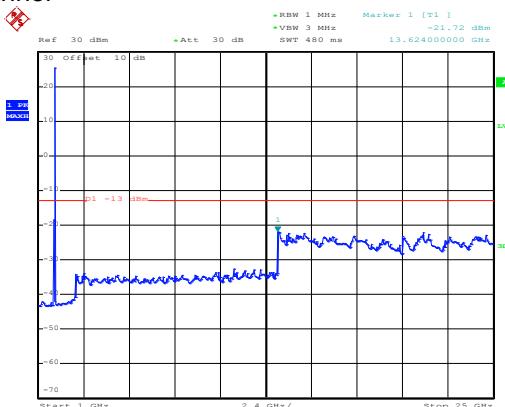
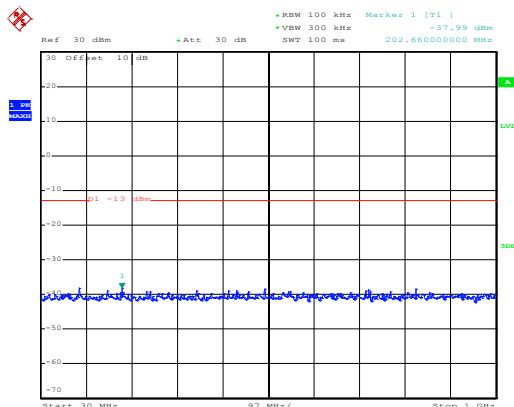
Date: 17.NOV.2017 18:38:47

30MHz~1GHz

Date: 17.NOV.2017 19:01:36

1GHz~25GHz

Middle channel



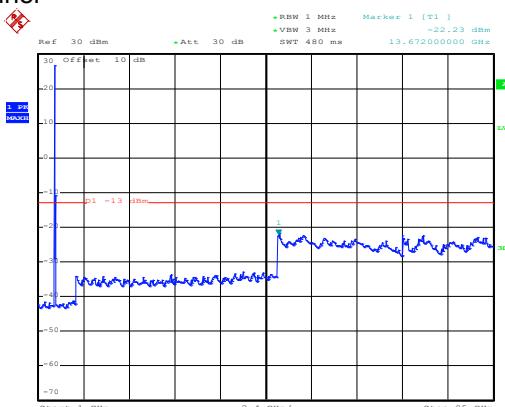
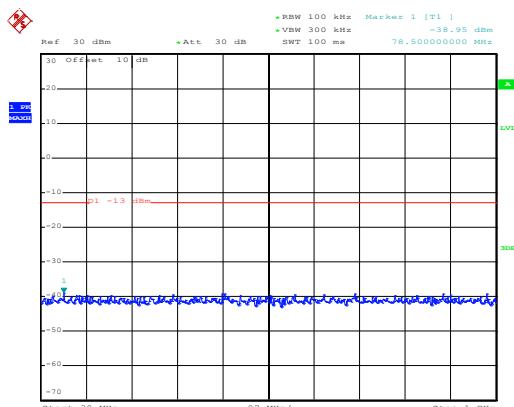
Date: 17.NOV.2017 18:39:30

30MHz~1GHz

Date: 17.NOV.2017 19:02:24

1GHz~25GHz

High channel



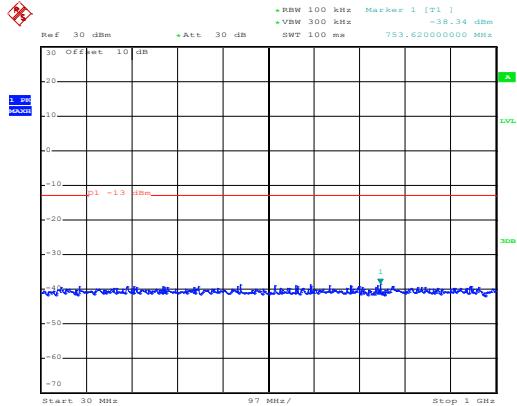
Date: 17.NOV.2017 18:40:42

30MHz~1GHz

Date: 17.NOV.2017 19:03:17

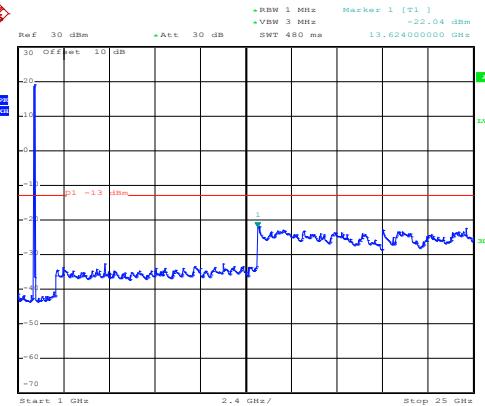
1GHz~25GHz

**QPSK & RB Size 100
Lowest channel**



Date: 17.NOV.2017 18:39:10

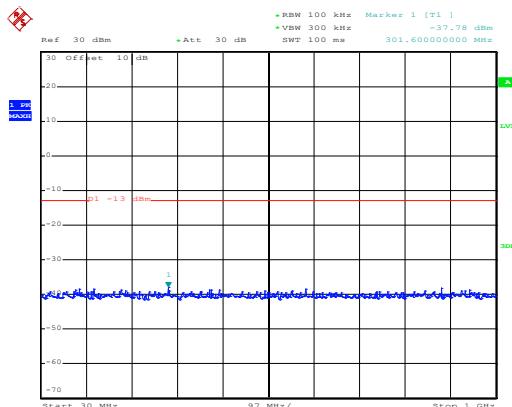
30MHz~1GHz



Date: 17.NOV.2017 19:01:57

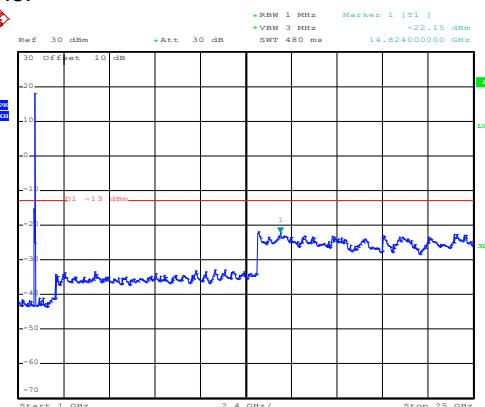
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 18:40:20

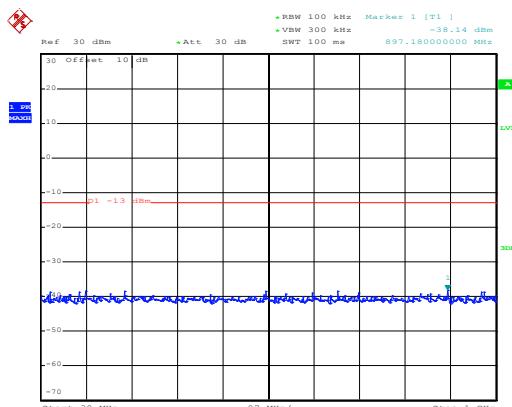
30MHz~1GHz



Date: 17.NOV.2017 19:02:48

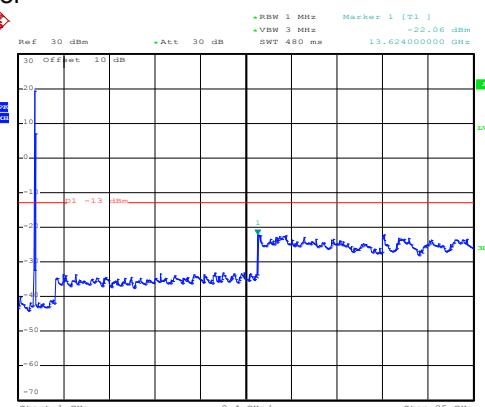
1GHz~25GHz

High channel



Date: 17.NOV.2017 18:41:12

30MHz~1GHz

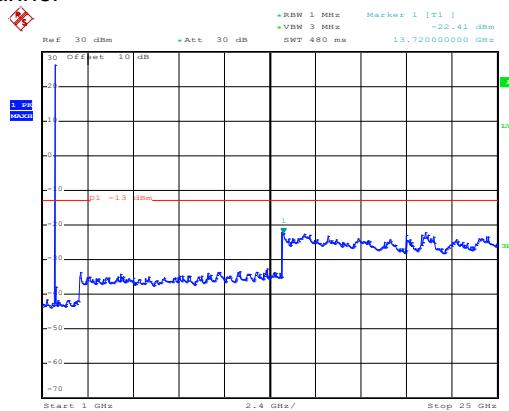
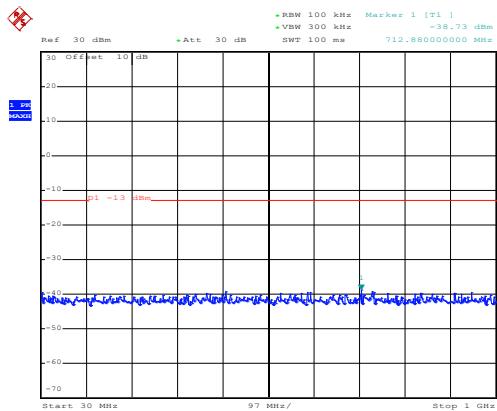


Date: 17.NOV.2017 19:03:41

1GHz~25GHz

LTE band 4/66, 1.4MHz

16 QAM & RB Size 1 Lowest channel

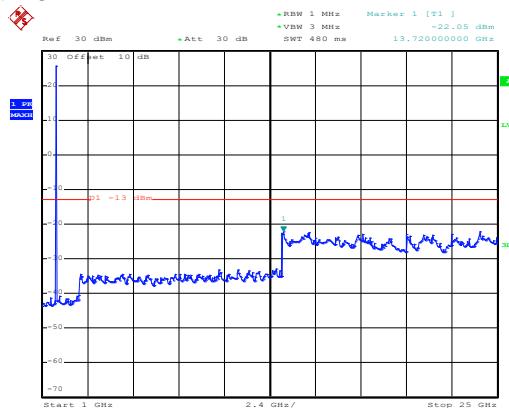
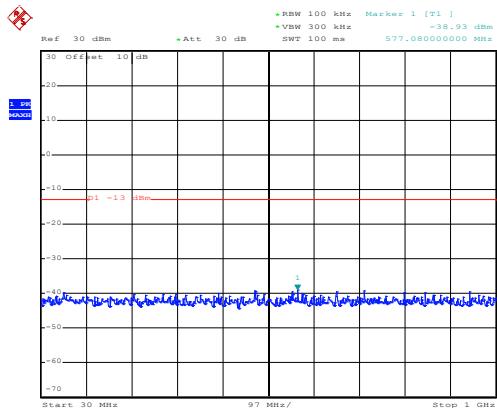


Date: 17.NOV.2017 19:34:48

Date: 17.NOV.2017 19:12:50

30MHz~1GHz

Middle channel

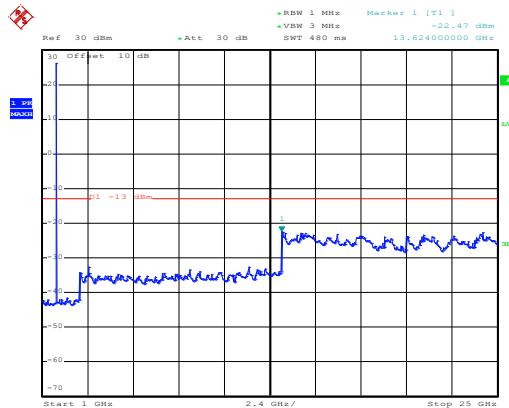
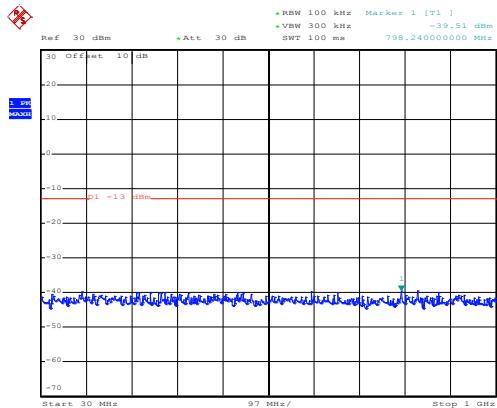


Date: 17.NOV.2017 19:35:12

Date: 17.NOV.2017 19:19:05

30MHz~1GHz

High channel



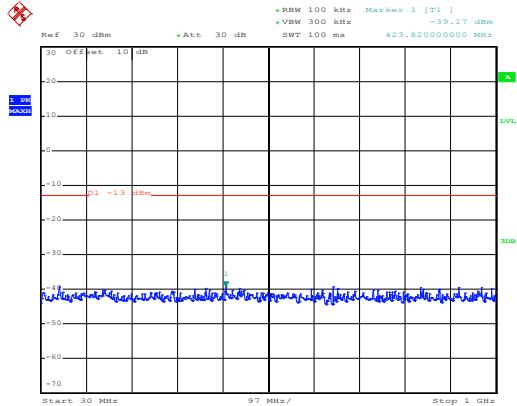
Date: 17.NOV.2017 19:35:44

Date: 17.NOV.2017 19:19:57

30MHz~1GHz

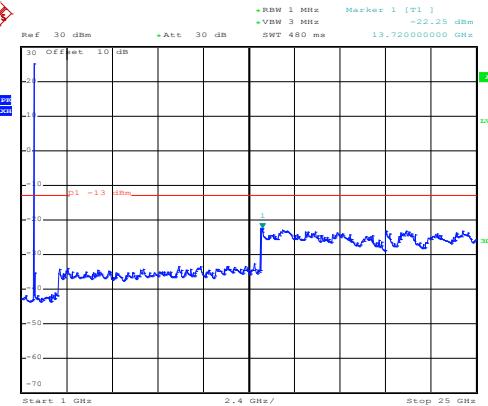
1GHz~25GHz

16 QAM & RB Size 6 Lowest channel



Date: 17.NOV.2017 19:34:57

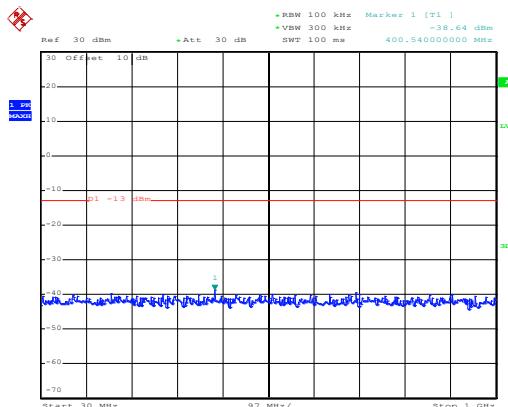
30MHz~1GHz



Date: 17.NOV.2017 19:13:17

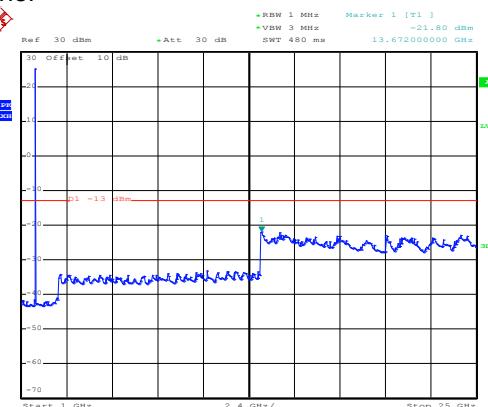
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 19:35:31

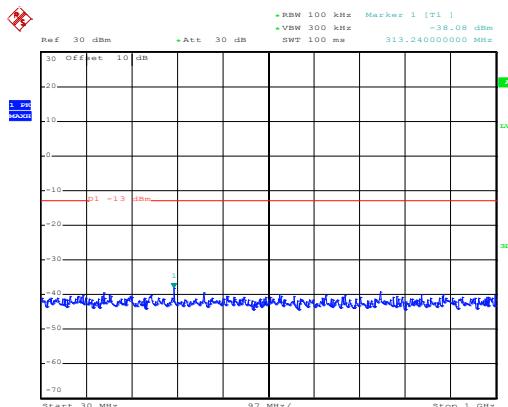
30MHz~1GHz



Date: 17.NOV.2017 19:19:29

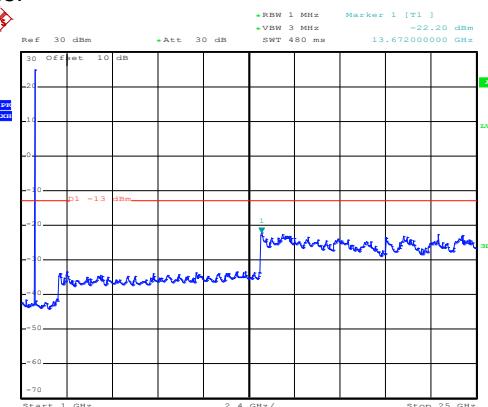
1GHz~25GHz

High channel



Date: 17.NOV.2017 19:35:52

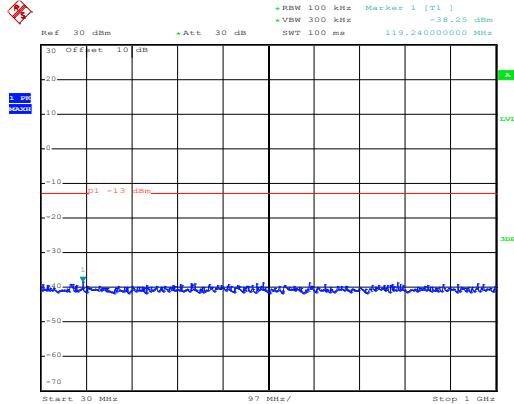
30MHz~1GHz



Date: 17.NOV.2017 19:20:23

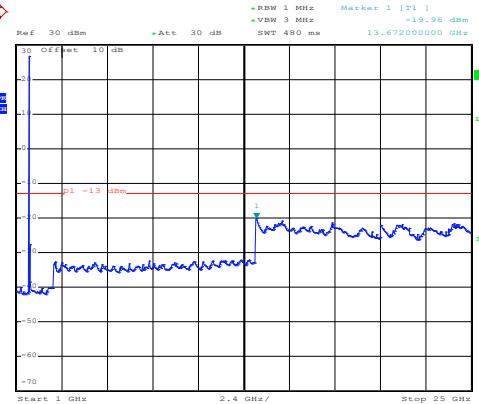
1GHz~25GHz

QPSK & RB Size 1 Lowest channel



Date: 17.NOV.2017 19:34:44

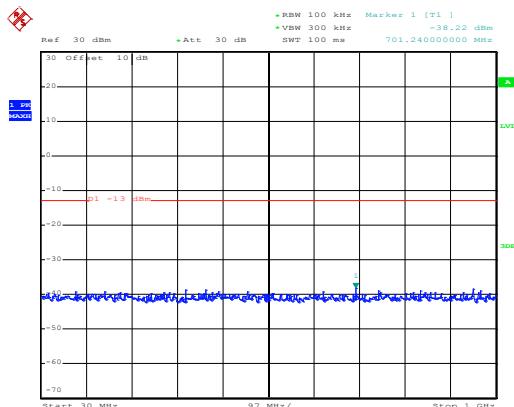
30MHz~1GHz



Date: 17.NOV.2017 19:12:42

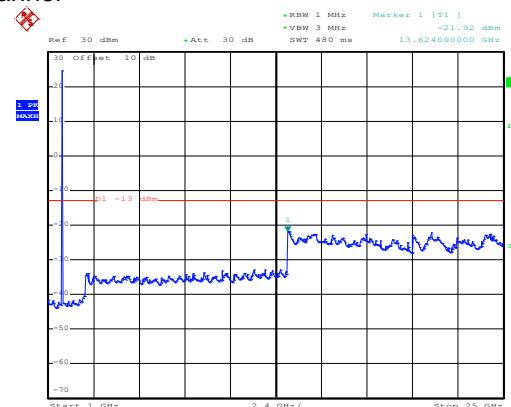
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 19:35:08

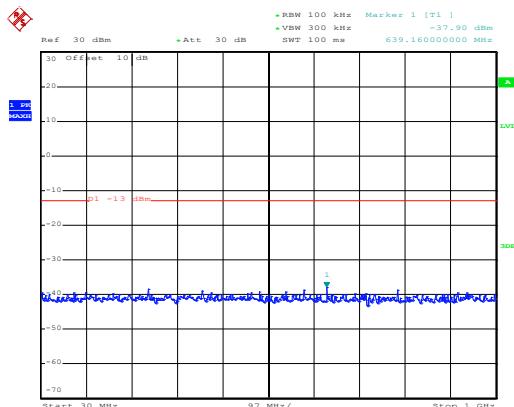
30MHz~1GHz



Date: 17.NOV.2017 19:18:56

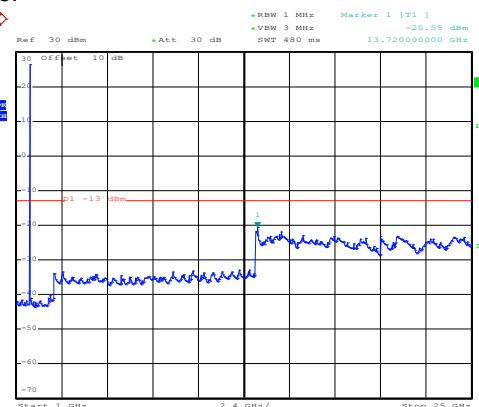
1GHz~25GHz

High channel



Date: 17.NOV.2017 19:35:40

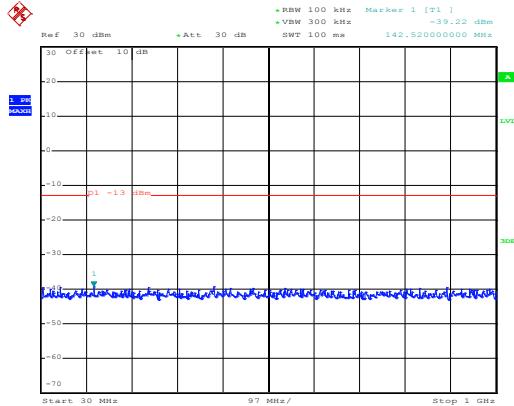
30MHz~1GHz



Date: 17.NOV.2017 19:19:48

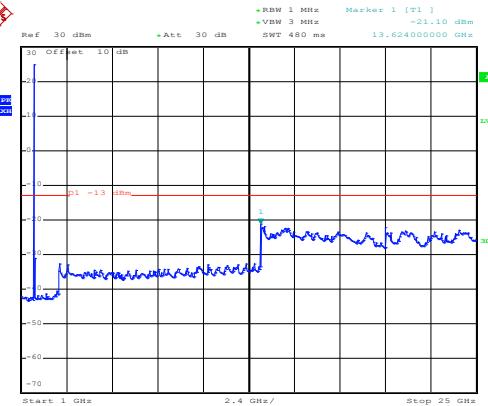
1GHz~25GHz

**QPSK & RB Size 6
Lowest channel**



Date: 17.NOV.2017 19:34:54

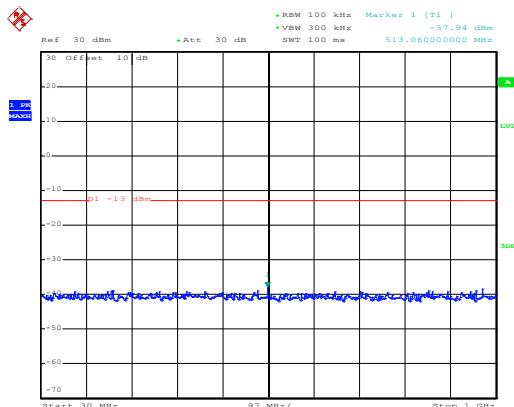
30MHz~1GHz



Date: 17.NOV.2017 19:13:08

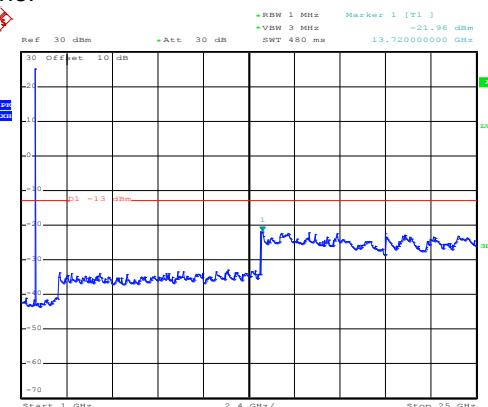
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 19:35:28

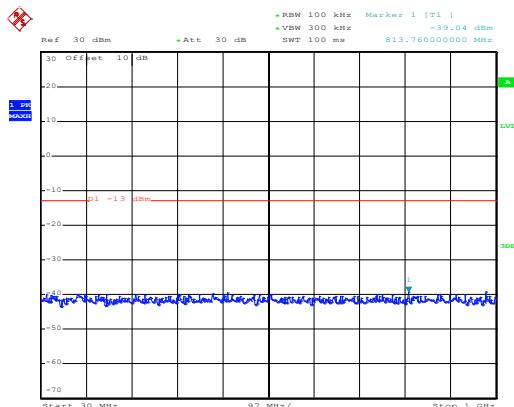
30MHz~1GHz



Date: 17.NOV.2017 19:19:19

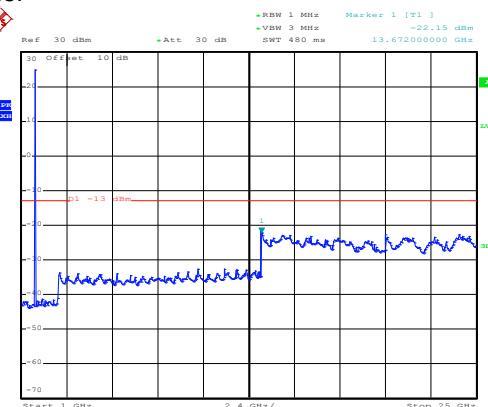
1GHz~25GHz

High channel



Date: 17.NOV.2017 19:35:49

30MHz~1GHz

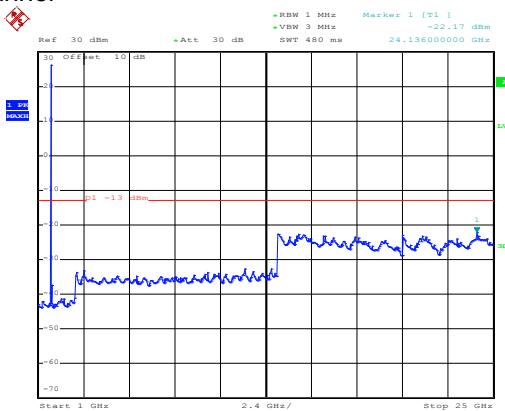
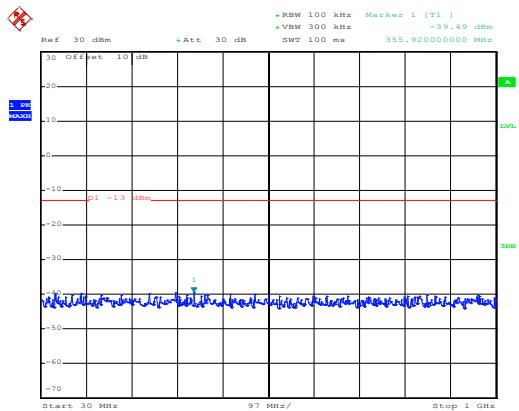


Date: 17.NOV.2017 19:20:08

1GHz~25GHz

3MHz

16 QAM & RB Size 1 Lowest channel



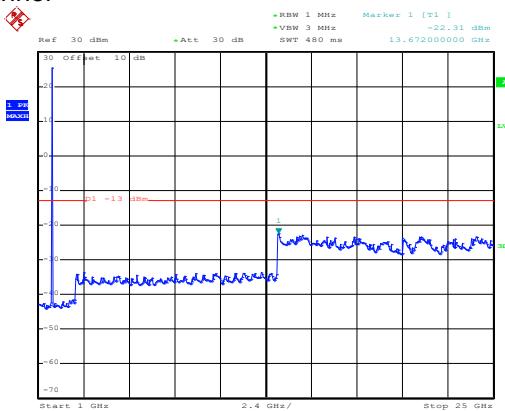
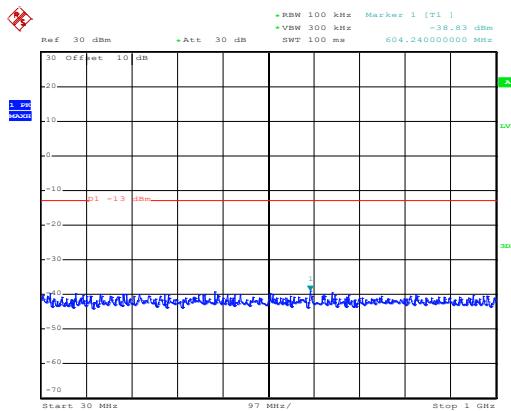
Date: 17.NOV.2017 19:36:06

Date: 17.NOV.2017 19:21:24

30MHz~1GHz

1GHz~25GHz

Middle channel



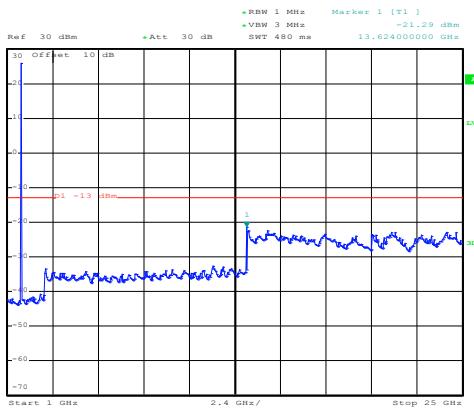
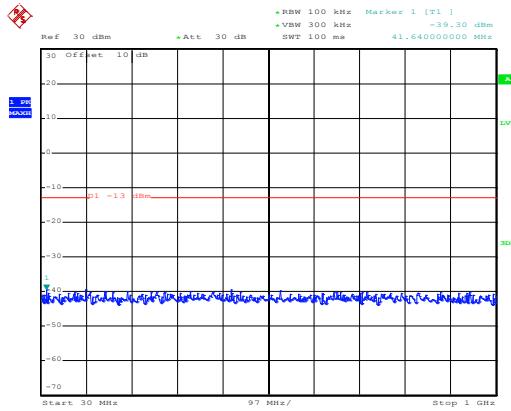
Date: 17.NOV.2017 19:36:29

Date: 17.NOV.2017 19:22:04

30MHz~1GHz

1GHz~25GHz

High channel



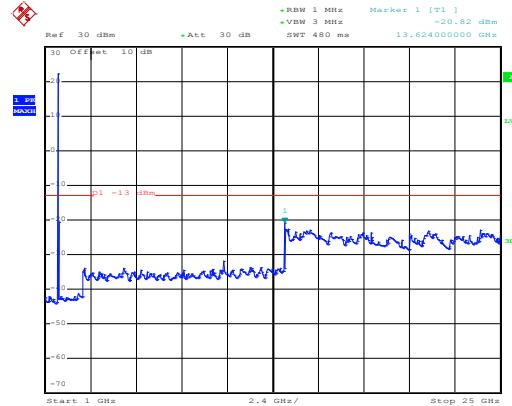
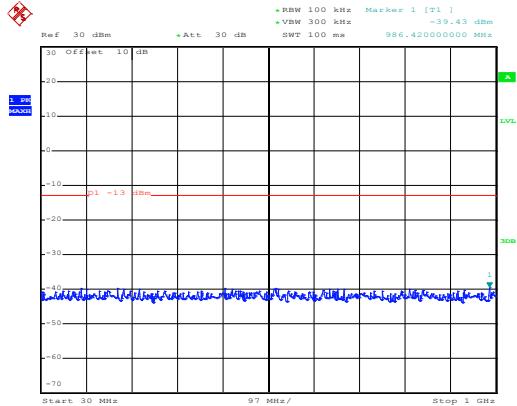
Date: 17.NOV.2017 19:36:52

Date: 17.NOV.2017 19:22:53

30MHz~1GHz

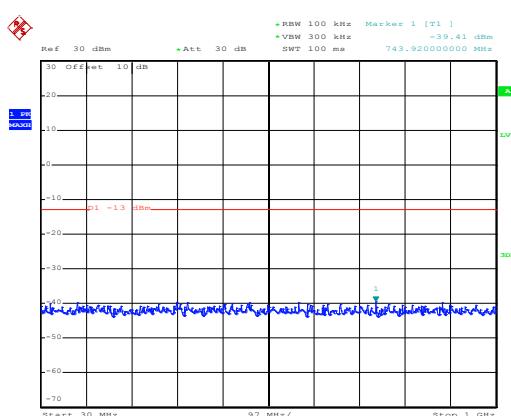
1GHz~25GHz

16 QAM & RB Size 15 Lowest channel

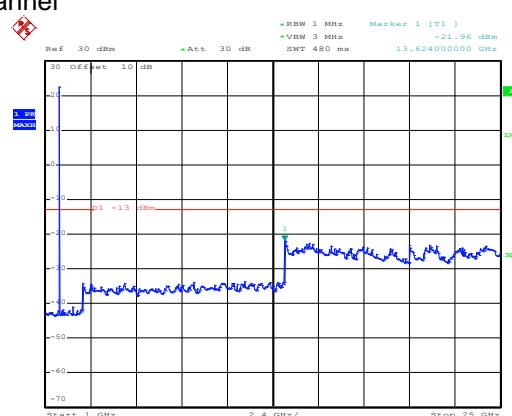


Date: 17.NOV.2017 19:36:17

30MHz~1GHz

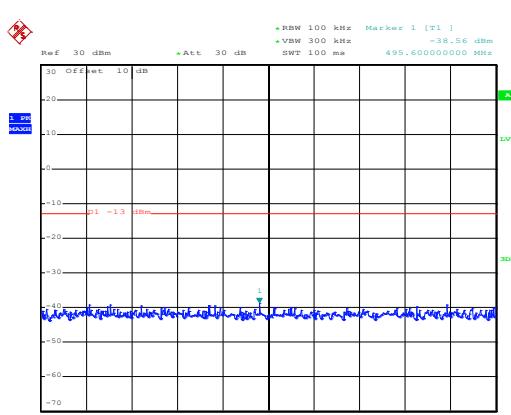


Middle channel

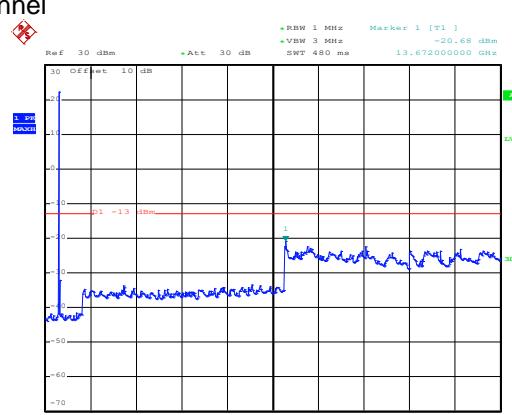


Date: 17.NOV.2017 19:36:41

30MHz~1GHz



High channel



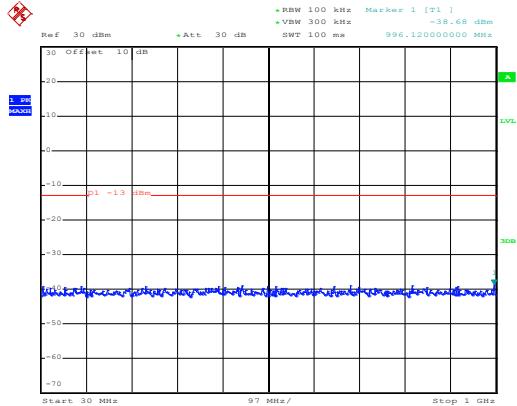
Date: 17.NOV.2017 19:37:05

30MHz~1GHz

Date: 17.NOV.2017 19:23:14

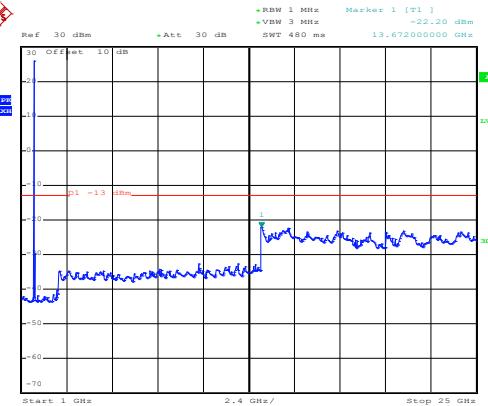
1GHz~25GHz

**QPSK & RB Size 1
Lowest channel**



Date: 17.NOV.2017 19:36:03

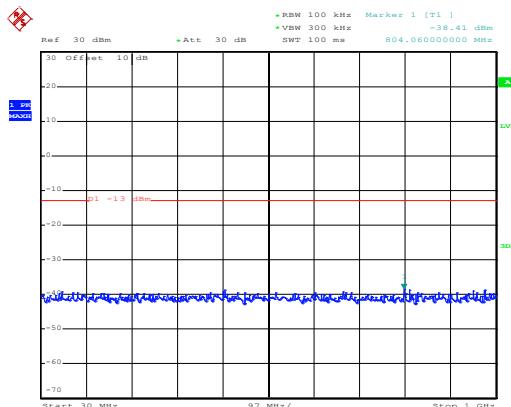
30MHz~1GHz



Date: 17.NOV.2017 19:21:16

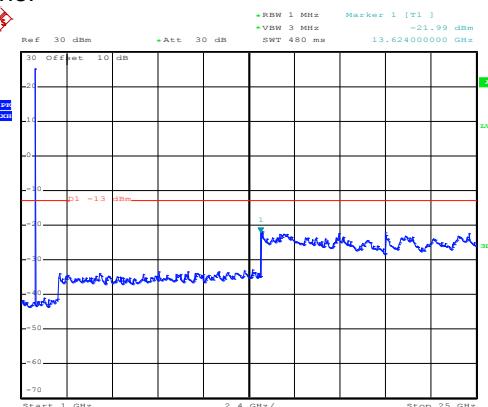
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 19:36:25

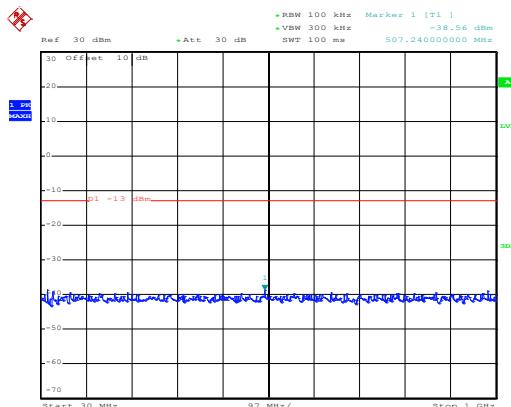
30MHz~1GHz



Date: 17.NOV.2017 19:21:57

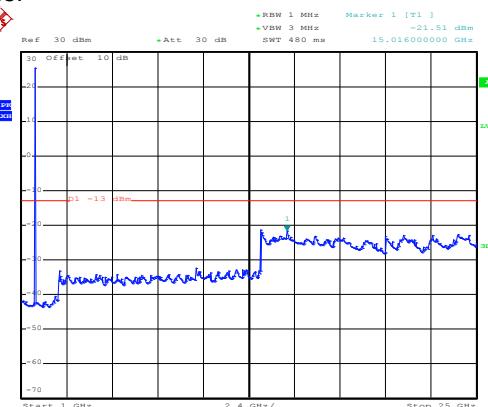
1GHz~25GHz

High channel



Date: 17.NOV.2017 19:36:49

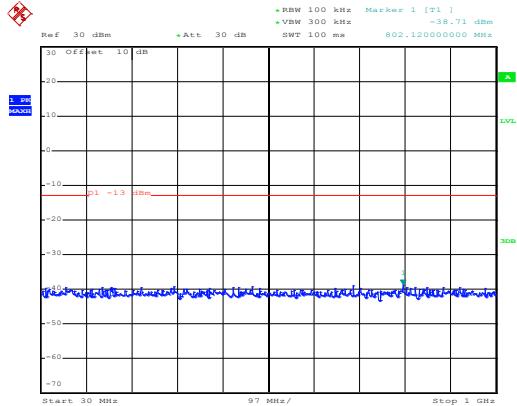
30MHz~1GHz



Date: 17.NOV.2017 19:22:43

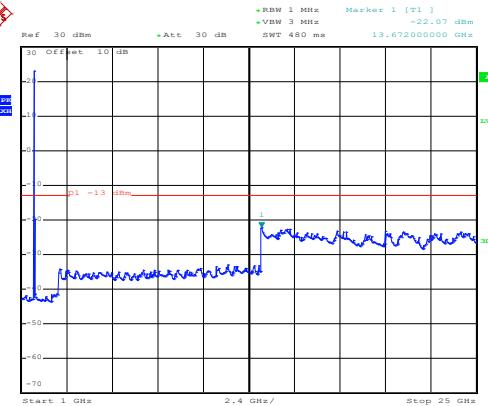
1GHz~25GHz

**QPSK & RB Size 15
Lowest channel**



Date: 17.NOV.2017 19:36:13

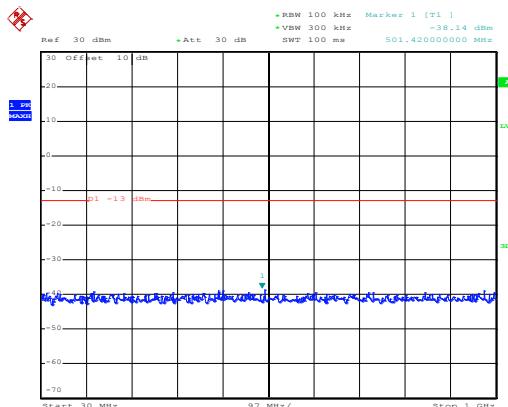
30MHz~1GHz



Date: 17.NOV.2017 19:21:35

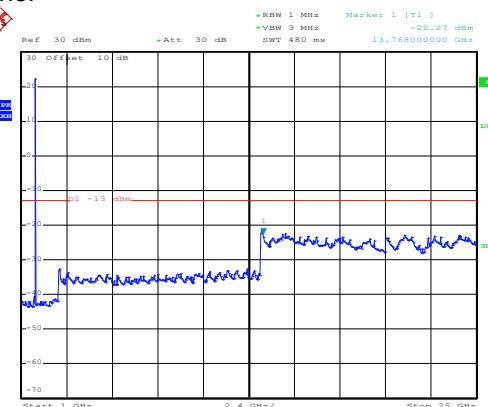
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 19:36:37

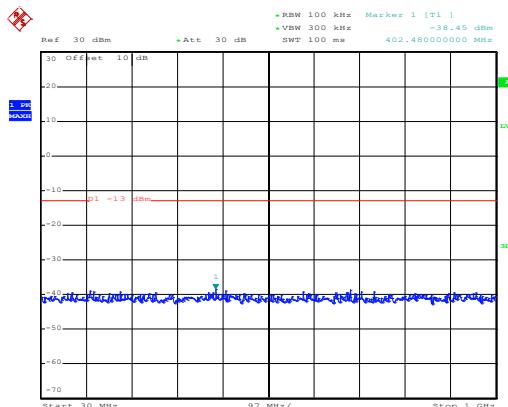
30MHz~1GHz



Date: 17.NOV.2017 19:22:17

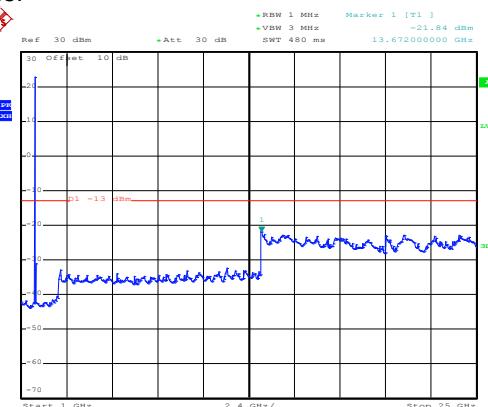
1GHz~25GHz

High channel



Date: 17.NOV.2017 19:37:00

30MHz~1GHz

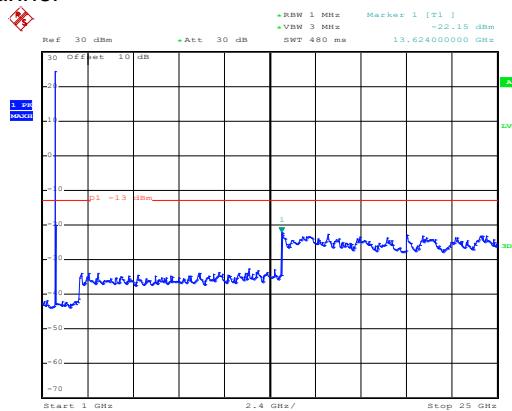
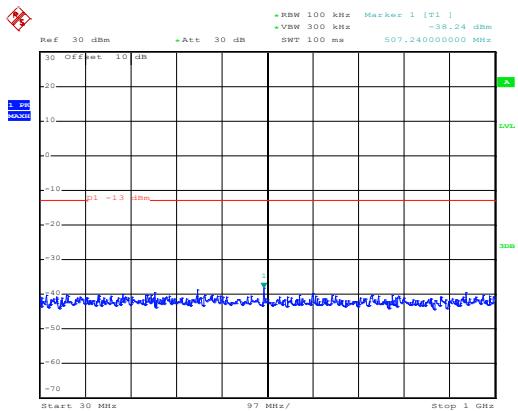


Date: 17.NOV.2017 19:23:08

1GHz~25GHz

5MHz

16 QAM & RB Size 1 Lowest channel



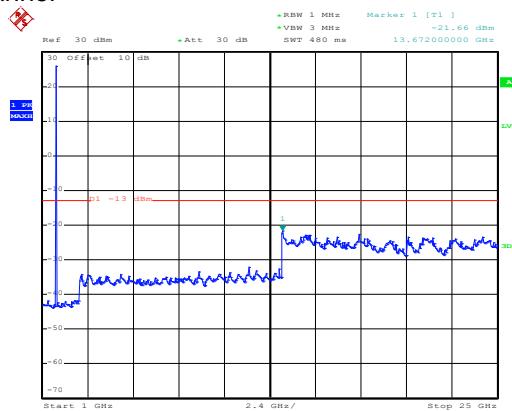
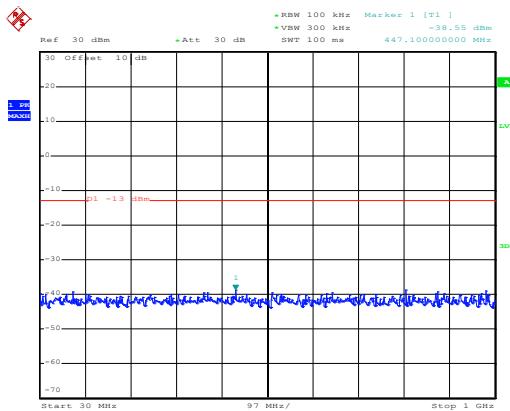
Date: 17.NOV.2017 19:37:19

Date: 17.NOV.2017 19:23:47

30MHz~1GHz

1GHz~25GHz

Middle channel



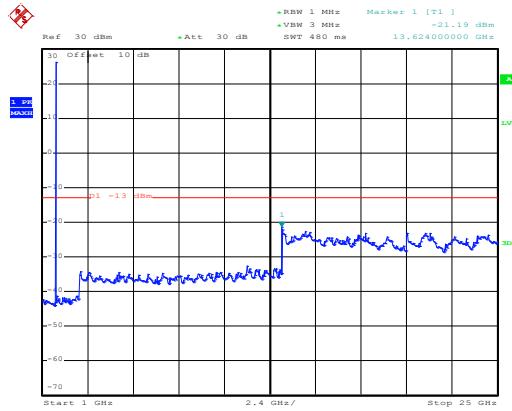
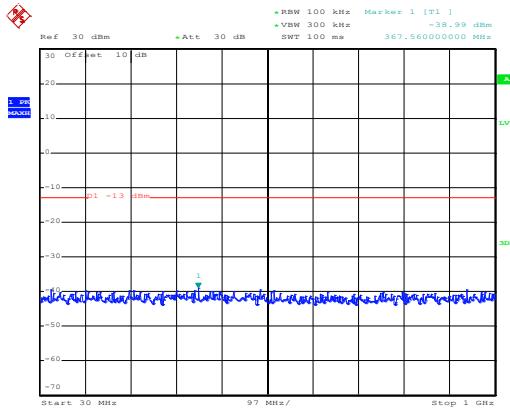
Date: 17.NOV.2017 19:37:48

Date: 17.NOV.2017 19:24:35

30MHz~1GHz

1GHz~25GHz

High channel



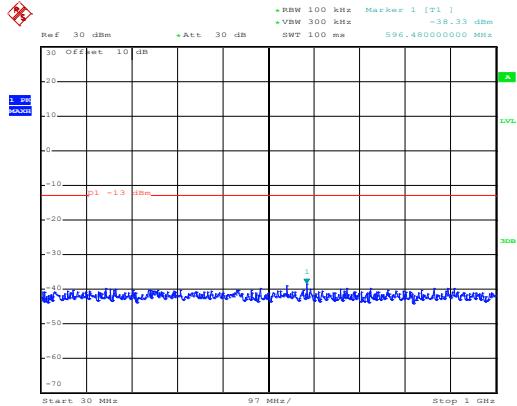
Date: 17.NOV.2017 19:38:14

Date: 17.NOV.2017 19:25:18

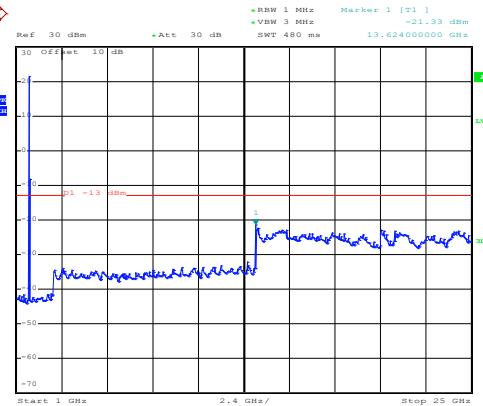
30MHz~1GHz

1GHz~25GHz

16 QAM & RB Size 25 Lowest channel



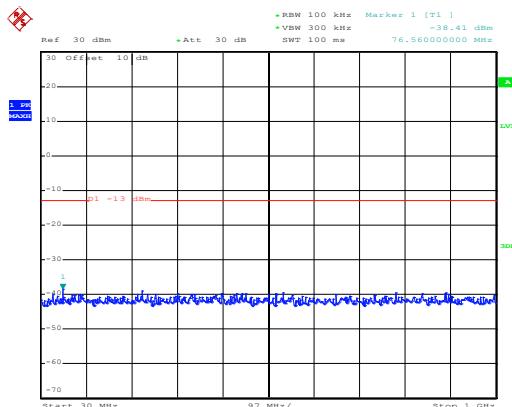
Date: 17.NOV.2017 19:37:35



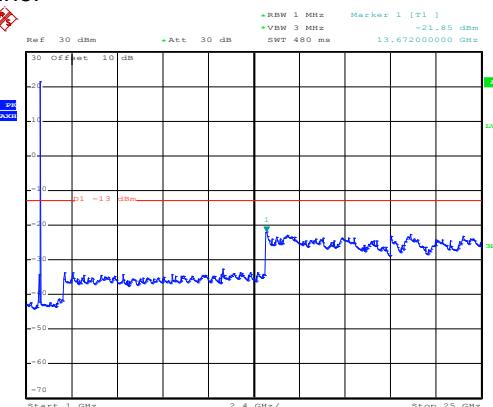
Date: 17.NOV.2017 19:24:08

30MHz~1GHz

Middle channel



Date: 17.NOV.2017 19:38:01

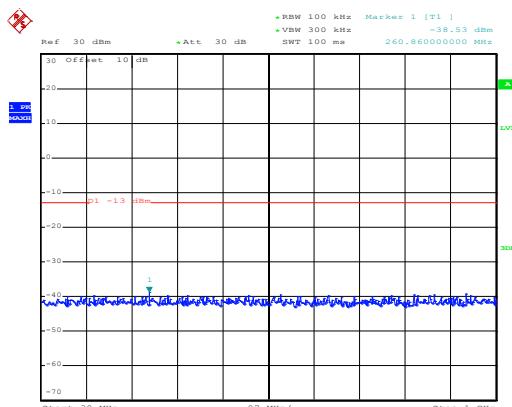


Date: 17.NOV.2017 19:24:55

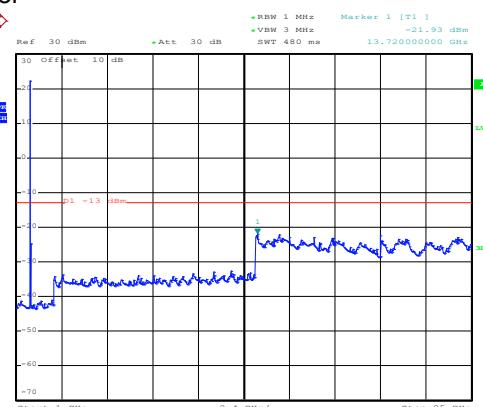
30MHz~1GHz

1GHz~25GHz

High channel



Date: 17.NOV.2017 19:38:26

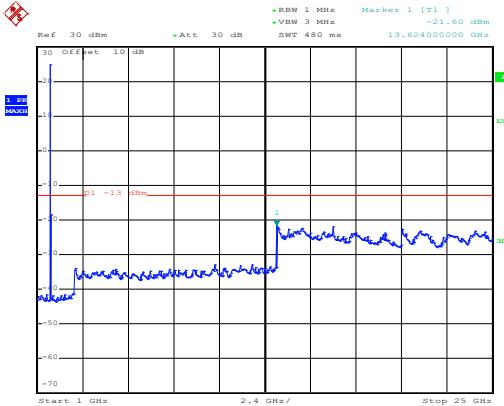
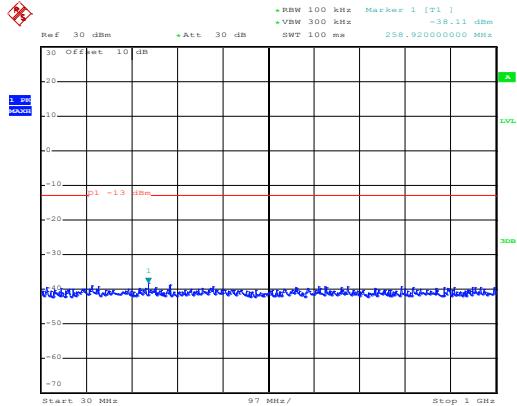


Date: 17.NOV.2017 19:25:38

30MHz~1GHz

1GHz~25GHz

**QPSK & RB Size 1
Lowest channel**



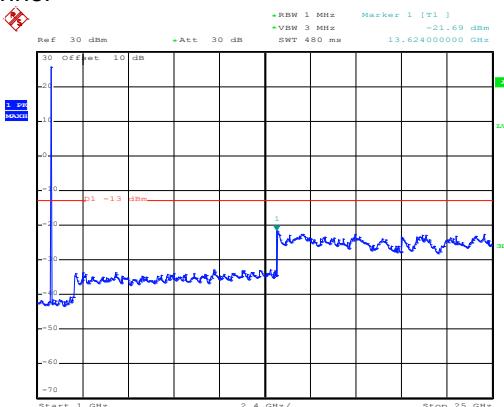
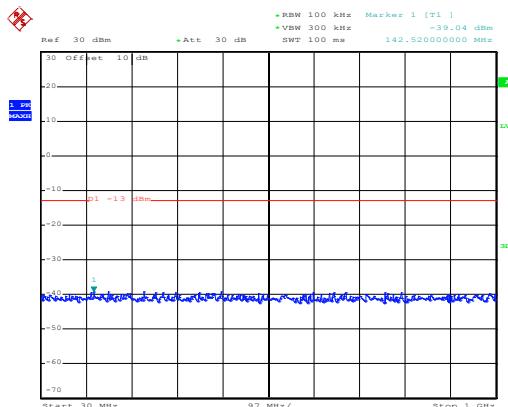
Date: 17.NOV.2017 19:37:14

30MHz~1GHz

Date: 17.NOV.2017 19:23:40

1GHz~25GHz

Middle channel



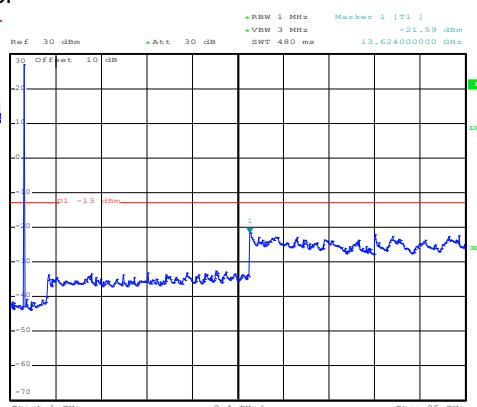
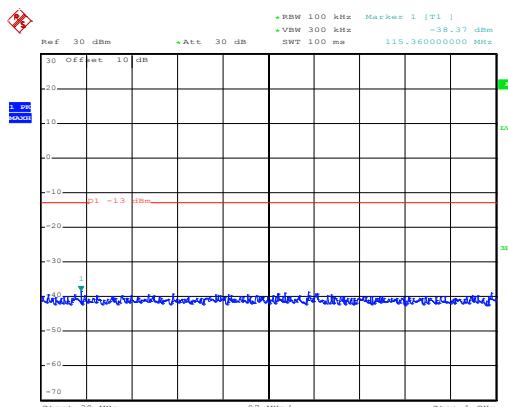
Date: 17.NOV.2017 19:37:44

30MHz~1GHz

Date: 17.NOV.2017 19:24:29

1GHz~25GHz

High channel



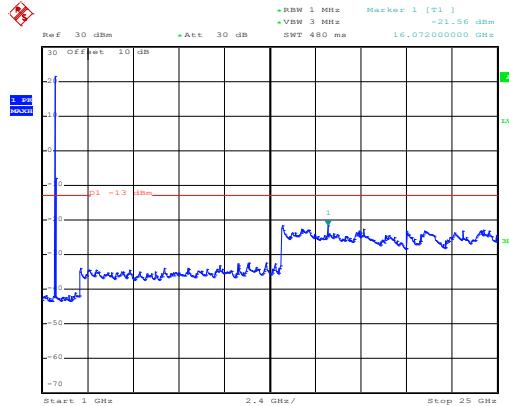
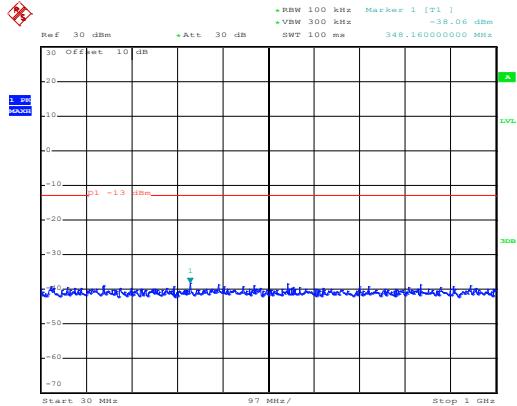
Date: 17.NOV.2017 19:38:10

30MHz~1GHz

Date: 17.NOV.2017 19:25:12

1GHz~25GHz

**QPSK & RB Size 25
Lowest channel**



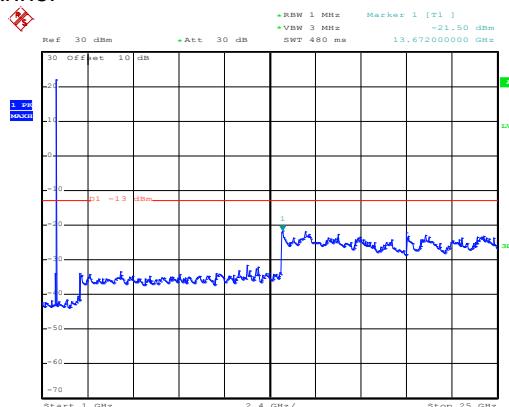
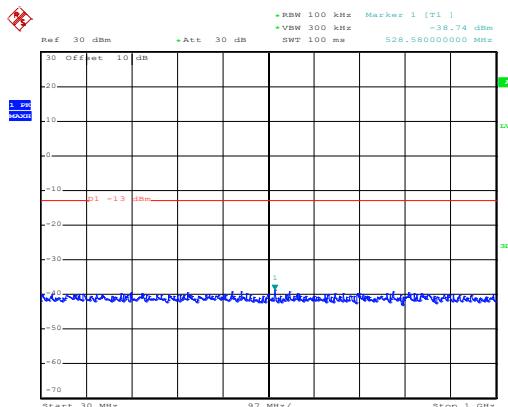
Date: 17.NOV.2017 19:37:31

30MHz~1GHz

Date: 17.NOV.2017 19:23:59

1GHz~25GHz

Middle channel



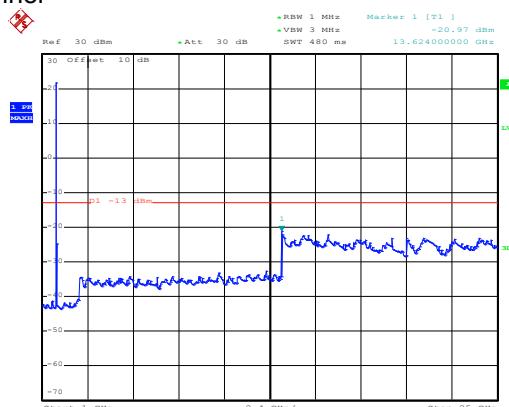
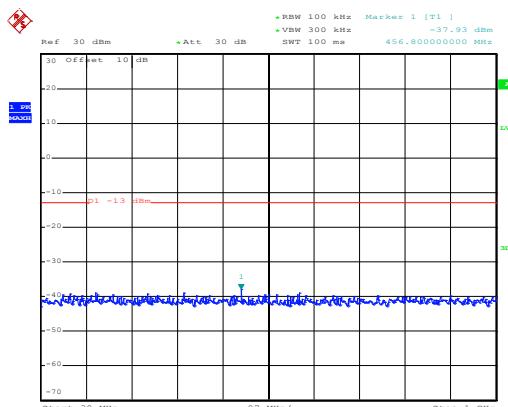
Date: 17.NOV.2017 19:37:56

30MHz~1GHz

Date: 17.NOV.2017 19:24:46

1GHz~25GHz

High channel



Date: 17.NOV.2017 19:38:21

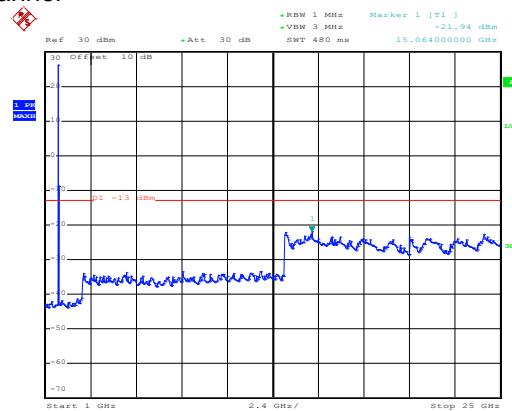
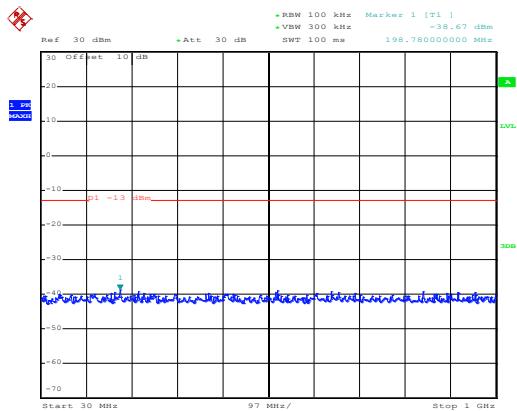
30MHz~1GHz

Date: 17.NOV.2017 19:25:30

1GHz~25GHz

10MHz

16 QAM & RB Size 1 Lowest channel

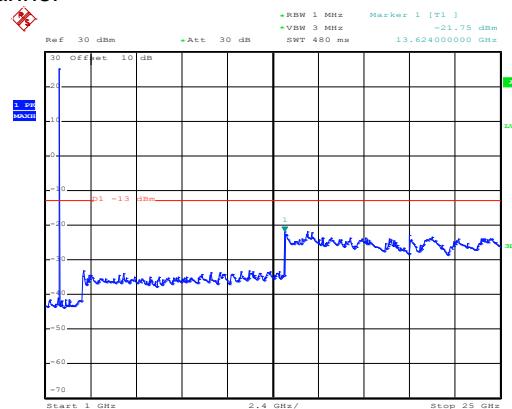
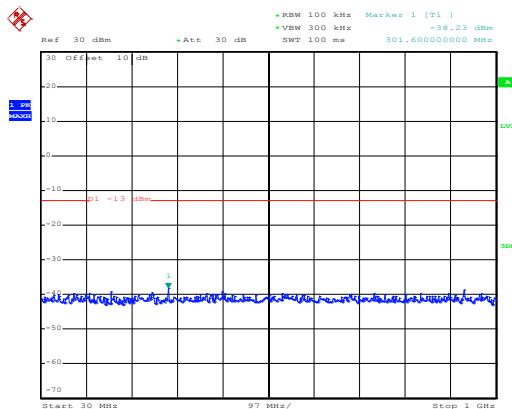


Date: 17.NOV.2017 19:38:45

Date: 17.NOV.2017 19:26:18

30MHz~1GHz

Middle channel



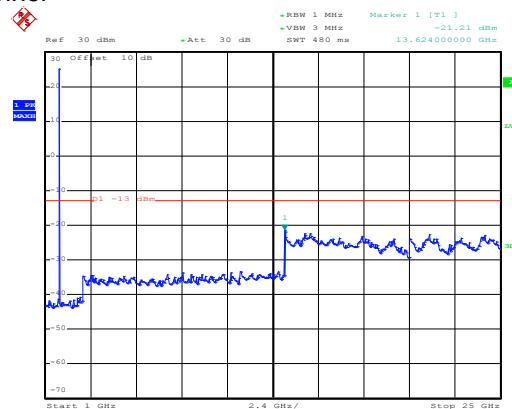
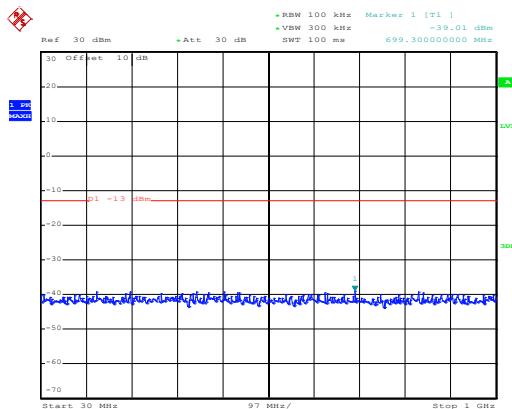
Date: 17.NOV.2017 19:39:19

Date: 17.NOV.2017 19:27:15

30MHz~1GHz

1GHz~25GHz

High channel



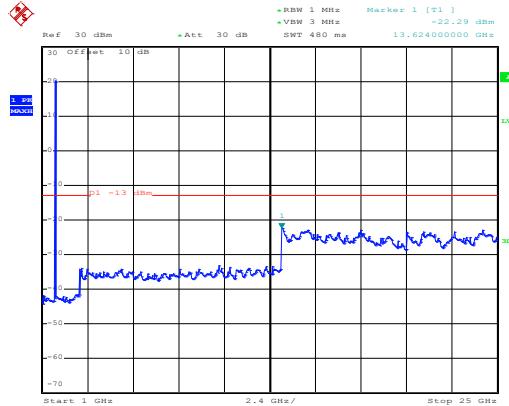
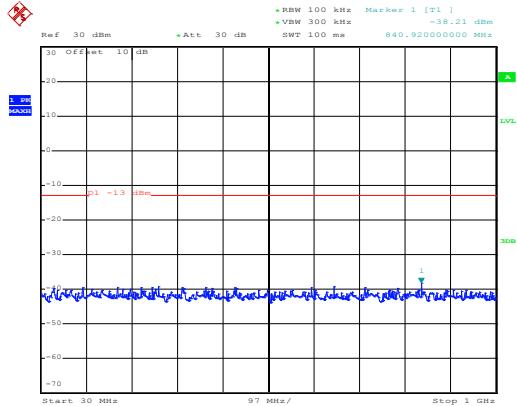
Date: 17.NOV.2017 19:39:58

Date: 17.NOV.2017 19:28:06

30MHz~1GHz

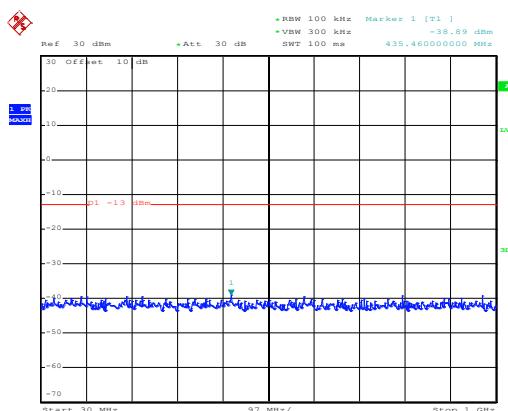
1GHz~25GHz

**16 QAM & RB Size 50
Lowest channel**

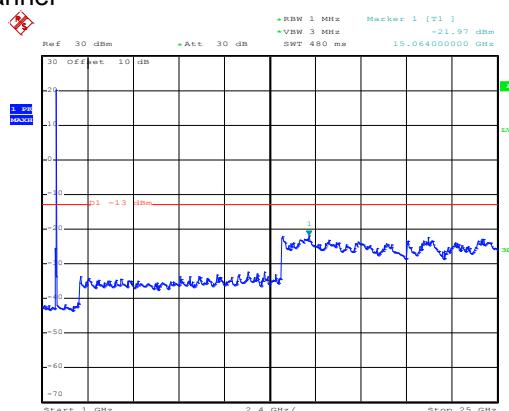


Date: 17.NOV.2017 19:39:03

30MHz~1GHz

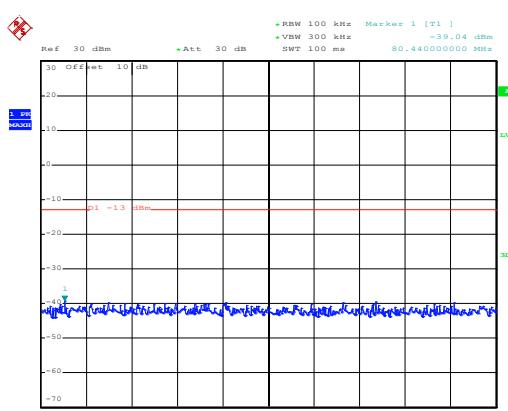


Middle channel

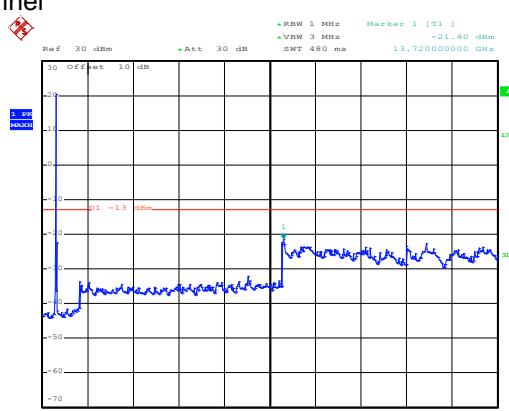


Date: 17.NOV.2017 19:39:44

30MHz~1GHz



High channel



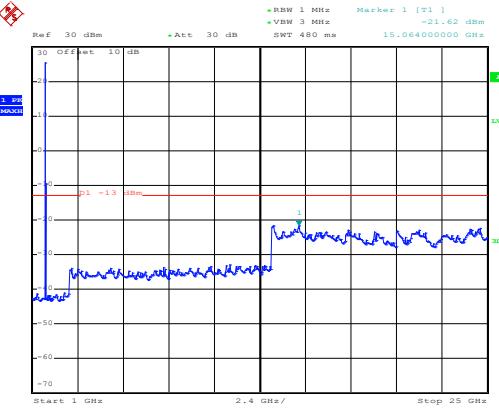
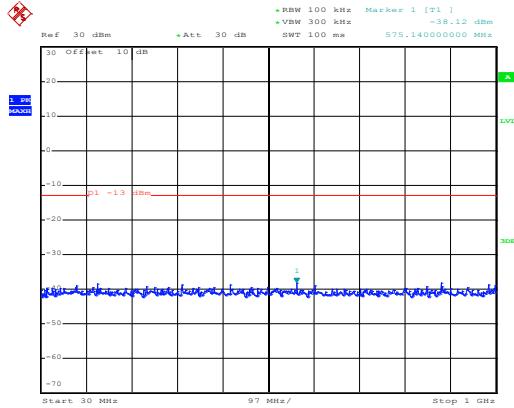
Date: 17.NOV.2017 19:40:09

30MHz~1GHz

Date: 17.NOV.2017 19:28:26

1GHz~25GHz

**QPSK & RB Size 1
Lowest channel**



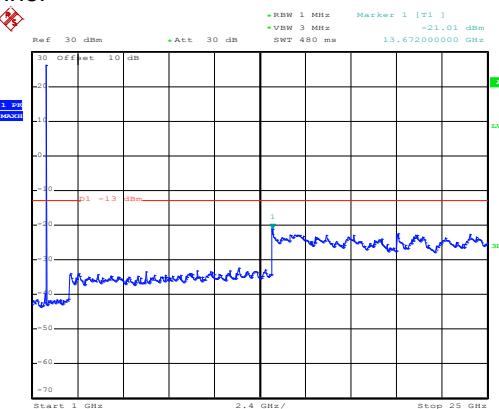
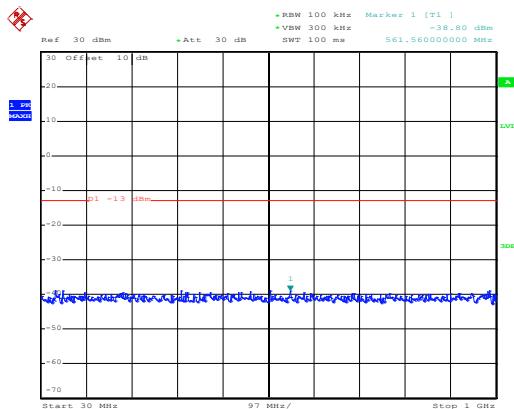
Date: 17.NOV.2017 19:38:36

30MHz~1GHz

Date: 17.NOV.2017 19:26:10

1GHz~25GHz

Middle channel



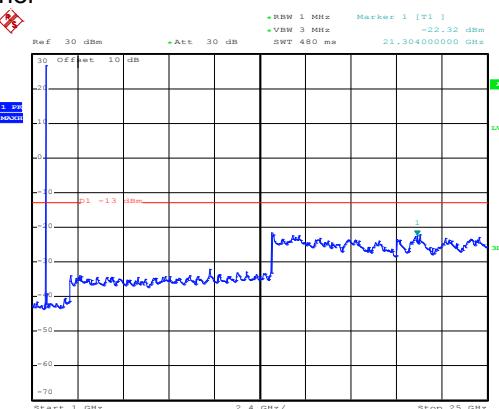
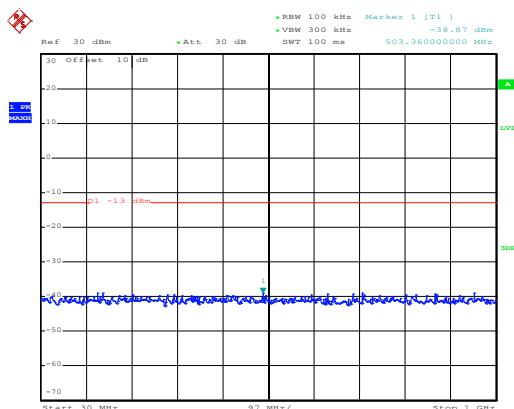
Date: 17.NOV.2017 19:39:12

30MHz~1GHz

Date: 17.NOV.2017 19:27:06

1GHz~25GHz

High channel



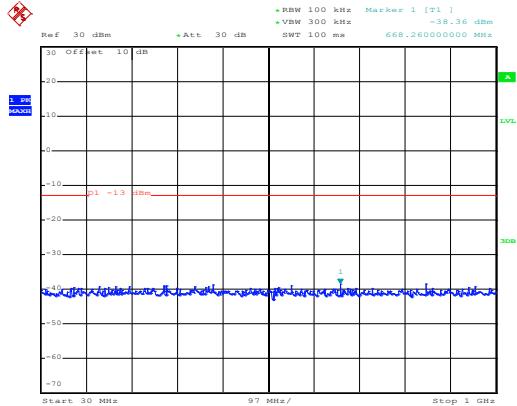
Date: 17.NOV.2017 19:39:53

30MHz~1GHz

Date: 17.NOV.2017 19:27:59

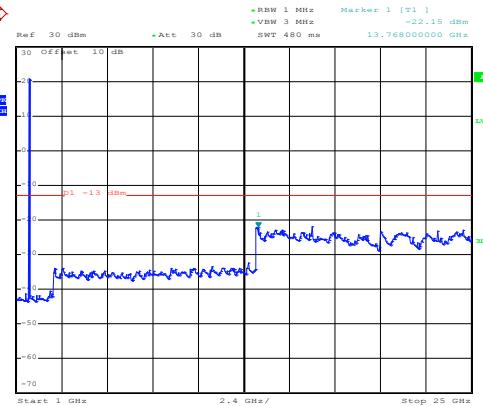
1GHz~25GHz

**QPSK & RB Size 50
Lowest channel**



Date: 17.NOV.2017 19:38:59

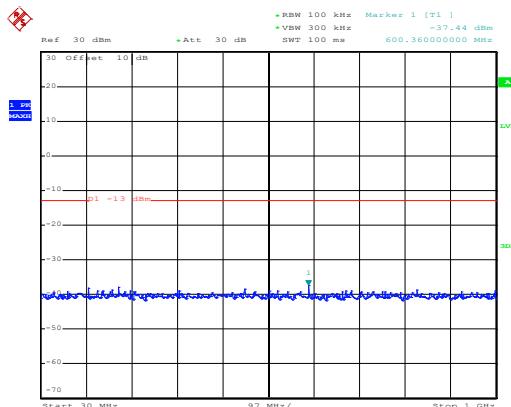
30MHz~1GHz



Date: 17.NOV.2017 19:26:37

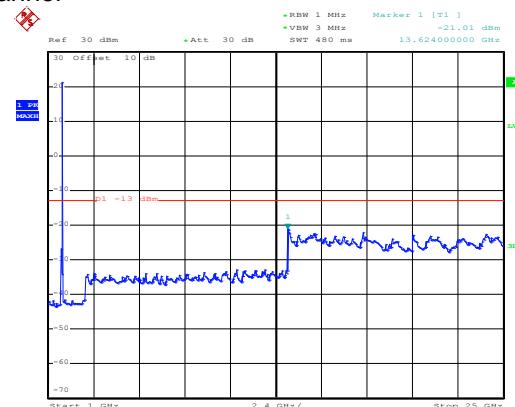
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 19:39:40

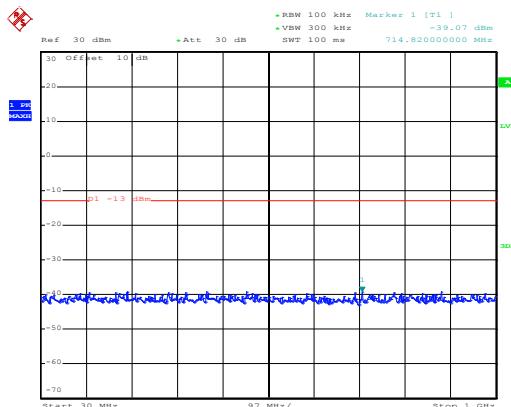
30MHz~1GHz



Date: 17.NOV.2017 19:27:30

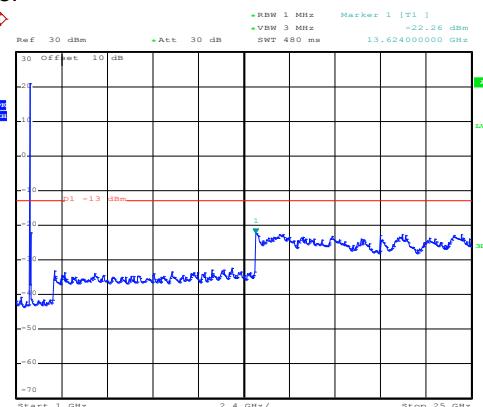
1GHz~25GHz

High channel



Date: 17.NOV.2017 19:40:05

30MHz~1GHz

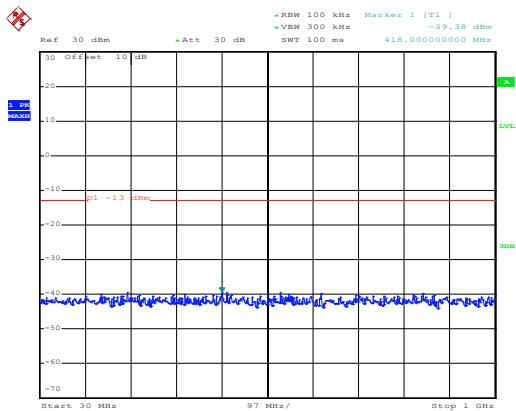


Date: 17.NOV.2017 19:28:21

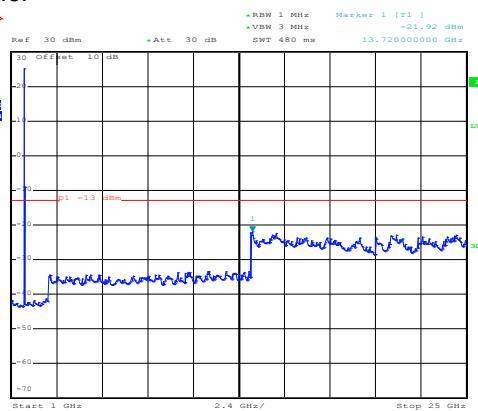
1GHz~25GHz

15MHz

16 QAM & RB Size 1 Lowest channel



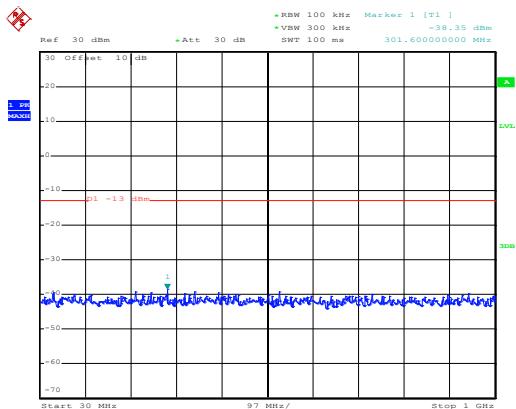
Date: 17.NOV.2017 19:40:26



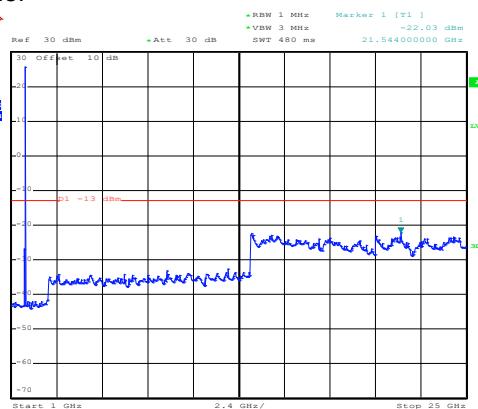
Date: 17.NOV.2017 19:29:01

30MHz~1GHz

Middle channel



Date: 17.NOV.2017 19:40:58

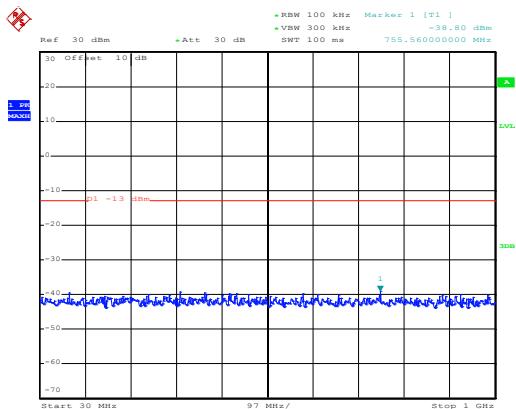


Date: 17.NOV.2017 19:29:57

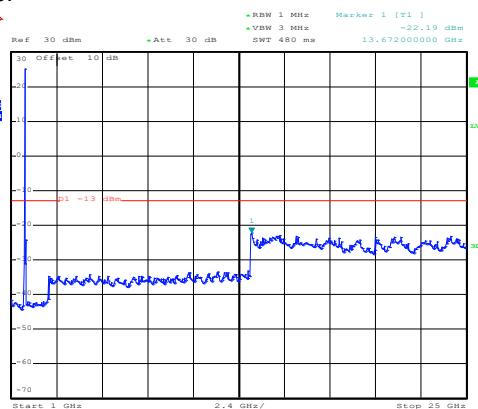
30MHz~1GHz

1GHz~25GHz

High channel



Date: 17.NOV.2017 19:41:21

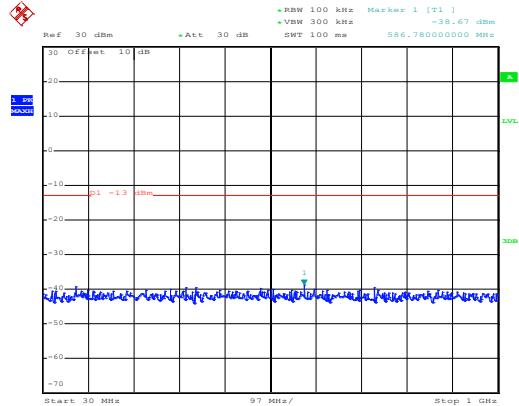


Date: 17.NOV.2017 19:31:10

30MHz~1GHz

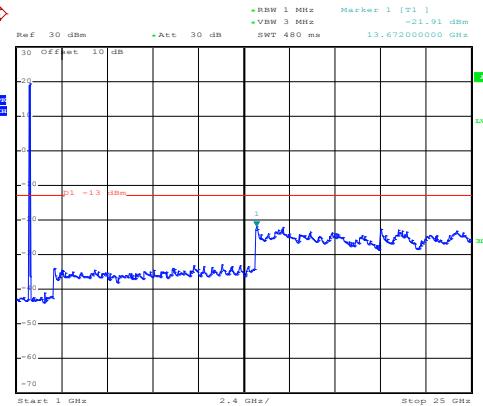
1GHz~25GHz

16 QAM & RB Size 75 Lowest channel



Date: 17.NOV.2017 19:40:39

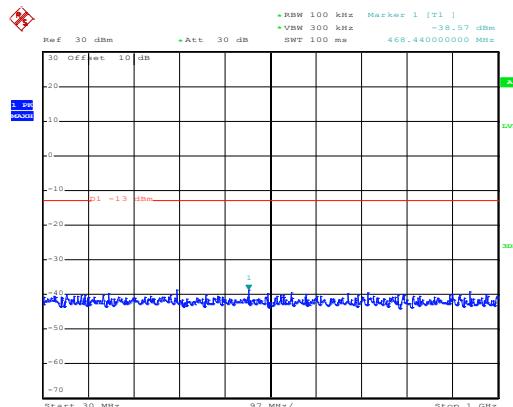
30MHz~1GHz



Date: 17.NOV.2017 19:29:25

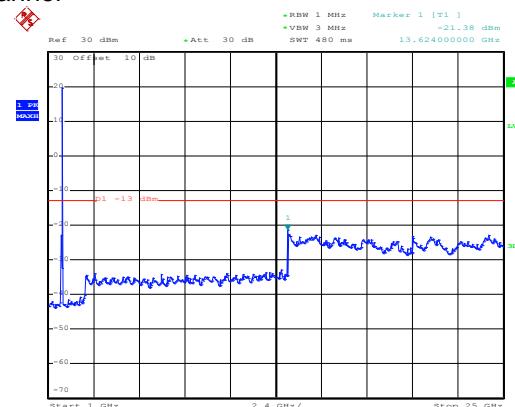
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 19:41:09

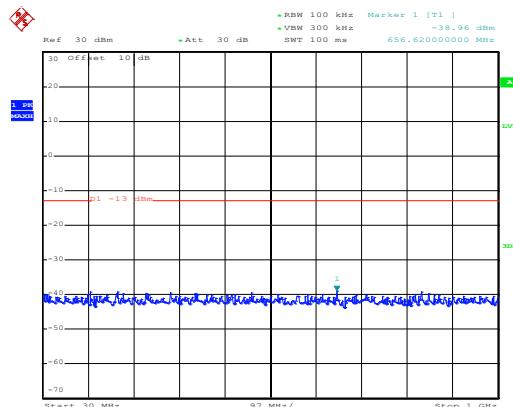
30MHz~1GHz



Date: 17.NOV.2017 19:30:18

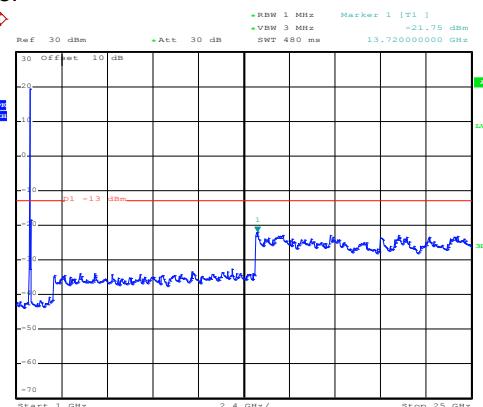
1GHz~25GHz

High channel



Date: 17.NOV.2017 19:41:34

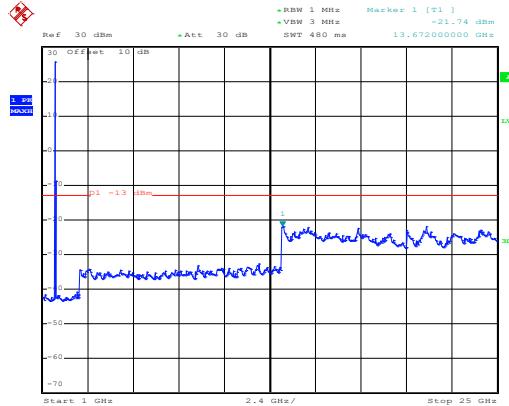
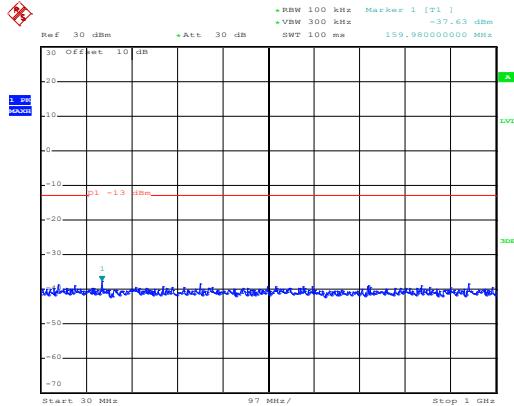
30MHz~1GHz



Date: 17.NOV.2017 19:31:32

1GHz~25GHz

**QPSK & RB Size 1
Lowest channel**



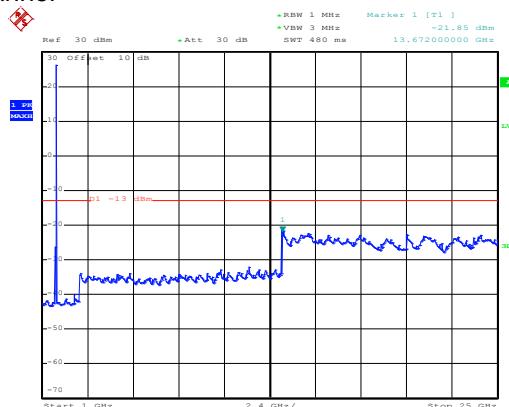
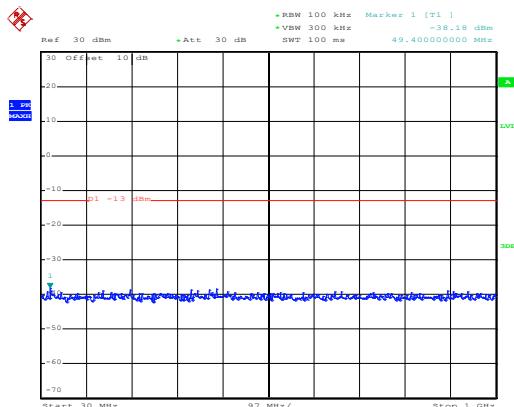
Date: 17.NOV.2017 19:40:21

30MHz~1GHz

Date: 17.NOV.2017 19:28:52

1GHz~25GHz

Middle channel



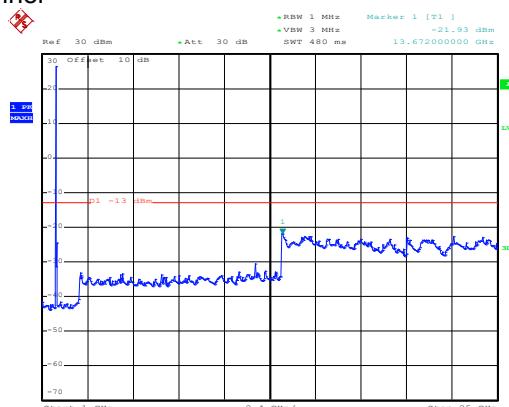
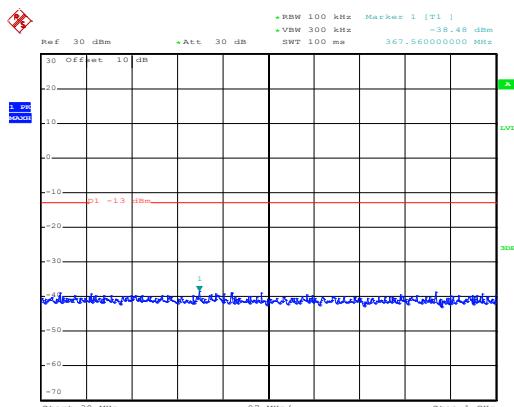
Date: 17.NOV.2017 19:40:54

30MHz~1GHz

Date: 17.NOV.2017 19:29:48

1GHz~25GHz

High channel



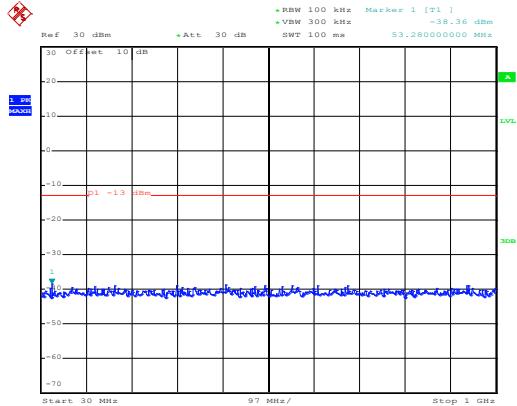
Date: 17.NOV.2017 19:41:17

30MHz~1GHz

Date: 17.NOV.2017 19:31:03

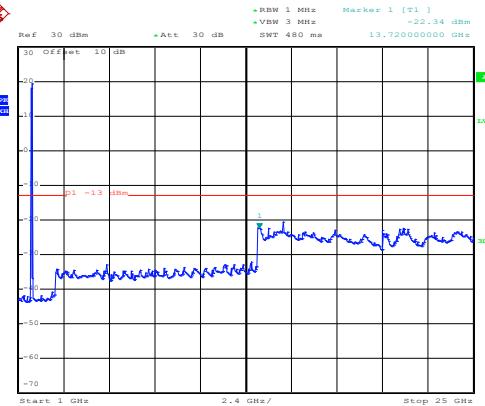
1GHz~25GHz

**QPSK & RB Size 75
Lowest channel**



Date: 17.NOV.2017 19:40:35

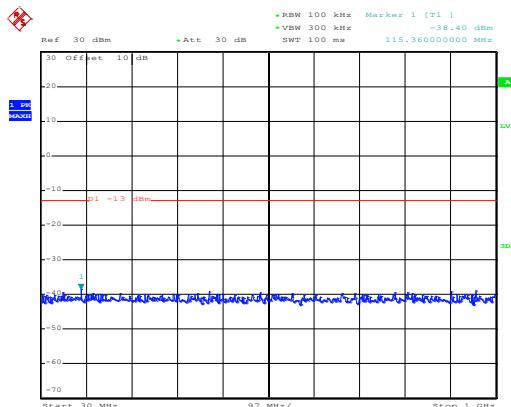
30MHz~1GHz



Date: 17.NOV.2017 19:29:15

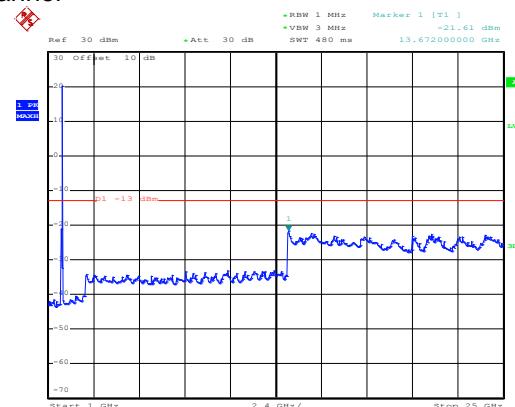
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 19:41:04

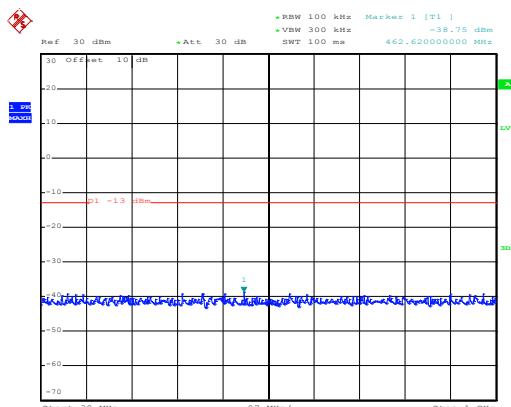
30MHz~1GHz



Date: 17.NOV.2017 19:30:09

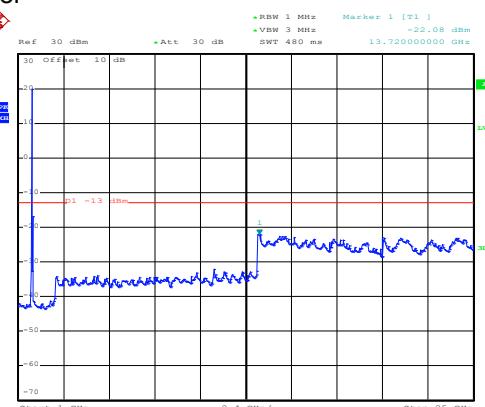
1GHz~25GHz

High channel



Date: 17.NOV.2017 19:41:28

30MHz~1GHz

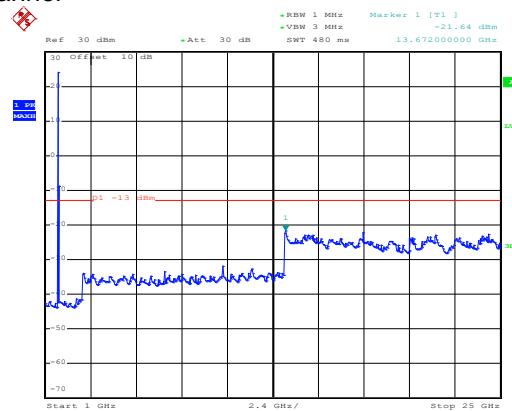
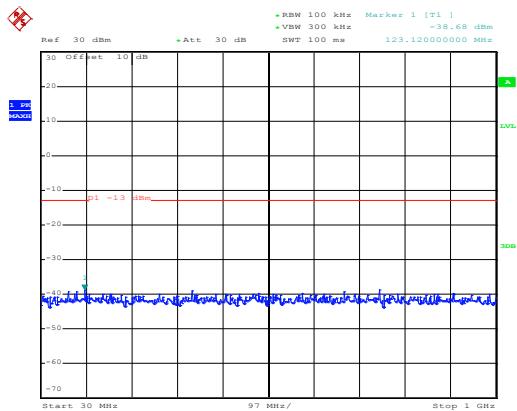


Date: 17.NOV.2017 19:31:23

1GHz~25GHz

20MHz

16 QAM & RB Size 1 Lowest channel

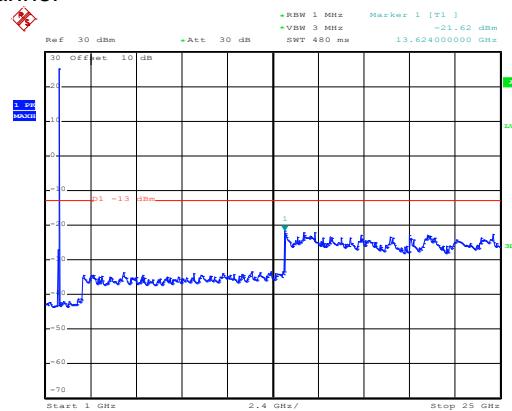
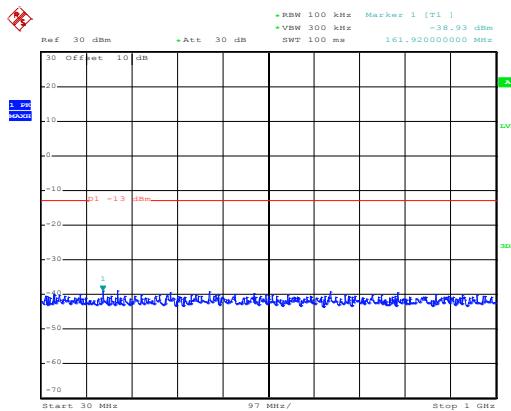


Date: 17.NOV.2017 19:41:49

Date: 17.NOV.2017 19:32:20

30MHz~1GHz

Middle channel



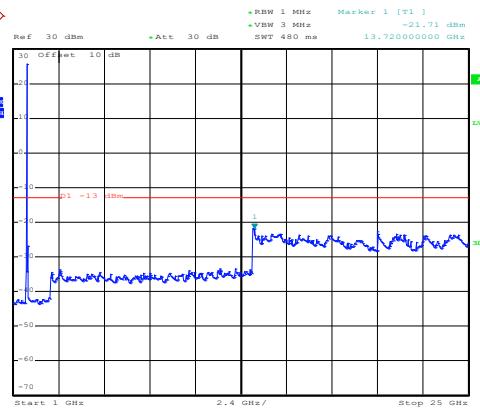
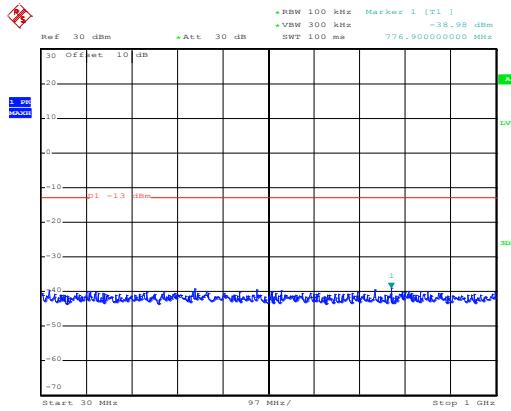
Date: 17.NOV.2017 19:42:15

Date: 17.NOV.2017 19:33:08

30MHz~1GHz

1GHz~25GHz

High channel



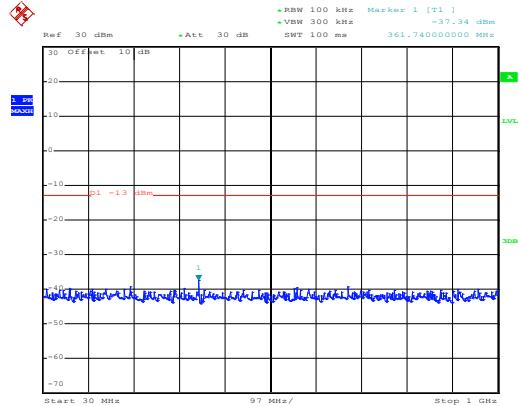
Date: 17.NOV.2017 19:42:47

Date: 17.NOV.2017 19:33:56

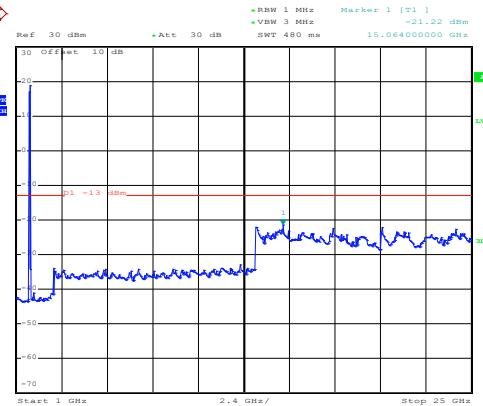
30MHz~1GHz

1GHz~25GHz

16 QAM & RB Size 100 Lowest channel



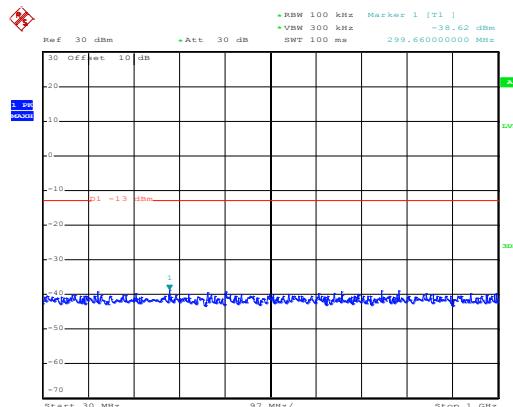
Date: 17.NOV.2017 19:42:01



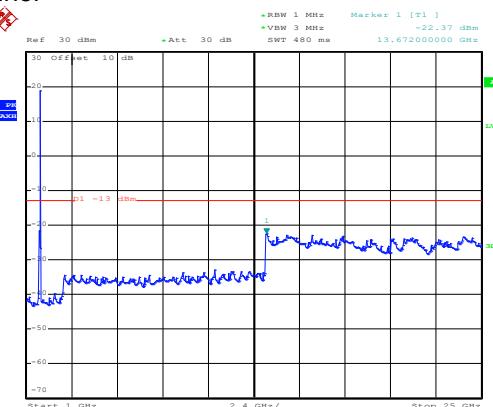
Date: 17.NOV.2017 19:32:42

30MHz~1GHz

Middle channel



Date: 17.NOV.2017 19:42:33

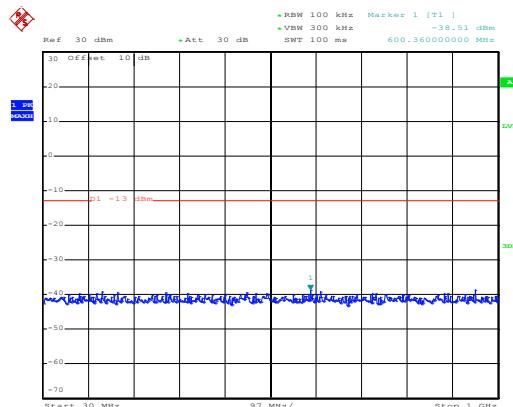


Date: 17.NOV.2017 19:33:29

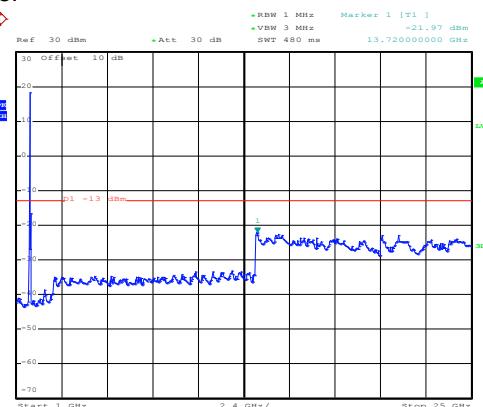
30MHz~1GHz

1GHz~25GHz

High channel



Date: 17.NOV.2017 19:43:02

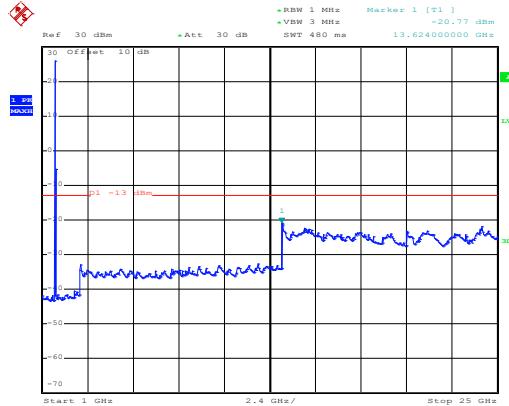
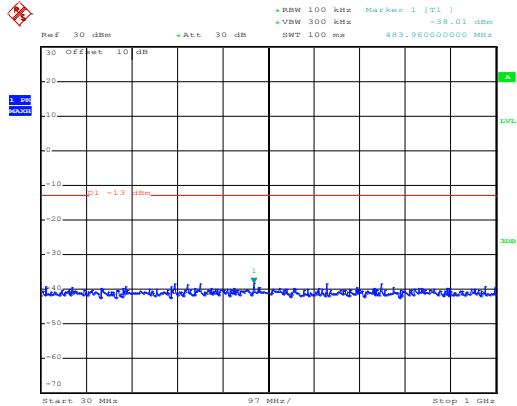


Date: 17.NOV.2017 19:34:17

30MHz~1GHz

1GHz~25GHz

**QPSK & RB Size 1
Lowest channel**



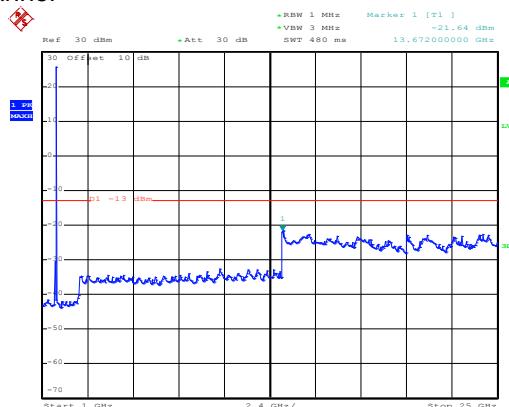
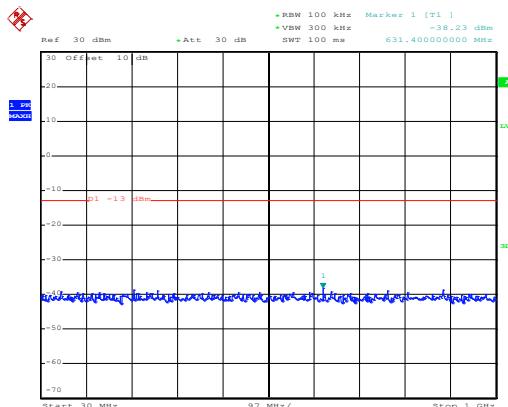
Date: 17.NOV.2017 19:41:44

30MHz~1GHz

Date: 17.NOV.2017 19:32:12

1GHz~25GHz

Middle channel



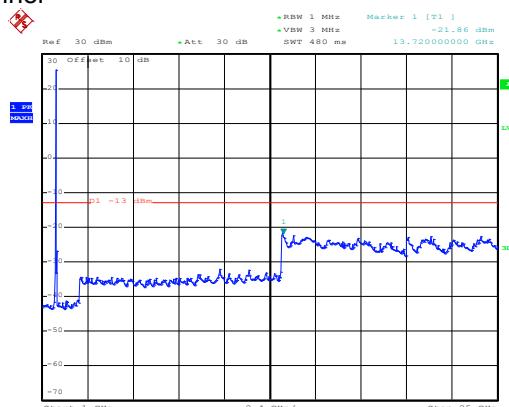
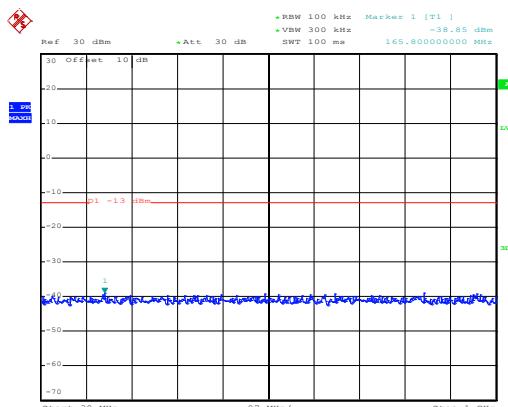
Date: 17.NOV.2017 19:42:10

30MHz~1GHz

Date: 17.NOV.2017 19:33:00

1GHz~25GHz

High channel



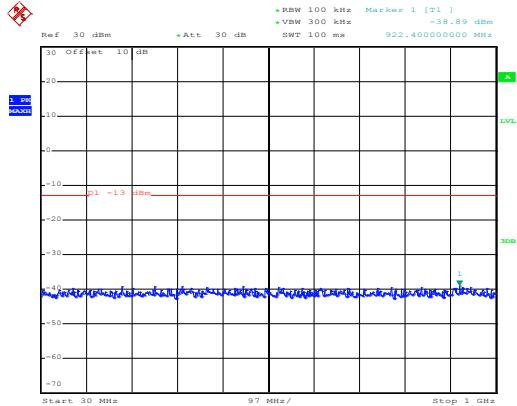
Date: 17.NOV.2017 19:42:42

30MHz~1GHz

Date: 17.NOV.2017 19:33:49

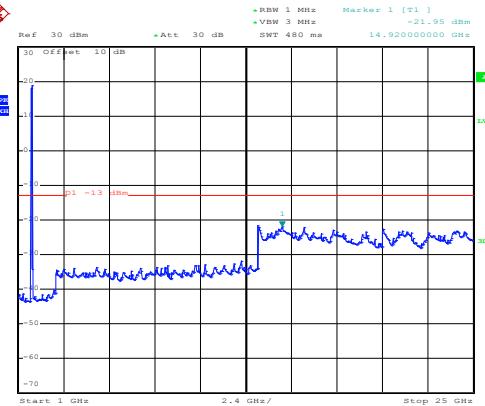
1GHz~25GHz

**QPSK & RB Size 100
Lowest channel**



Date: 17.NOV.2017 19:41:57

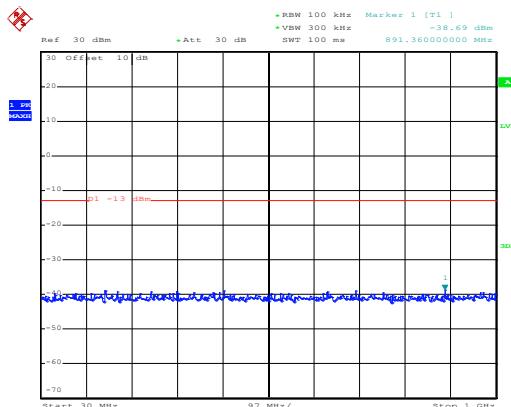
30MHz~1GHz



Date: 17.NOV.2017 19:32:33

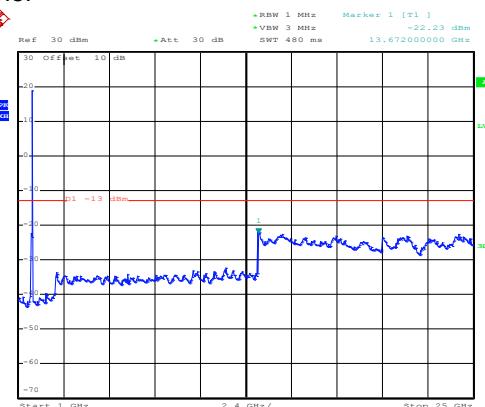
1GHz~25GHz

Middle channel



Date: 17.NOV.2017 19:42:27

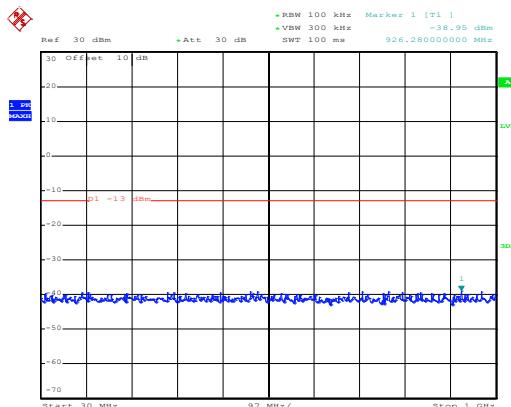
30MHz~1GHz



Date: 17.NOV.2017 19:33:21

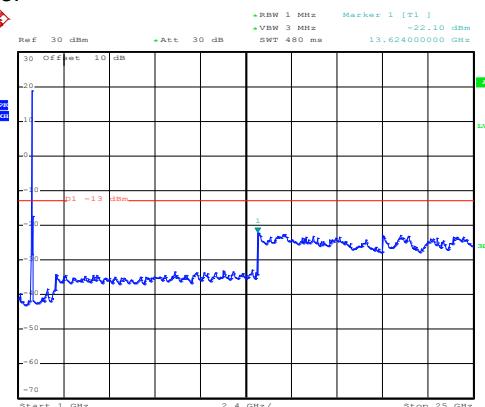
1GHz~25GHz

High channel



Date: 17.NOV.2017 19:42:56

30MHz~1GHz

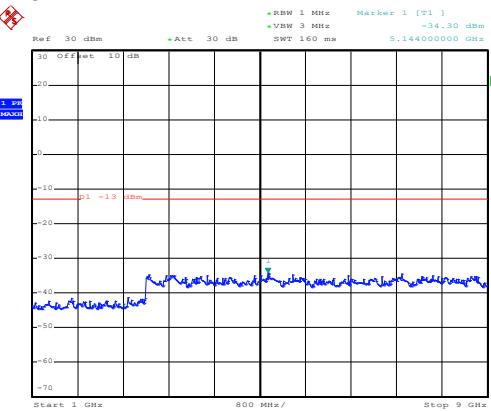
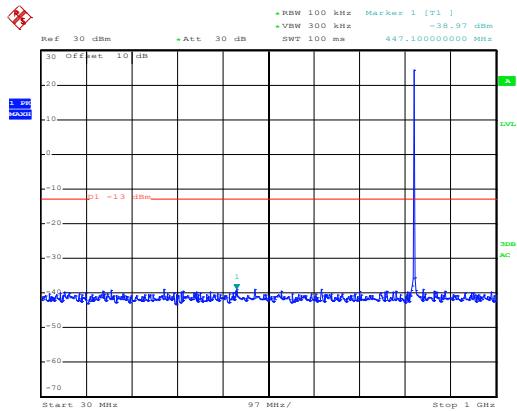


Date: 17.NOV.2017 19:34:10

1GHz~25GHz

LTE band 5, 1.4 MHz

16 QAM & RB Size 1 Lowest channel



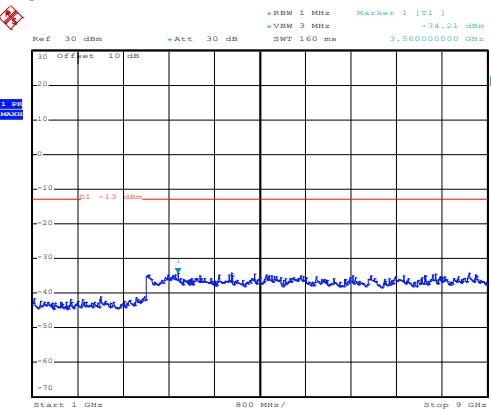
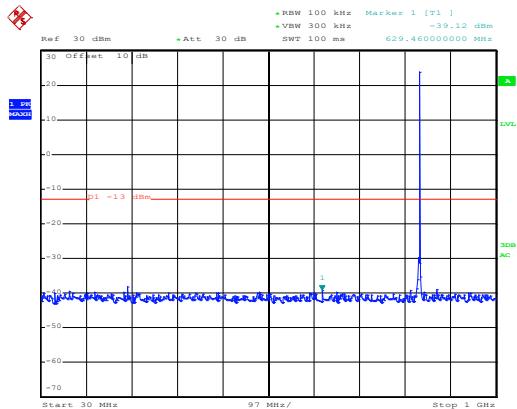
Date: 7.NOV.2017 22:07:37

30MHz~1GHz

Date: 17.NOV.2017 20:06:20

1GHz~9GHz

Middle channel



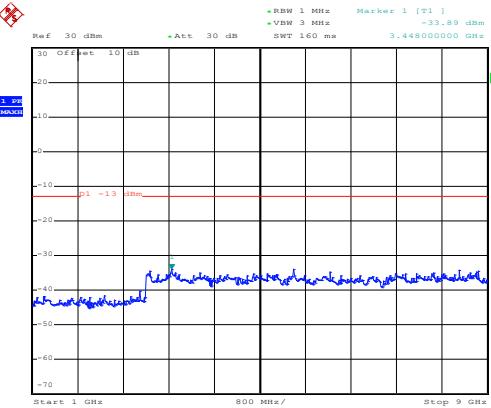
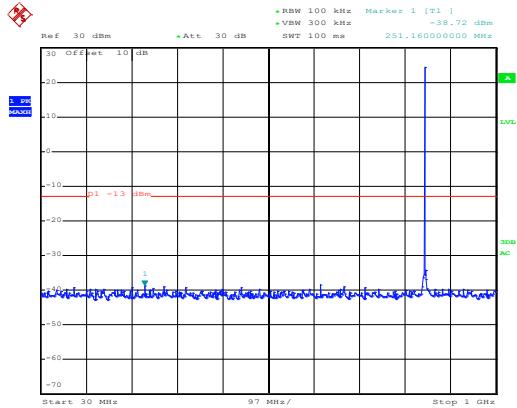
Date: 7.NOV.2017 22:09:06

30MHz~1GHz

Date: 17.NOV.2017 20:06:59

1GHz~9GHz

High channel



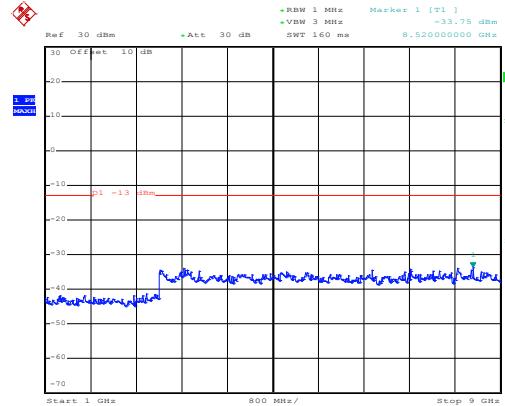
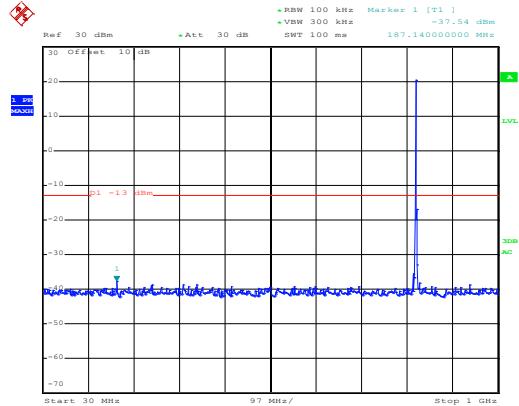
Date: 7.NOV.2017 22:10:27

30MHz~1GHz

Date: 17.NOV.2017 20:07:27

1GHz~9GHz

16 QAM & RB Size 6 Lowest channel



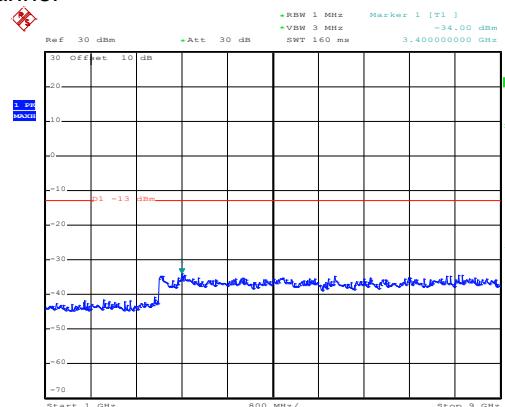
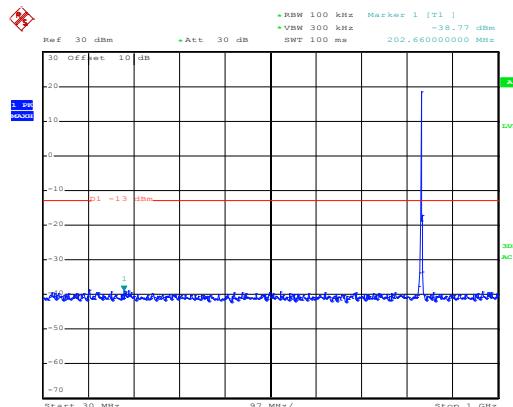
Date: 7.NOV.2017 22:08:38

Date: 17.NOV.2017 20:06:46

30MHz~1GHz

1GHz~9GHz

Middle channel



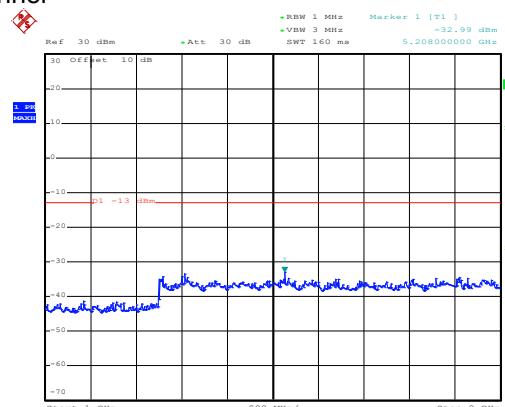
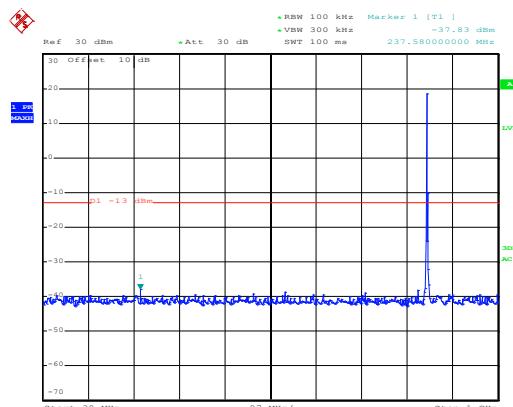
Date: 7.NOV.2017 22:09:56

Date: 17.NOV.2017 20:07:14

30MHz~1GHz

1GHz~9GHz

High channel



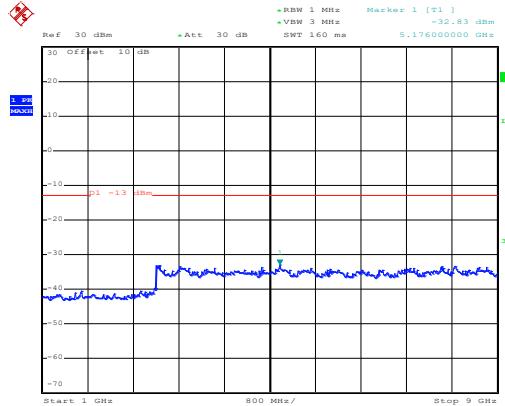
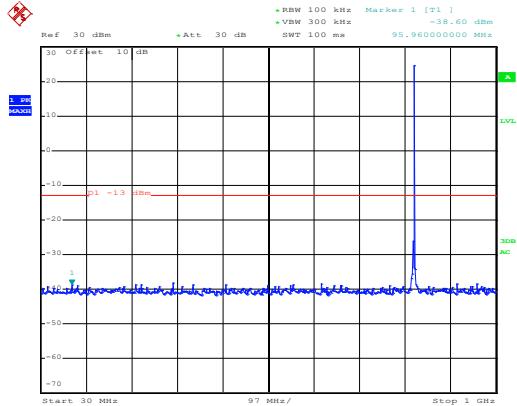
Date: 7.NOV.2017 22:11:09

Date: 17.NOV.2017 20:07:38

30MHz~1GHz

1GHz~9GHz

**QPSK & RB Size 1
Lowest channel**



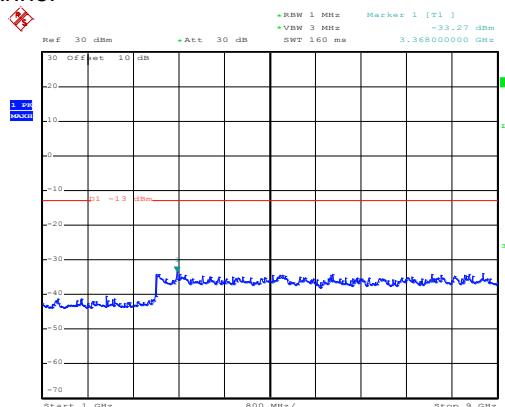
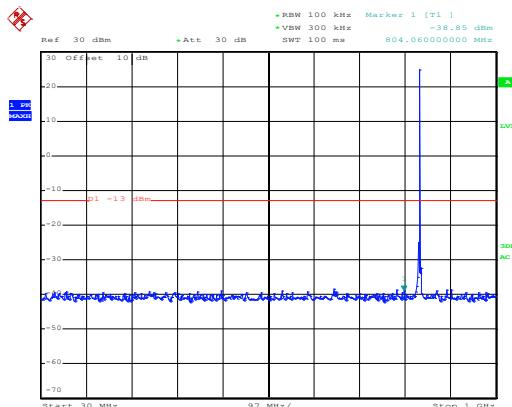
Date: 7.NOV.2017 22:07:27

30MHz~1GHz

Date: 17.NOV.2017 20:06:17

1GHz~9GHz

Middle channel



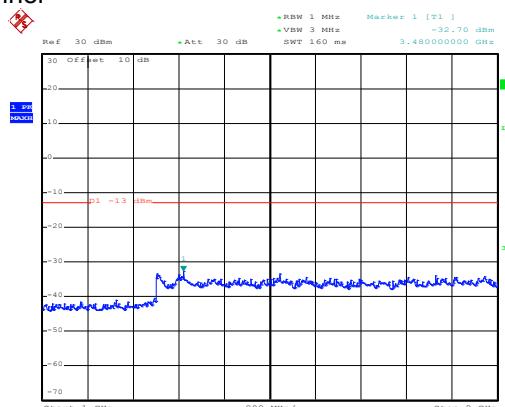
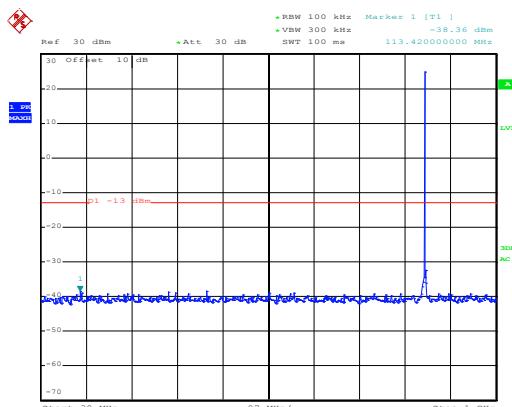
Date: 7.NOV.2017 22:08:58

30MHz~1GHz

Date: 17.NOV.2017 20:06:54

1GHz~9GHz

High channel



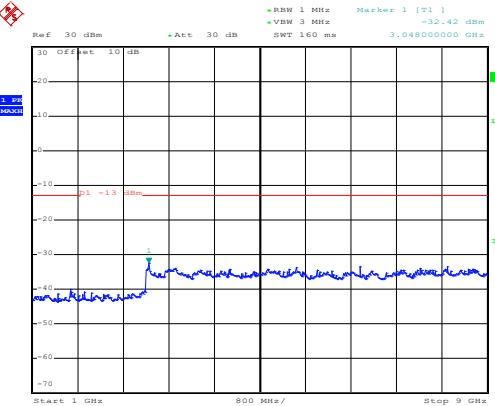
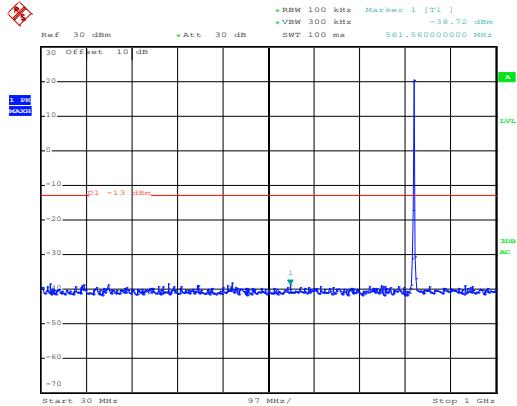
Date: 7.NOV.2017 22:10:18

30MHz~1GHz

Date: 17.NOV.2017 20:07:23

1GHz~9GHz

**QPSK & RB Size 6
Lowest channel**



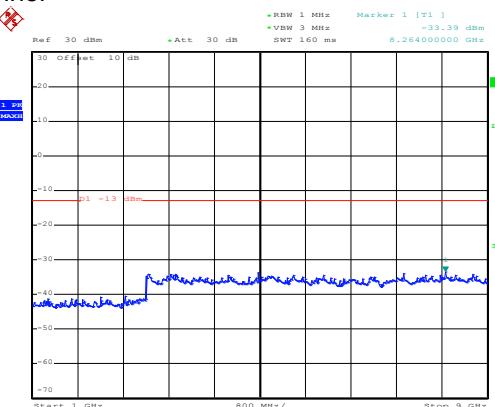
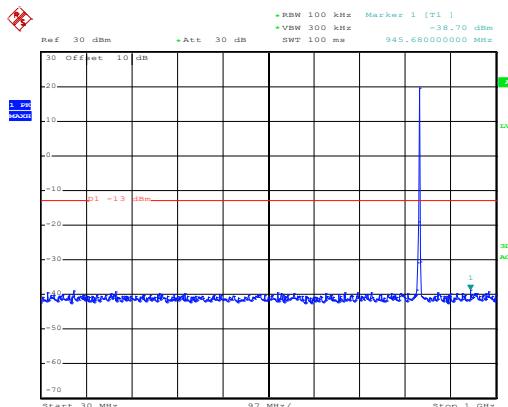
Date: 7.NOV.2017 22:08:23

30MHz~1GHz

Date: 17.NOV.2017 20:06:42

1GHz~9GHz

Middle channel



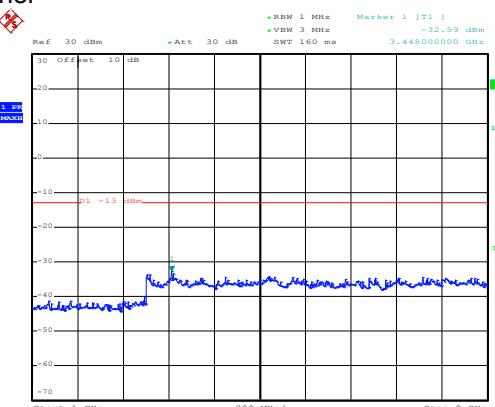
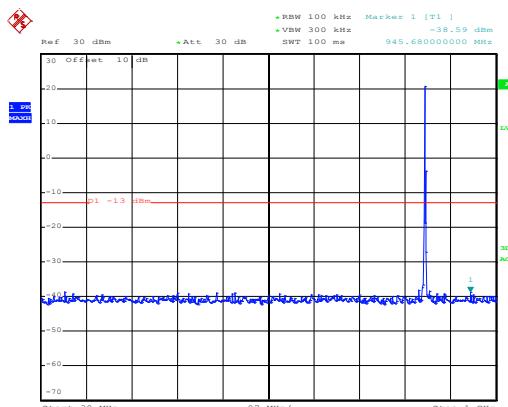
Date: 7.NOV.2017 22:09:42

30MHz~1GHz

Date: 17.NOV.2017 20:07:09

1GHz~9GHz

High channel



Date: 7.NOV.2017 22:11:01

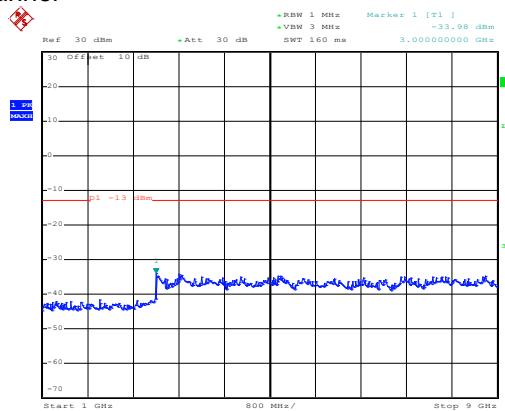
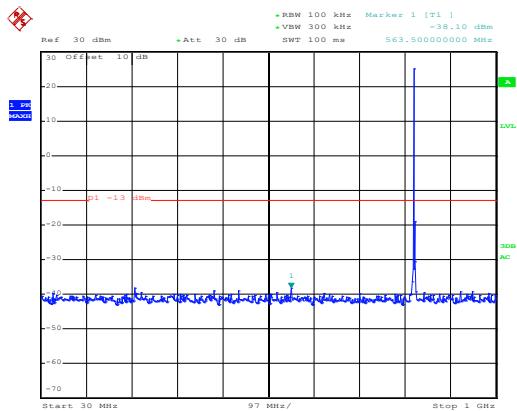
30MHz~1GHz

Date: 17.NOV.2017 20:07:34

1GHz~9GHz

3MHz

16 QAM & RB Size 1 Lowest channel

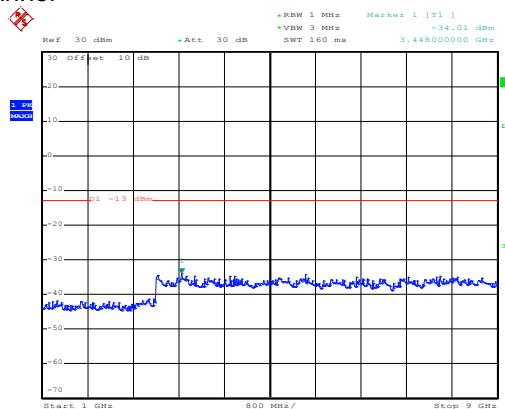
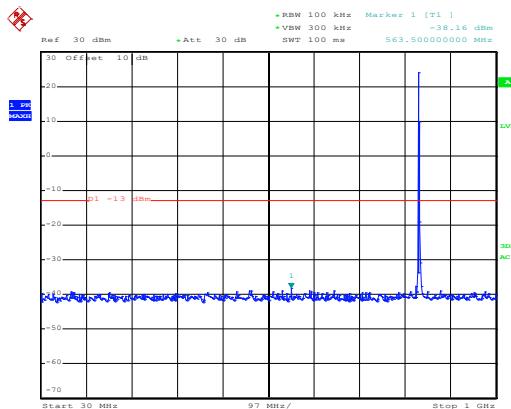


Date: 7.NOV.2017 22:11:58

Date: 17.NOV.2017 20:07:52

30MHz~1GHz

Middle channel



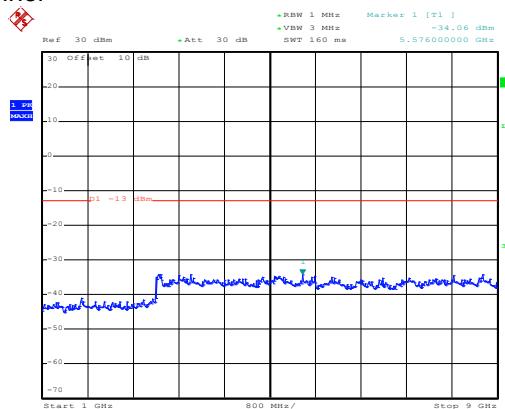
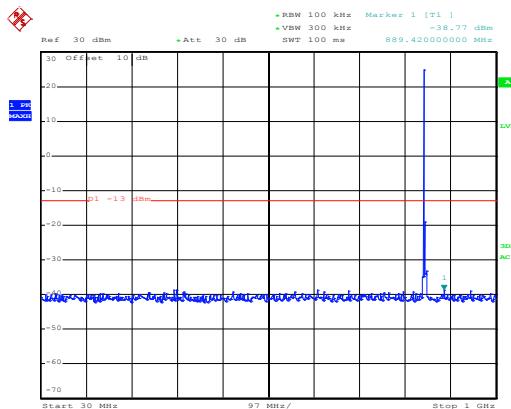
Date: 7.NOV.2017 22:13:18

Date: 17.NOV.2017 20:08:14

30MHz~1GHz

1GHz~9GHz

High channel



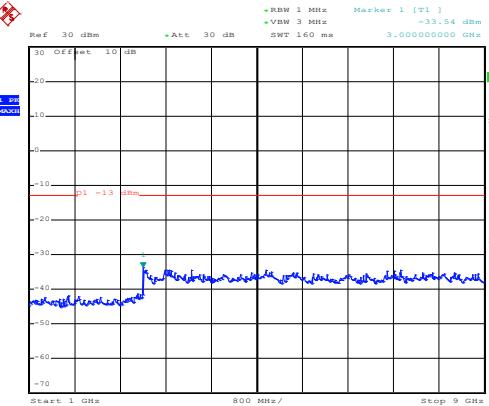
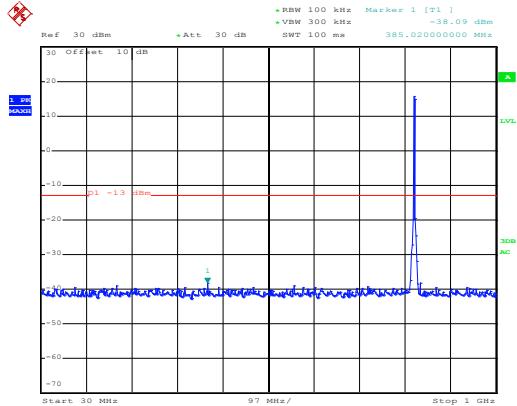
Date: 7.NOV.2017 22:14:39

Date: 17.NOV.2017 20:08:42

30MHz~1GHz

1GHz~9GHz

16 QAM & RB Size 15 Lowest channel



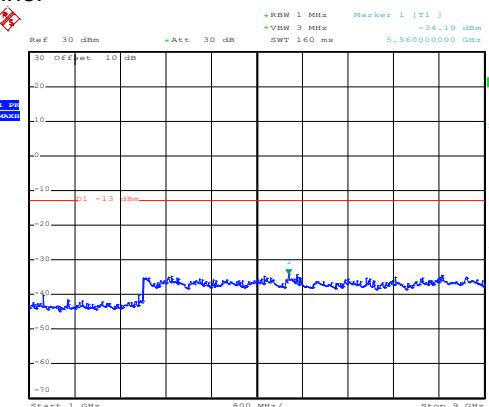
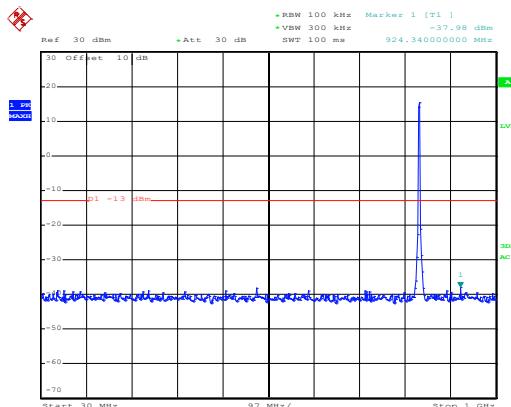
Date: 7.NOV.2017 22:12:48

30MHz~1GHz

Date: 17.NOV.2017 20:08:02

1GHz~9GHz

Middle channel



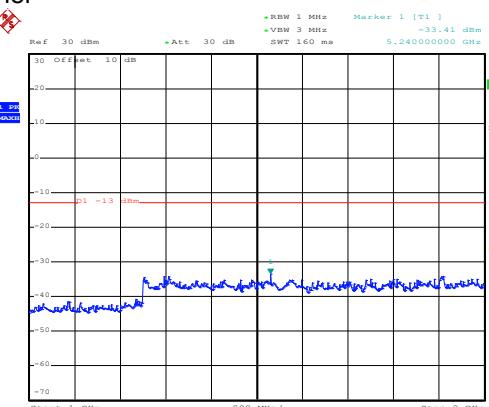
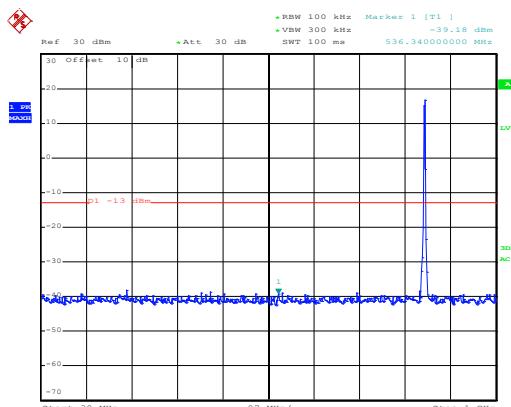
Date: 7.NOV.2017 22:14:00

30MHz~1GHz

Date: 17.NOV.2017 20:08:26

1GHz~9GHz

High channel



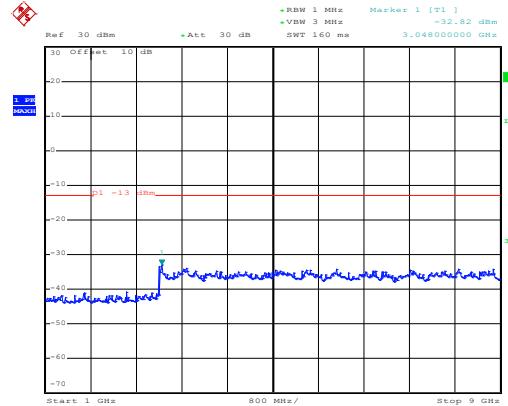
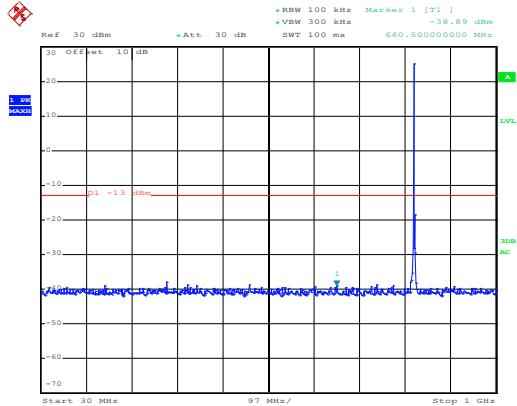
Date: 7.NOV.2017 22:15:23

30MHz~1GHz

Date: 17.NOV.2017 20:08:54

1GHz~9GHz

**QPSK & RB Size 1
Lowest channel**



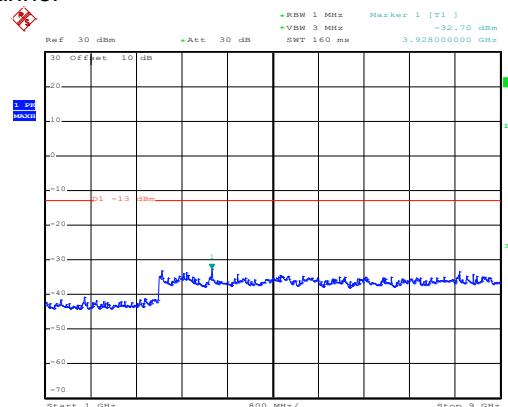
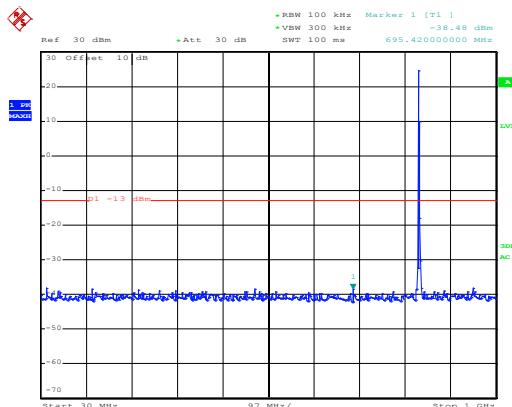
Date: 7.NOV.2017 22:11:49

30MHz~1GHz

Date: 17.NOV.2017 20:07:48

1GHz~9GHz

Middle channel



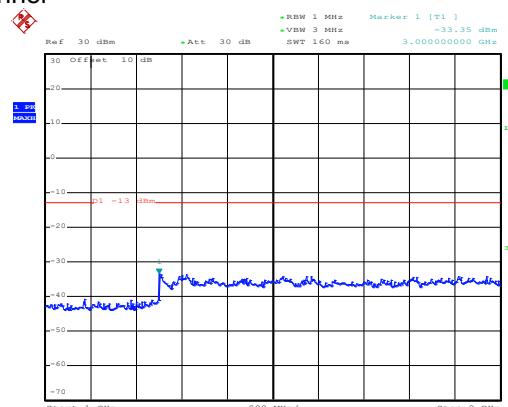
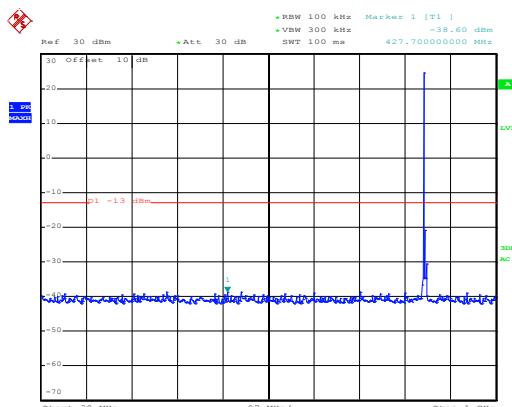
Date: 7.NOV.2017 22:13:05

30MHz~1GHz

Date: 17.NOV.2017 20:08:10

1GHz~9GHz

High channel



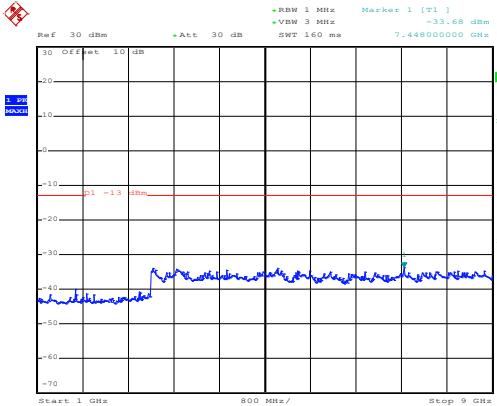
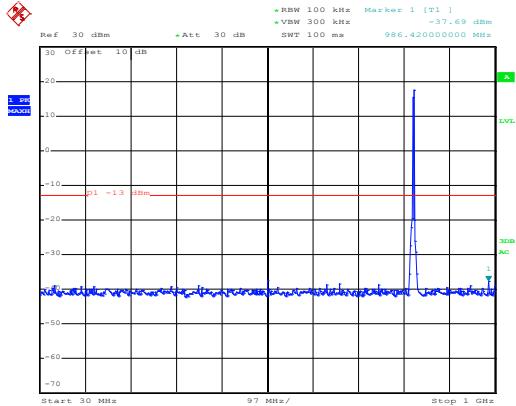
Date: 7.NOV.2017 22:14:26

30MHz~1GHz

Date: 17.NOV.2017 20:08:37

1GHz~9GHz

**QPSK & RB Size 15
Lowest channel**



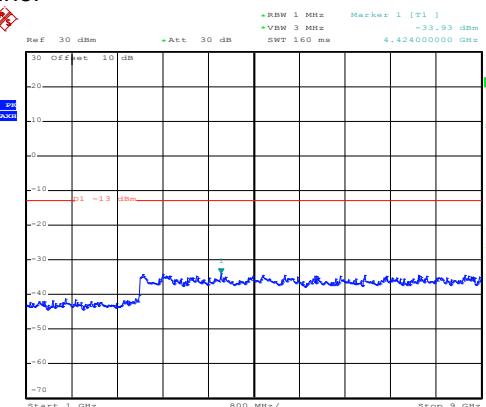
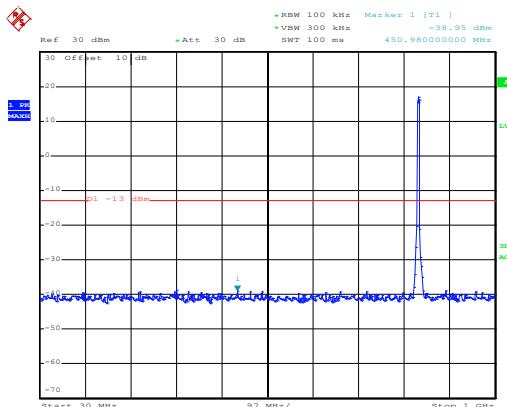
Date: 7.NOV.2017 22:12:38

30MHz~1GHz

Date: 17.NOV.2017 20:07:58

1GHz~9GHz

Middle channel



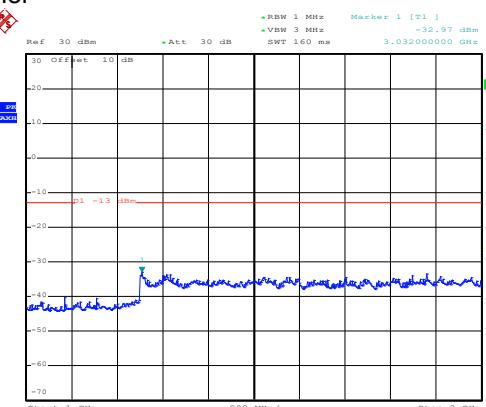
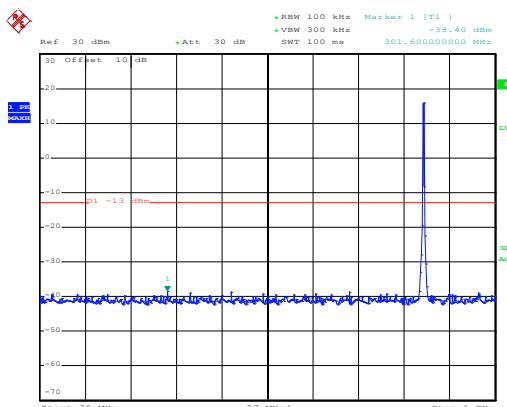
Date: 7.NOV.2017 22:13:48

30MHz~1GHz

Date: 17.NOV.2017 20:08:22

1GHz~9GHz

High channel



Date: 7.NOV.2017 22:15:12

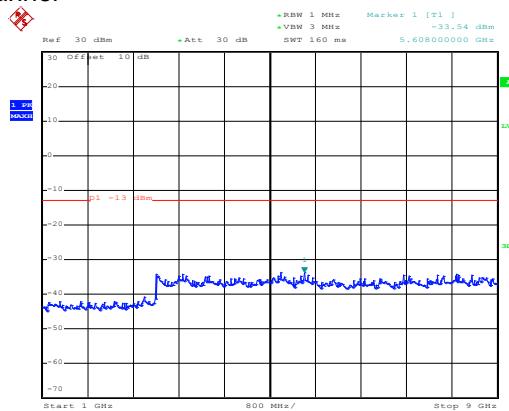
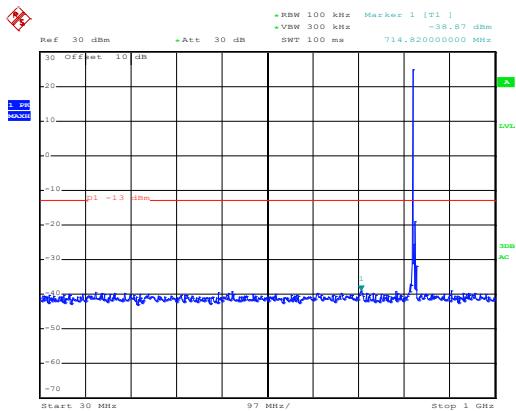
30MHz~1GHz

Date: 17.NOV.2017 20:08:50

1GHz~9GHz

5MHz

16 QAM & RB Size 1 Lowest channel

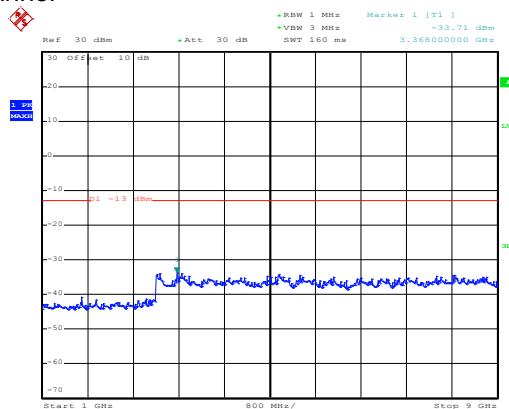
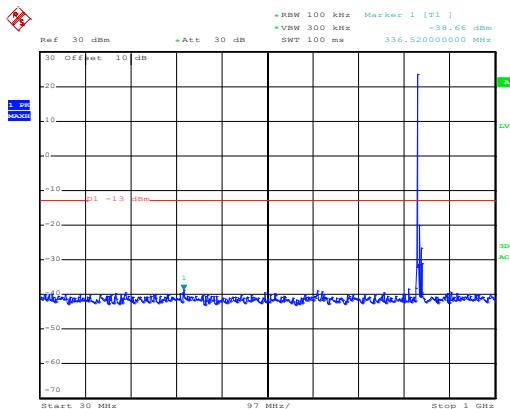


Date: 7.NOV.2017 22:16:05

Date: 17.NOV.2017 20:09:10

30MHz~1GHz

Middle channel



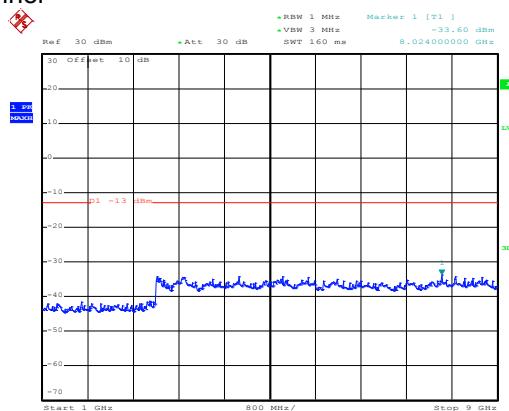
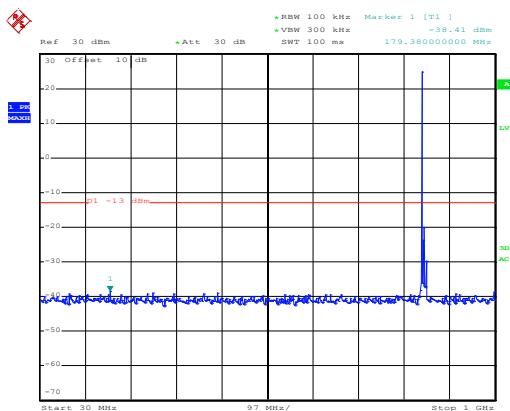
Date: 7.NOV.2017 22:17:31

Date: 17.NOV.2017 20:09:47

30MHz~1GHz

1GHz~9GHz

High channel



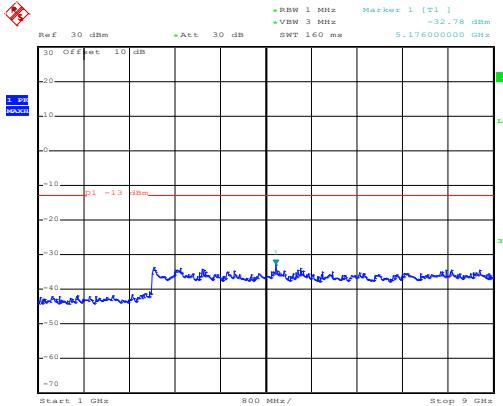
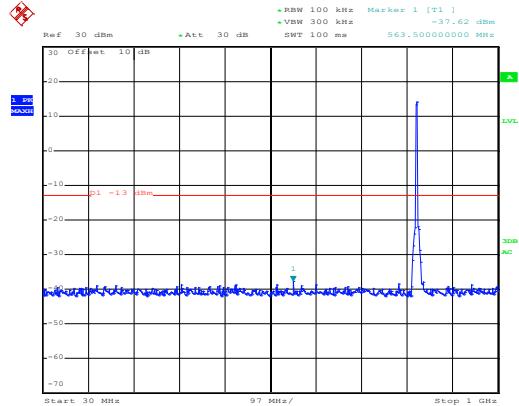
Date: 7.NOV.2017 22:18:53

Date: 17.NOV.2017 20:10:21

30MHz~1GHz

1GHz~9GHz

16 QAM & RB Size 25 Lowest channel



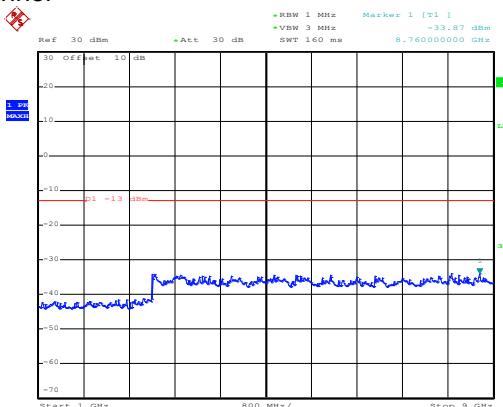
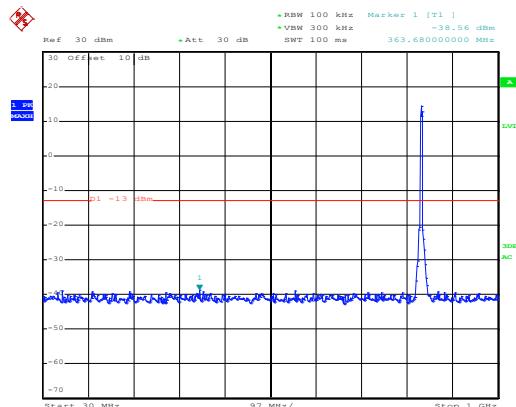
Date: 7.NOV.2017 22:17:03

Date: 17.NOV.2017 20:09:32

30MHz~1GHz

1GHz~9GHz

Middle channel



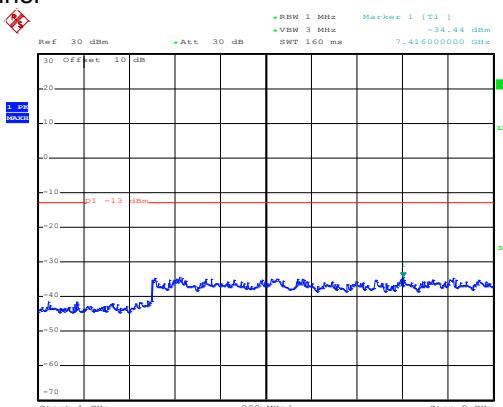
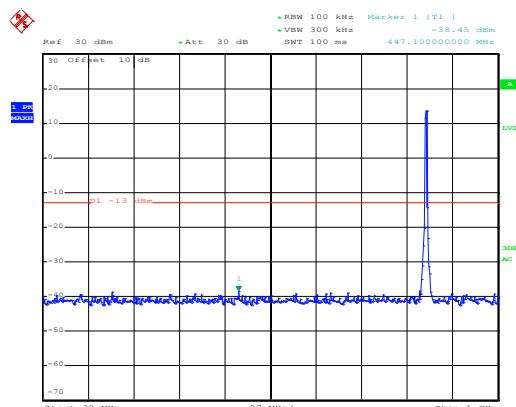
Date: 7.NOV.2017 22:18:19

Date: 17.NOV.2017 20:10:05

30MHz~1GHz

1GHz~9GHz

High channel



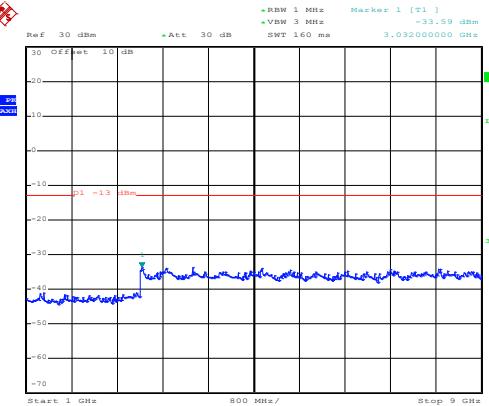
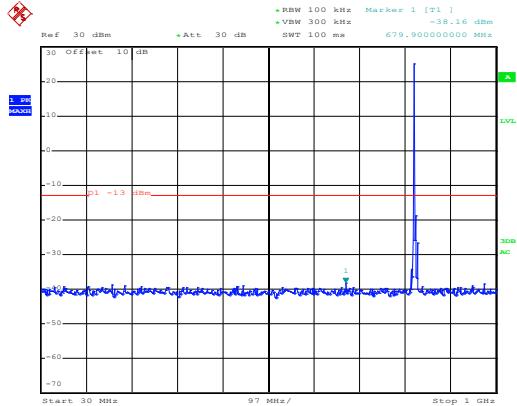
Date: 7.NOV.2017 22:19:45

Date: 17.NOV.2017 20:10:36

30MHz~1GHz

1GHz~9GHz

**QPSK & RB Size 1
Lowest channel**



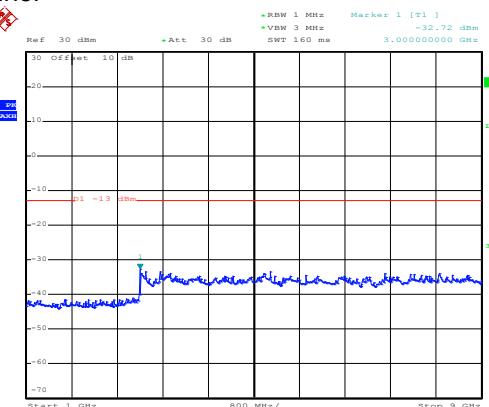
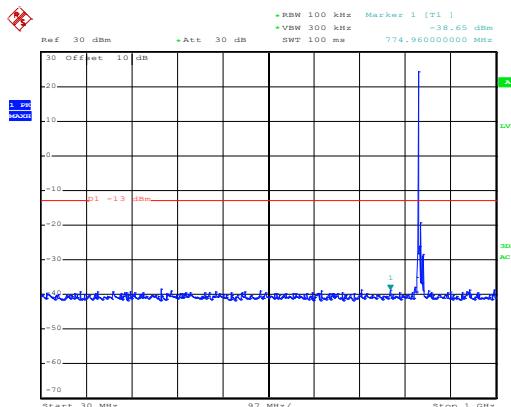
Date: 7.NOV.2017 22:15:57

30MHz~1GHz

Date: 17.NOV.2017 20:09:04

1GHz~9GHz

Middle channel



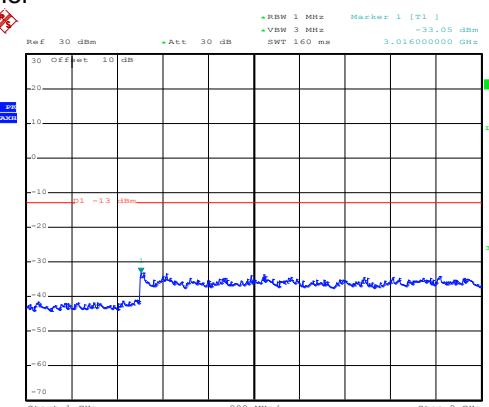
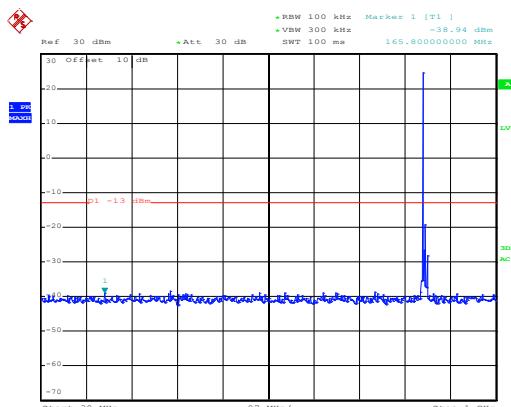
Date: 7.NOV.2017 22:17:23

30MHz~1GHz

Date: 17.NOV.2017 20:09:42

1GHz~9GHz

High channel



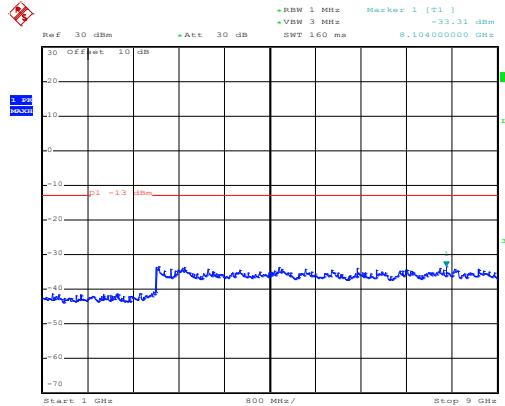
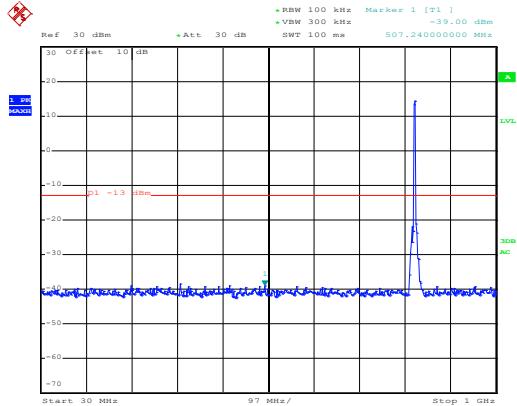
Date: 7.NOV.2017 22:18:40

30MHz~1GHz

Date: 17.NOV.2017 20:10:16

1GHz~9GHz

**QPSK & RB Size 25
Lowest channel**



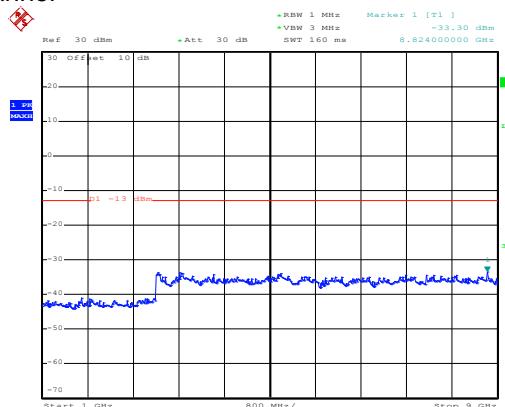
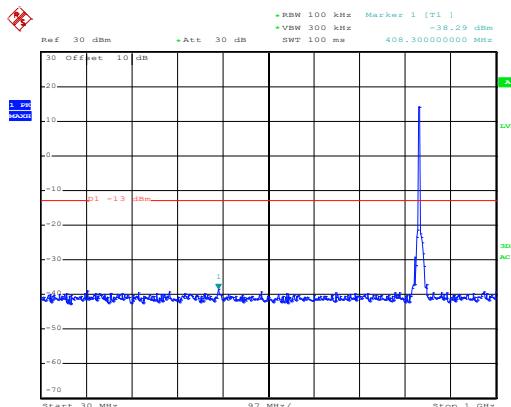
Date: 7.NOV.2017 22:16:47

30MHz~1GHz

Date: 17.NOV.2017 20:09:25

1GHz~9GHz

Middle channel



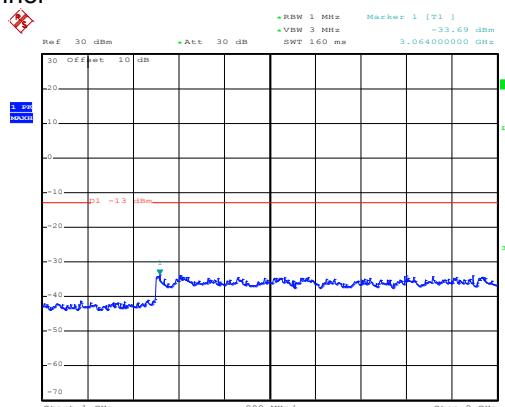
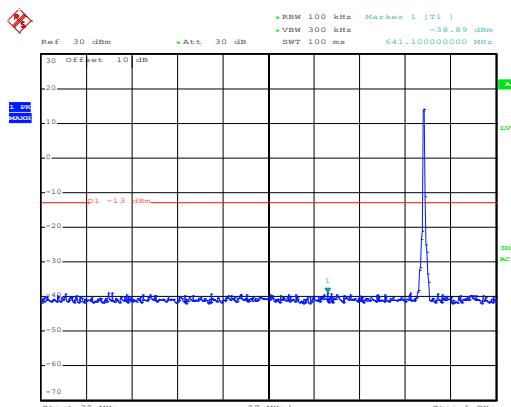
Date: 7.NOV.2017 22:18:10

30MHz~1GHz

Date: 17.NOV.2017 20:09:58

1GHz~9GHz

High channel



Date: 7.NOV.2017 22:19:35

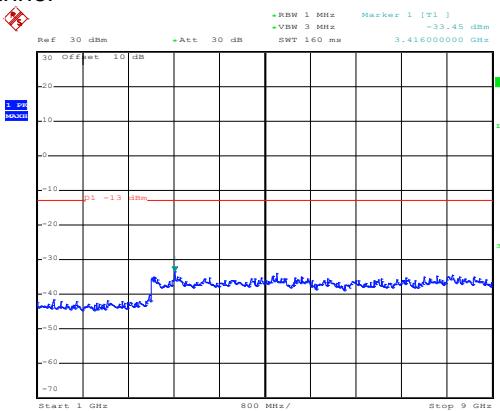
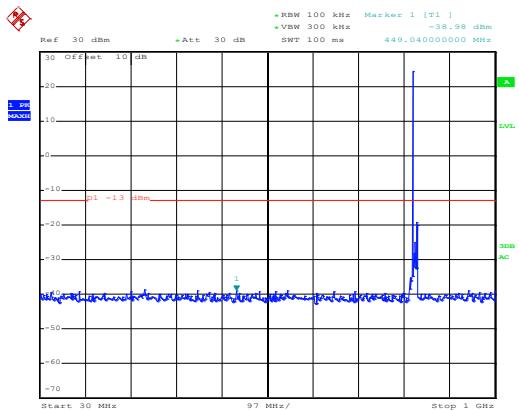
30MHz~1GHz

Date: 17.NOV.2017 20:10:31

1GHz~9GHz

10MHz

16 QAM & RB Size 1 Lowest channel

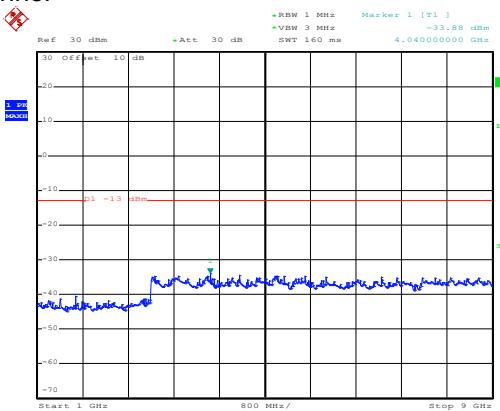
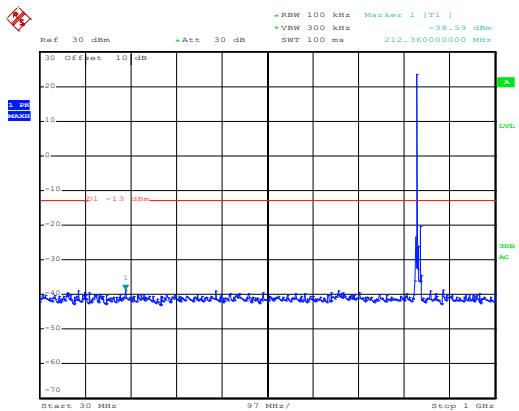


Date: 7.NOV.2017 22:20:29

Date: 17.NOV.2017 20:10:59

30MHz~1GHz

Middle channel

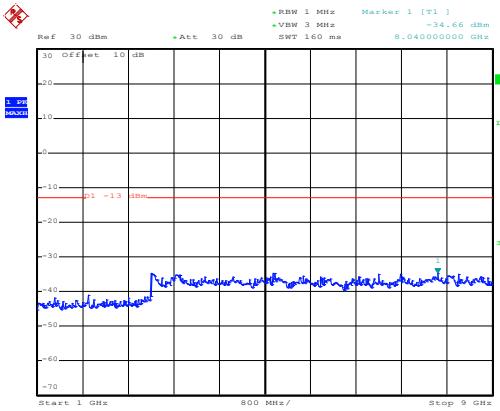
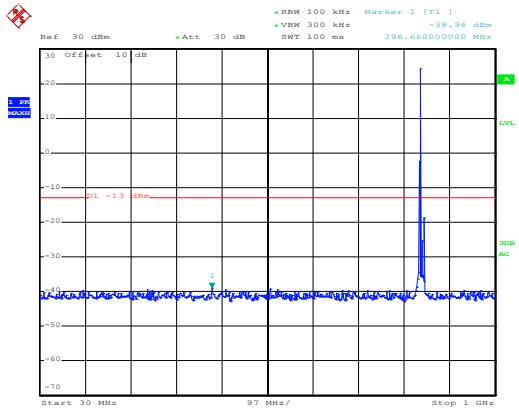


Date: 7.NOV.2017 22:21:54

Date: 17.NOV.2017 20:11:32

30MHz~1GHz

High channel



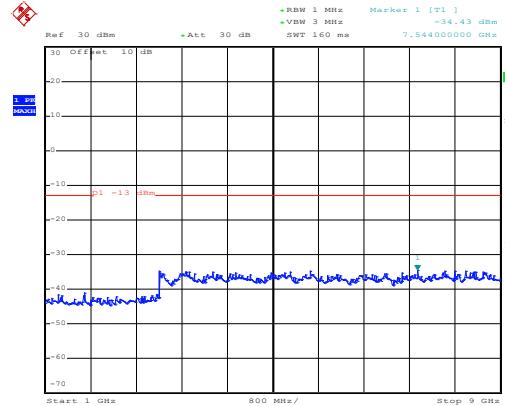
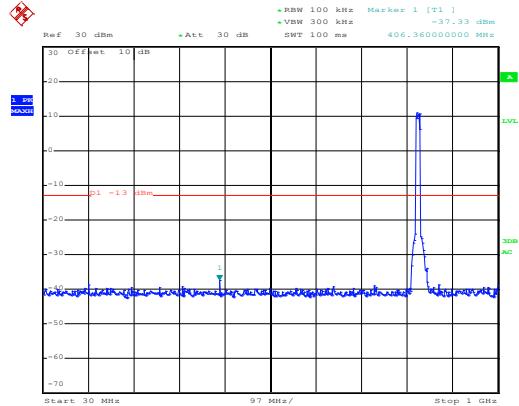
Date: 7.NOV.2017 22:23:11

Date: 17.NOV.2017 20:12:01

30MHz~1GHz

1GHz~9GHz

16 QAM & RB Size 50 Lowest channel



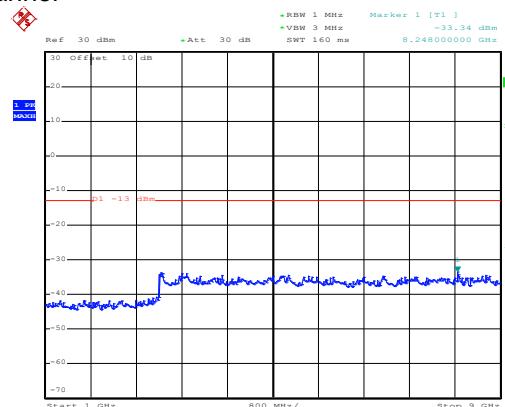
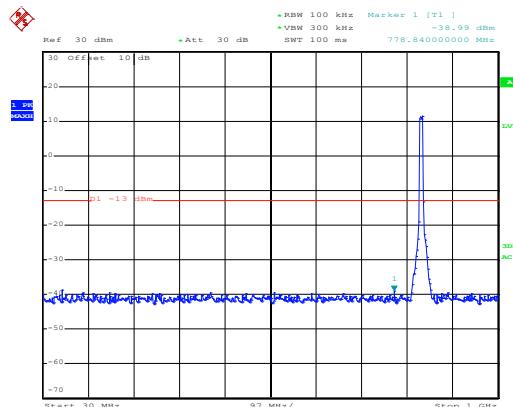
Date: 7.NOV.2017 22:21:24

30MHz~1GHz

Date: 17.NOV.2017 20:11:18

1GHz~9GHz

Middle channel



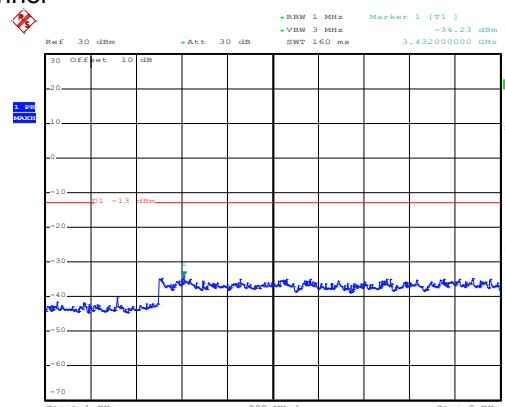
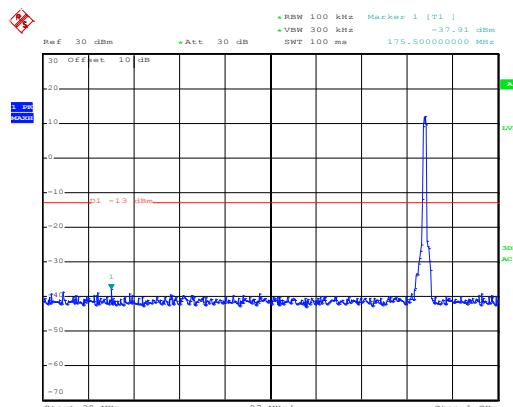
Date: 7.NOV.2017 22:22:42

30MHz~1GHz

Date: 17.NOV.2017 20:11:48

1GHz~9GHz

High channel



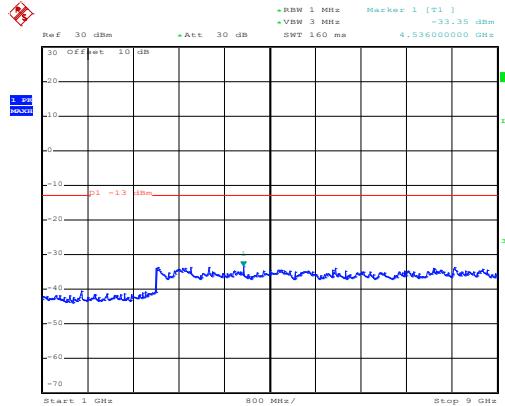
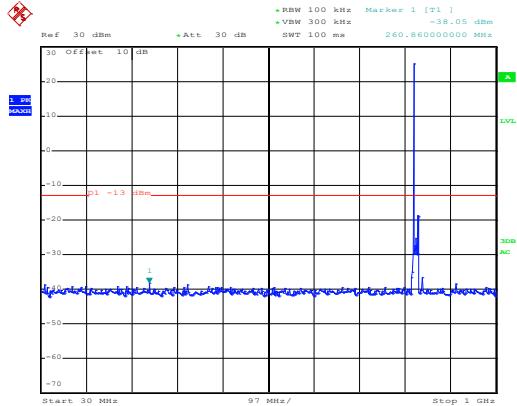
Date: 7.NOV.2017 22:23:55

30MHz~1GHz

Date: 17.NOV.2017 20:12:11

1GHz~9GHz

**QPSK & RB Size 1
Lowest channel**



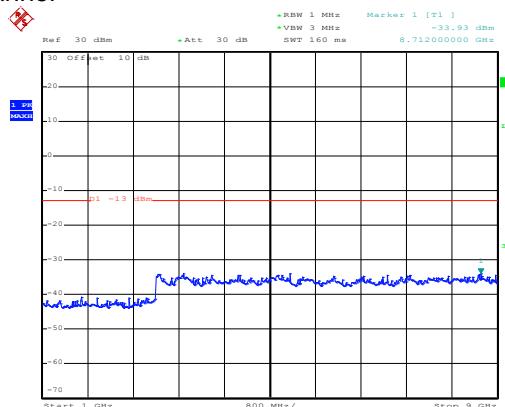
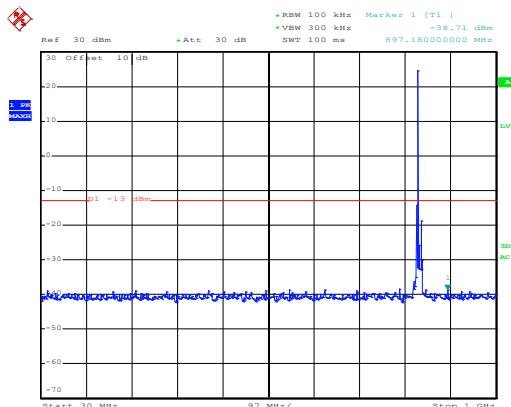
Date: 7.NOV.2017 22:20:17

30MHz~1GHz

Date: 17.NOV.2017 20:10:55

1GHz~9GHz

Middle channel



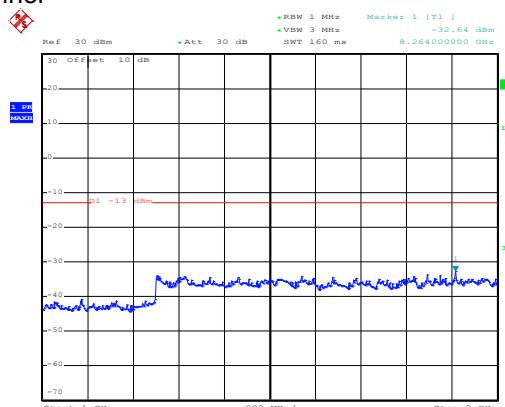
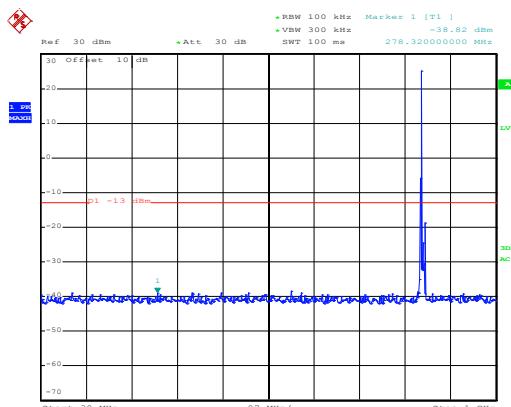
Date: 7.NOV.2017 22:21:44

30MHz~1GHz

Date: 17.NOV.2017 20:11:28

1GHz~9GHz

High channel



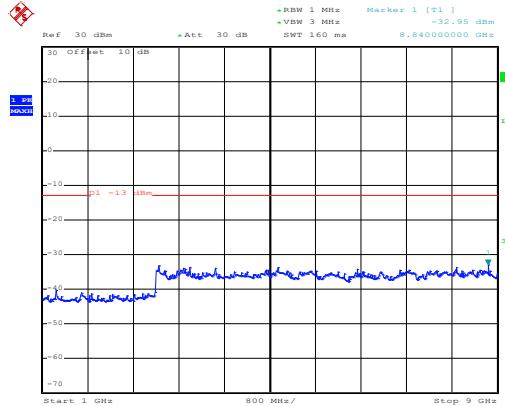
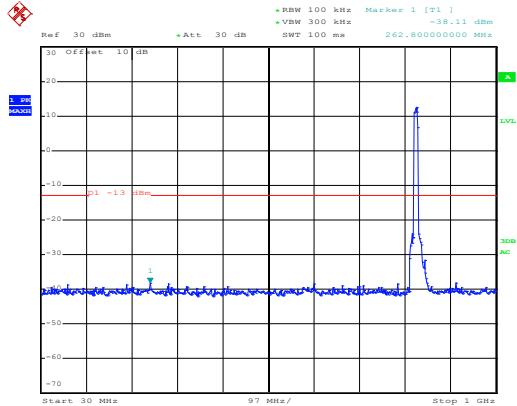
Date: 7.NOV.2017 22:23:00

30MHz~1GHz

Date: 17.NOV.2017 20:11:57

1GHz~9GHz

**QPSK & RB Size 50
Lowest channel**



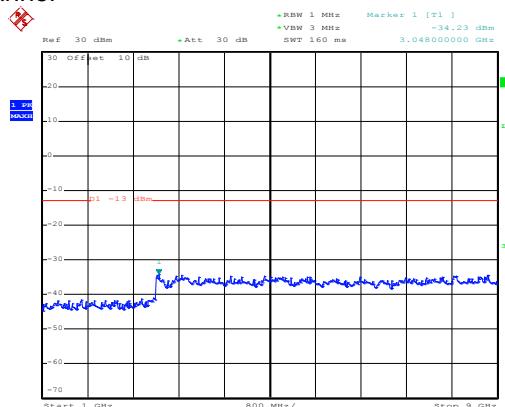
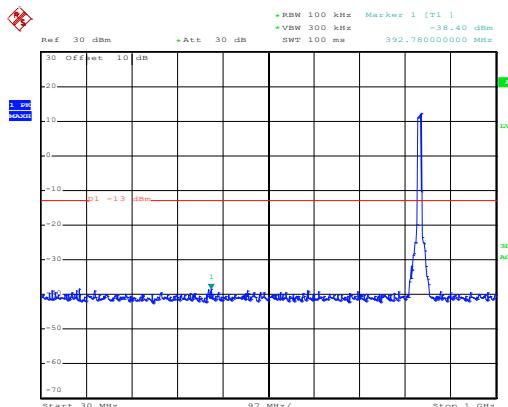
Date: 7.NOV.2017 22:21:13

30MHz~1GHz

Date: 17.NOV.2017 20:11:14

1GHz~9GHz

Middle channel



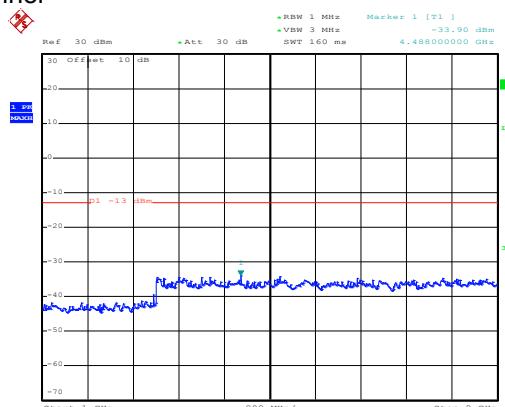
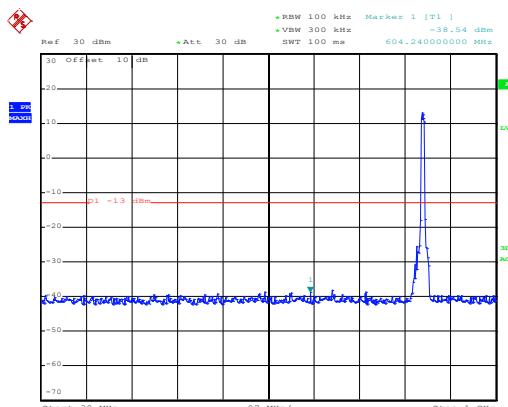
Date: 7.NOV.2017 22:22:32

30MHz~1GHz

Date: 17.NOV.2017 20:11:40

1GHz~9GHz

High channel



Date: 7.NOV.2017 22:23:47

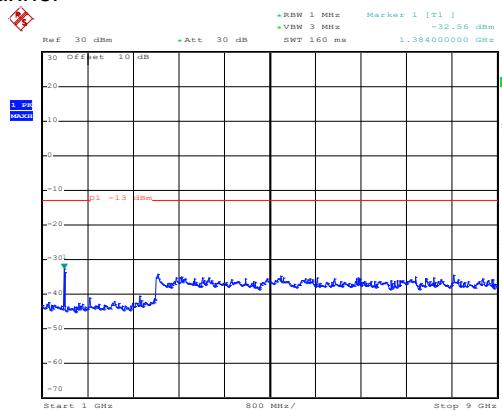
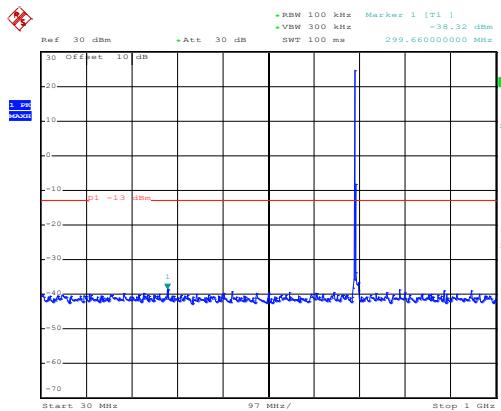
30MHz~1GHz

Date: 17.NOV.2017 20:12:07

1GHz~9GHz

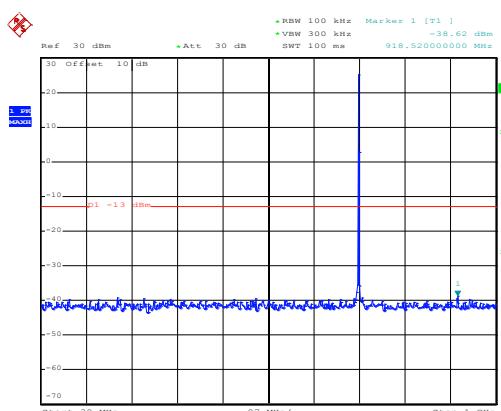
LTE band 12, 1.4MHz:

16 QAM & RB Size 1 Lowest channel

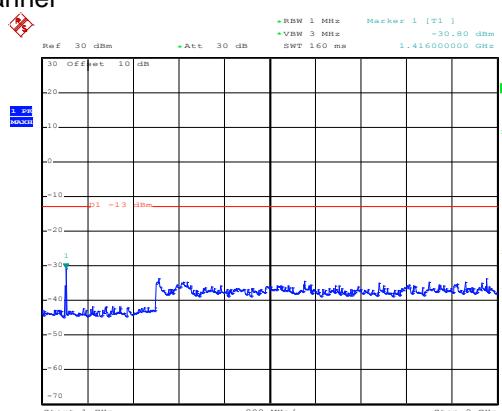


Date: 17.NOV.2017 20:31:29

30MHz~1GHz

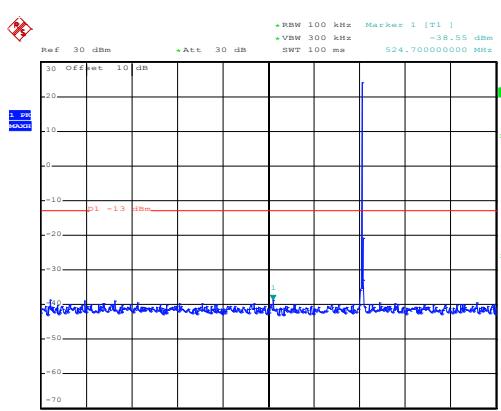


Middle channel

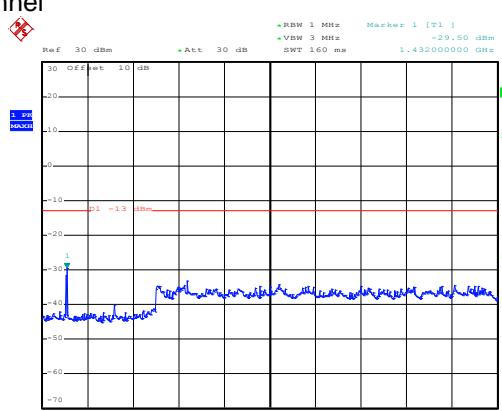


Date: 17.NOV.2017 20:32:14

30MHz~1GHz



High channel

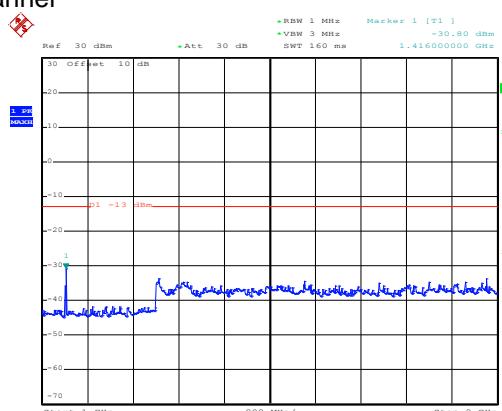


Date: 17.NOV.2017 20:33:04

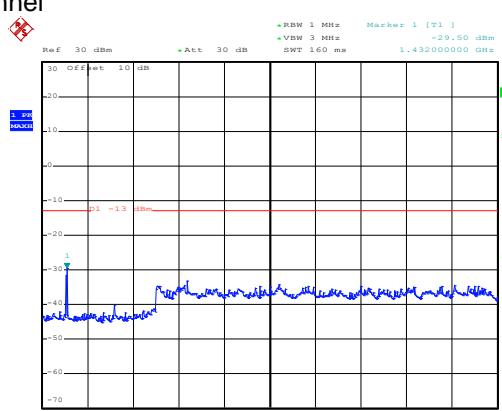
30MHz~1GHz

Date: 17.NOV.2017 20:19:45

1GHz~9GHz

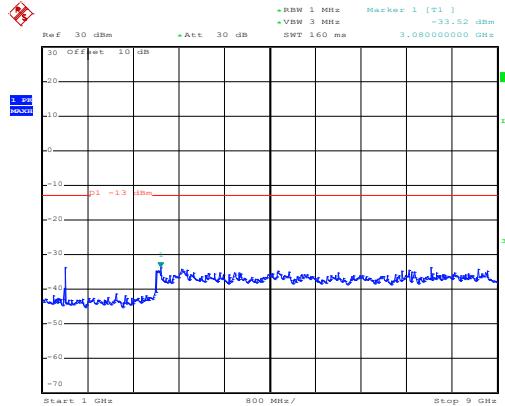
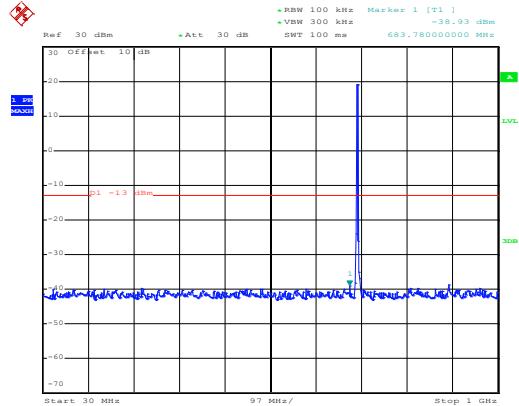


1GHz~9GHz



1GHz~9GHz

16 QAM & RB Size 6 Lowest channel



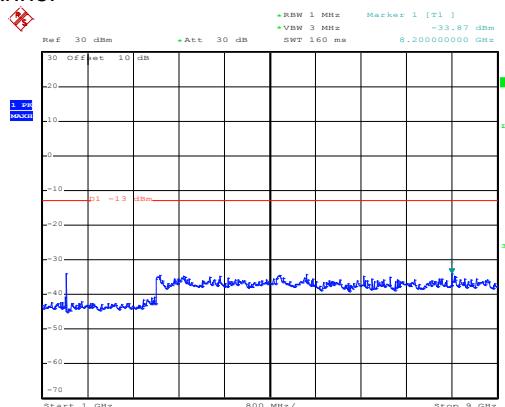
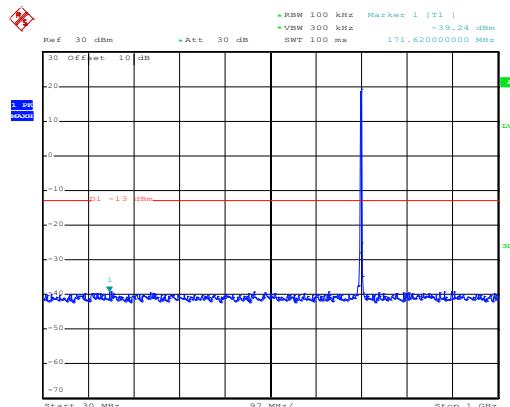
Date: 17.NOV.2017 20:31:48

30MHz~1GHz

Date: 17.NOV.2017 20:19:30

1GHz~9GHz

Middle channel



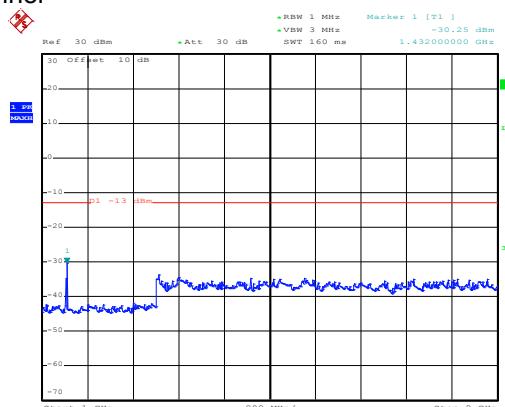
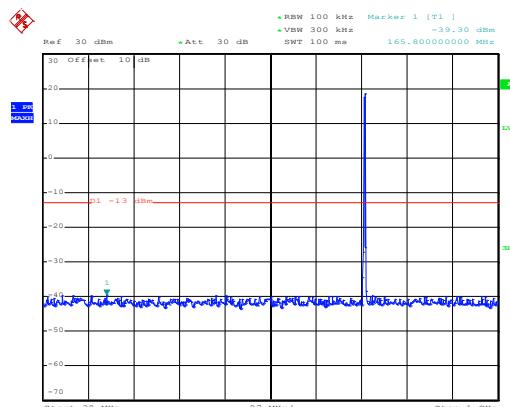
Date: 17.NOV.2017 20:32:39

30MHz~1GHz

Date: 17.NOV.2017 20:19:55

1GHz~9GHz

High channel



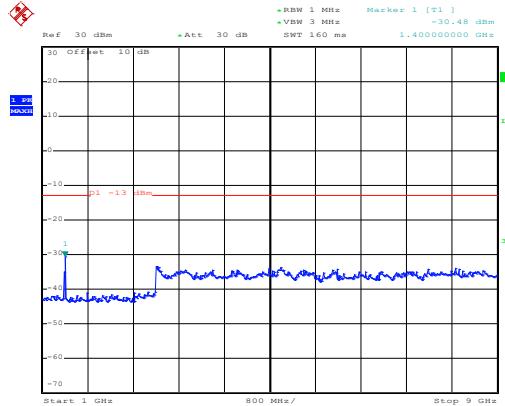
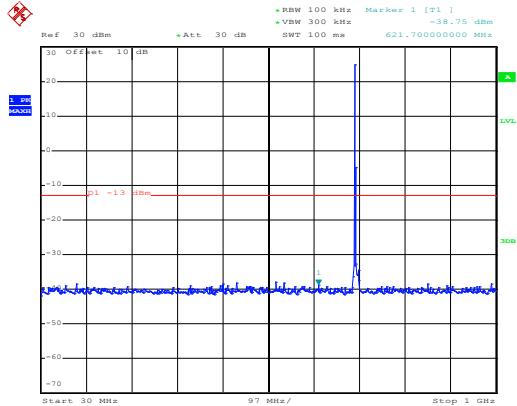
Date: 17.NOV.2017 20:34:07

30MHz~1GHz

Date: 17.NOV.2017 20:20:23

1GHz~9GHz

**QPSK & RB Size 1
Lowest channel**



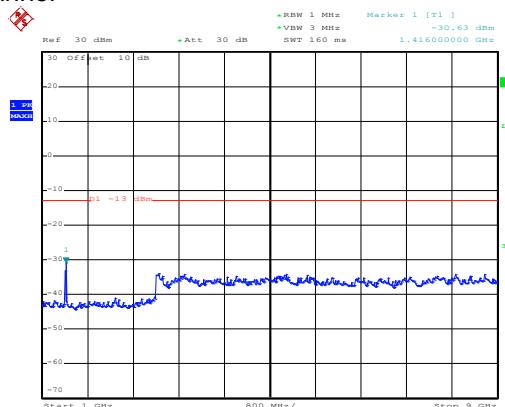
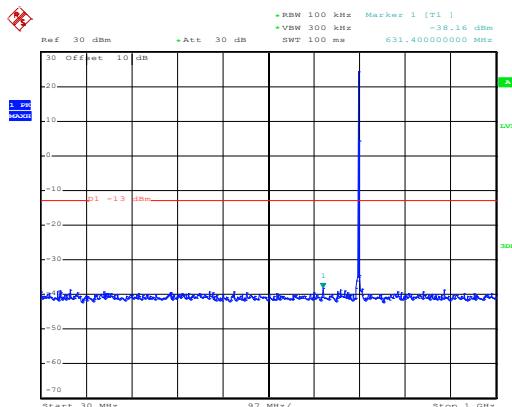
Date: 17.NOV.2017 20:31:20

30MHz~1GHz

Date: 17.NOV.2017 20:19:17

1GHz~9GHz

Middle channel



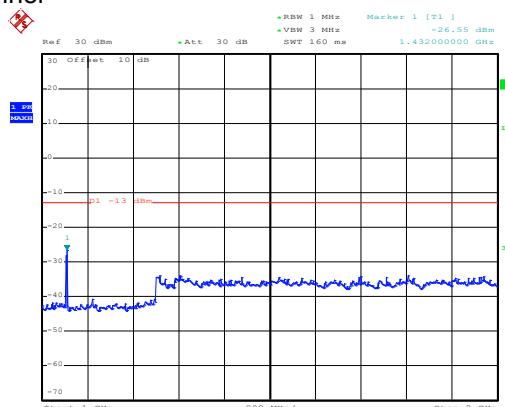
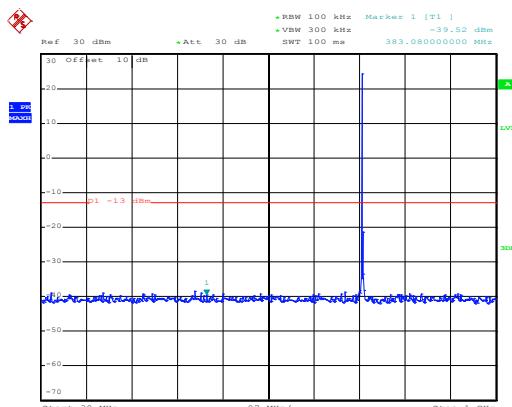
Date: 17.NOV.2017 20:32:07

30MHz~1GHz

Date: 17.NOV.2017 20:19:42

1GHz~9GHz

High channel



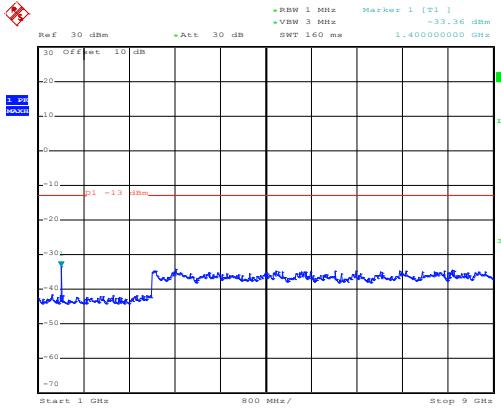
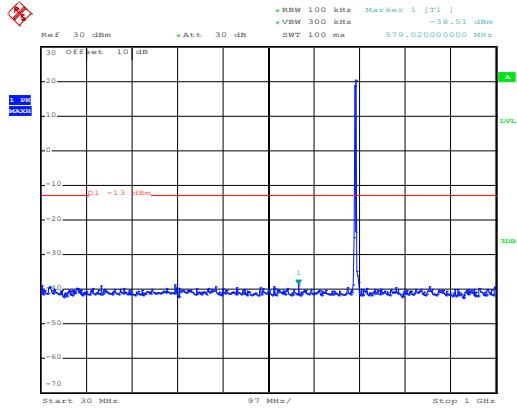
Date: 17.NOV.2017 20:32:58

30MHz~1GHz

Date: 17.NOV.2017 20:20:09

1GHz~9GHz

**QPSK & RB Size 6
Lowest channel**



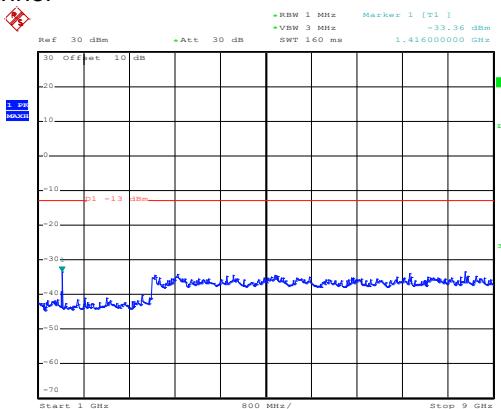
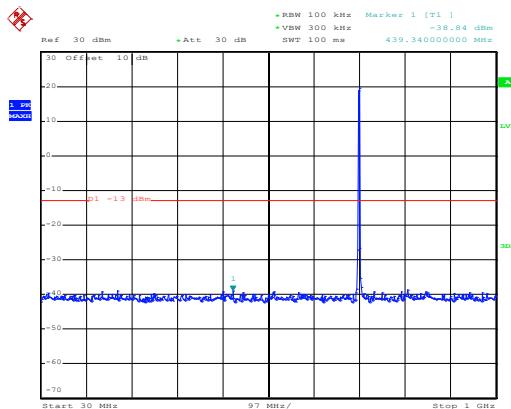
Date: 17.NOV.2017 20:31:42

30MHz~1GHz

Date: 17.NOV.2017 20:19:27

1GHz~9GHz

Middle channel



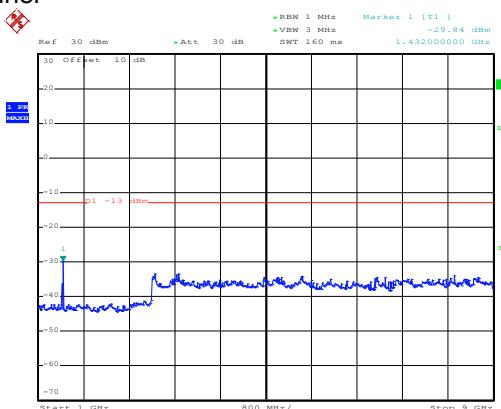
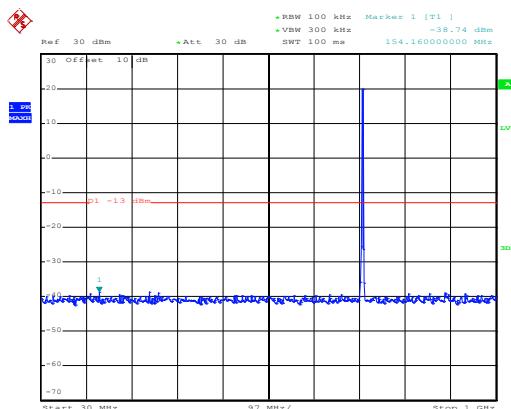
Date: 17.NOV.2017 20:32:26

30MHz~1GHz

Date: 17.NOV.2017 20:19:52

1GHz~9GHz

High channel



Date: 17.NOV.2017 20:34:00

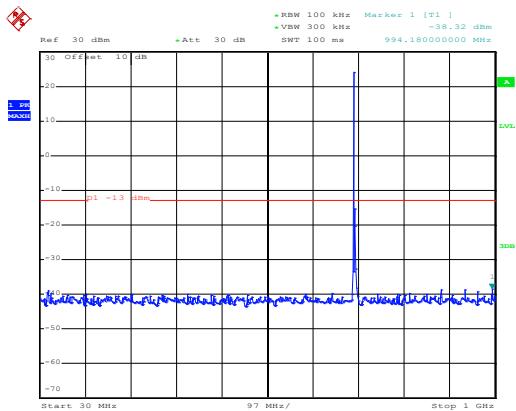
30MHz~1GHz

Date: 17.NOV.2017 20:20:19

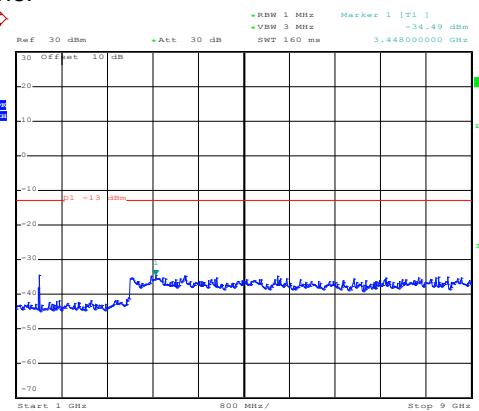
1GHz~9GHz

3MHz

16 QAM & RB Size 1 Lowest channel



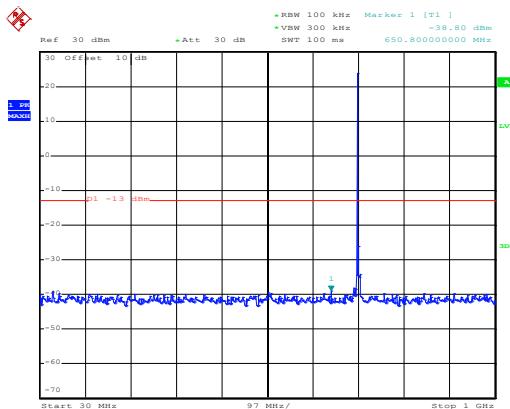
Date: 17.NOV.2017 20:34:35



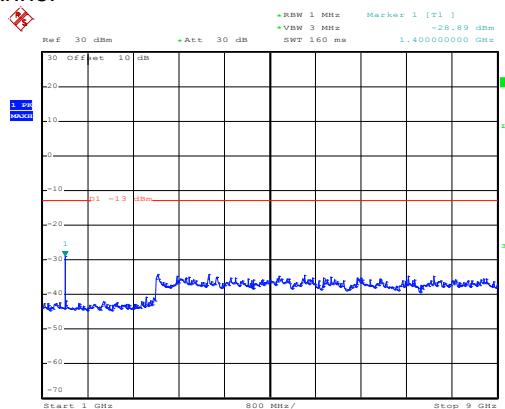
Date: 17.NOV.2017 20:20:44

30MHz~1GHz

Middle channel



Date: 17.NOV.2017 20:36:03

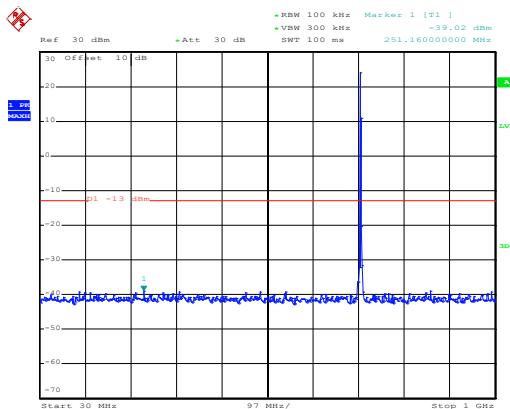


Date: 17.NOV.2017 20:21:11

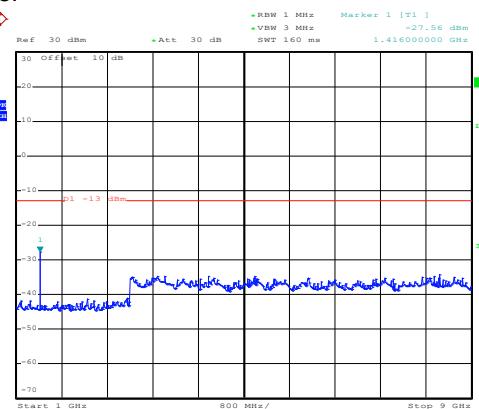
30MHz~1GHz

1GHz~9GHz

High channel



Date: 17.NOV.2017 20:37:13

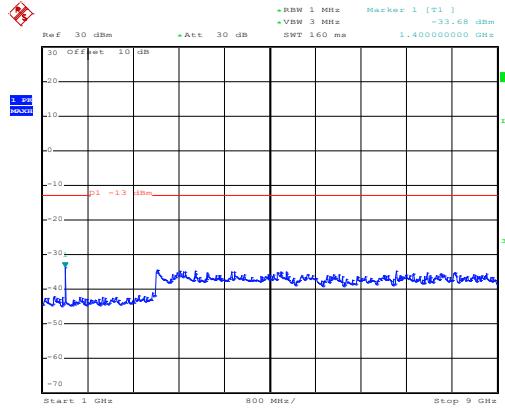
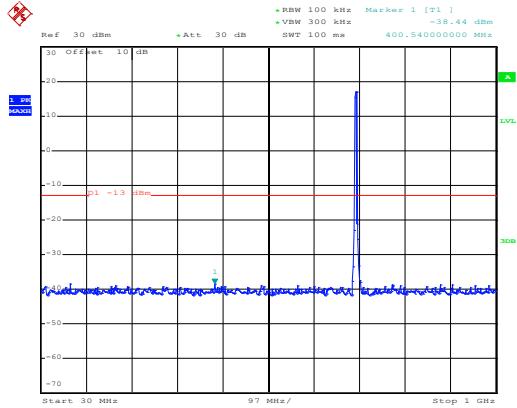


Date: 17.NOV.2017 20:21:43

30MHz~1GHz

1GHz~9GHz

**16 QAM & RB Size 15
Lowest channel**



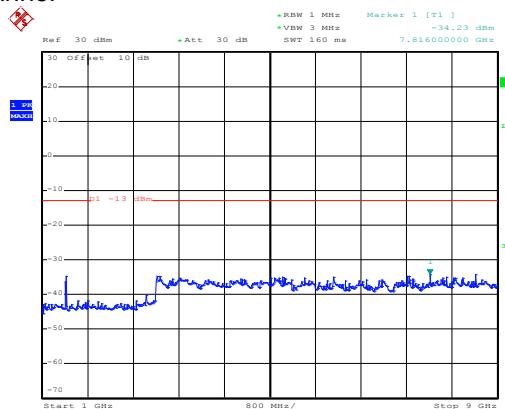
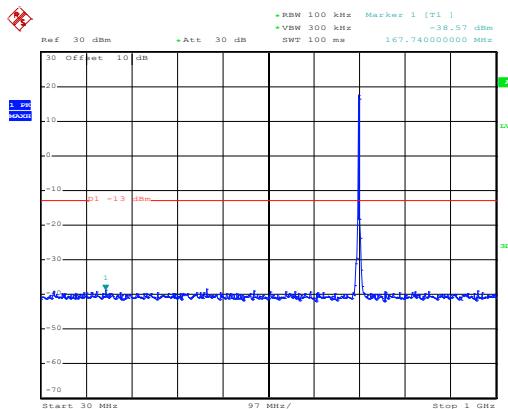
Date: 17.NOV.2017 20:35:28

30MHz~1GHz

Date: 17.NOV.2017 20:20:56

1GHz~9GHz

Middle channel



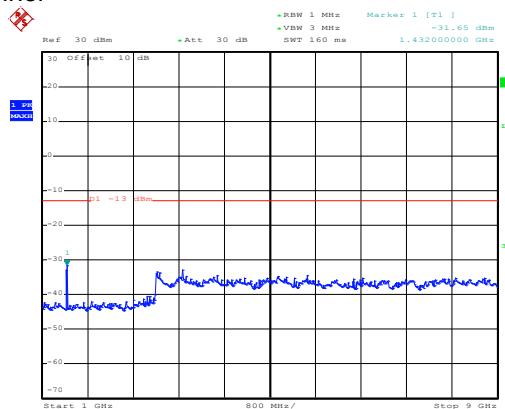
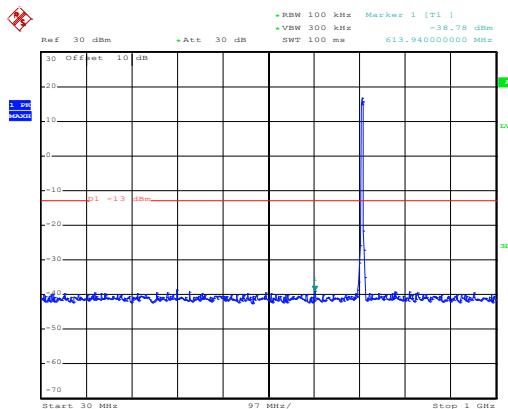
Date: 17.NOV.2017 20:36:33

30MHz~1GHz

Date: 17.NOV.2017 20:21:26

1GHz~9GHz

High channel



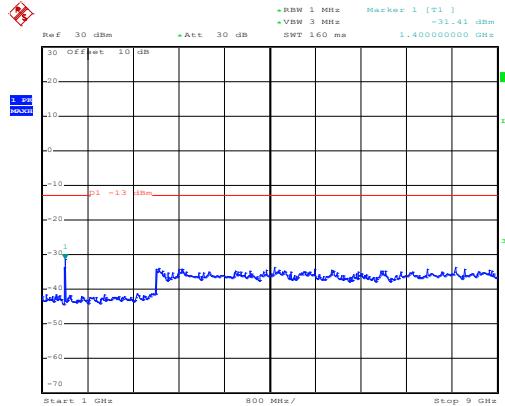
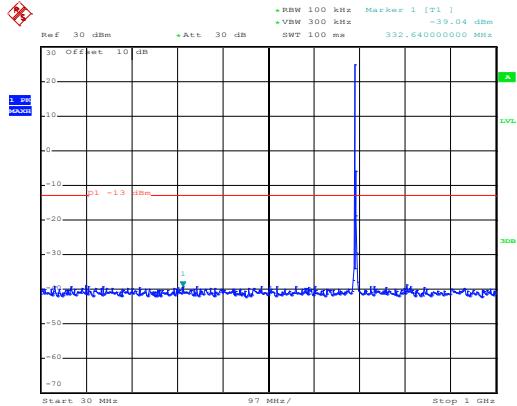
Date: 17.NOV.2017 20:37:38

30MHz~1GHz

Date: 17.NOV.2017 20:21:55

1GHz~9GHz

**QPSK & RB Size 1
Lowest channel**



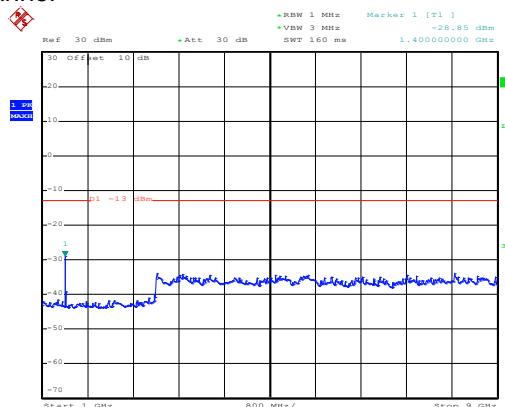
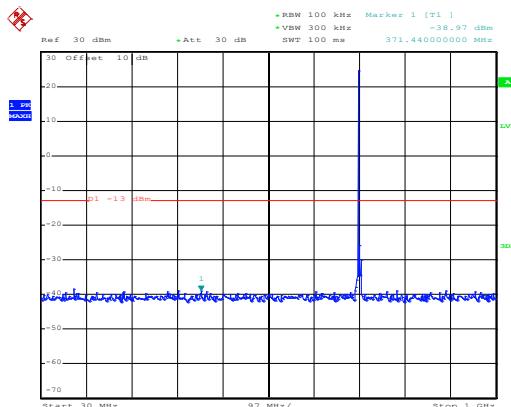
Date: 17.NOV.2017 20:34:29

30MHz~1GHz

Date: 17.NOV.2017 20:20:40

1GHz~9GHz

Middle channel



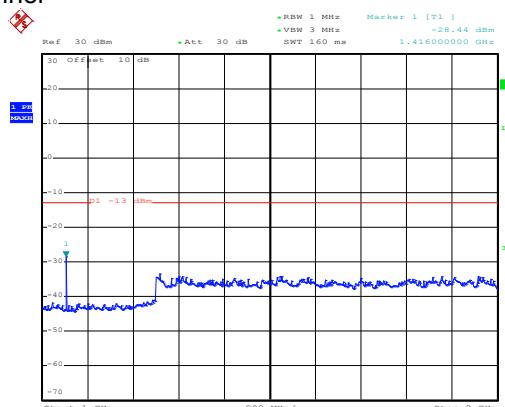
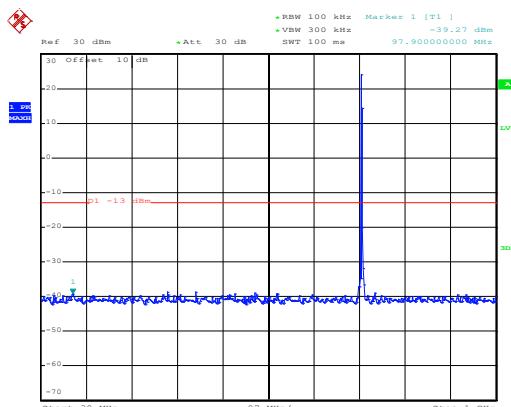
Date: 17.NOV.2017 20:35:56

30MHz~1GHz

Date: 17.NOV.2017 20:21:08

1GHz~9GHz

High channel



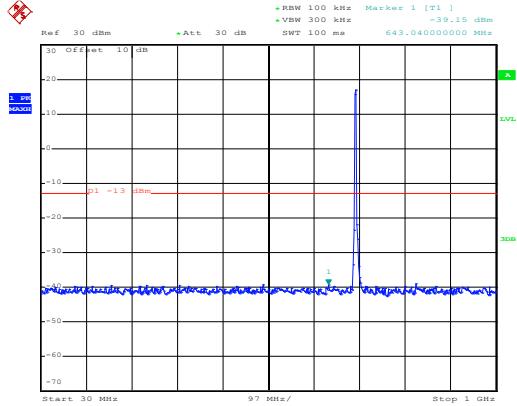
Date: 17.NOV.2017 20:37:05

30MHz~1GHz

Date: 17.NOV.2017 20:21:39

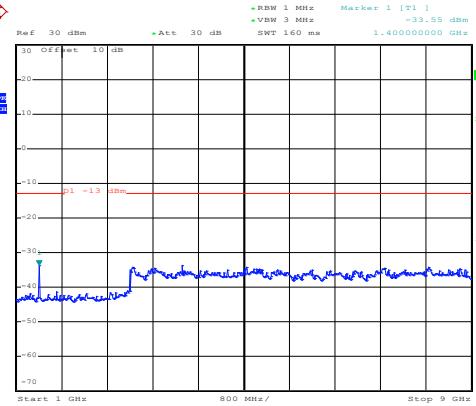
1GHz~9GHz

QPSK & RB Size 15
Lowest channel



Date: 17.NOV.2017 20:35:06

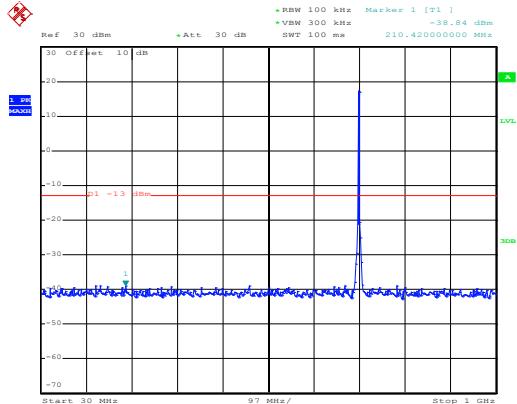
30MHz~1GHz



Date: 17.NOV.2017 20:20:53

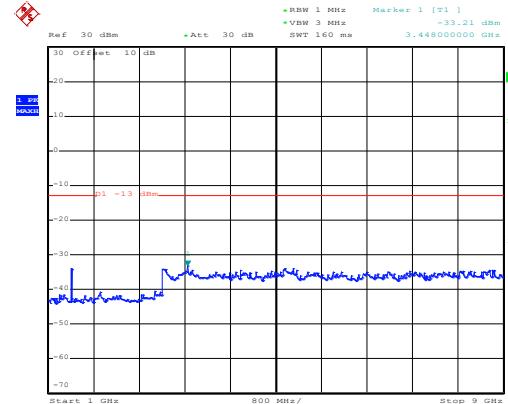
1GHz~9GHz

Middle channel



Date: 17.NOV.2017 20:36:14

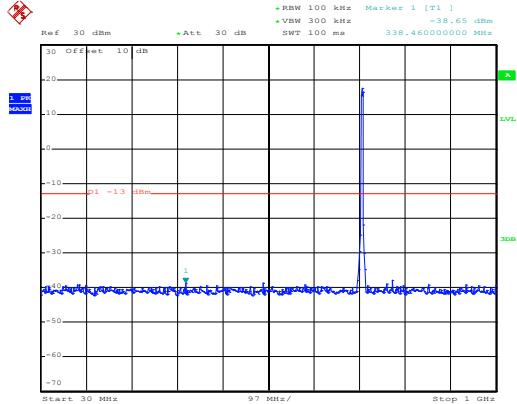
30MHz~1GHz



Date: 17.NOV.2017 20:21:23

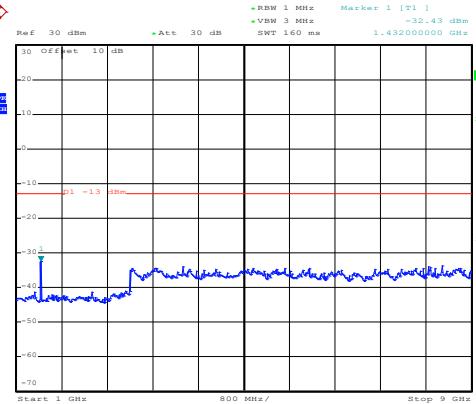
1GHz~9GHz

High channel



Date: 17.NOV.2017 20:37:26

30MHz~1GHz

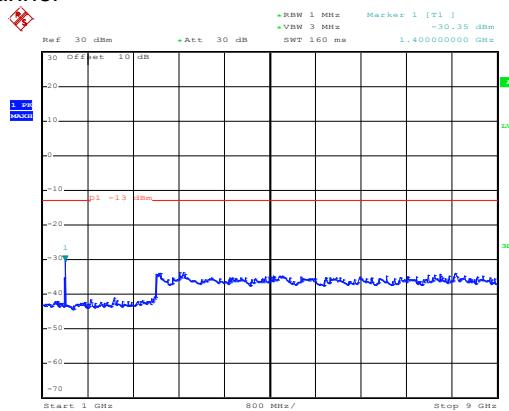
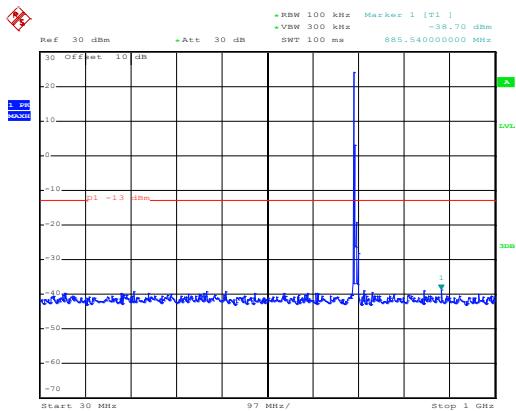


Date: 17.NOV.2017 20:21:50

1GHz~9GHz

5MHz

16 QAM & RB Size 1 Lowest channel



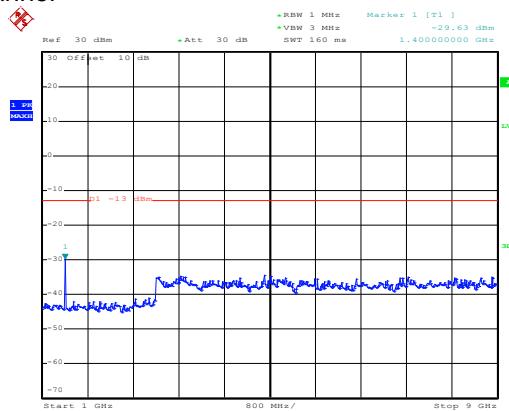
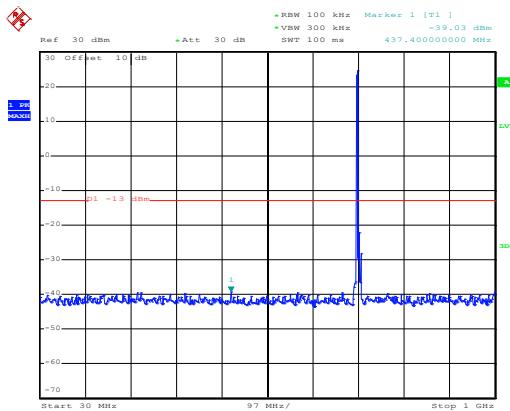
Date: 17.NOV.2017 20:38:09

Date: 17.NOV.2017 20:22:38

30MHz~1GHz

1GHz~9GHz

Middle channel



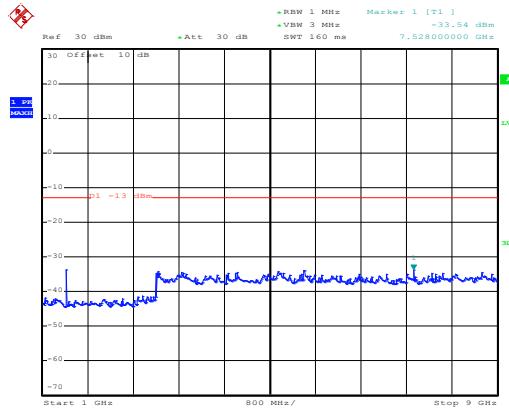
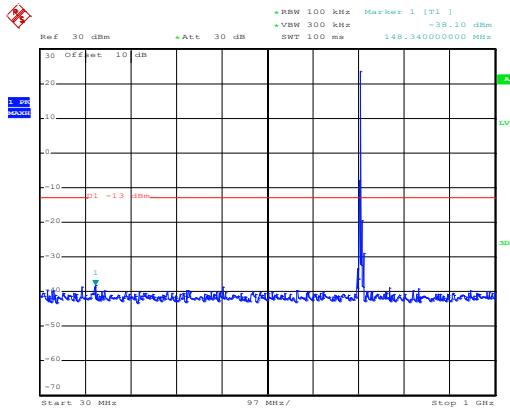
Date: 17.NOV.2017 20:39:03

Date: 17.NOV.2017 20:23:22

30MHz~1GHz

1GHz~9GHz

High channel



Date: 17.NOV.2017 20:39:46

Date: 17.NOV.2017 20:23:59

30MHz~1GHz

1GHz~9GHz