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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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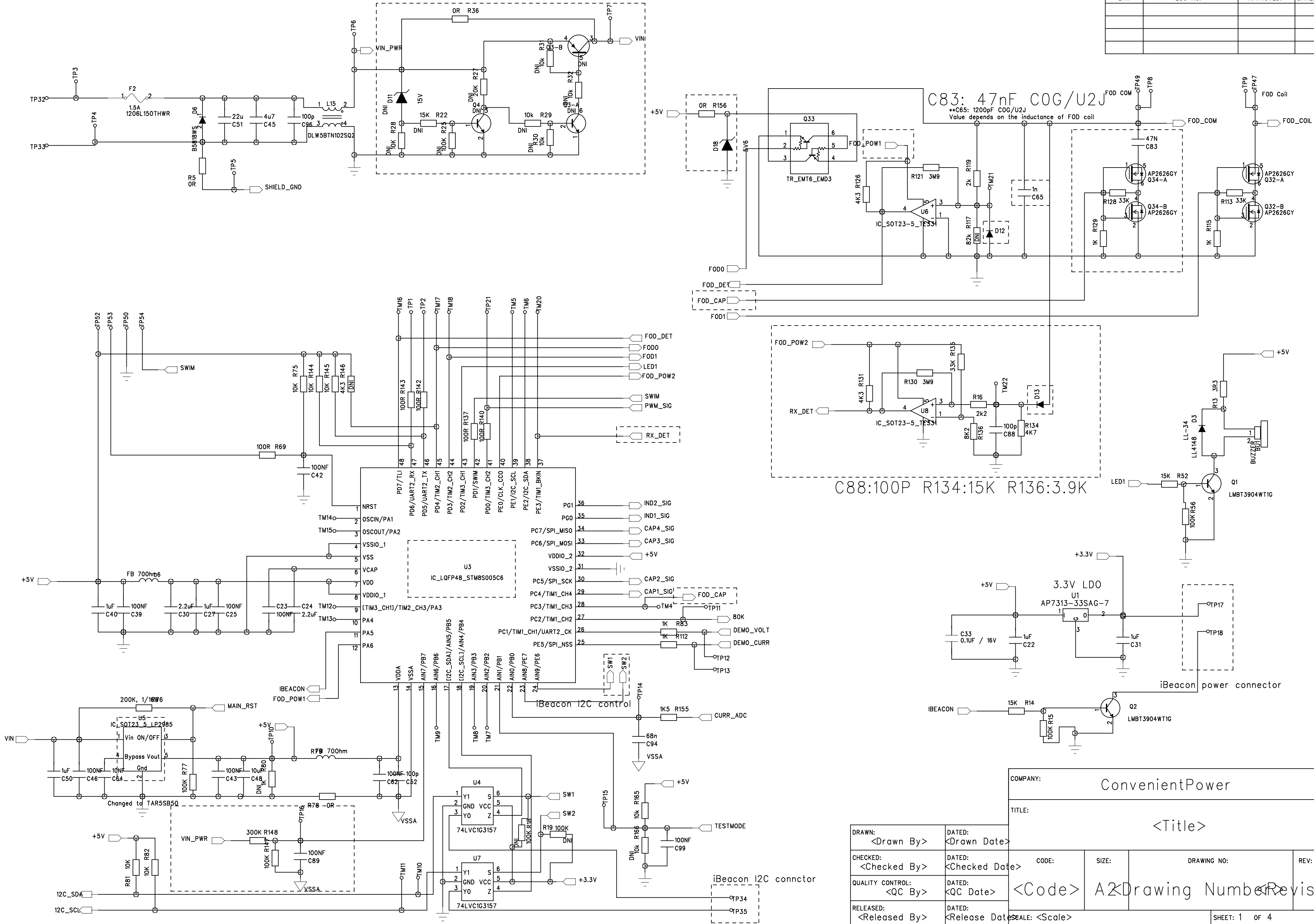
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DRAWN:	<Drawn By>	DATED:	<Drawn Date>
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

COMPANY: ConvenientPower			
TITLE: <Title>			
CODE: <Code>	SIZE: A2	DRAWING NO: Drawing Number	REV: Revision
SHEET: 1 OF 4			

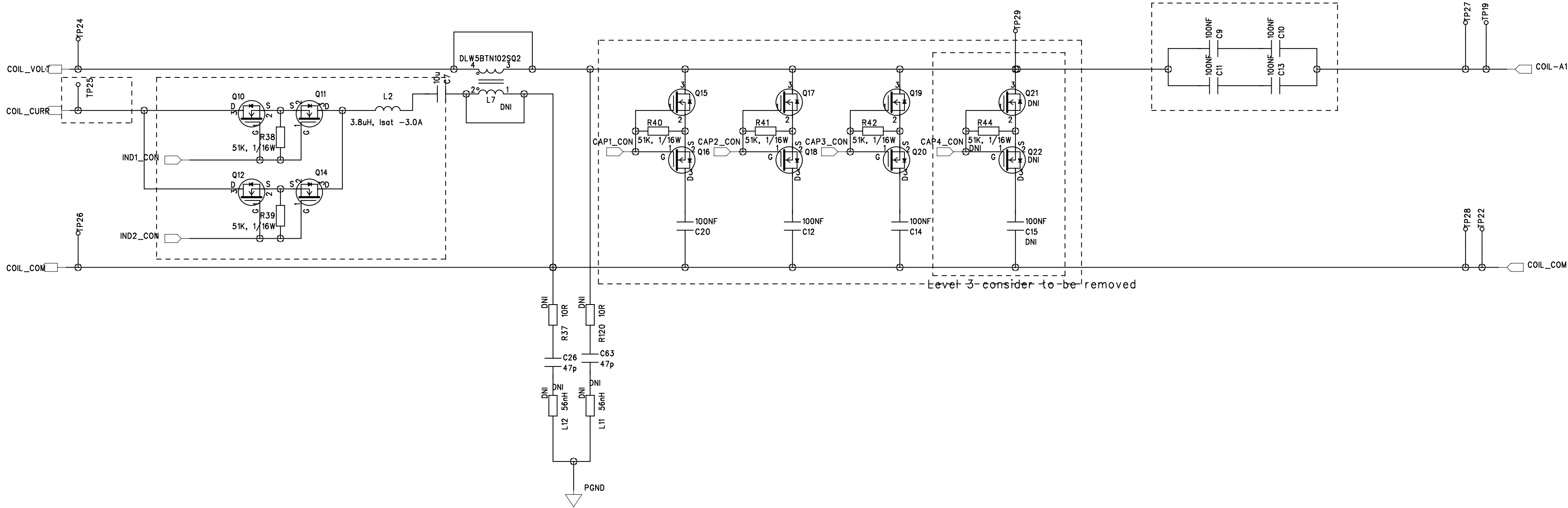
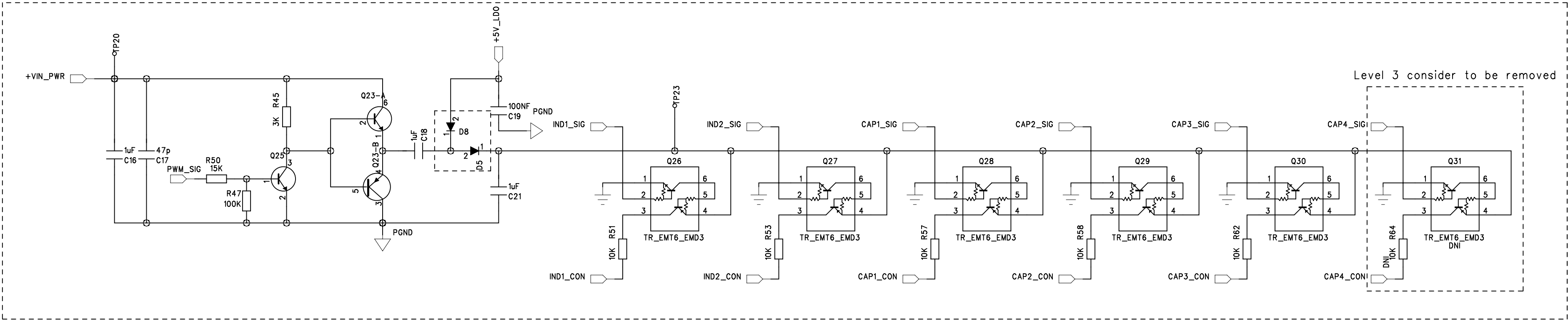
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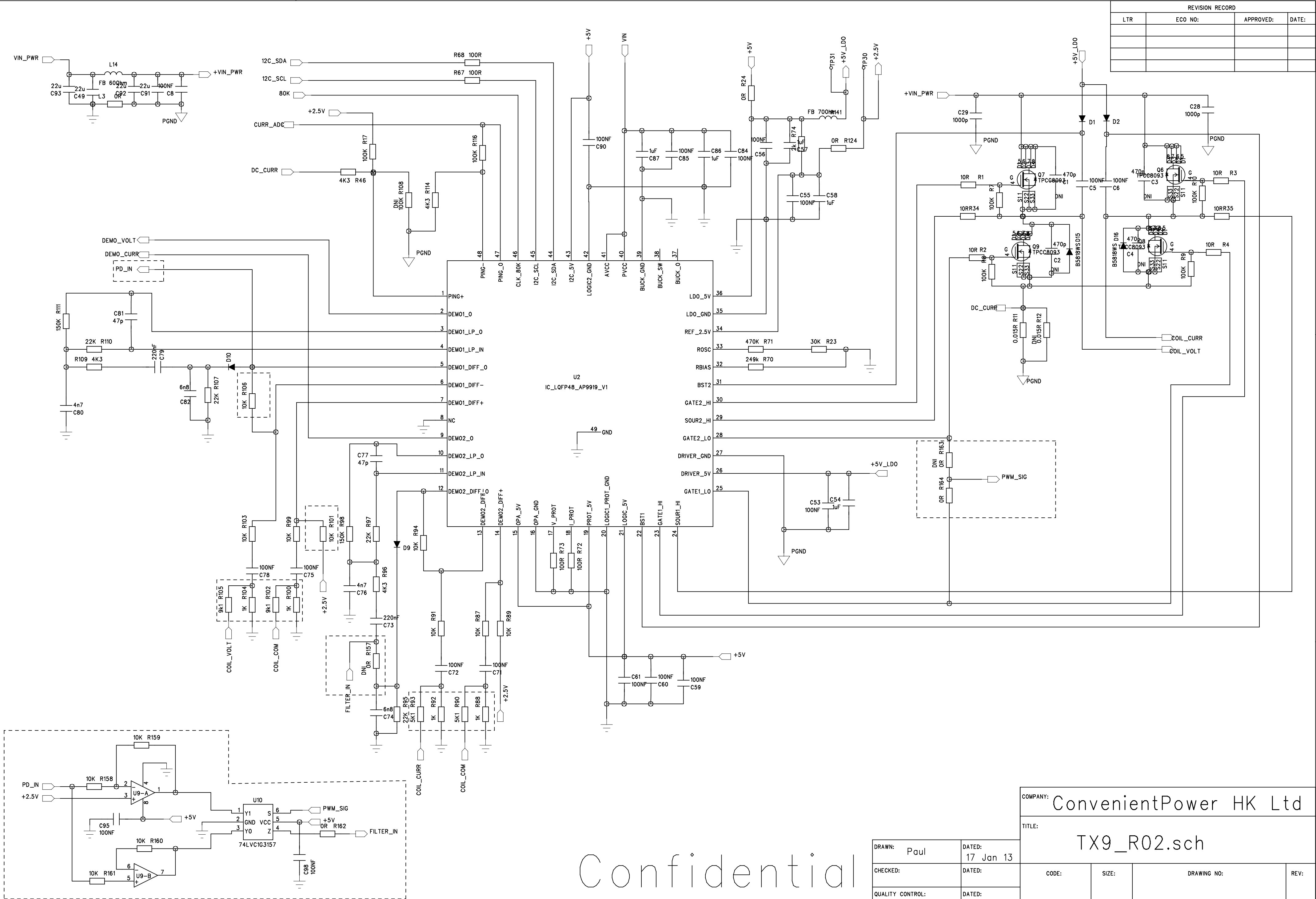
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Confidential

COMPANY: ConvenientPower HK Ltd			
TITLE: TX9_R02.sch			
DRAWN: Paul	DATED: 17 Jan 13	CODE:	SIZE:
CHECKED:	DATED:	DRAWING NO:	REV:
QUALITY CONTROL:	DATED:		
RELEASED:	DATED:		
SCALE:			SHEET: 2 OF 4

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Confidential

COMPANY: ConvenientPower HK Ltd			
TITLE: TX9_R02.sch			
CODE:	SIZE:	DRAWING NO:	REV:
SCALE:		SHEET: 3 OF 4	

DRAWN: Paul	DATED: 17 Jan 13
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Phase	Description	Version	Date	Owner
Pre-Study	1. initial schematic	V01	17 Jan 14	Paul
R0.2	1. initial schematic for TX9 with adaptive control	V02	26 May 14	Paul
R0.3	1. Remove C101 / 10uF 2. Change U3 pin 38 to U3 pin 28 3. Change D5, D8, D12, D13 to Sohottky BAT54XV2T1G 4. Change R102, R105 to 9.1k and R90, R93 to 5.1k 5. Change R88, R92, R100, R104 to 1k 6. Change R89, R94 to 10k 7. R163 to DNI and remove DNI from R164 8. Change C65 to 4.7nF ~ 6.8nF U2J or COG	V03	15 Jul 14	Paul
	1. interchange Pin43 and Pin12 of MCU 2. Changed R136 to 2k2 3. Changed R134 to 4k7 4. Changed C88 to 1000p 5. Changed C65 to 5600p 6. Added R16			
	1. Added TP35/TP36 for iBeacon I2C connecting 2. Changed C83 to 47nF 3. Changed C88 to 100p 4. Changed R134 to15K		30 Jul 15	
	1. exchange mcu pin for sw1 and sw2			

Confidential

COMPANY:

ConvenientPower HK Ltd

TITLE:

TX9_R02.sch

DRAWN: Paul

DATED: 17 Jan 14

CHECKED:

DATED:

QUALITY CONTROL:

DATED:

RELEASED:

DATED:

CODE:

SIZE:

DRAWING NO:

REV:

SCALE:

SHEET: 4 OF 4