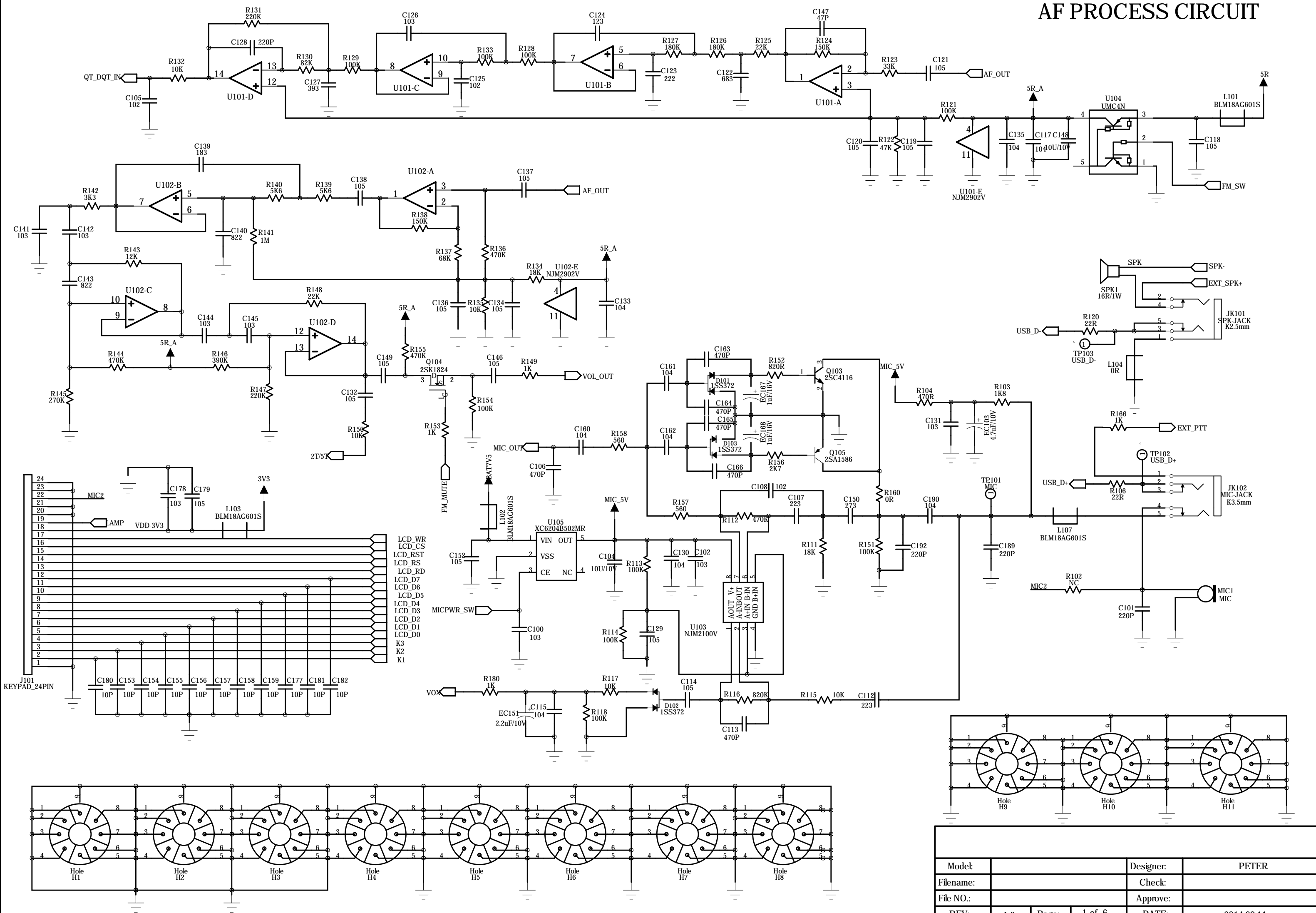
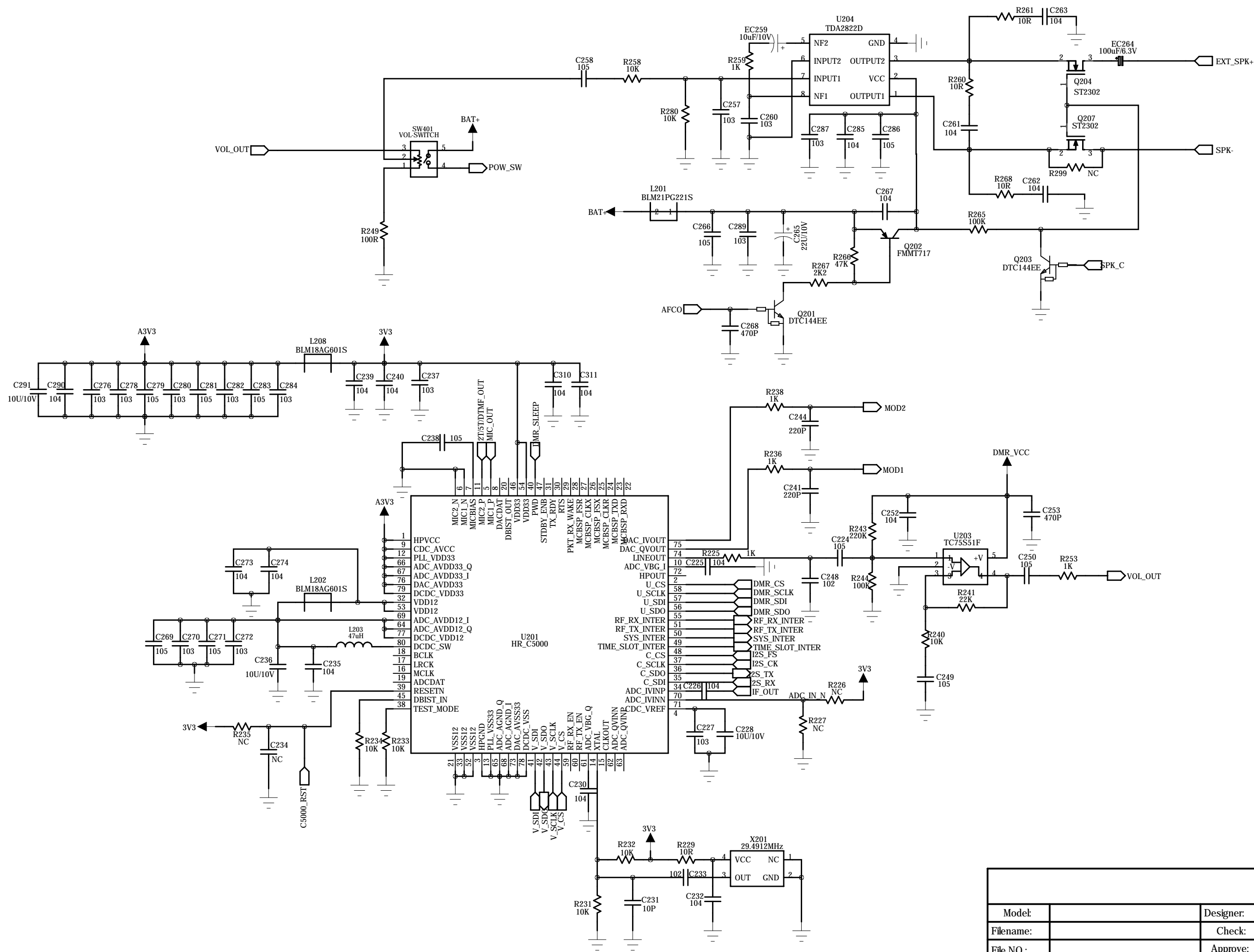
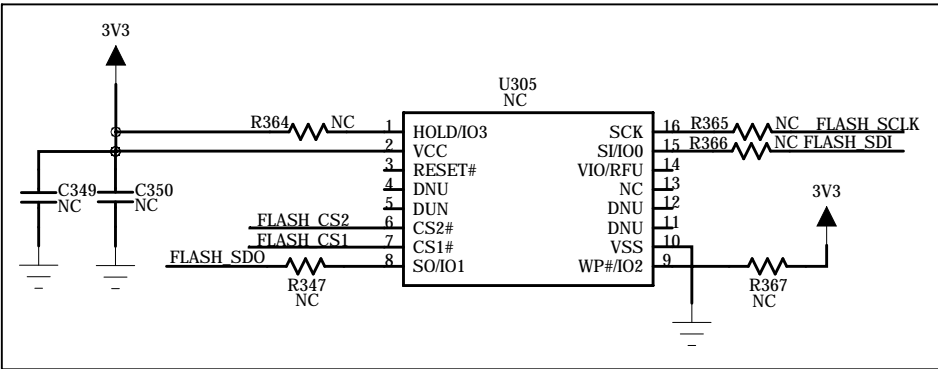
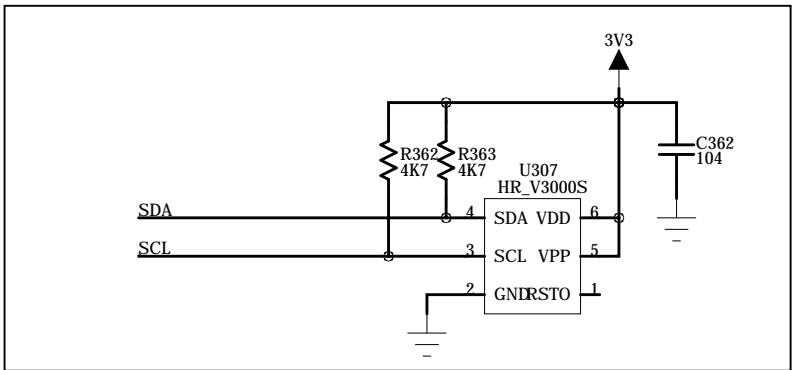
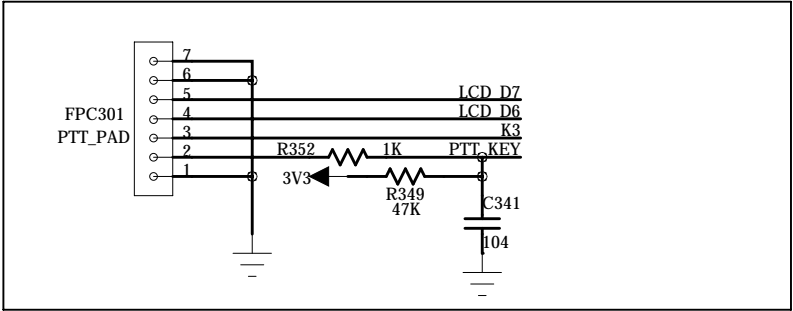
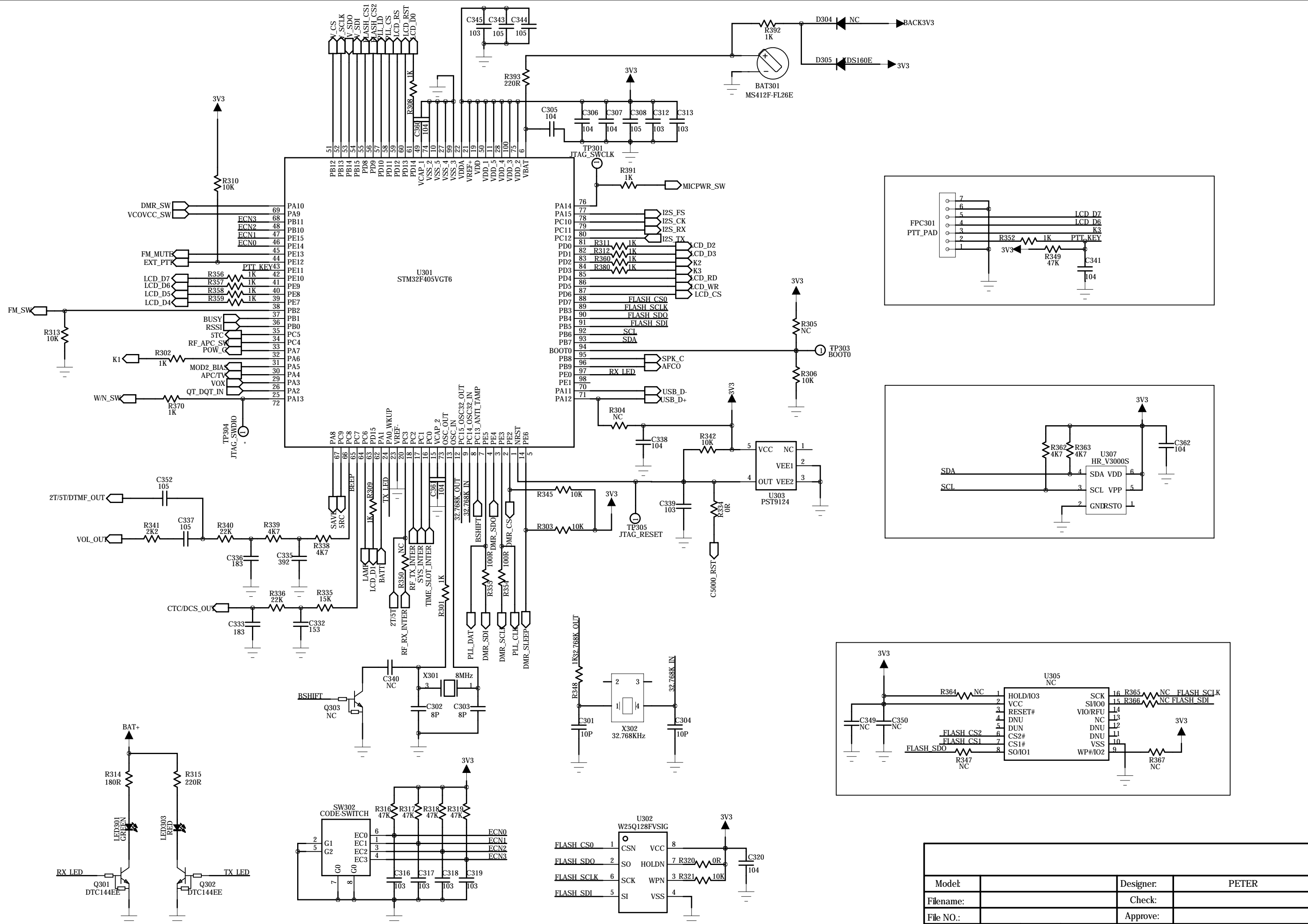


AF PROCESS CIRCUIT



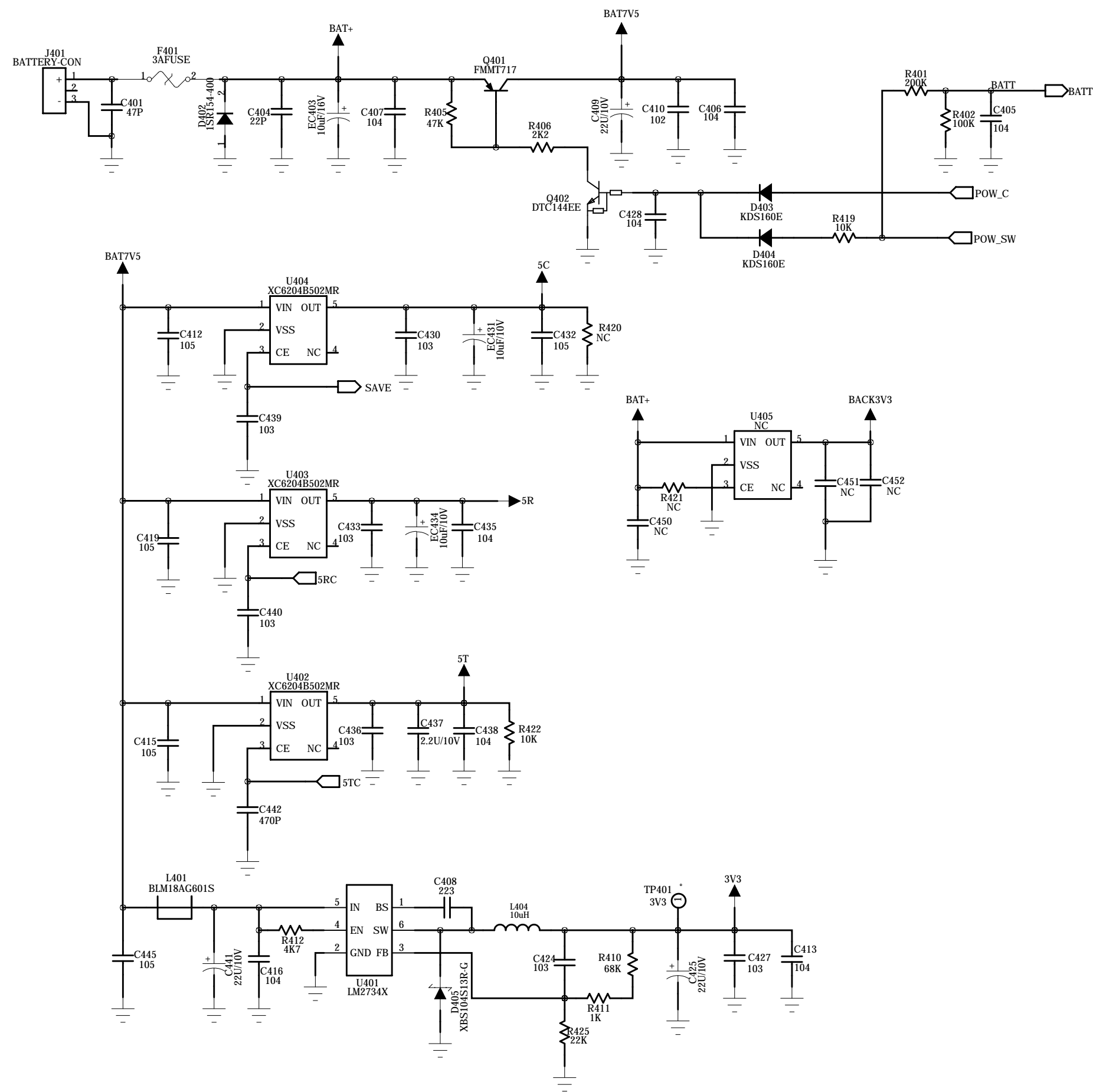


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Filename:				Check:		
File NO.:				Approve:		
REV:	1.0	Page:	2 of 6	DATE:	2014.08.11	

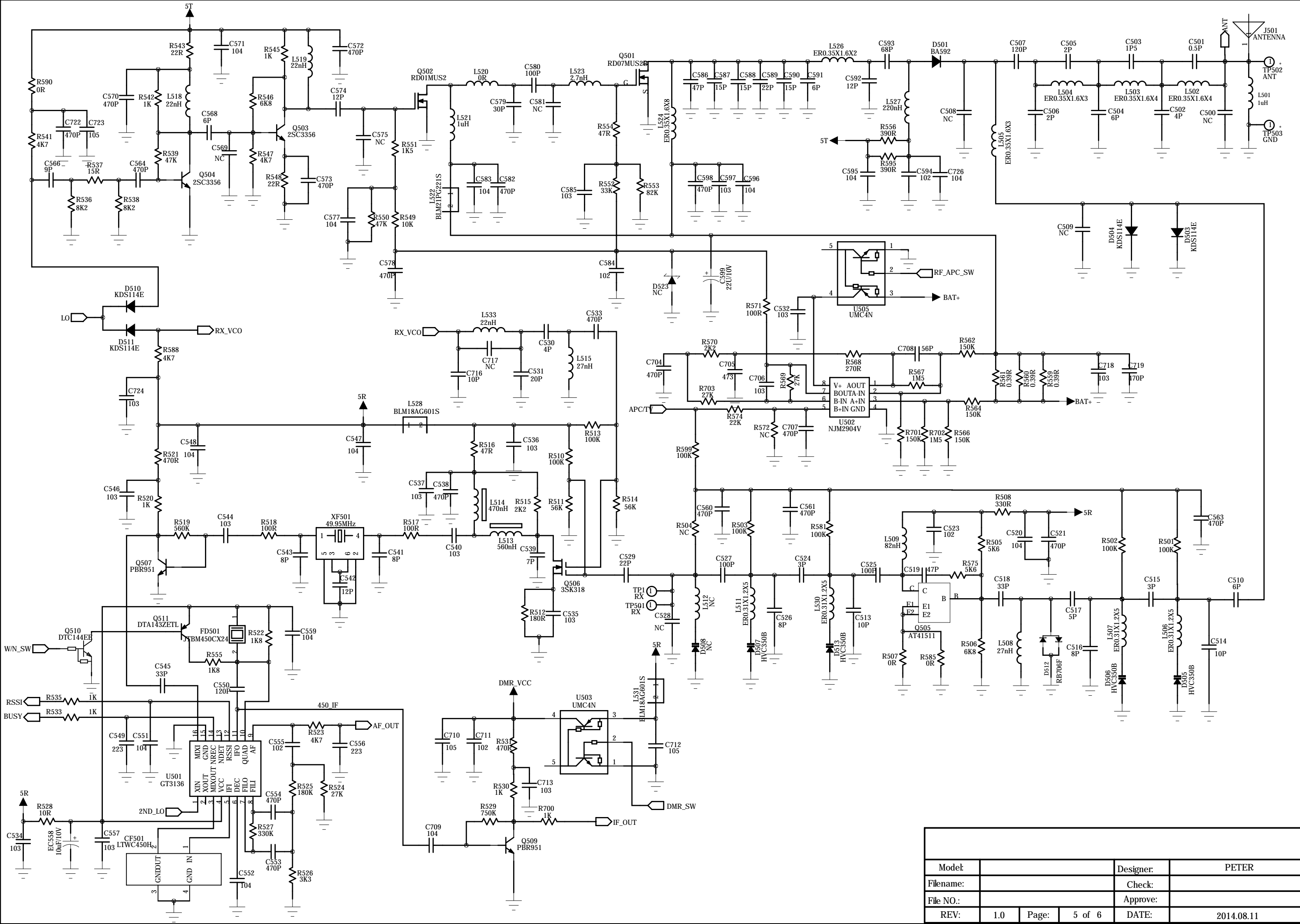


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DATE:	2014.08.11		

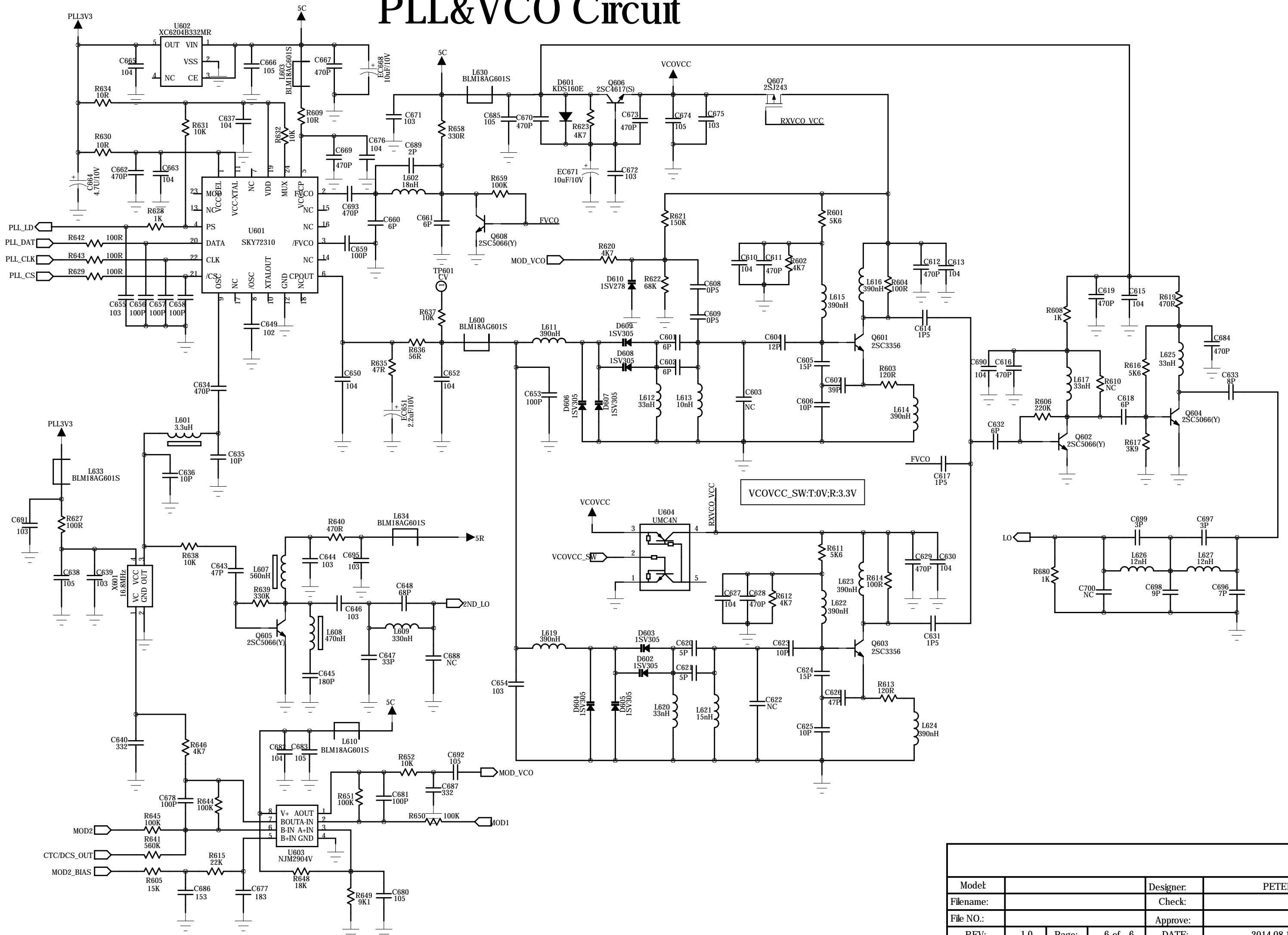
POWER CIRCUIT



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PLL&VCO Circuit



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