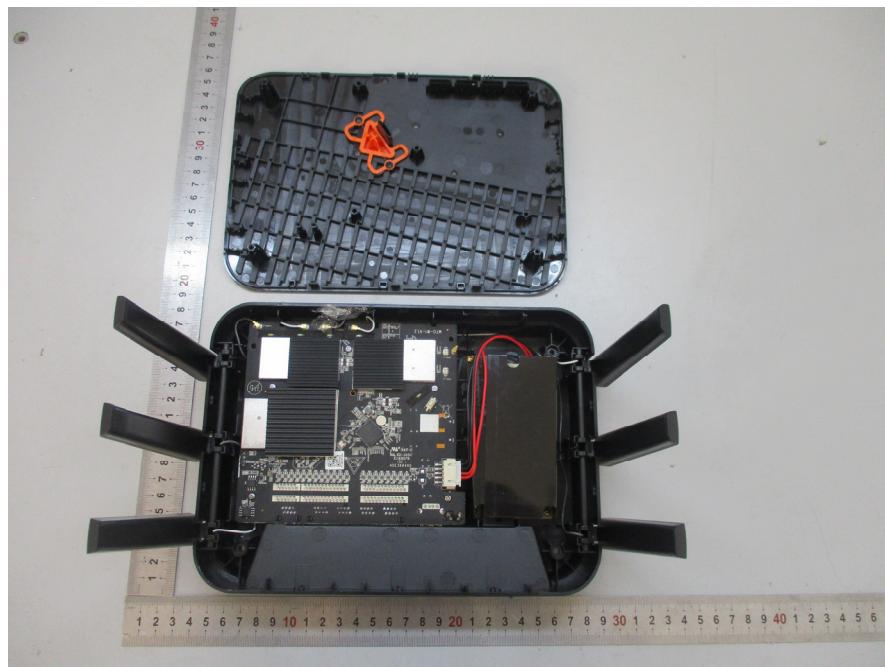
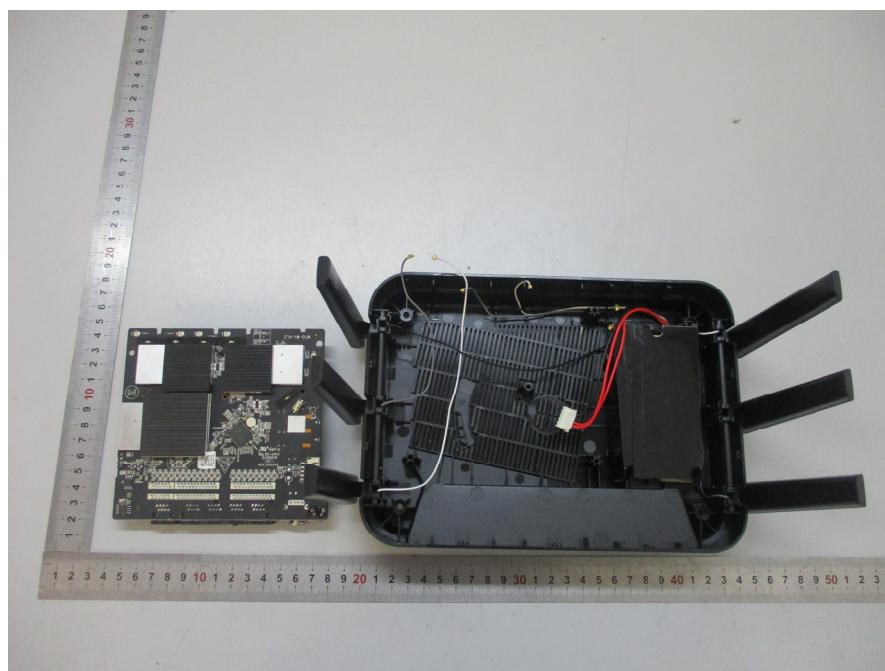


EXHIBIT B - EUT INTERNAL PHOTOGRAPHS

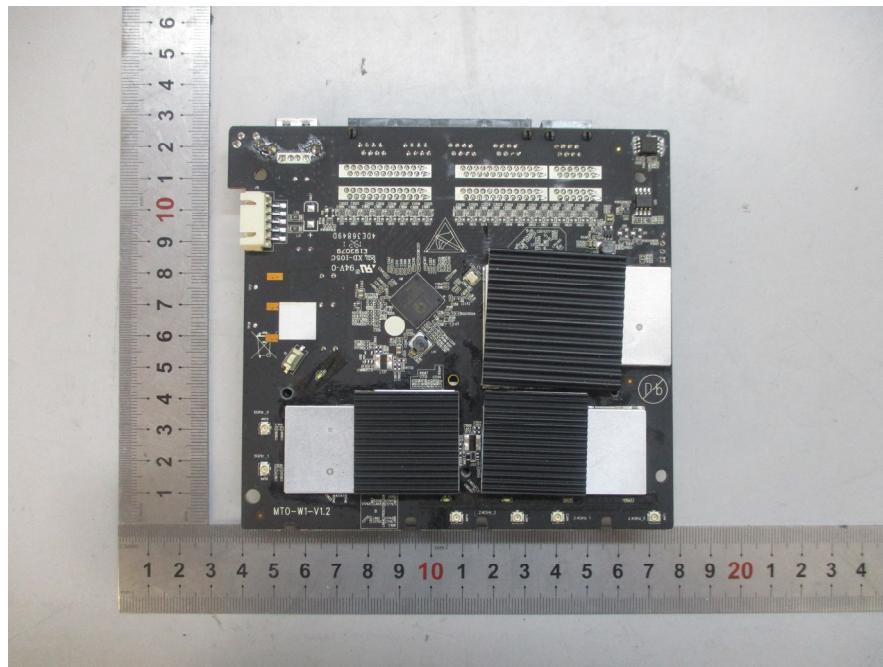
EUT – Cover off View-1



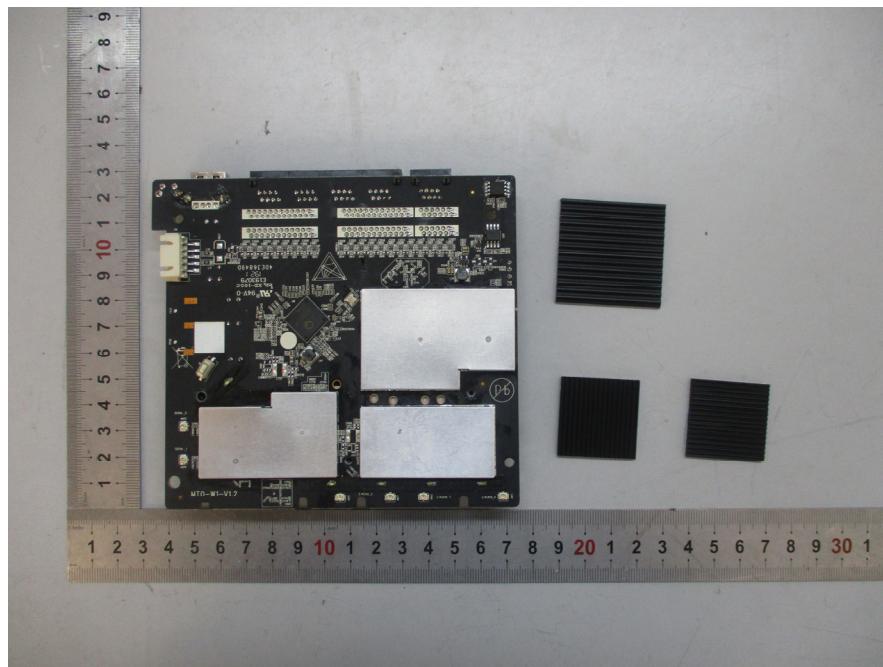
EUT – Cover off View-2



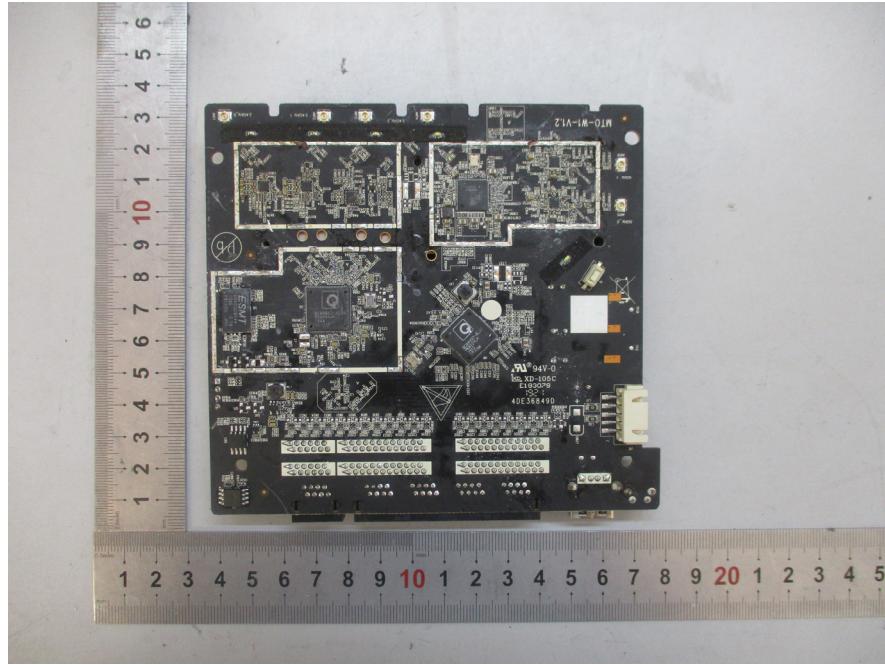
EUT –PCB1 Top View-1



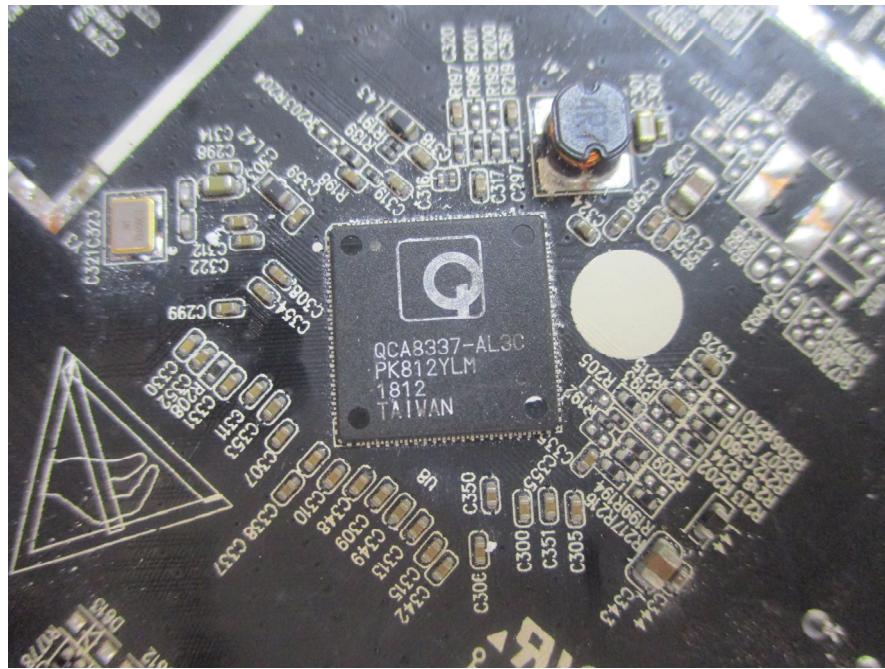
EUT –PCB1 Top Shielding off View



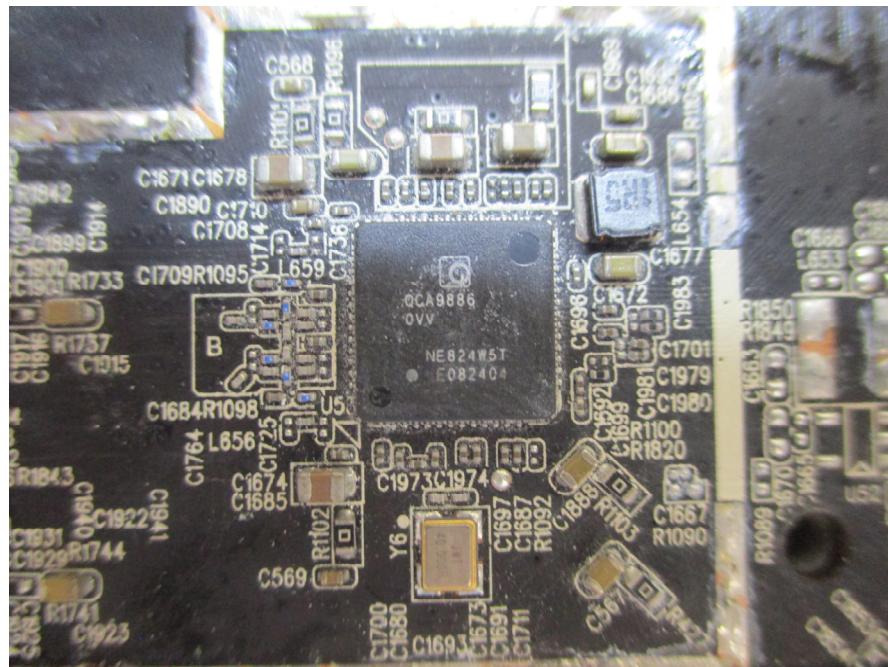
EUT -PCB1 Top View-2



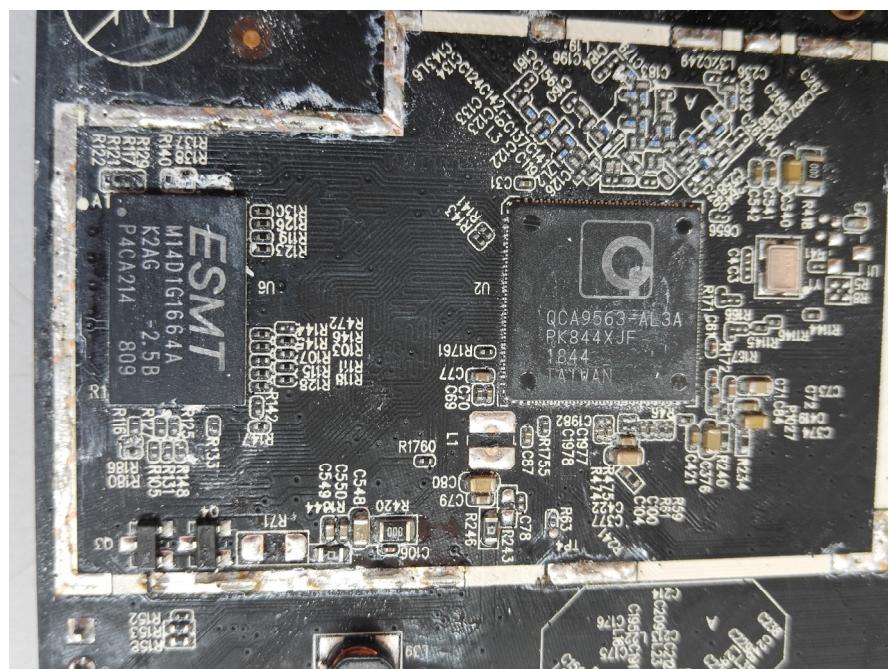
EUT –PCB1 Chip View-1



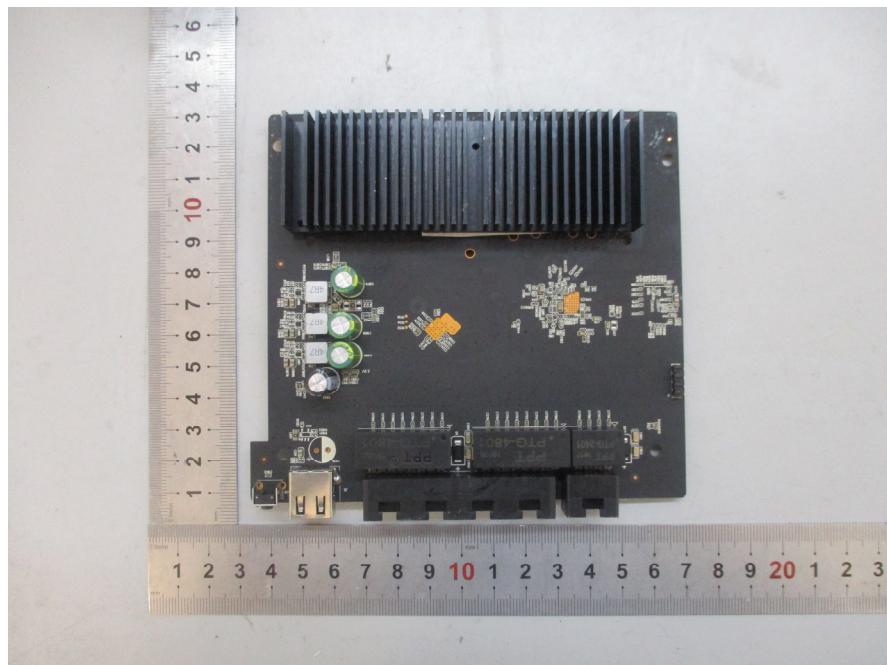
EUT –PCB1 Chip View-2



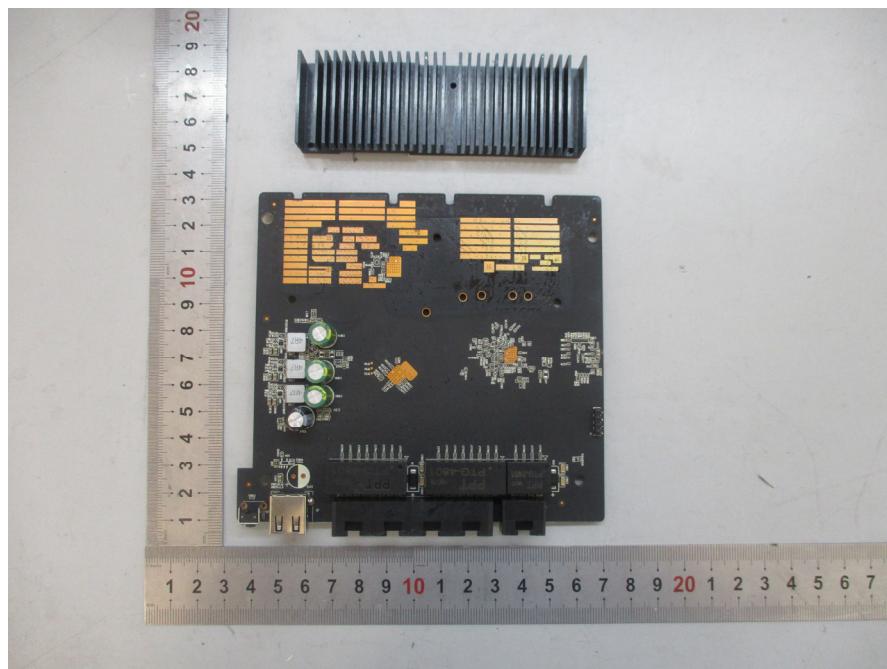
EUT –PCB1 Chip View-3



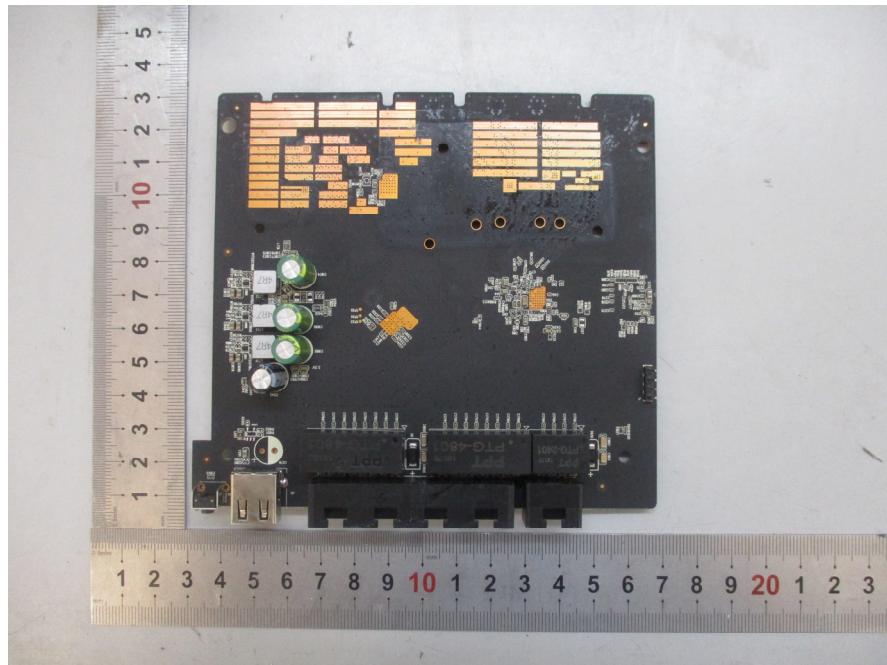
EUT –PCB1 Bottom View-1



EUT –PCB1 Bottom Shielding off View



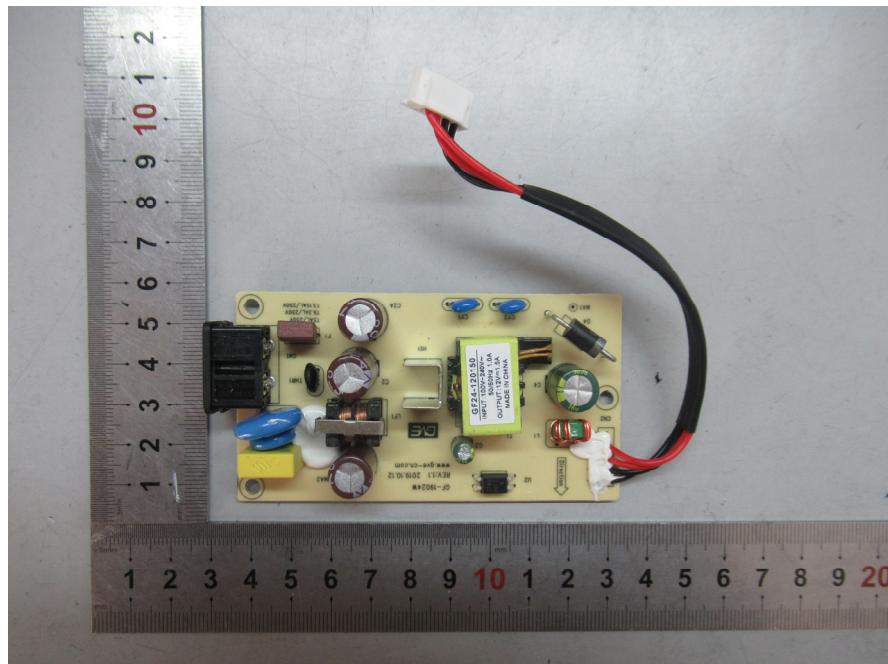
EUT –PCB1 Bottom View-2



EUT – Cover off View-3



EUT – PCB2 Top View



EUT – PCB2 Bottom View

