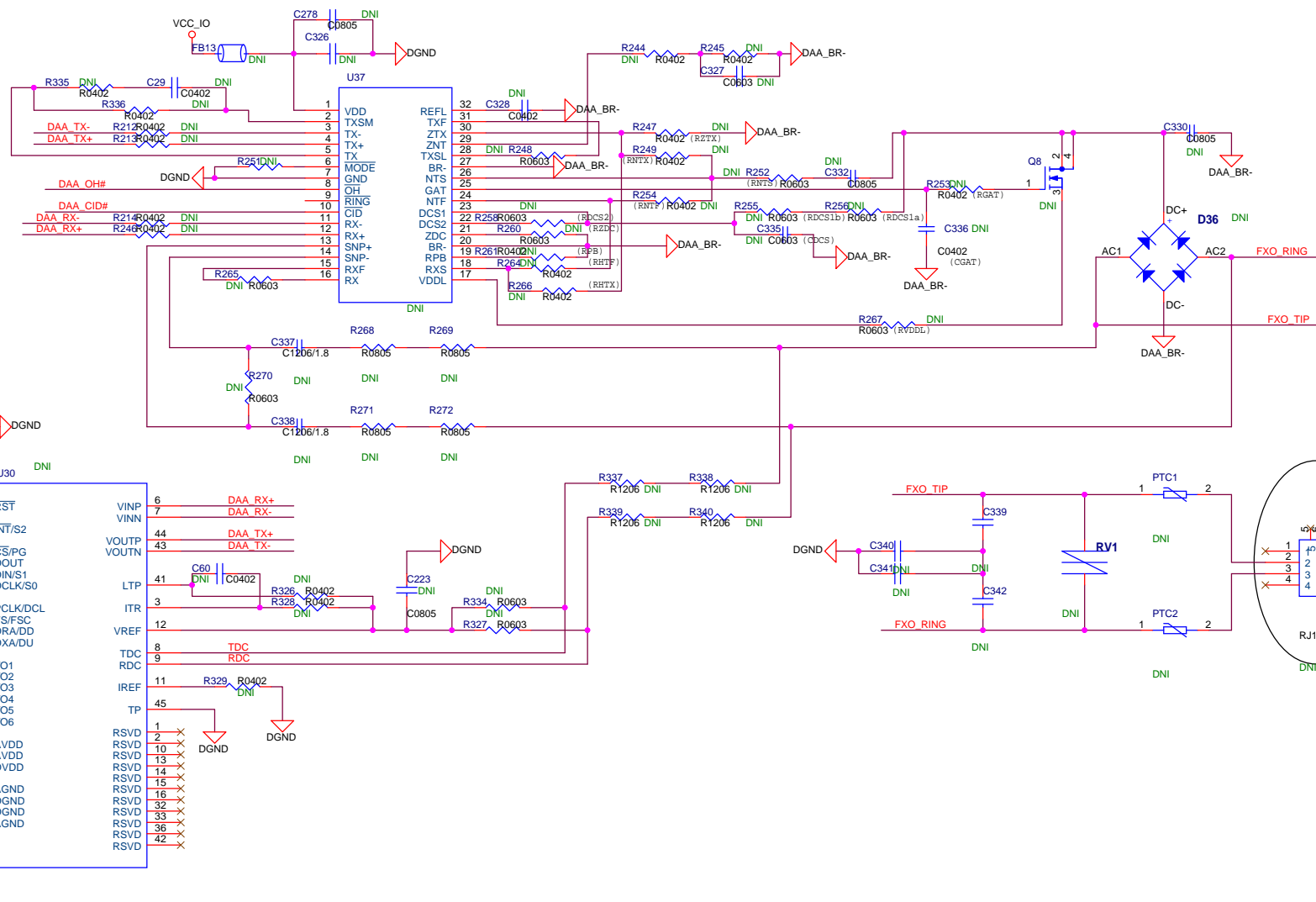
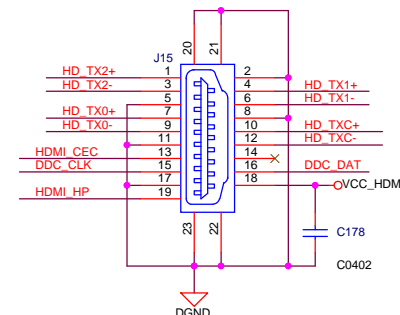
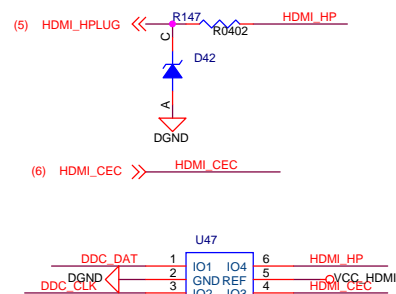
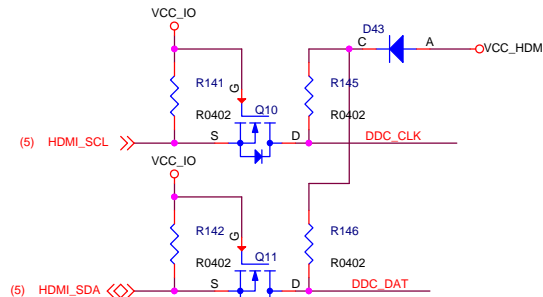
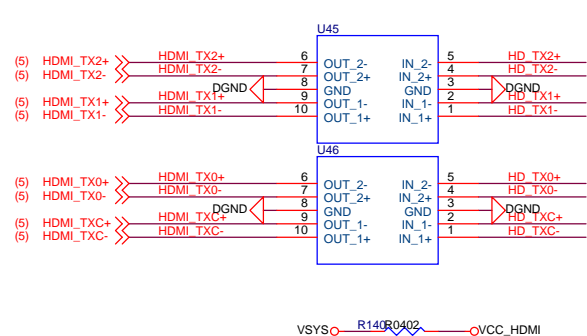
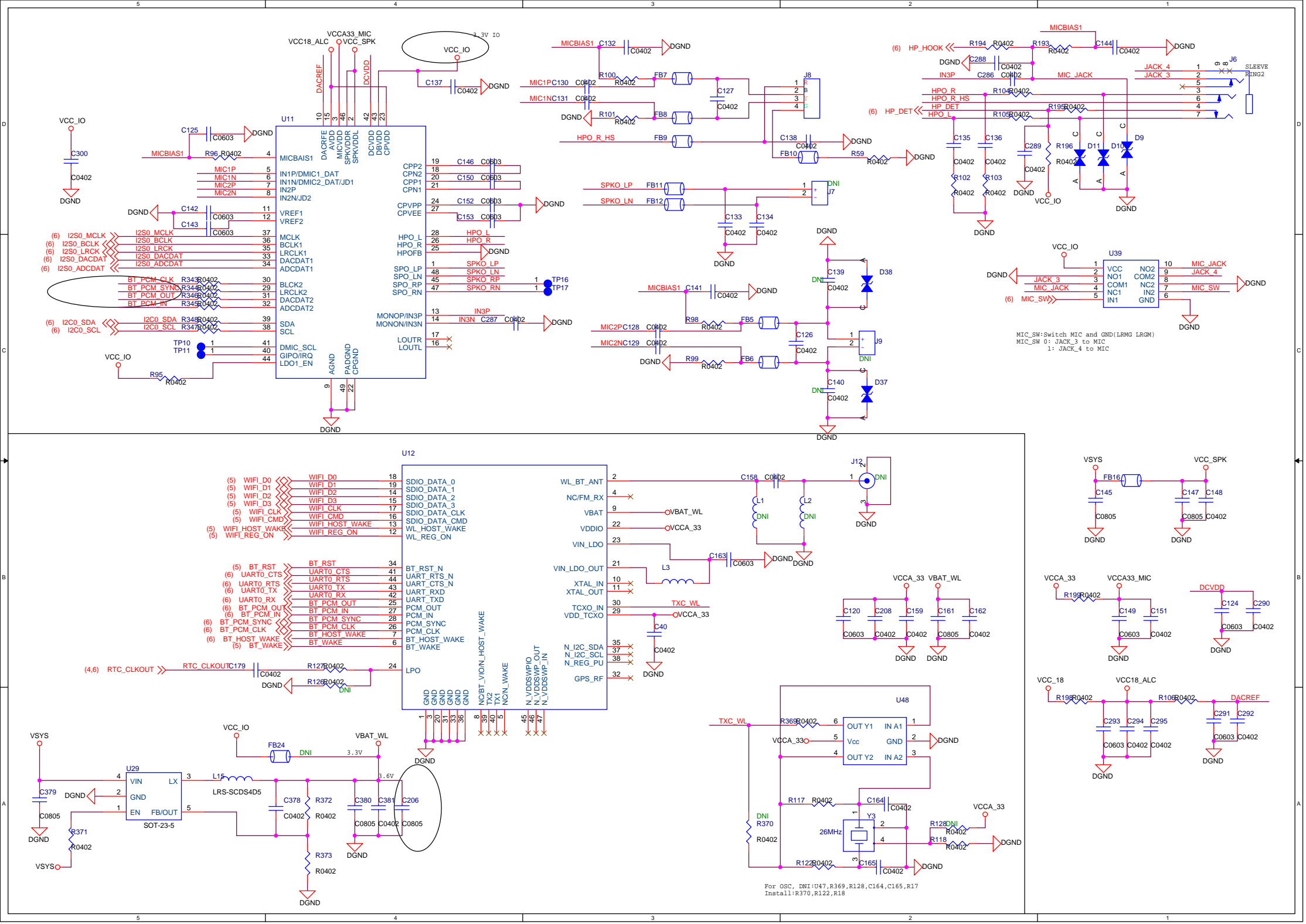
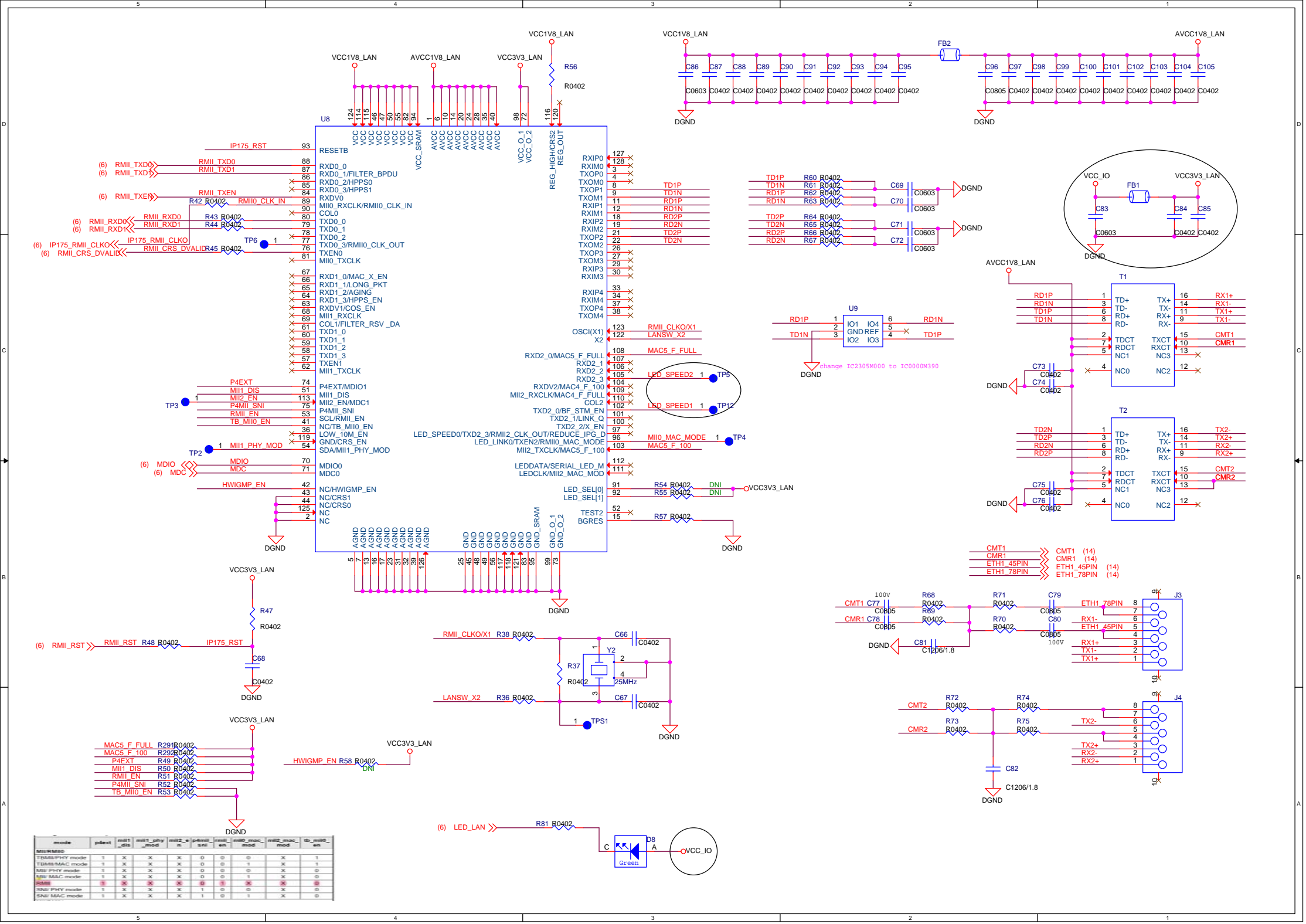
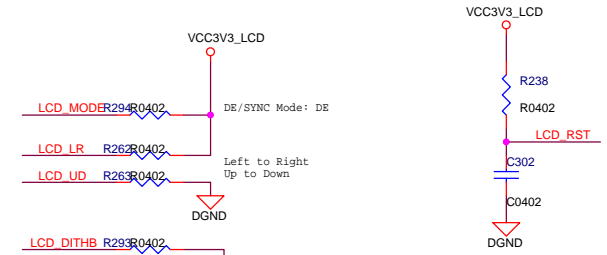
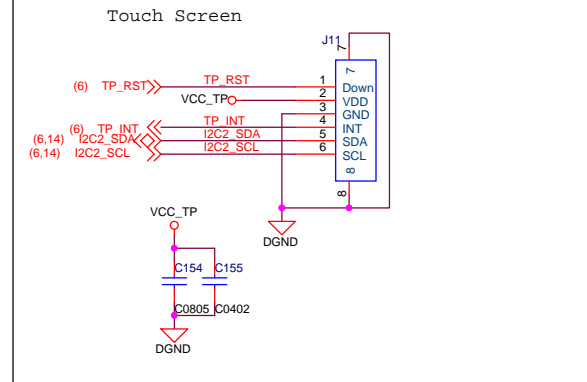
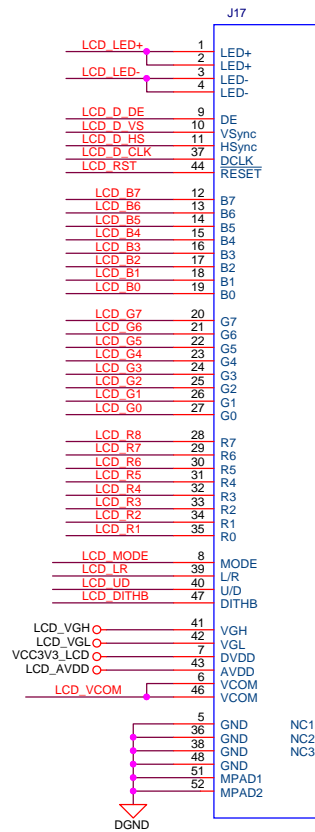
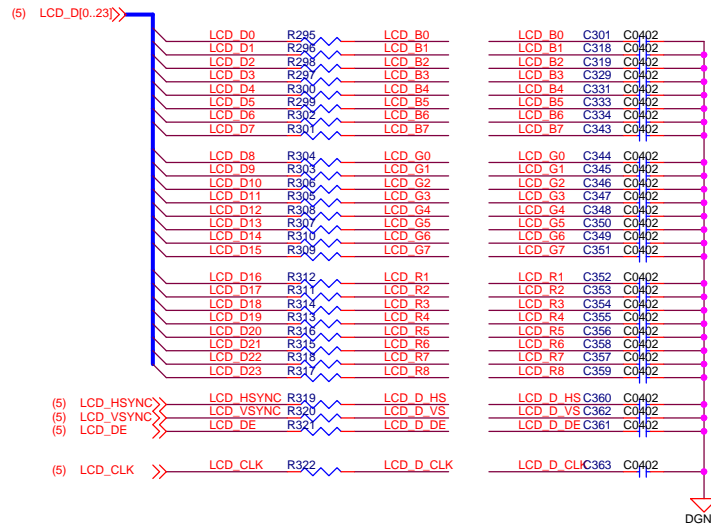


BACKUP BAT.

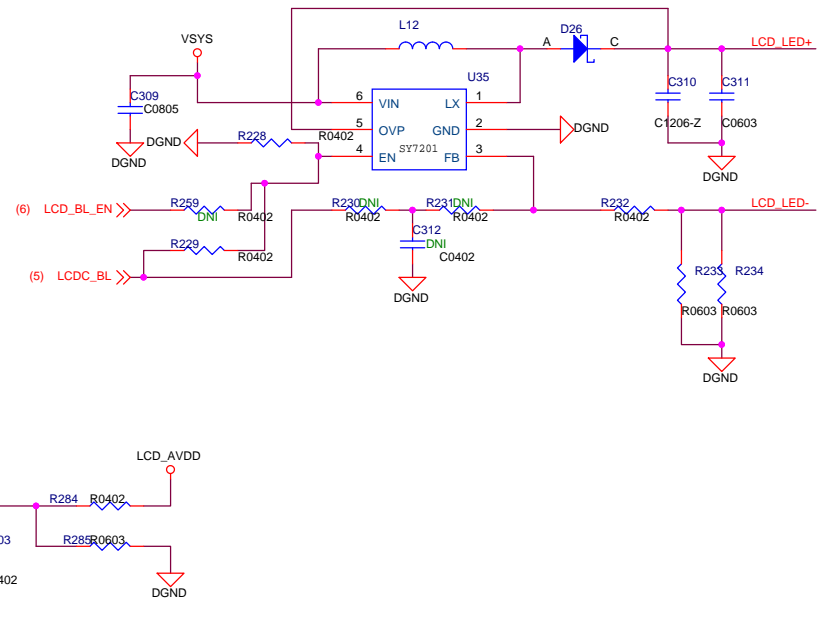
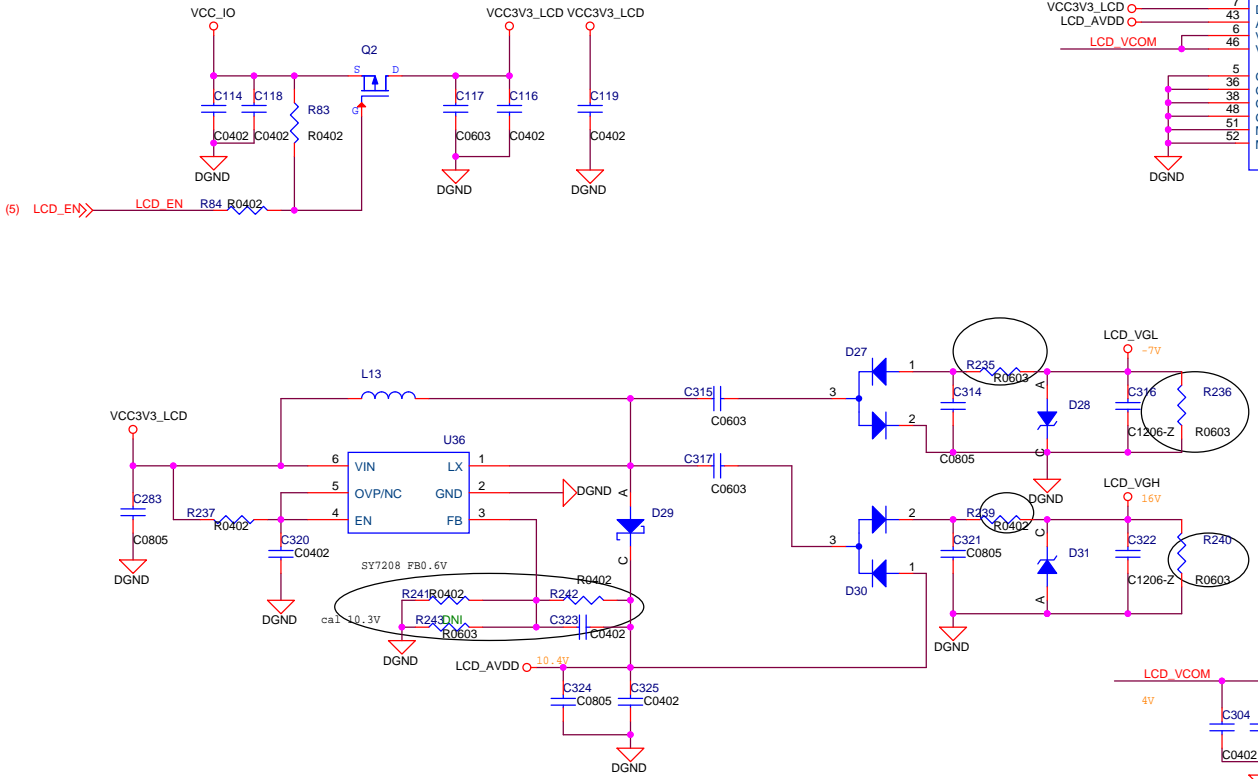






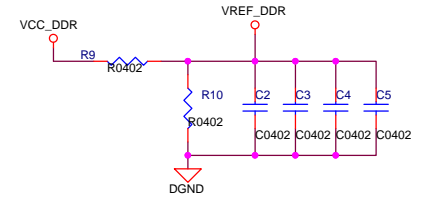
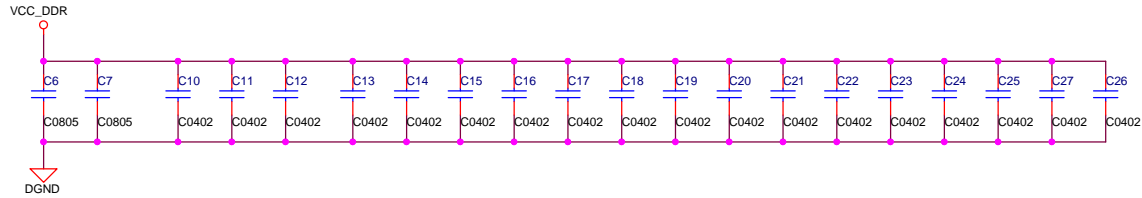
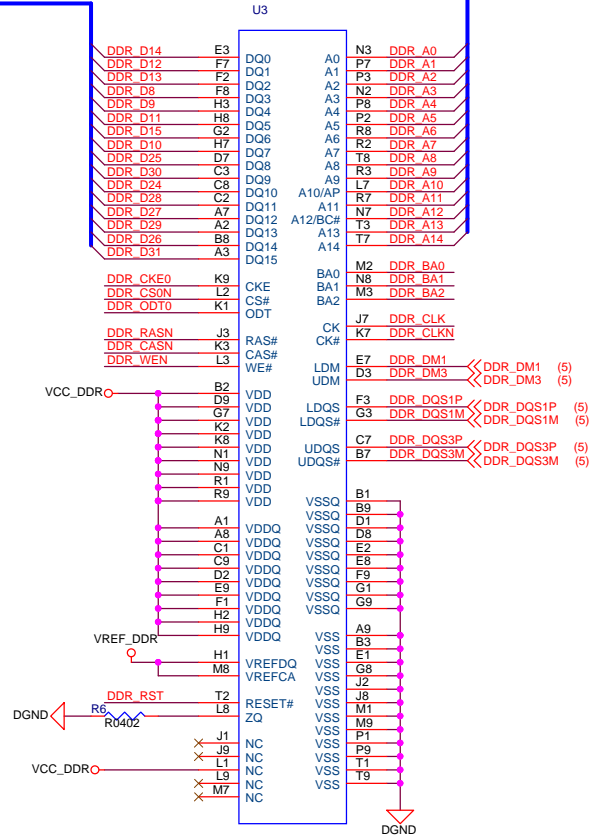
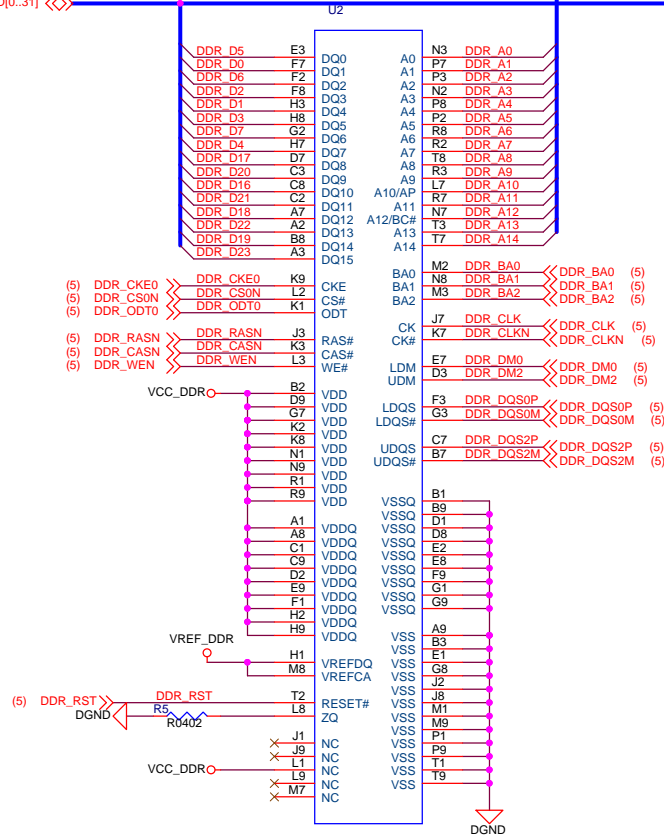


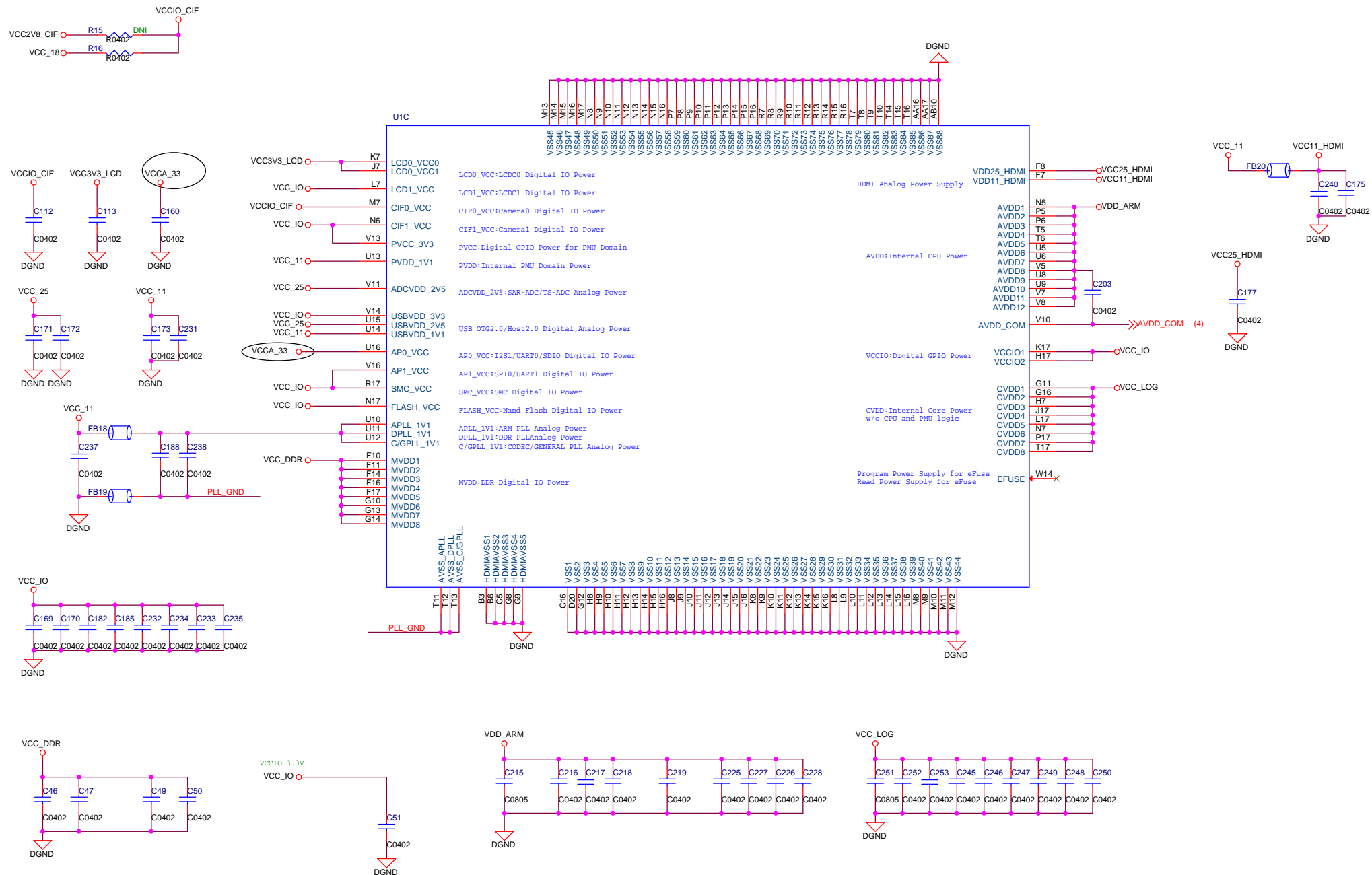
RGB 6 bit,DITHB=1,Disable internal dithering
RGB 8 bit DITHB=0,Enable internal dithering

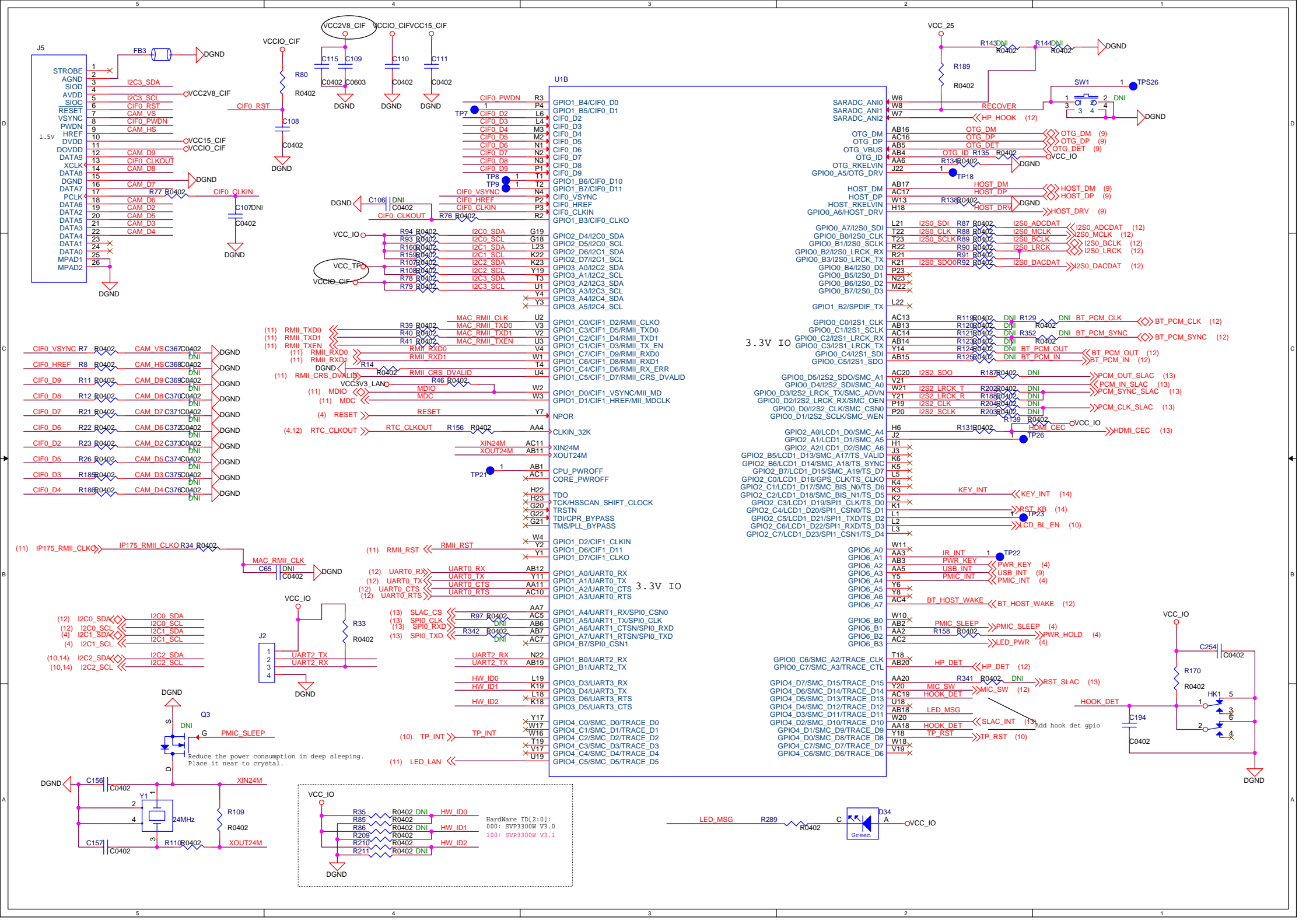


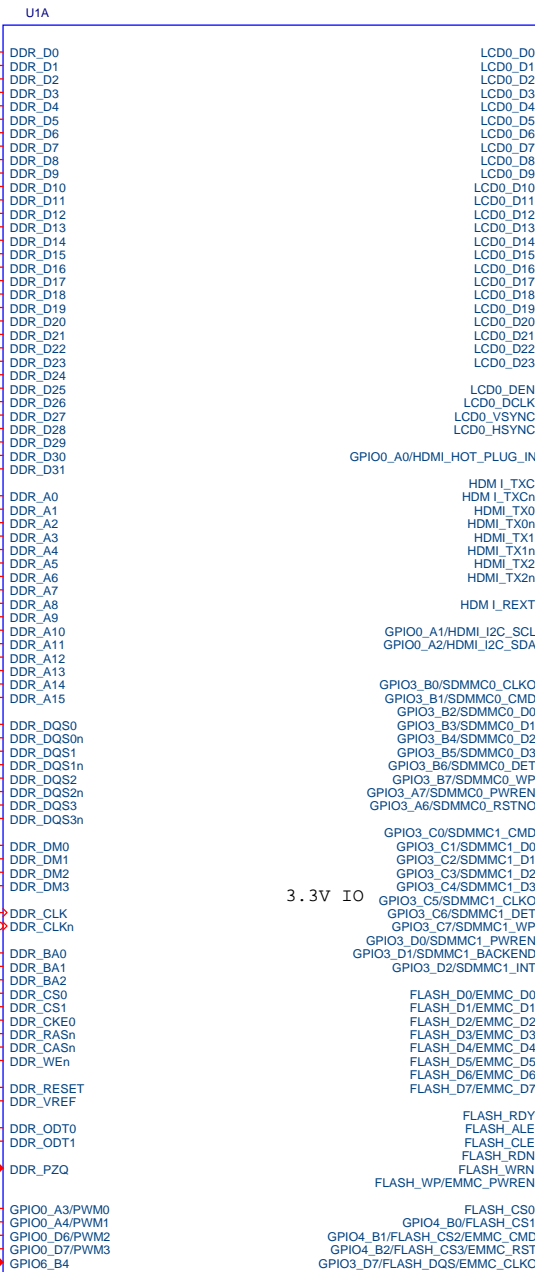
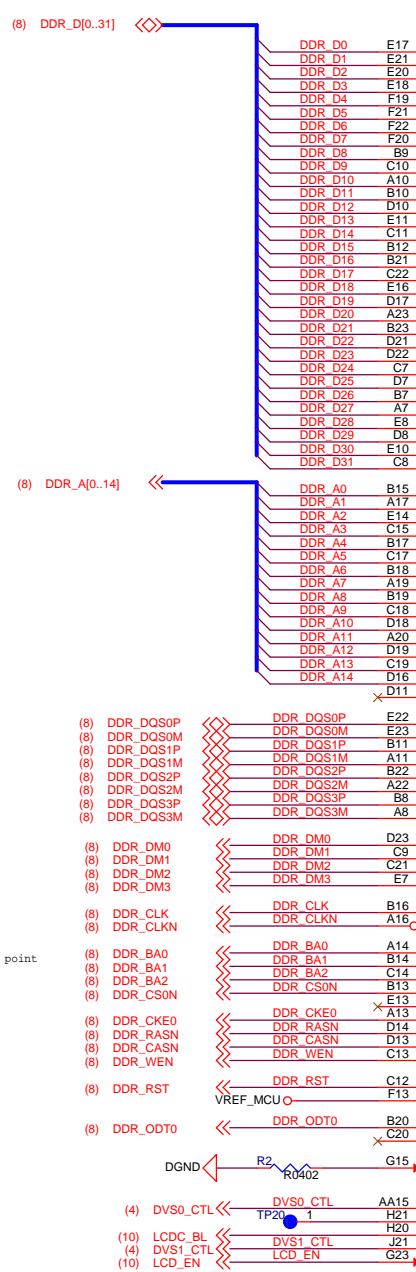
(5) DDR_A[0..14] >>

(5) DDR_D[0..31] <<









LCDC_D0~D7: B0~B7

LCDC_D8~D15: G0~G7

LCDC_D16~D23: R1~R7

R130 P/N should chang to 1k

3.3V IO

PCB1

YY33000311

NAND

