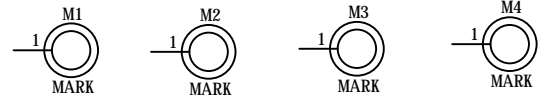
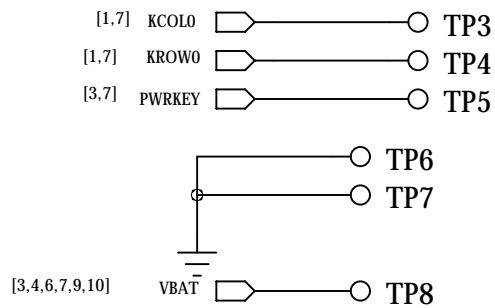
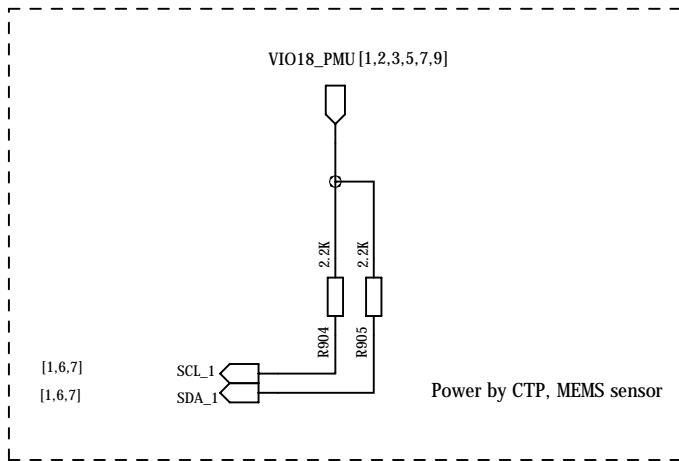


MT6572 support JTAG from below :

1. KP (recommand)
2. MC1
3. CAM

for JTAG pin out from MC1/CAM, refer to HW design notice



Charger

Charger

VBUS

VCDT rating: 1.268V

VCDT [3]

R701 330K 1%

R702 39K 1%

R703 3.3K

CHR_IDO [3]

Q702 SSM3K35MFV

VDRV [3]

R704 0.122K

ISENSE [3]

VBAT 3, 4, 6, 7, 9, 10

AUXADC_REF [3.9]

R705 39K

VBAT1 3, 4, 6, 7, 9, 10

BAT_ON [3]

R706 1K

R707 10K

T701

T702

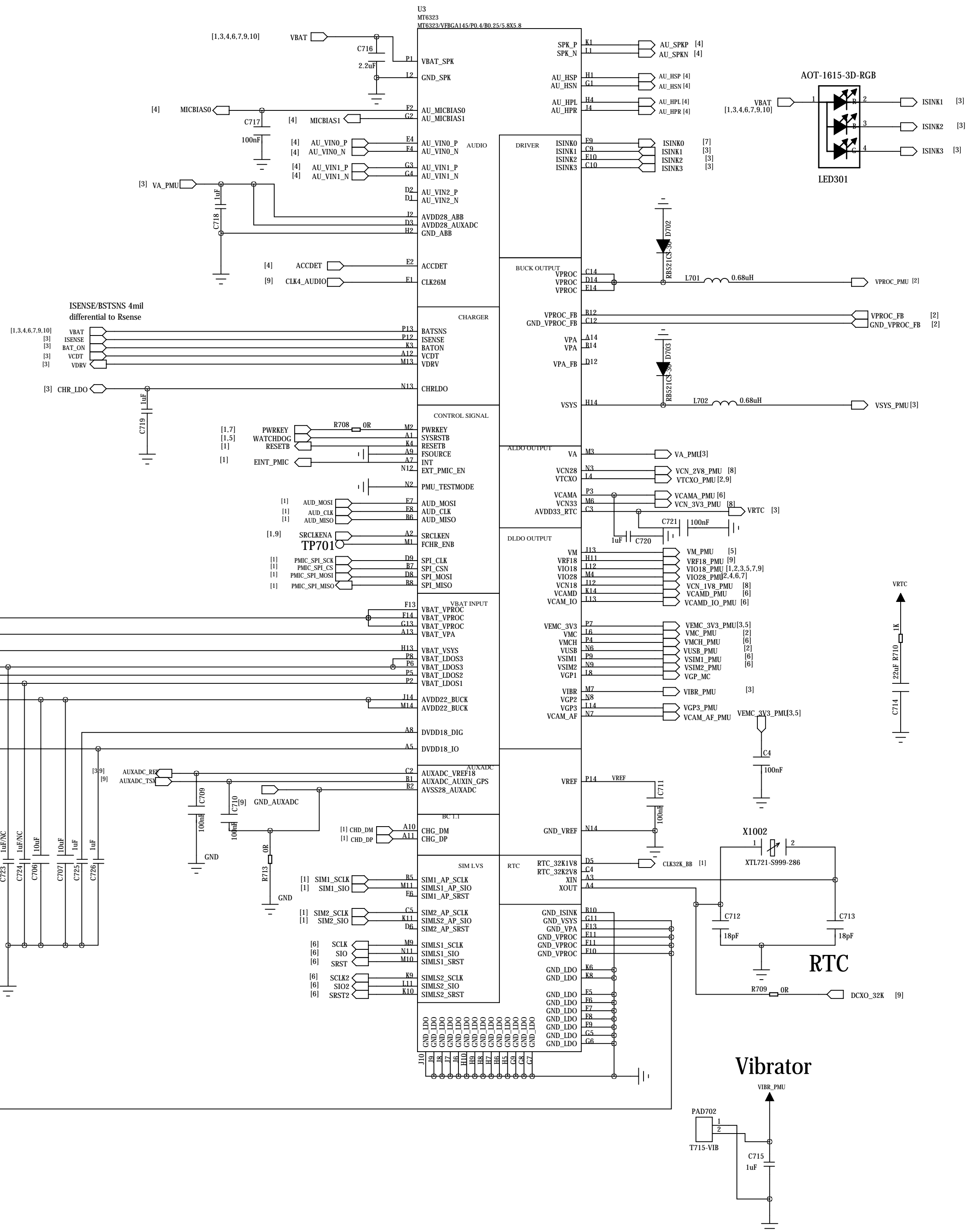
GND

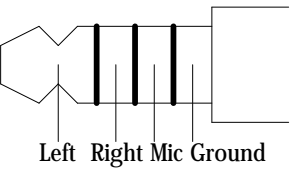
BATTERY CONNECTOR

J302

B25-AB03P231

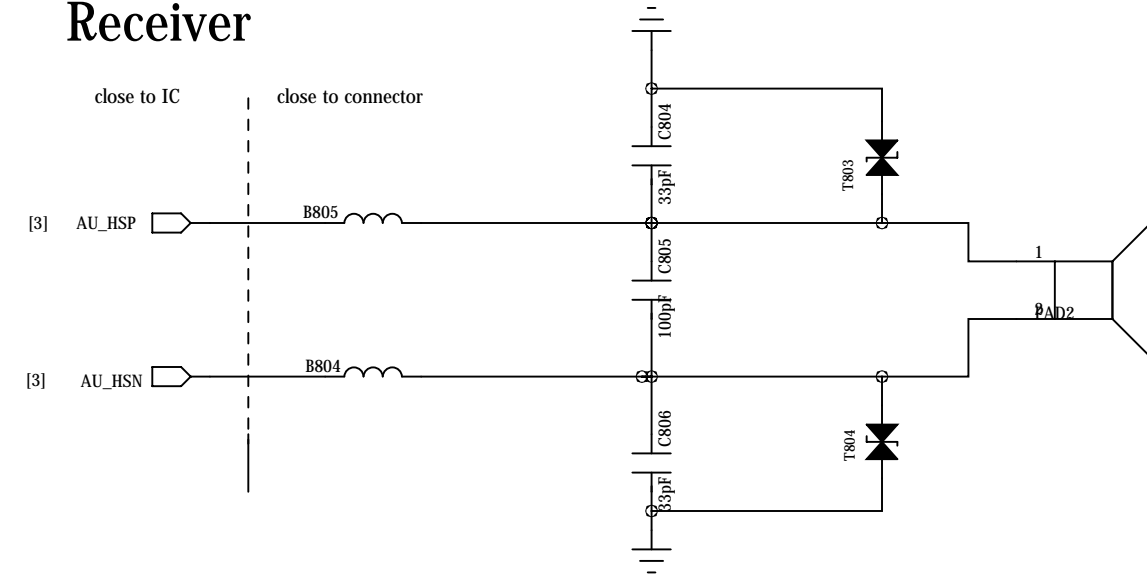
R705 R707 must be close to PMIC AUXADC_REF pin



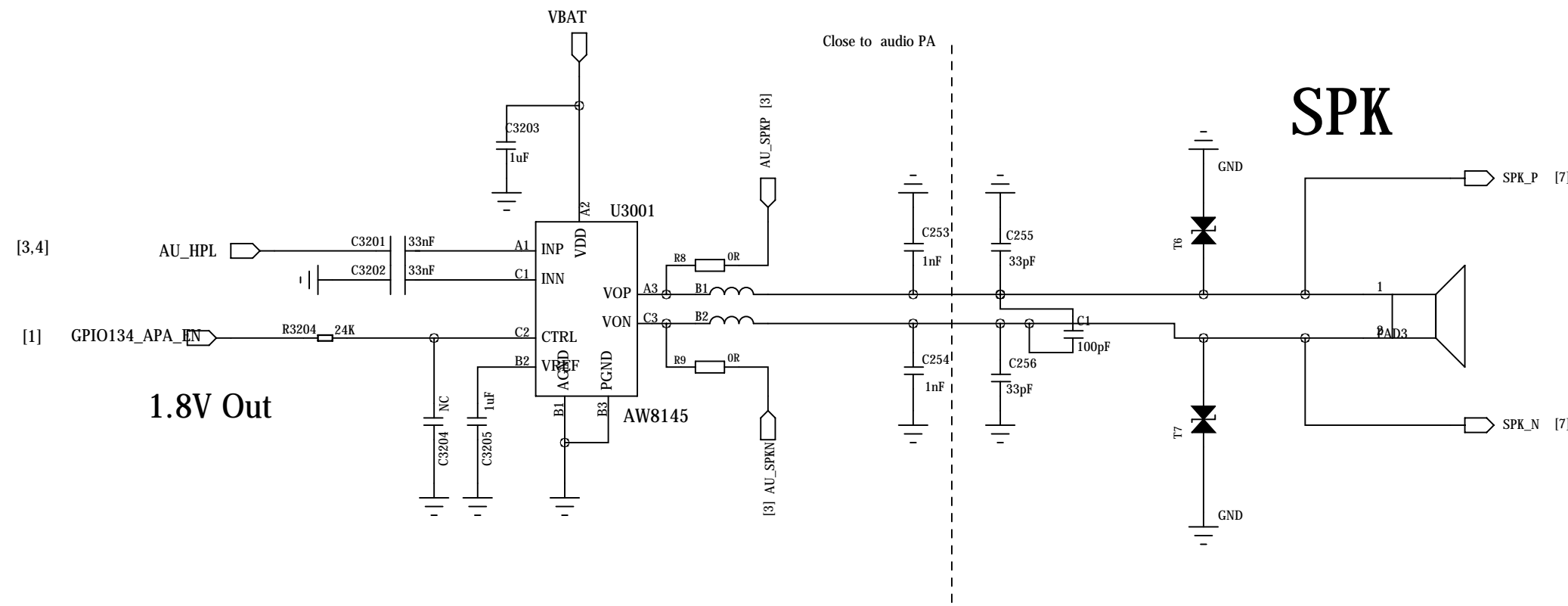
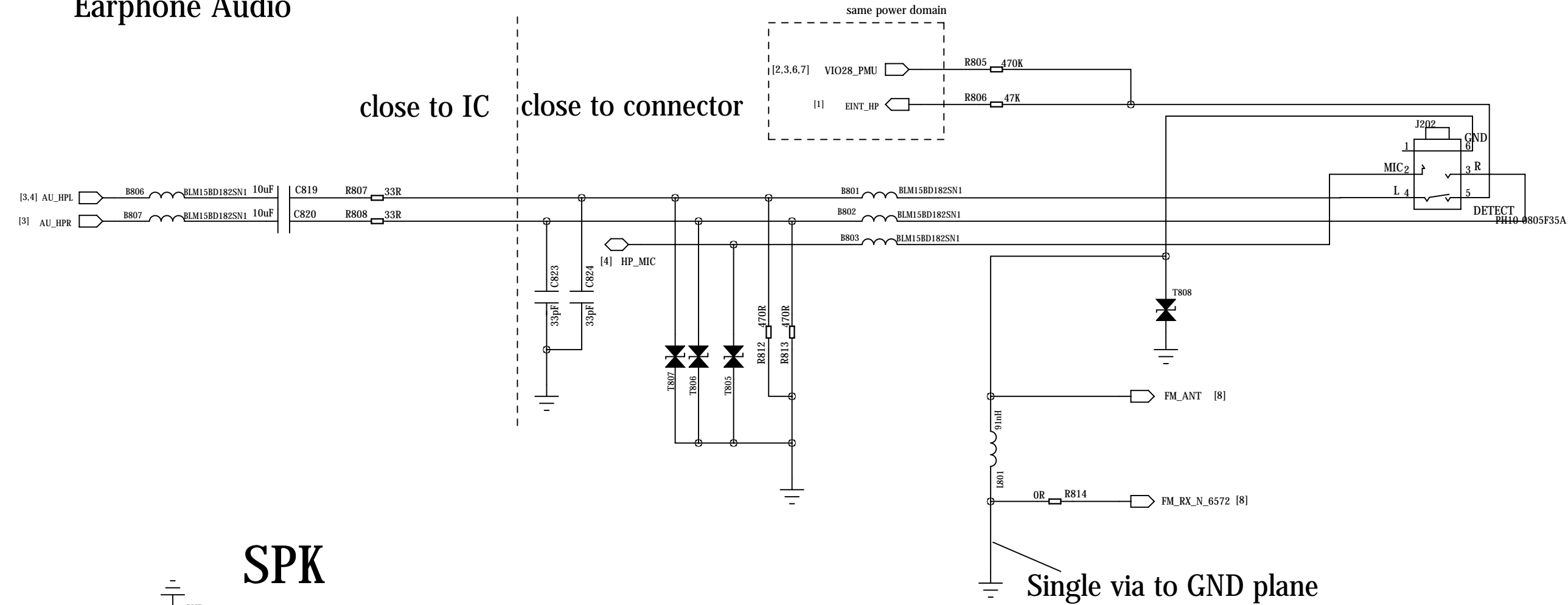


Receiver

Receiver

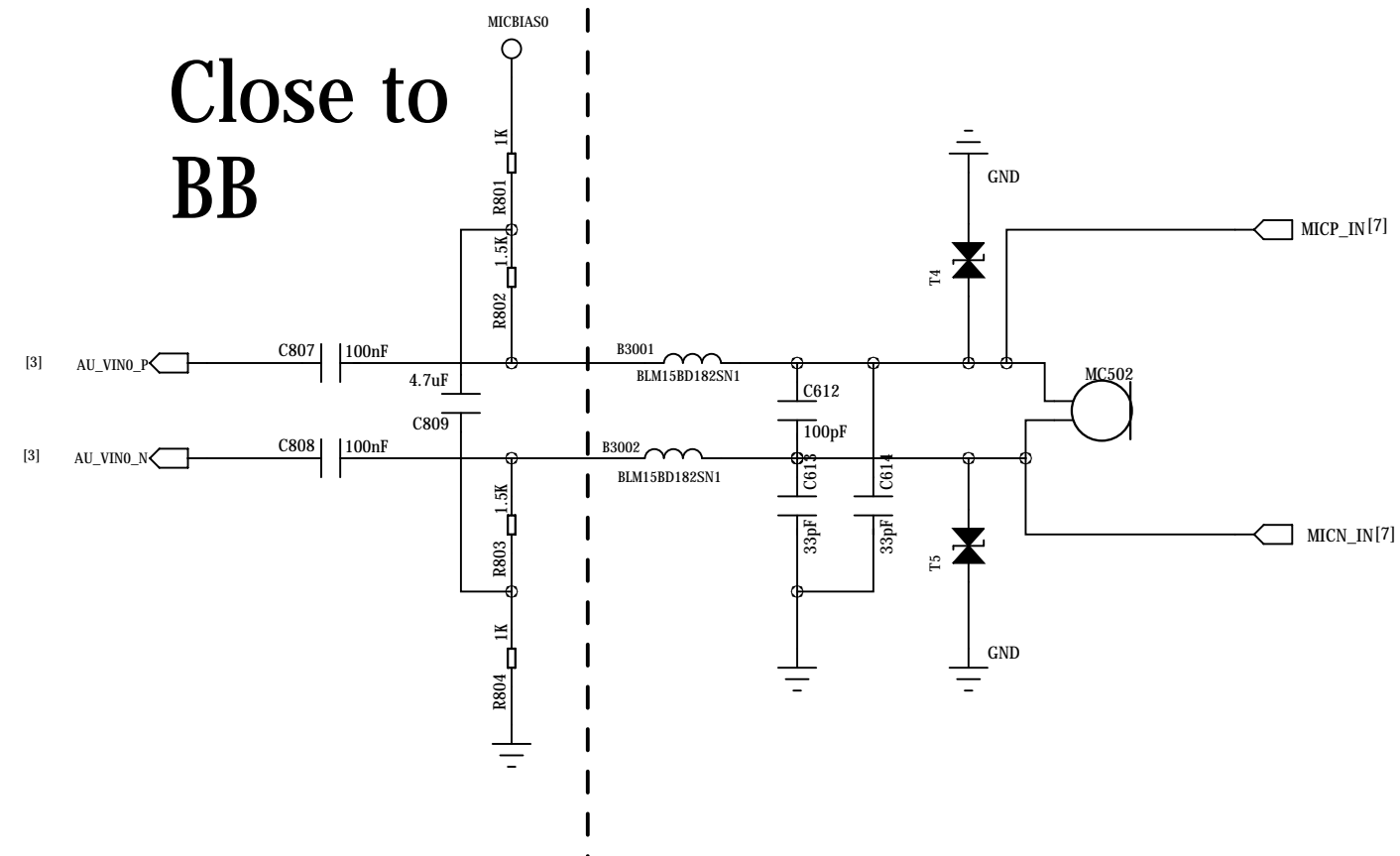


Earphone Audio



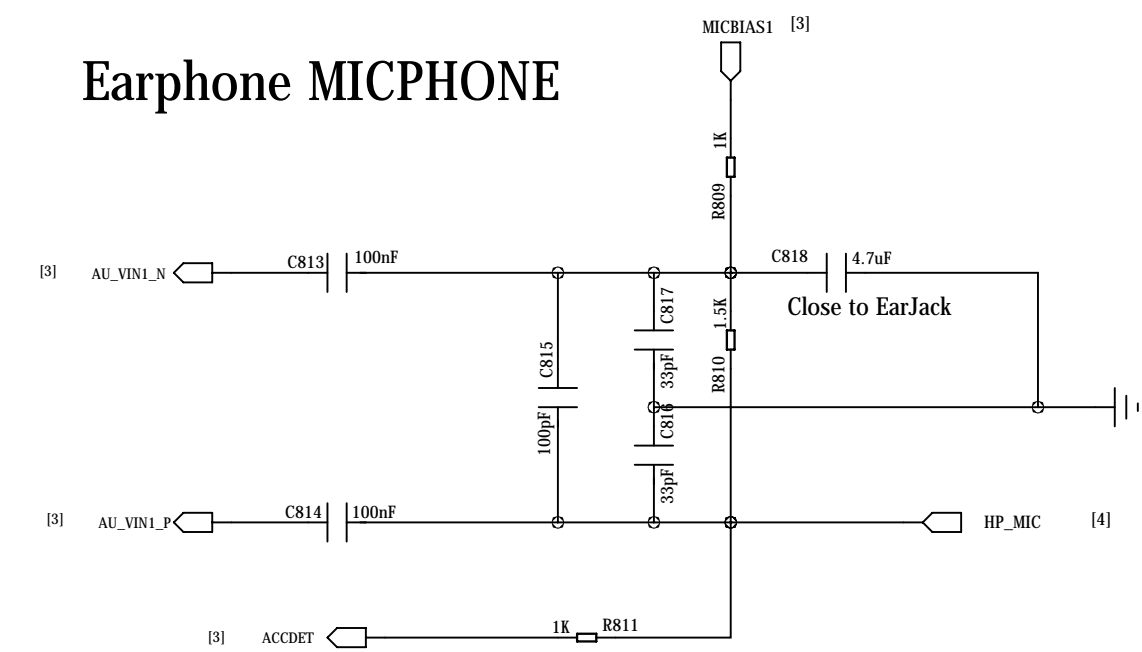
Handset Microphone 1

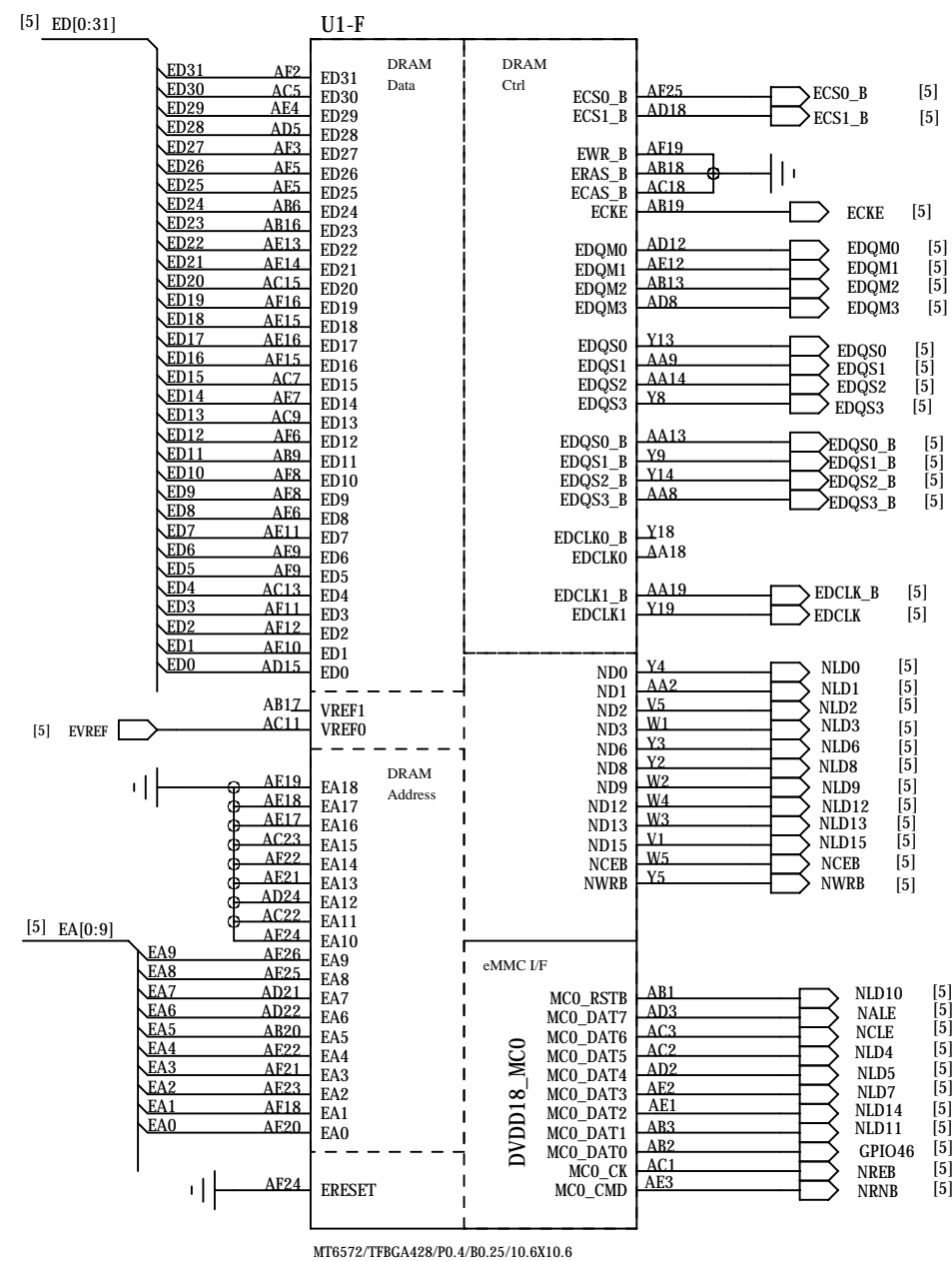
Close to BB



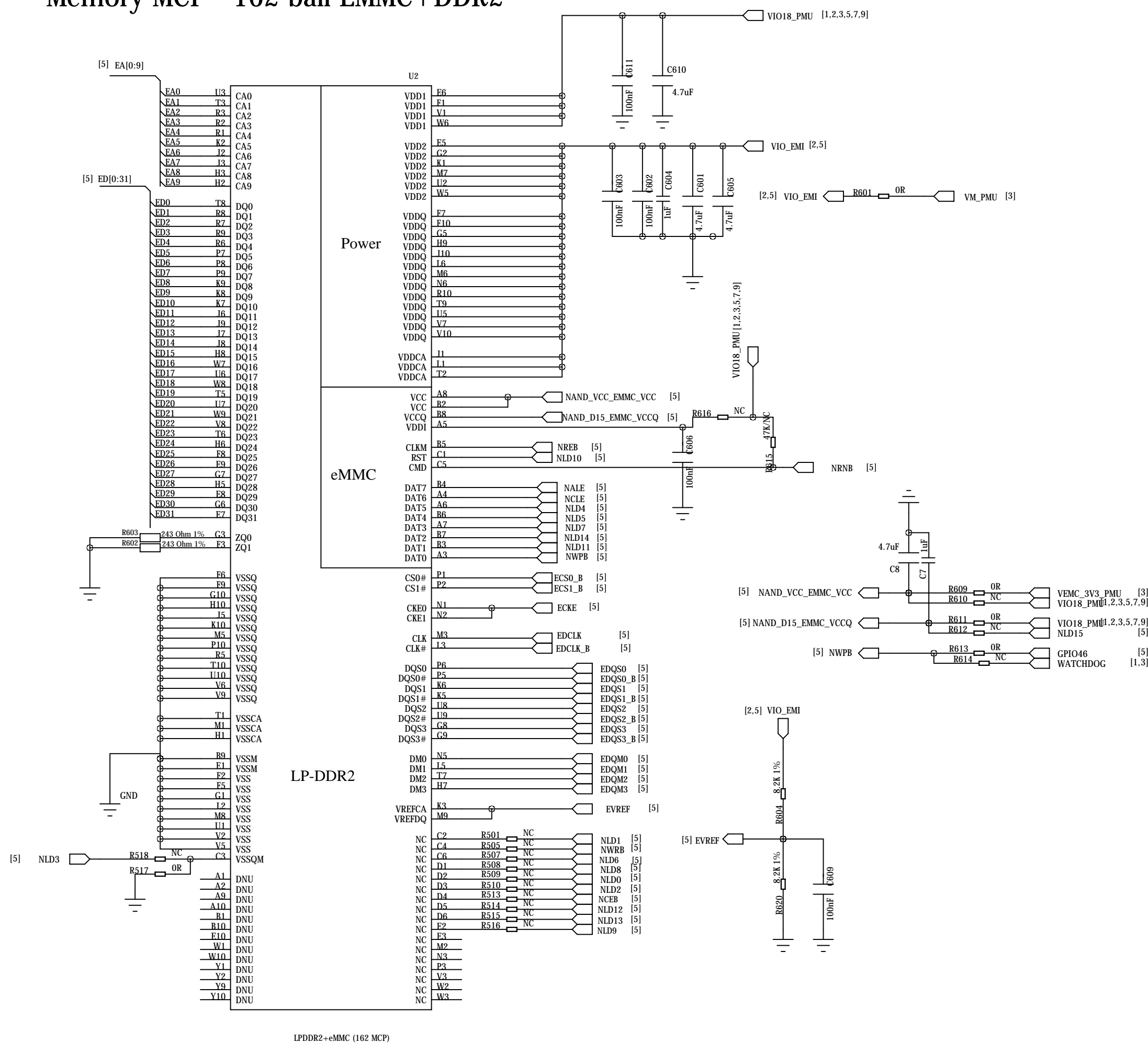
MIC

Earphone MICPHONE

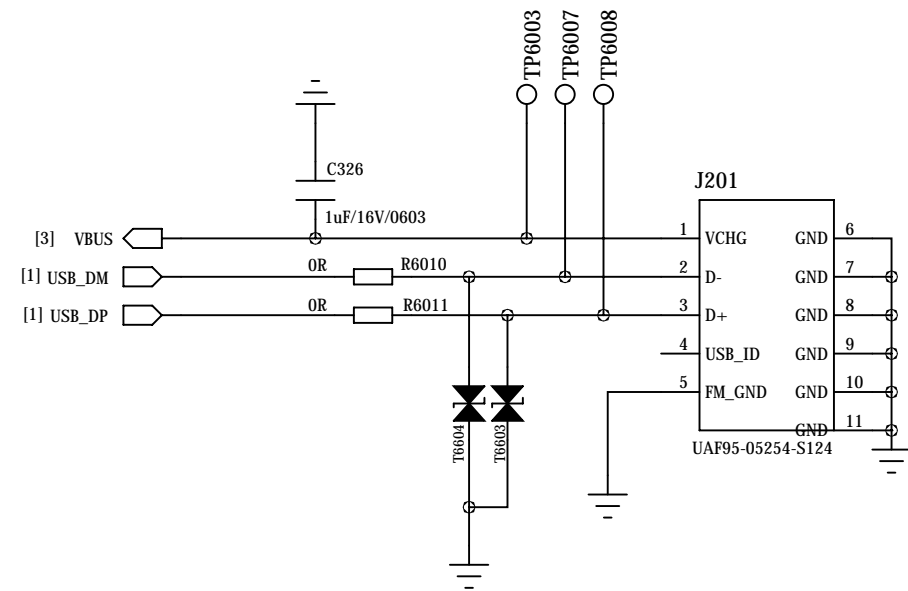




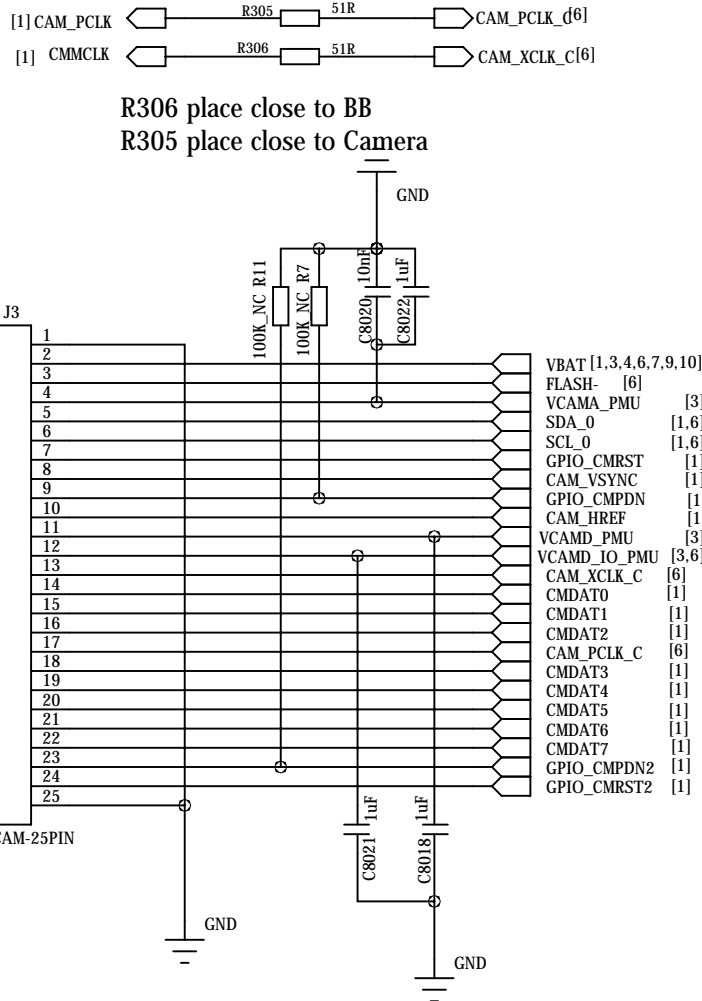
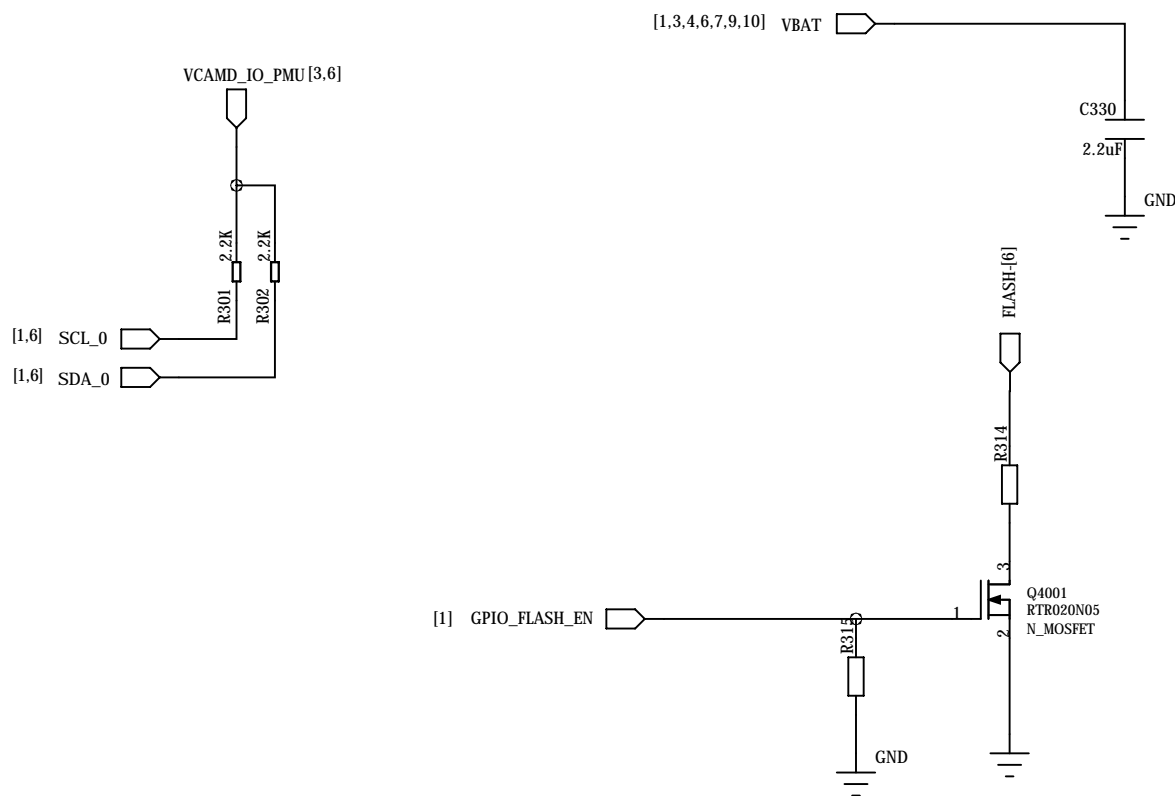
Memory MCP - 162 ball EMMC+DDR2



USB



Camera

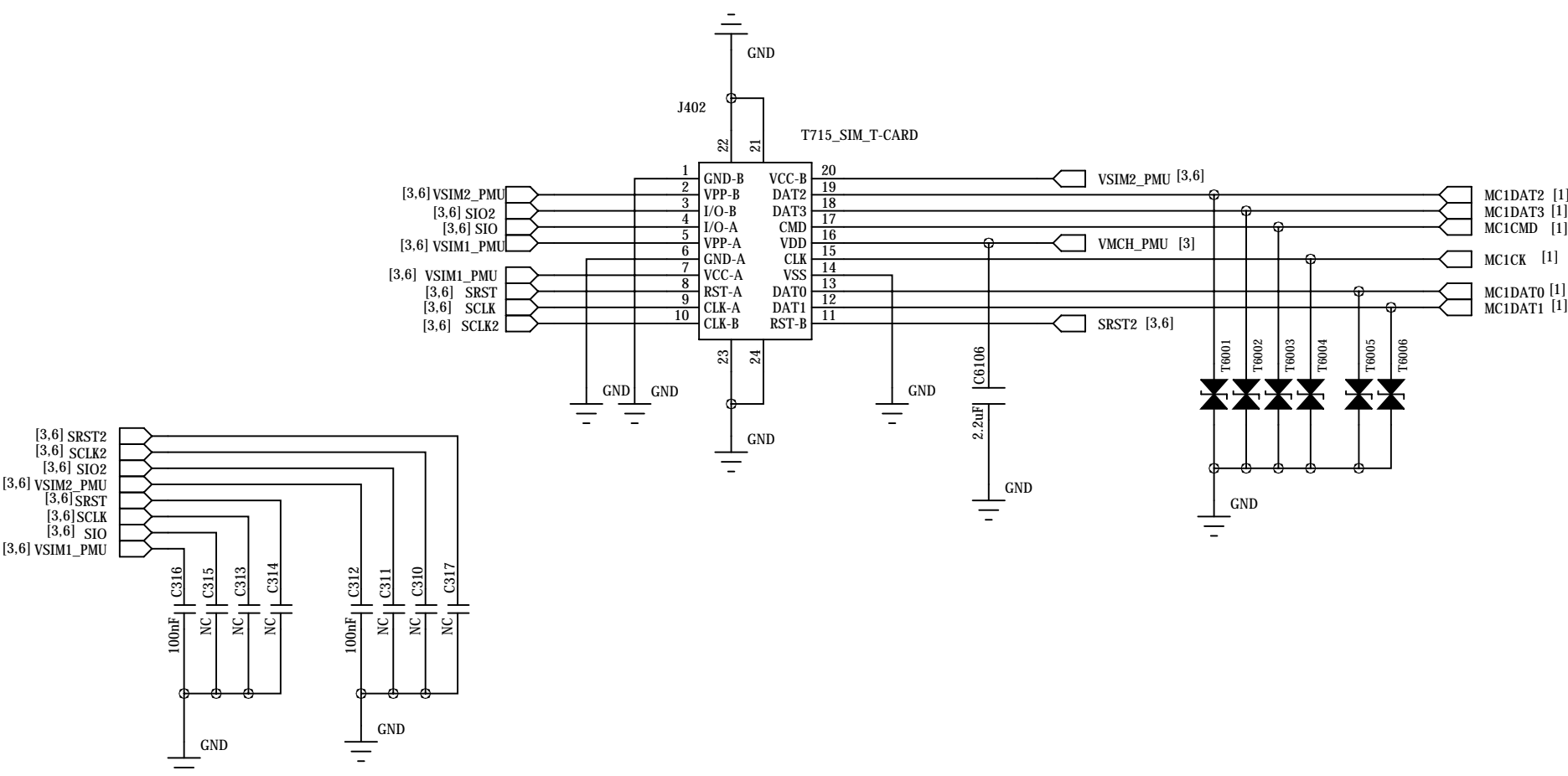
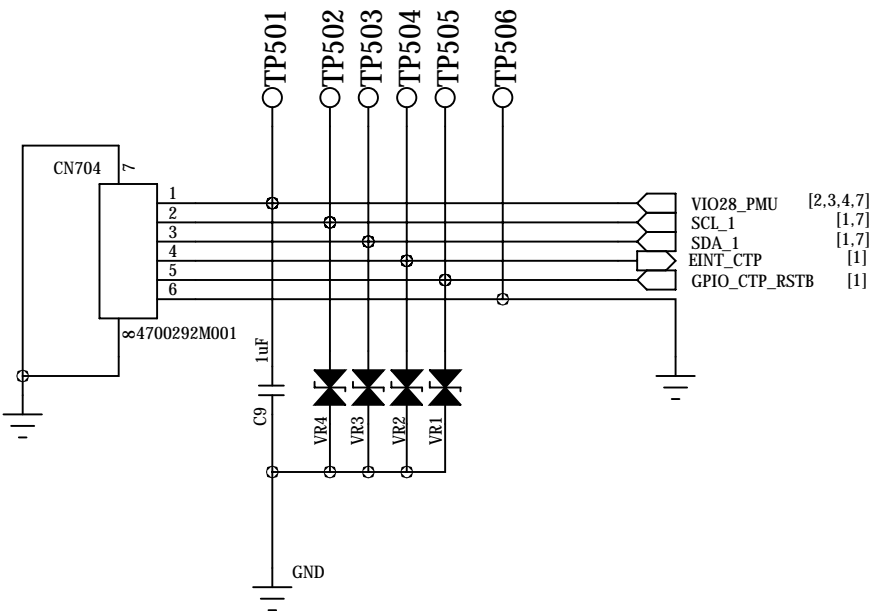


2014.03.20

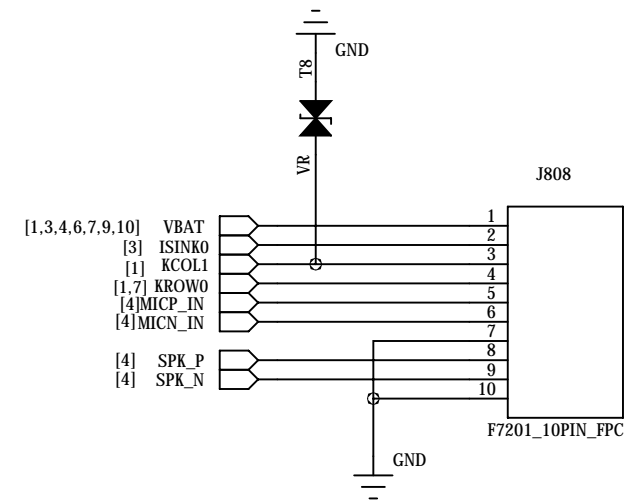
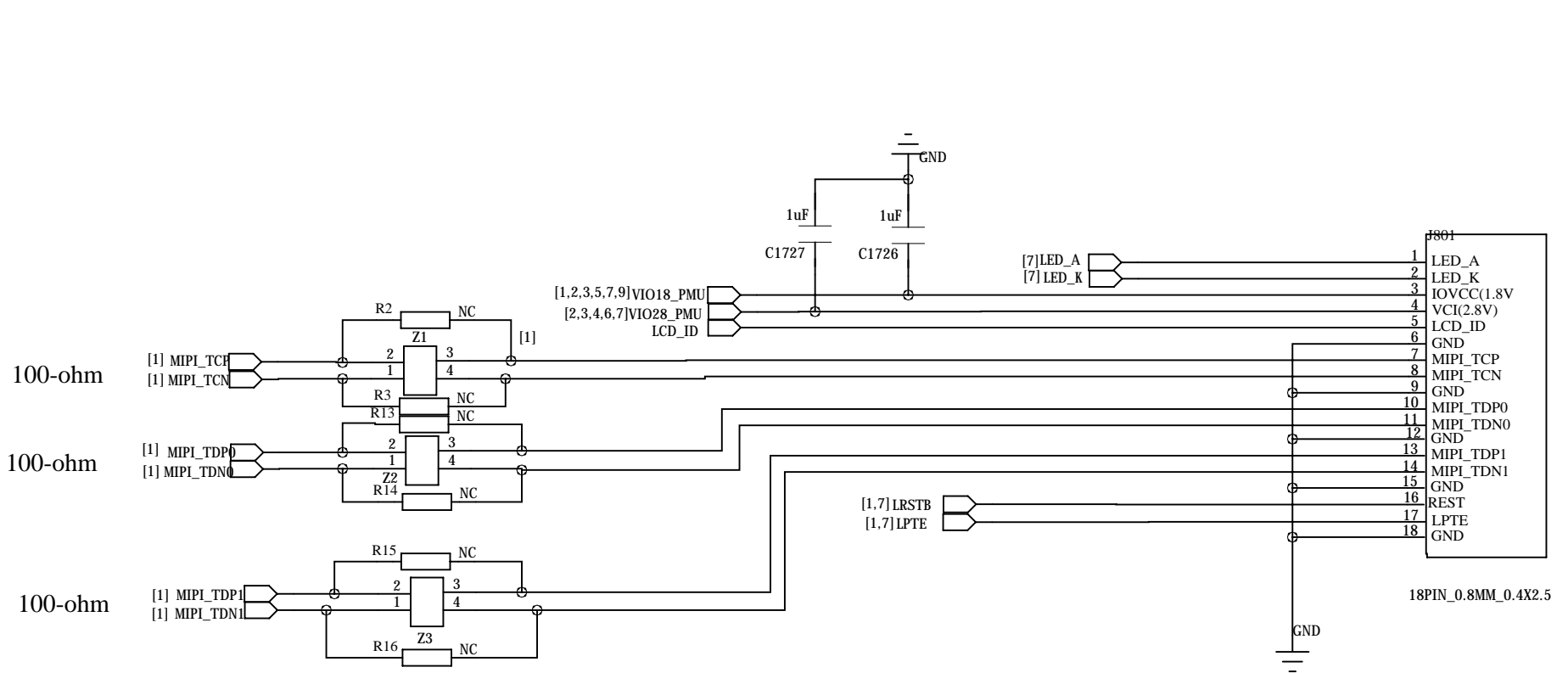
VCAMA_PMUD

SIM CARD&T CARD

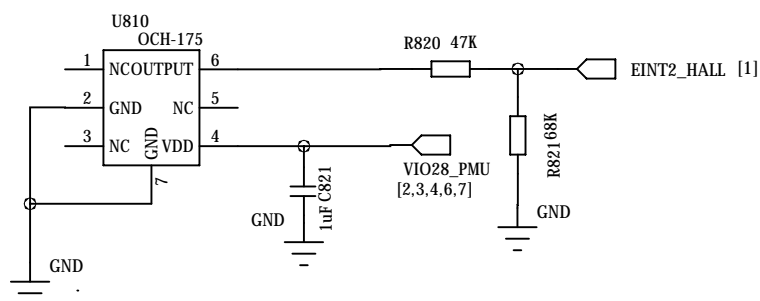
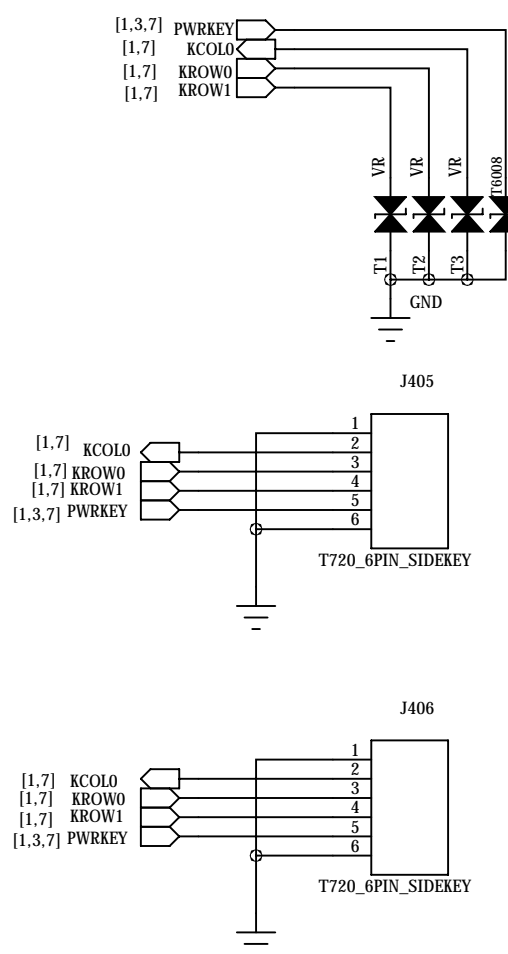
TP



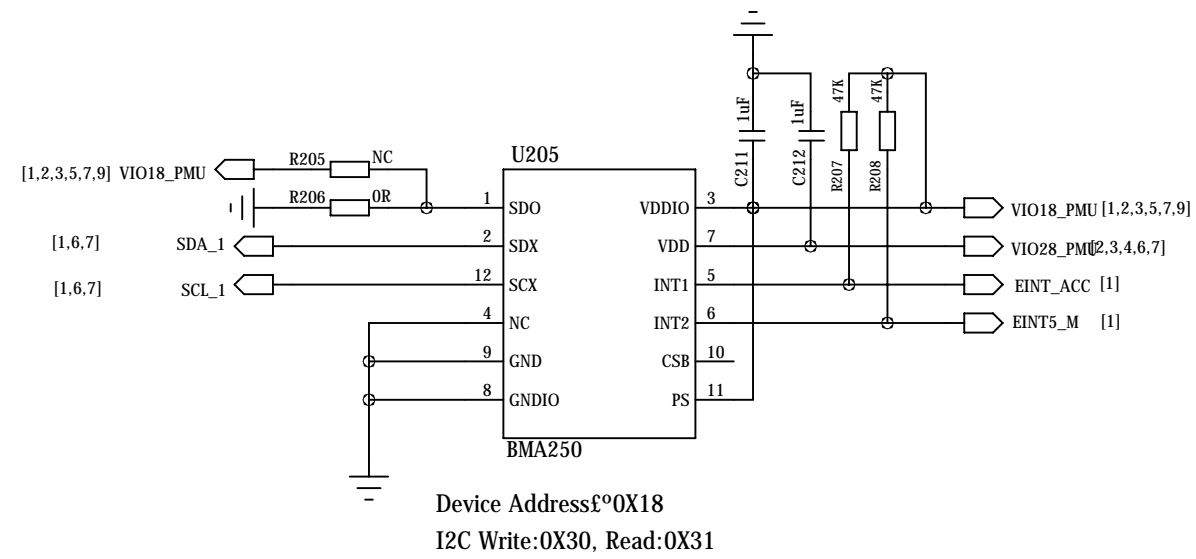
MIPI

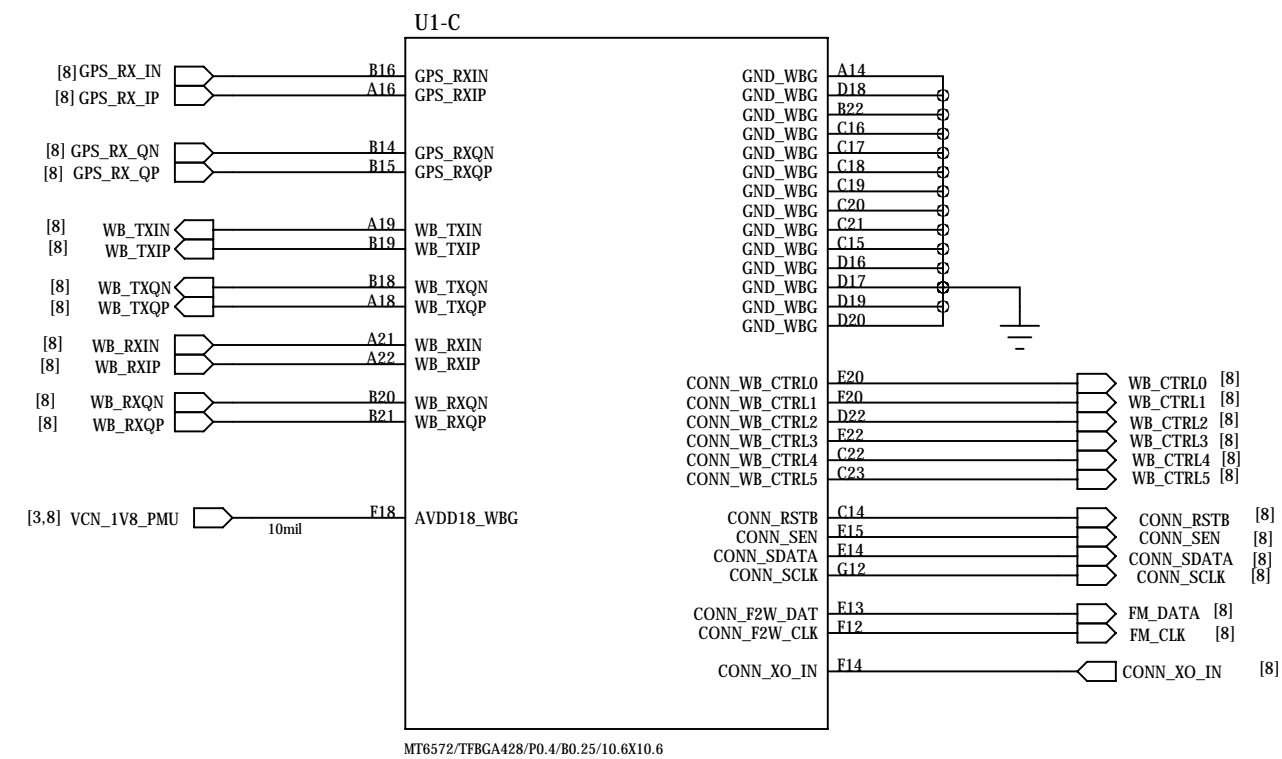
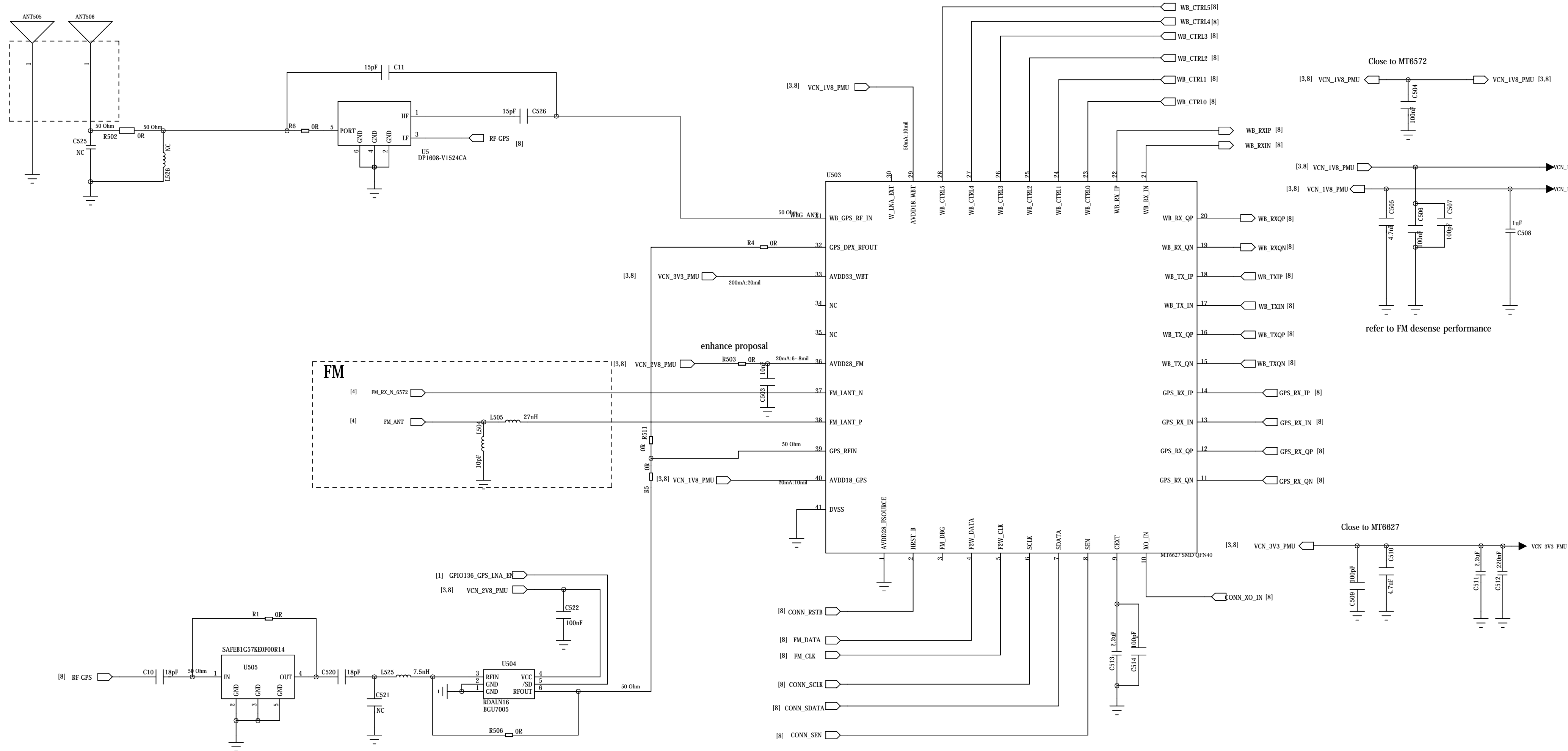


Hall-Sensor



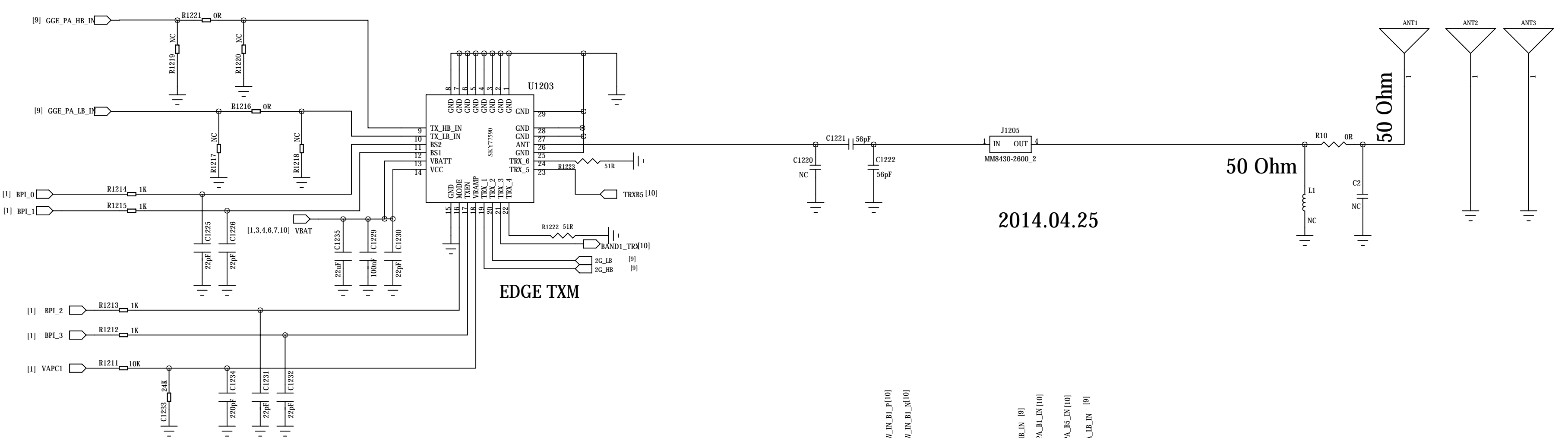
G-SENSOR



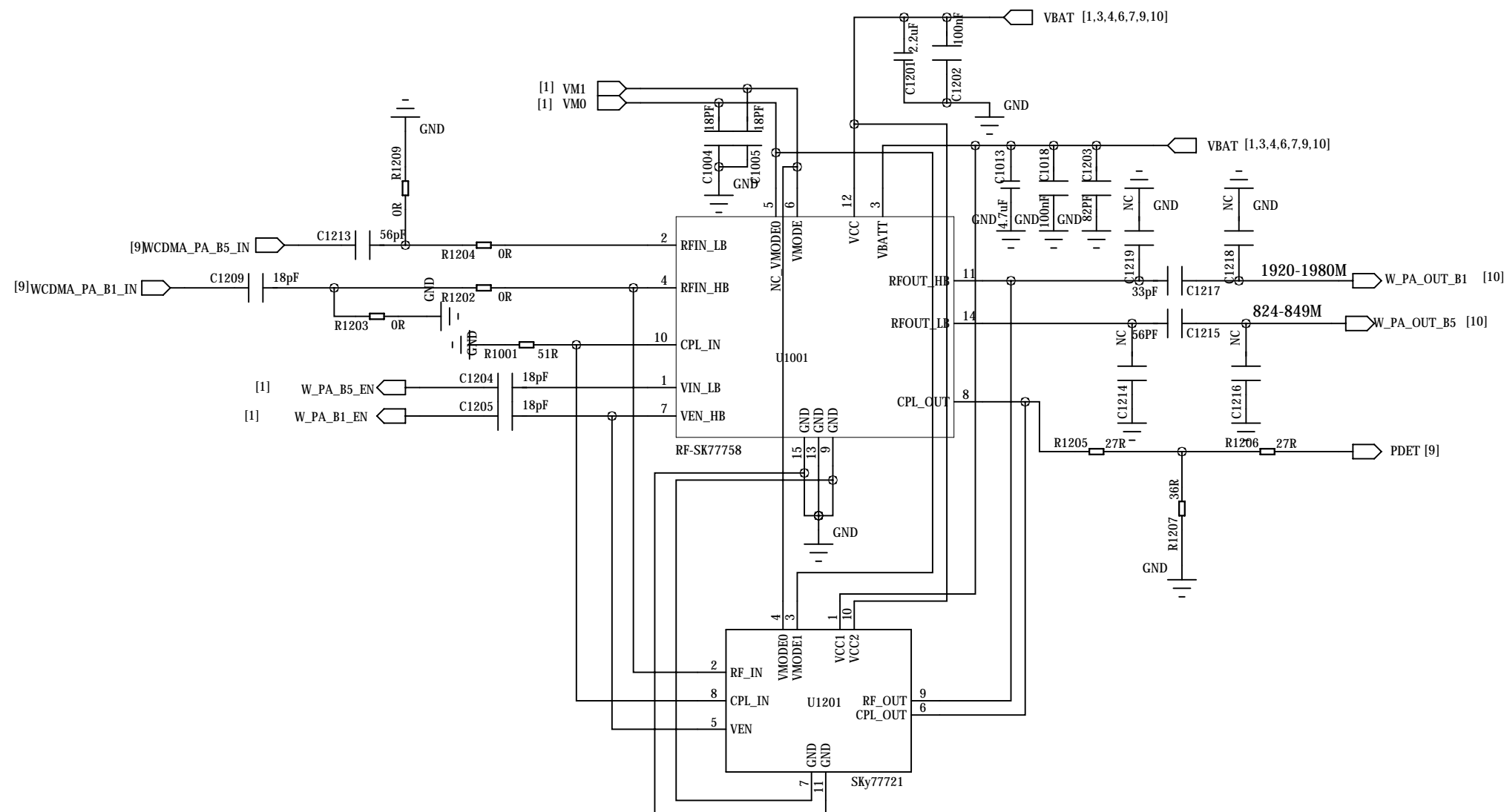
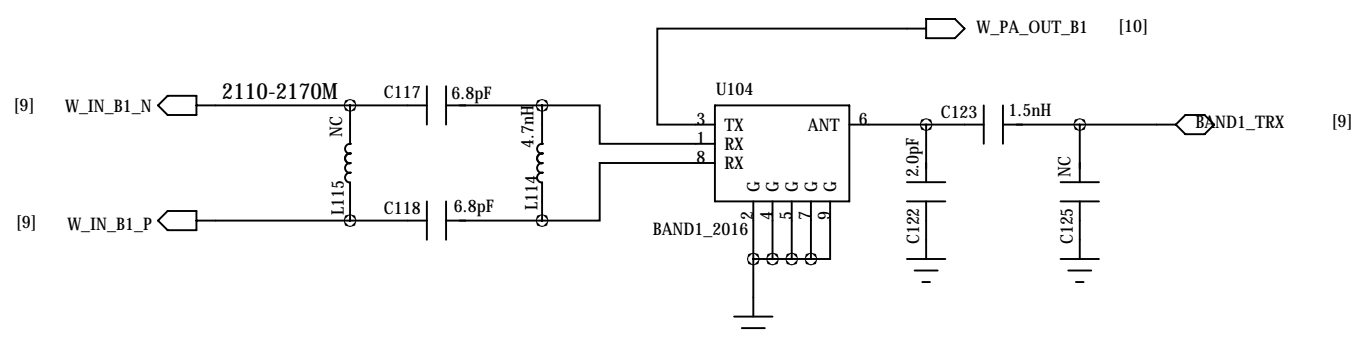
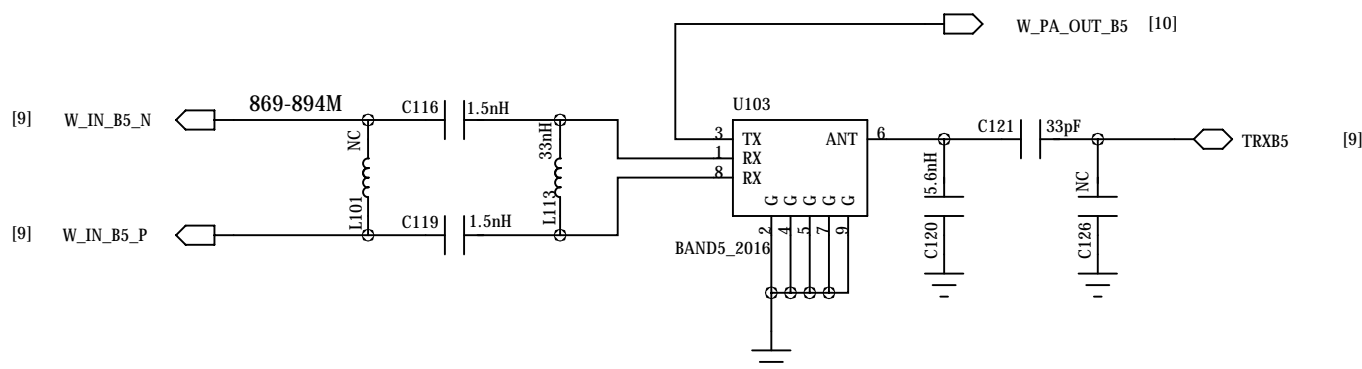


SKY77590 control logic table

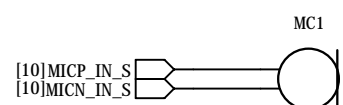
	Enable	VctC	VctB	VctA
LB_GMSK_TX	H	L	L	H
HB_GMSK_TX	H	L	H	H
LB_EDGE_TX	H	H	L	H
HB_EDGE_TX	H	H	H	H
TRX1	L	H	L	L
TRX2	L	H	H	L
TRX3	L	H	L	H
TRX4	L	H	H	H
TRX5	L	L	H	L
TRX6	L	L	L	H



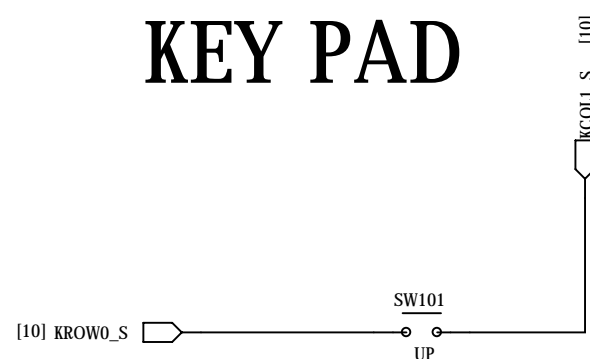
Case1: Band 1
Case2: Band 1+Band 5
Case2: Band 1+Band 5+Band 2



MIC



KEY PAD



SPK

