TX Block Diagram

KEY

2408 - 2467 MHz
Antenna

U1

2.4G SOC
Controller unit

CP-TX2A

BATT
3V

X1

Oscillator
16MHz

2kW OTP & Task controller		VDD/2 Bias Voltage Generator
128 Bytes SRAM	U1 2.4 GHz MCU CP-TX2A	Interrupt Controller
Power-on / LVR	internal data bus	16-bit Counter (TM16)
Power Management		I/O port
Watchdog Timer		Comparator

VDD/2 Bias Voltage Generator
Interrupt Controller
16-bit Counter (TM16)
I/O port
Comparator
8-bit Counter #2 (TM2)
8-bit Counter #3 (TM3)
11-bit PMW Generator #0 (PWMG0)
11-bit PMW Generator #1 (PWMG1)
11-bit PMW Generator #2 (PWMG2)

low-noise amplifier
Filter
GFSK Demodulator
RX FIFO
Communication Baseband Protocol stack
2408 - 2467 MHz
ANT
Match Network
Power Amplifier
DAC
GFSK Modulator
TX FIFO
SPI
Register
Digital baseband
X1
16 MHz Crystal
Power Management
CSN
SCK
MOSI
MISO
IRQ
CE
XC1
XC2
VSS
VDD