**BLOCK** DDR Class D AMP NAND EMMC ΤF LINEOUT LINEIN SPKL&SPKR DDR HPOUT MEMORY AUDIO USB1 AMICx3 USB2.0 OTG USB2.0 Port AUDIO USB0 26MHz GPU DMICx8 CPU  $H \square H$ 4\*A53 Mail T760MP2 DVP PCM1 I2S&PCMx3 **Antenna A63** MIPI-DSI WIFI&BT DSI0&DSI1:Max 2560\*1600@60H SDIO BOE **AP6212** MIPI-DSI0 UART1 UARTx4 TWI0 Light Sensor & RTC MIPI-DSI1 TWI1 G Sensor & MFI & BOE LCD Power TWIx5 TWI3 Security Touch A (R/L) Video TWI4 Touch B (U/D) eDP:Max 2048\*1536@60Hz eDP Engine System SPIx2 KEY GPADC TWIx2 CPUS Cortex M4 RTC ⊕ ⊝ <u>©</u> POWER RSB UARTx1 SPIx1 RTC OSC DCXO 32.768KHz 32.768KHz 24MHz DEBUG DCIN AC Adapter (5V/3A) VBUS FCC ID : 2AI5H-PU1-1801 **AXP802** 

Project
PU1-1801

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HW2-Dennis

Date
Monday, June 17, 2019

PCB/PCBA Number
Aura V1.6 MB

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