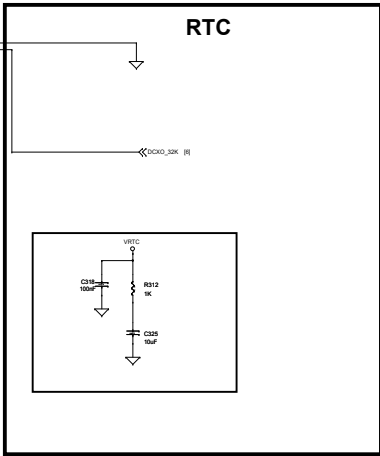
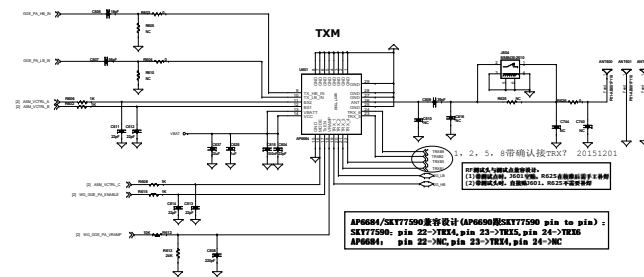
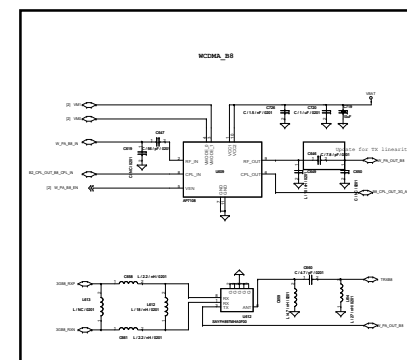
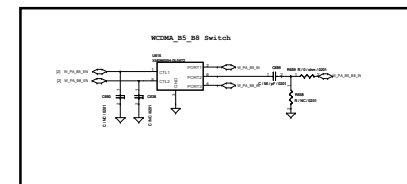
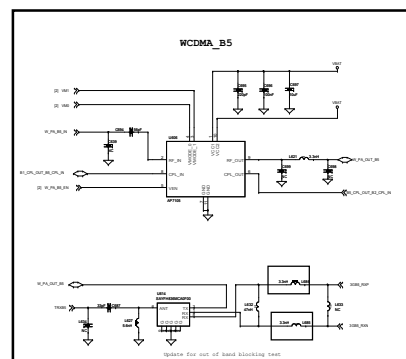
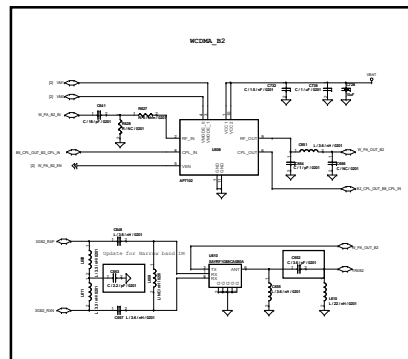
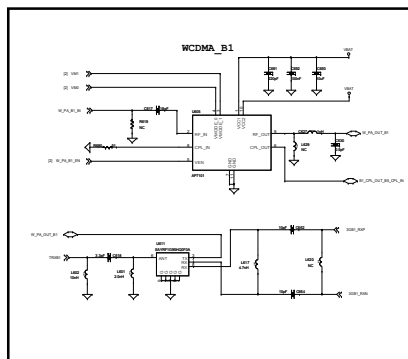
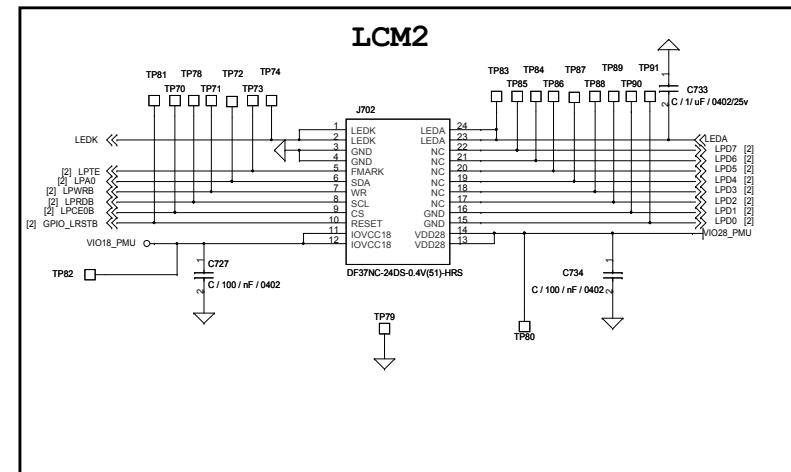
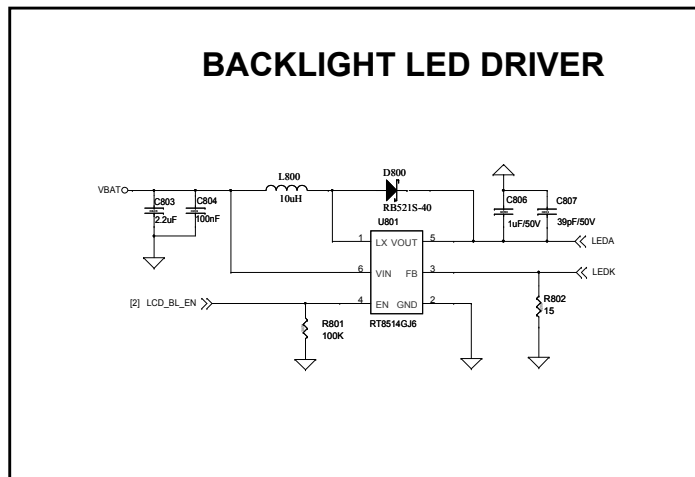
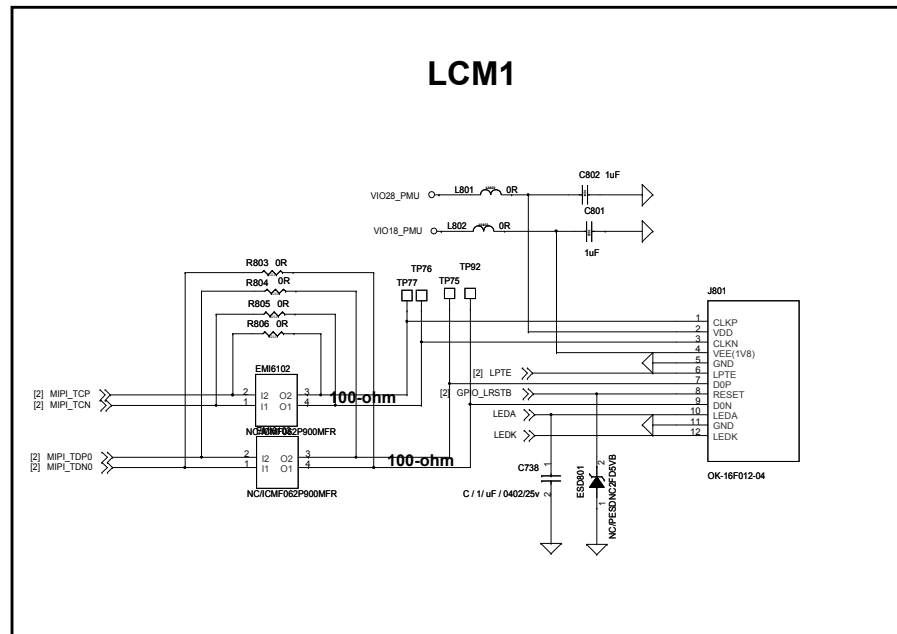


[illegible][illegible]

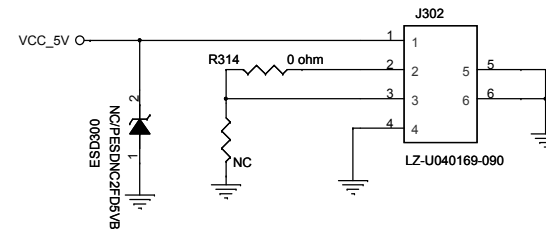
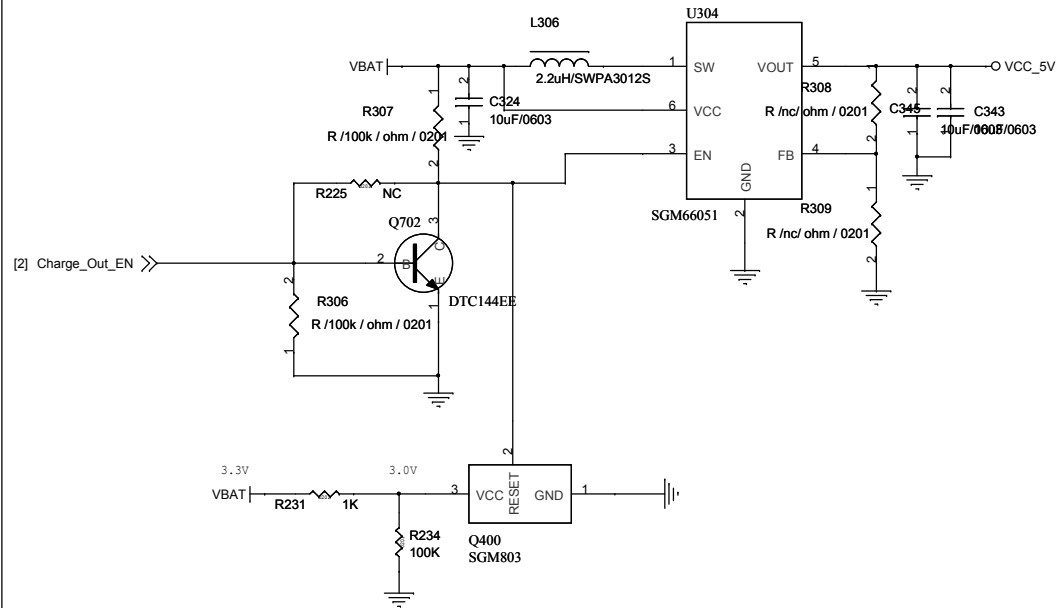
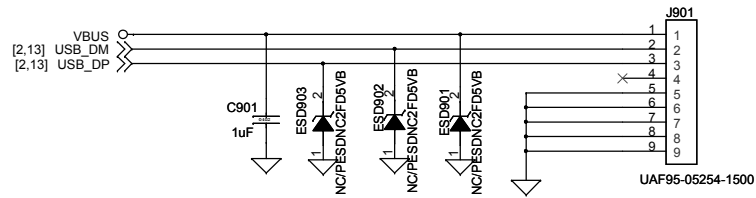


2KV7750 PA選路控制表				
	Enable	VoutC	VoutB	VoutA
LB_00H_TX	N	L	L	N
HW_00H_TX	N	N	N	N
LB_0104_TX	N	N	L	N
HW_0104_TX	N	N	N	N
TRX1	L	N	L	L
TRX2	L	N	N	N
TRX3	L	N	L	L
TRX4	L	N	N	N
TRX5	L	L	N	N
TRX6	L	L	L	N
TDC200A	N	N	N	N



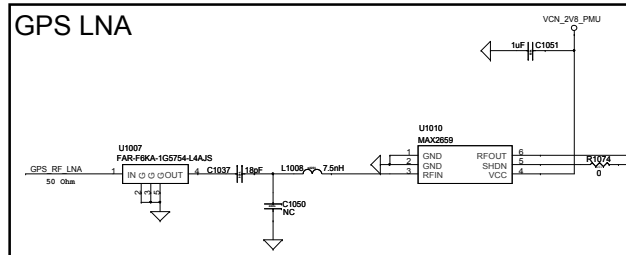
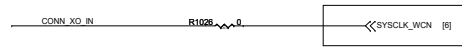


USB

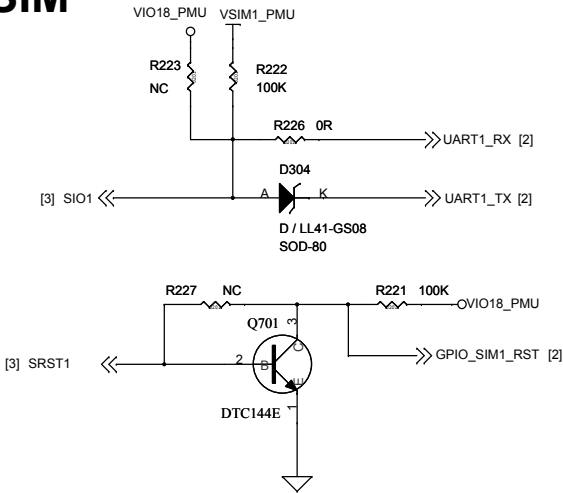


MEDIATEK

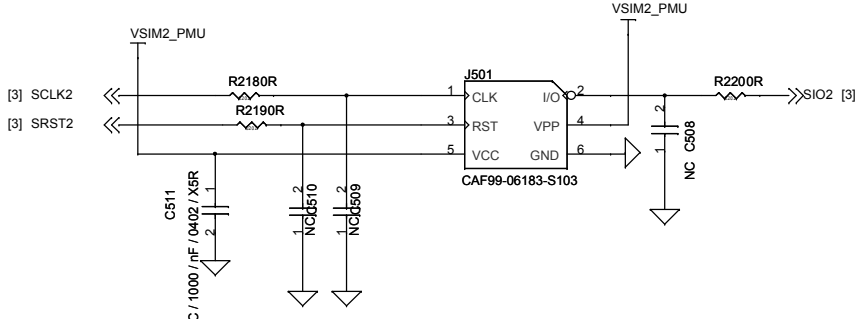
Title		USB	
Size	Document Number	P612	
B		Rev	
		V1.0	
Date:		Friday, December 25, 2015	
Sheet		9 of 99	



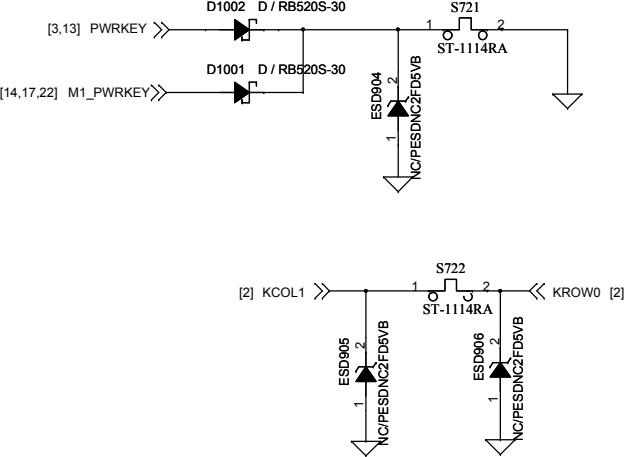
Virtual SIM



SIM2



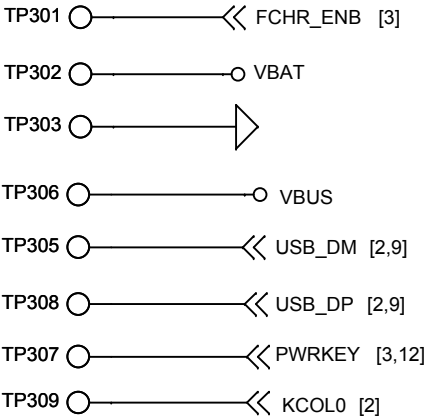
Power key



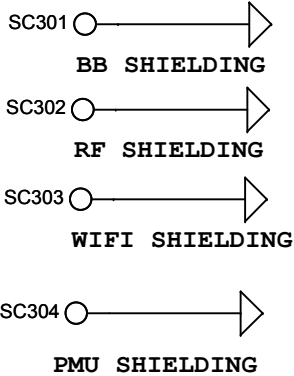
MEDIATEK

Title			KEYPAD		
Size	Document Number		P612		Rev
B					V1.0
Date:	Friday, December 25, 2015		Sheet	12 of 99	

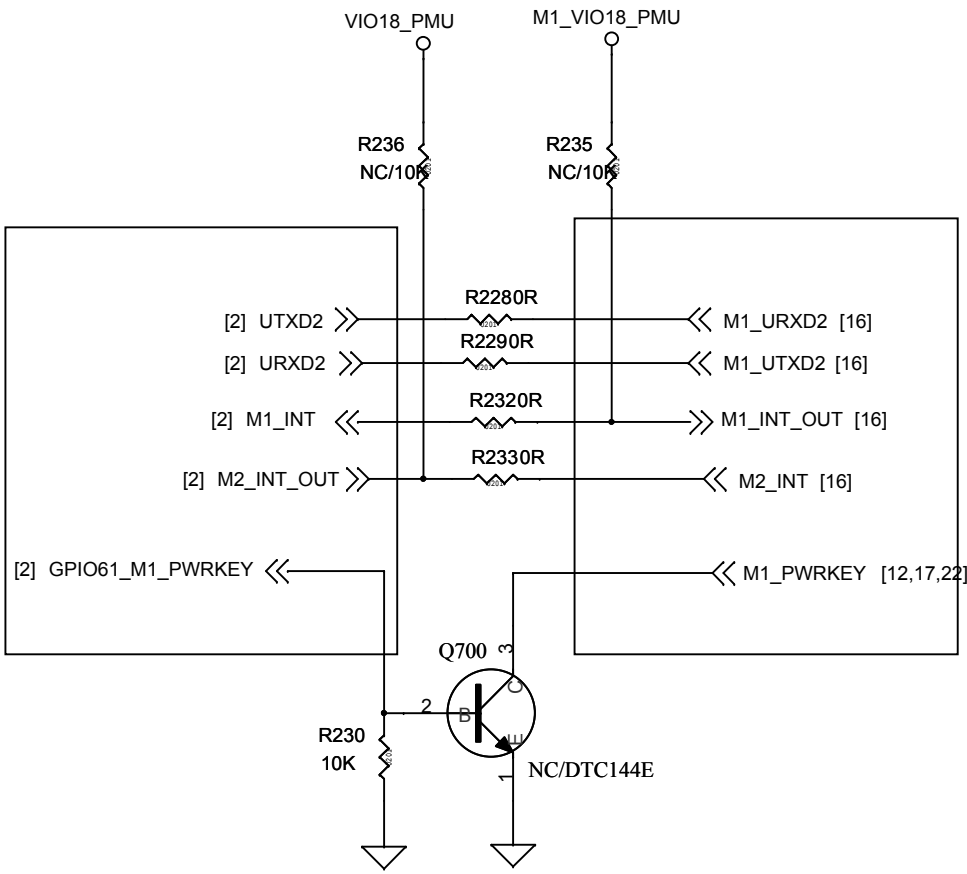
测试点



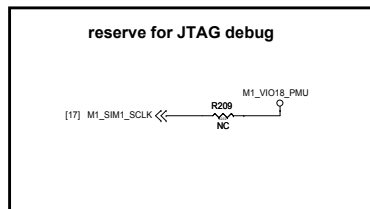
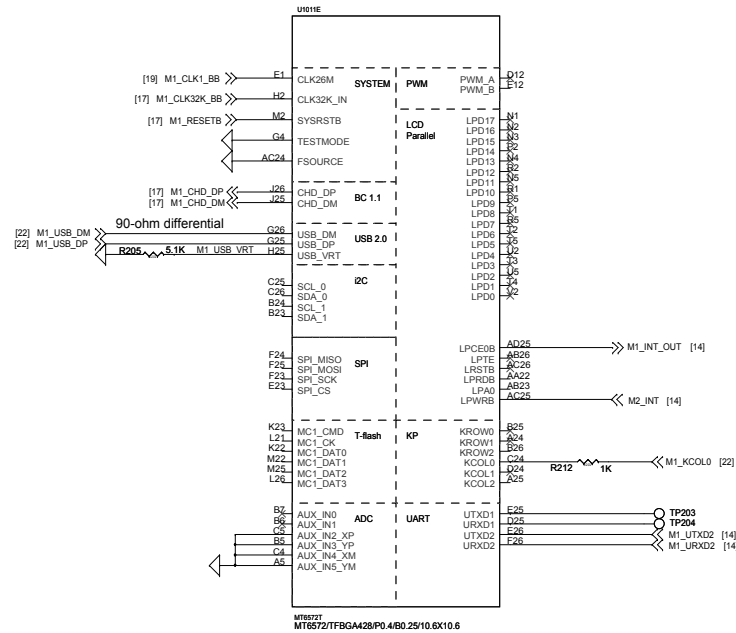
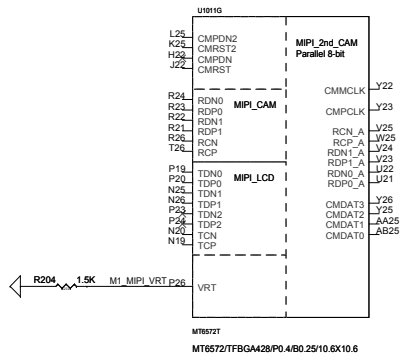
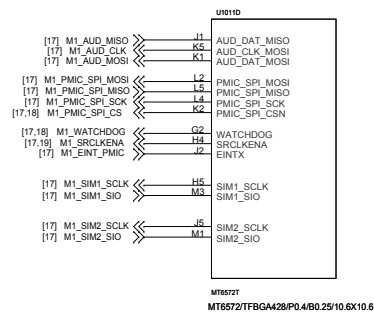
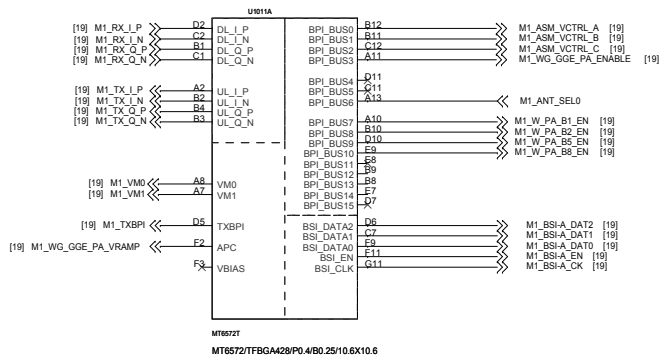
屏蔽罩

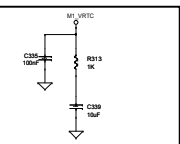


Title			
<Title>			
Size	Document Number		Rev
A	<Doc>		<RevCode>
Date:	Friday, December 25, 2015	Sheet 13 of 99	

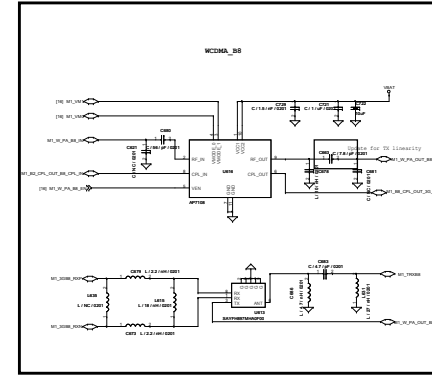
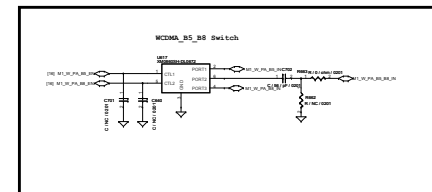
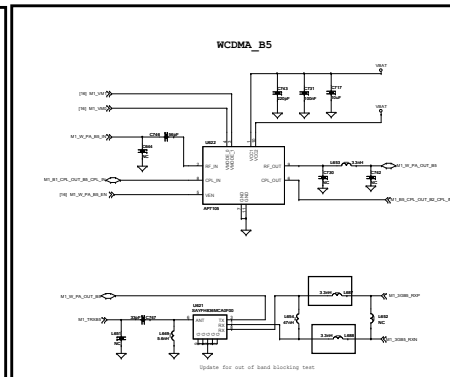
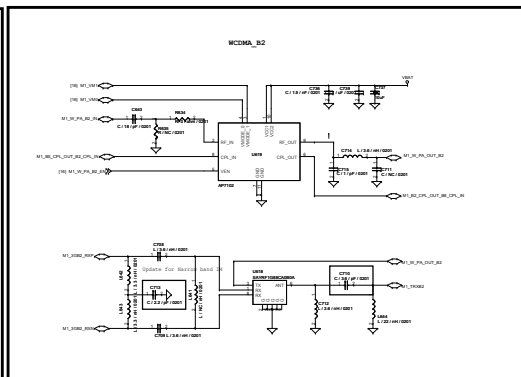
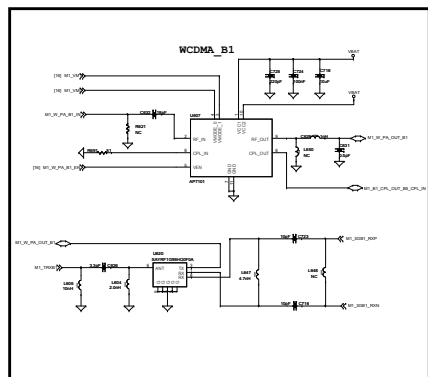
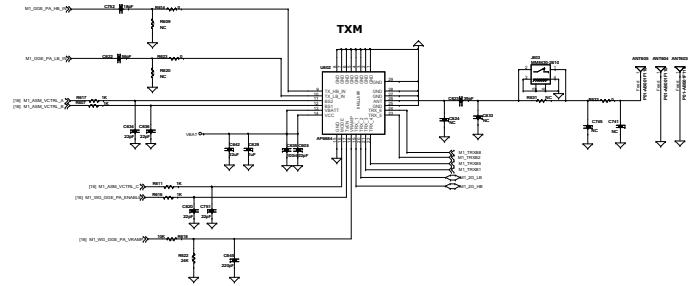
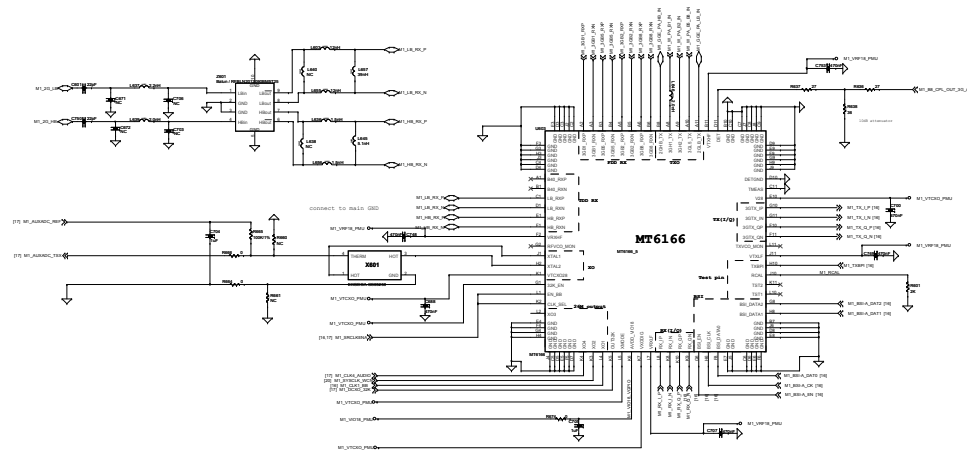


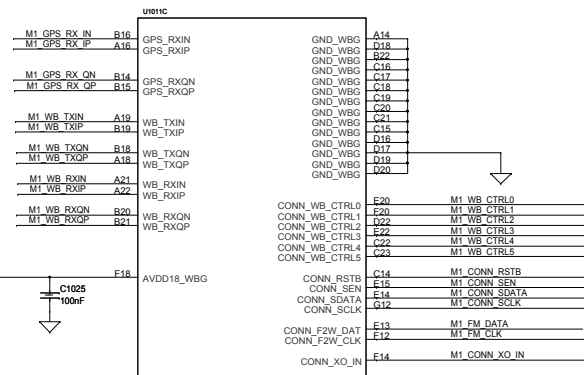
Title			
<Title>			
Size	Document Number		Rev
A	<Doc>		<RevCode>
Date:	Friday, December 25, 2015	Sheet	14 of 99





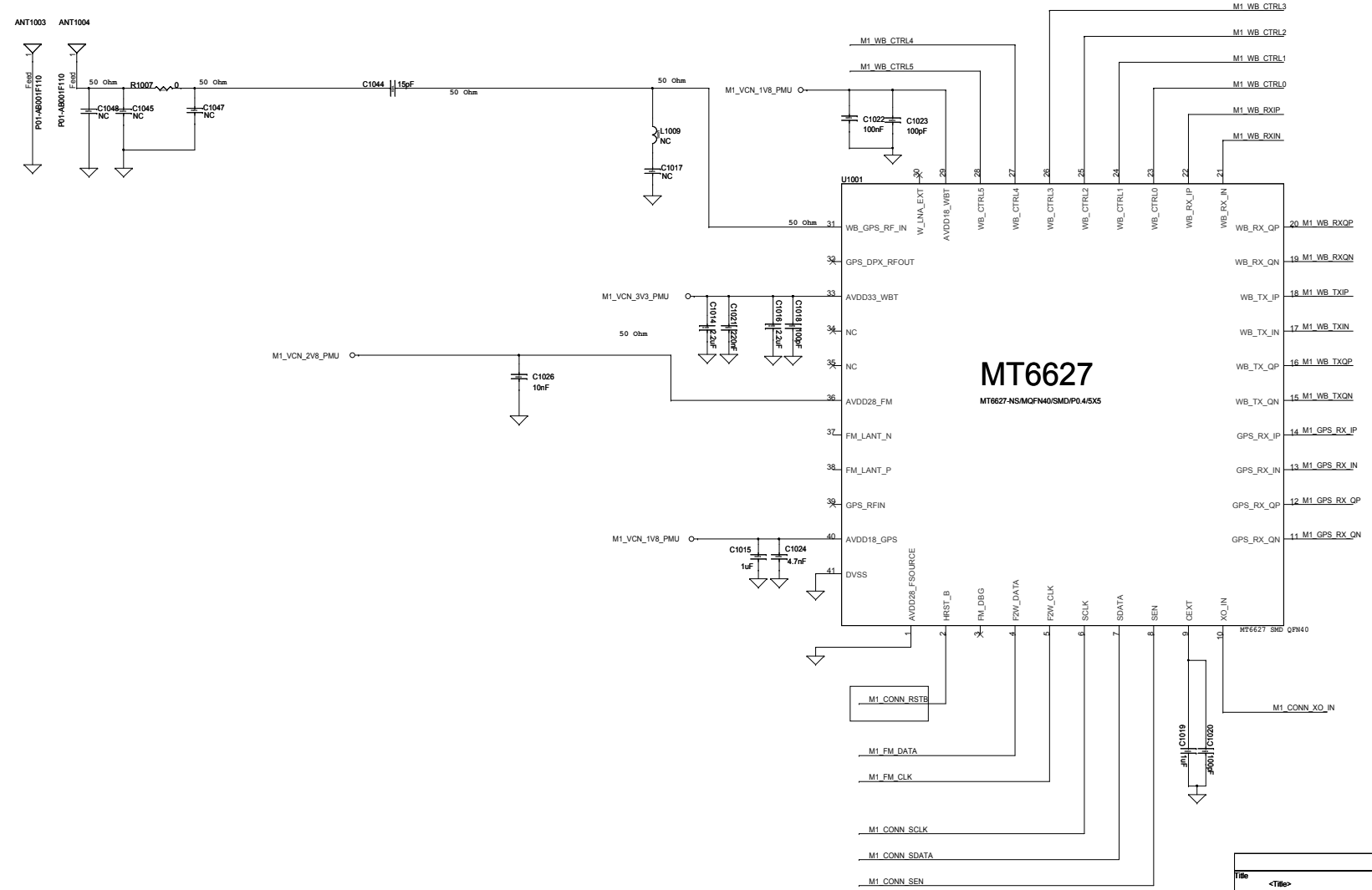
Block	Name	Unit	Current	Default On	On/Off control
D/CDC	VCORE	0.7-1.4 (0.6VDC-1.0V)	2800	V	Always On
	VR1	1.8/2.2-2.2	100	V	1 Always on 2 Register (default)
	VPA	0.6-1.4V	600	V	1 Register (default) 2 SBUKEN
	VM	1.21 3/1.51 1.81	300 (1.8V) 700 (1.2V)	V	1 Always on 2 Register (default) 3 SBUKEN
VSYS LOG	VMF18	1.8/25	200	V	1 Register (default) 2 SBUKEN
	VIO1	1.8	200	V	1 Always on 2 Register (default)
	VCM18	1.8	120	V	1 Register (default) 2 SBUKEN
	VCAM10	1.21 3/1.51 1.81	150	V	1 Register (default) 2 SBUKEN
Analog LOG	VGPI3	1.8	100	V	Register
	VGP3	1.21 3/1.51 1.8	200	V	1 Always On 2 Register
	VM	2.0	150	V	1 Always On 2 Enable
	VTCIO	2.0	40	V	1 Register (default) 2 SBUKEN
	VTCIOB	2.0	40	V	1 Register (default) 2 SBUKEN
	VCM18	1.6/1.8/2.0/2.8	150	V	1 Register (default) 2 SBUKEN
	VMCH3	3.3/3.4/5.0/5.6	240	V	1 Register (default) 2 SBUKEN
Digital LOG	VMCH28	2.8	200	V	1 Always On 2 Enable
	VMS3	3.3	20	V	1 Register (default) 2 SBUKEN
	VMC	1.8/3.3	100	V	1 Register (default) 2 SBUKEN
	VMCH	3.0/3.3	400	V	1 Register (default) 2 SBUKEN
	VMCH	3.0/3.3	400	V	1 Register (default) 2 SBUKEN
	VMCH	3.0/3.3	400	V	1 Register (default) 2 SBUKEN
	VMCH	3.0/3.3	400	V	1 Register (default) 2 SBUKEN
	VMCH	3.0/3.3	400	V	1 Register (default) 2 SBUKEN
	VMCH	3.0/3.3	400	V	1 Register (default) 2 SBUKEN
	VMCH	3.0/3.3	400	V	1 Register (default) 2 SBUKEN
VRTC	ICM18	1.21/3.151/1.82/2.0 2.8/3.0/3.3	100	V	Register
	VSBR1	1.8/3.0	50	V	Register
	VSBR2	1.8/3.0	50	V	Register
	VGPI1	1.3/1.51/1.8/2.0 3.0/3.3	100	V	Register
	VGPI2	1.3/1.51/1.8/2.0 3.0/3.3	100	V	Register
	VGPI3	1.3/1.51/1.8/2.0 3.0/3.3	100	V	Register
	VIO18	1.8	200	V	Register
	VRTC	2.0	2	V	Always On



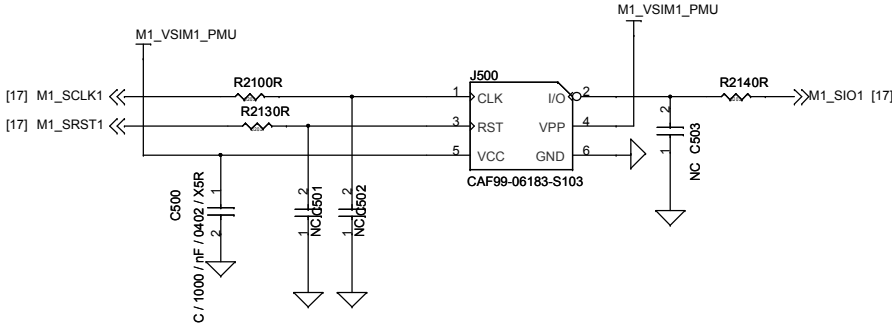


MT6627

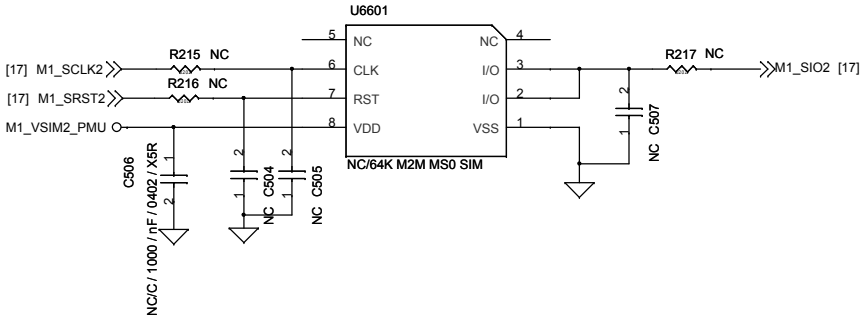
MT6572/TFBGA428/P0.4/B0.25/10.6X10.6



SIM1

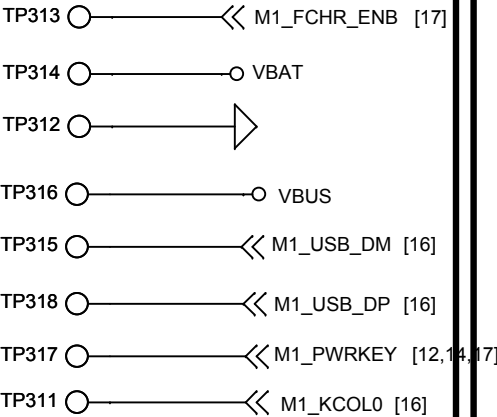


SIM2: SIM IC

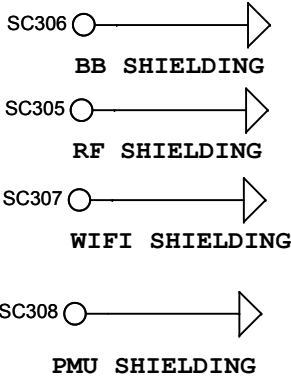


Title		
<Title>		
Size	Document Number	Rev
B	<Doc>	<RevCode>
Date:	Friday, December 25, 2015	Sheet 21 of 99

测试点



屏蔽罩



Title			
<Title>			
Size	Document Number		Rev
A	<Doc>		<RevCode>
Date:	Friday, December 25, 2015	Sheet 22 of 99	