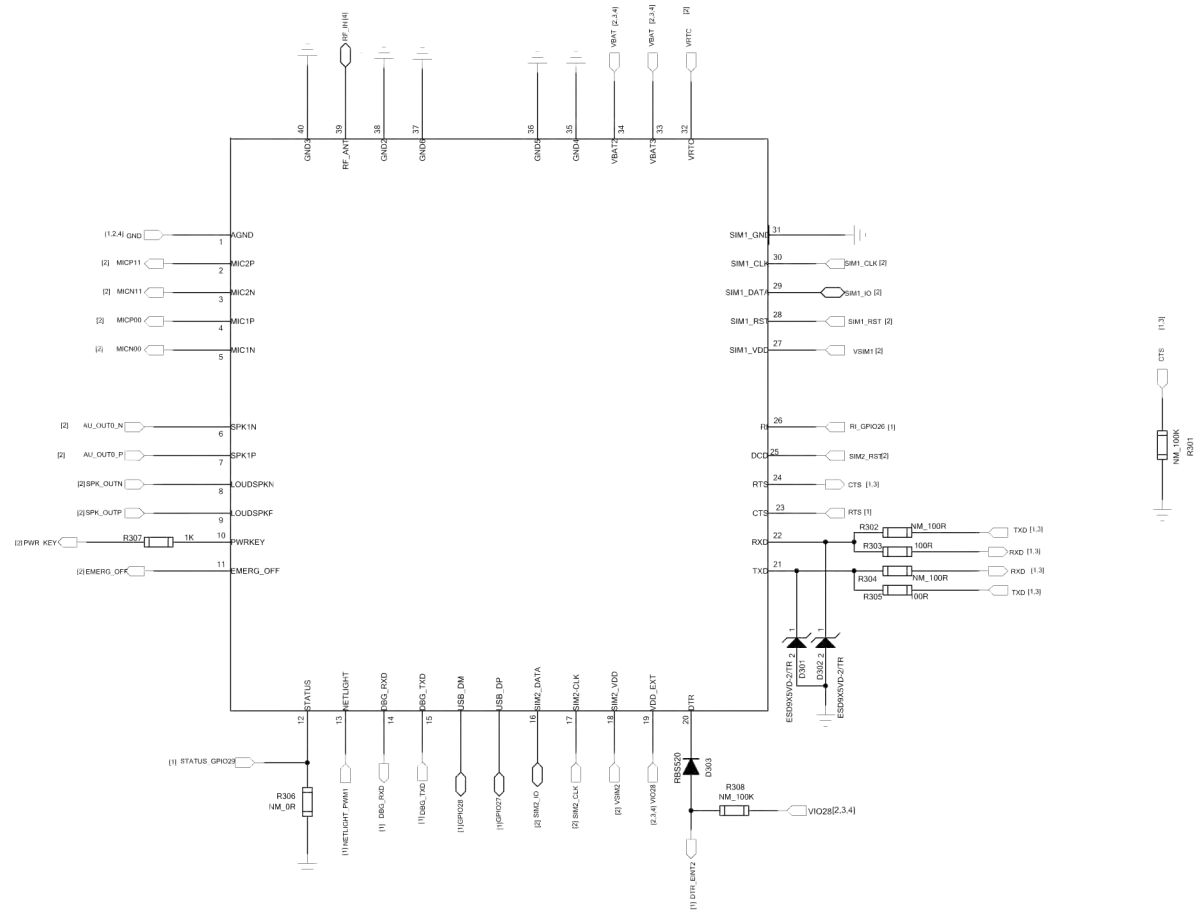


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SHEET 3 of 4		2014.12.12



D

D

C

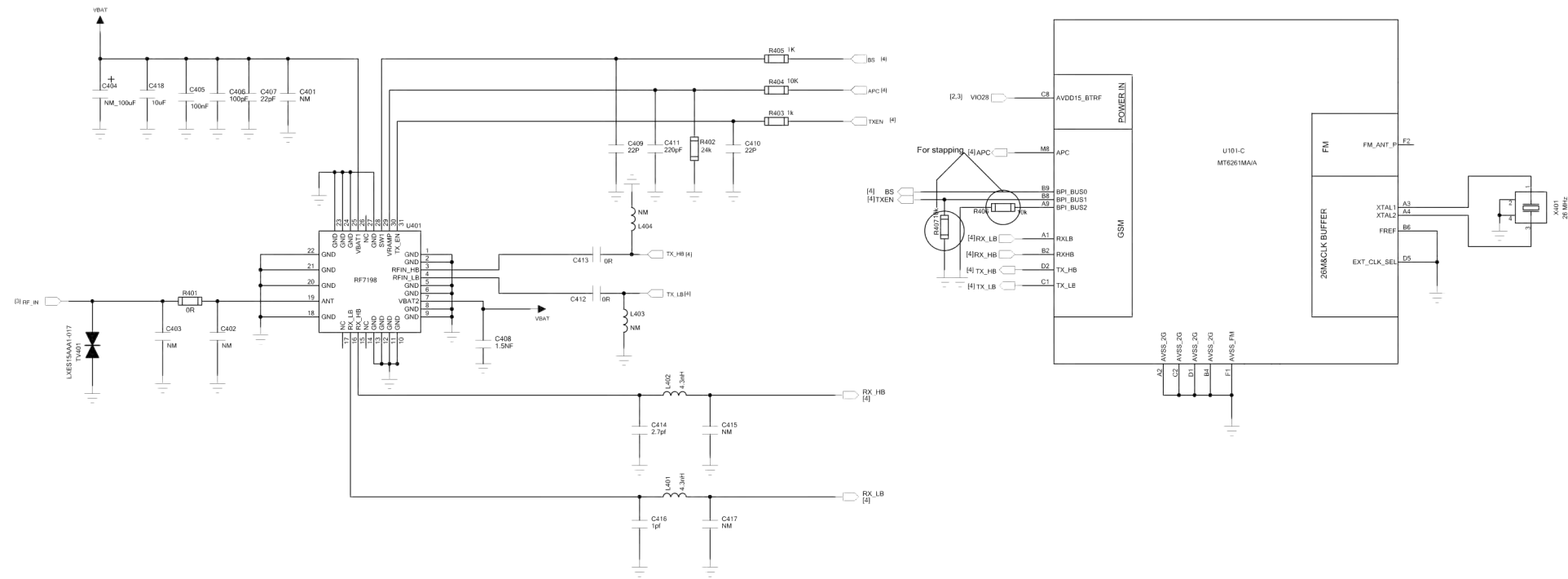
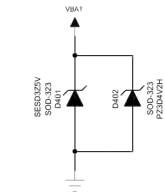
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B

B

A

A



TX_EN(BUS3)	SW1(bus1)	VRAMP	TX Module Mode
0	0	X	Standby
0	0	X	RX_LB
0	1	X	RX_HB
1	0	PwrCtl	TX_LB
1	1	PwrCtl	TX_HB

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