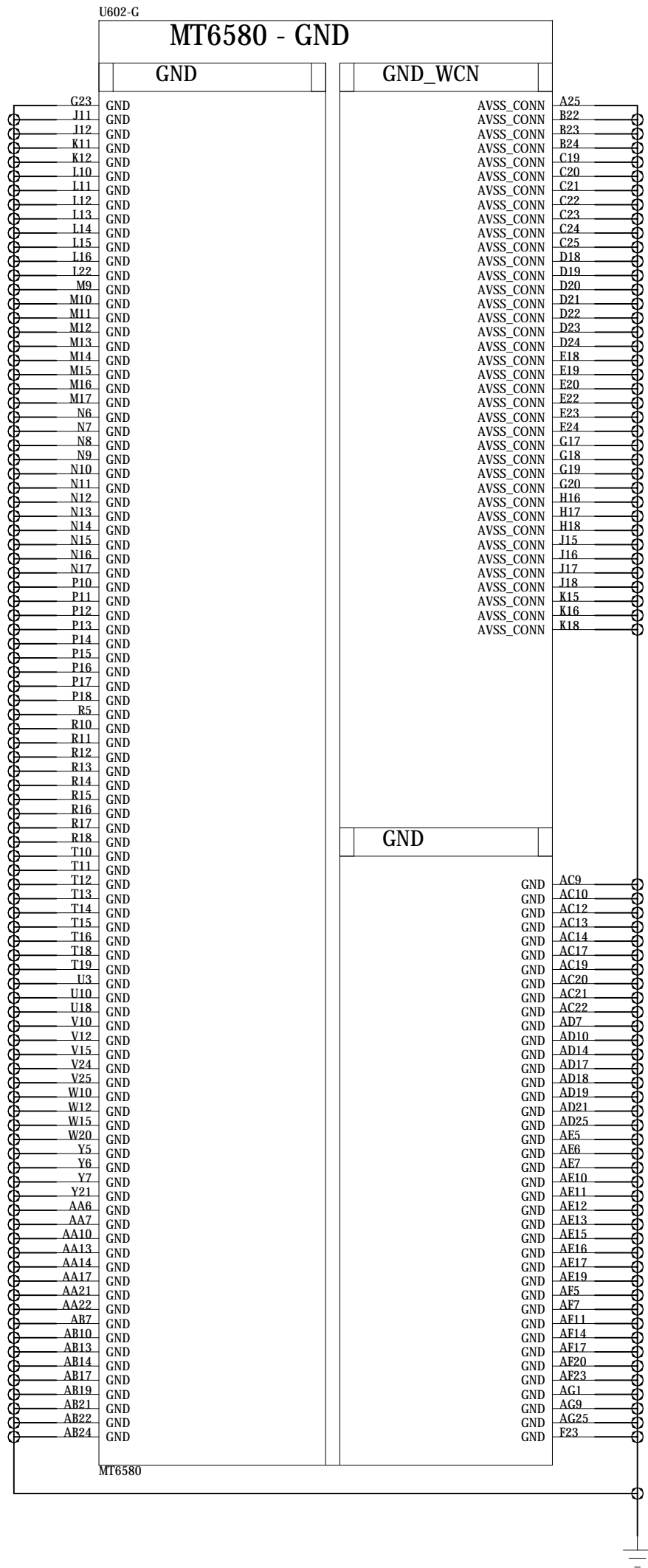
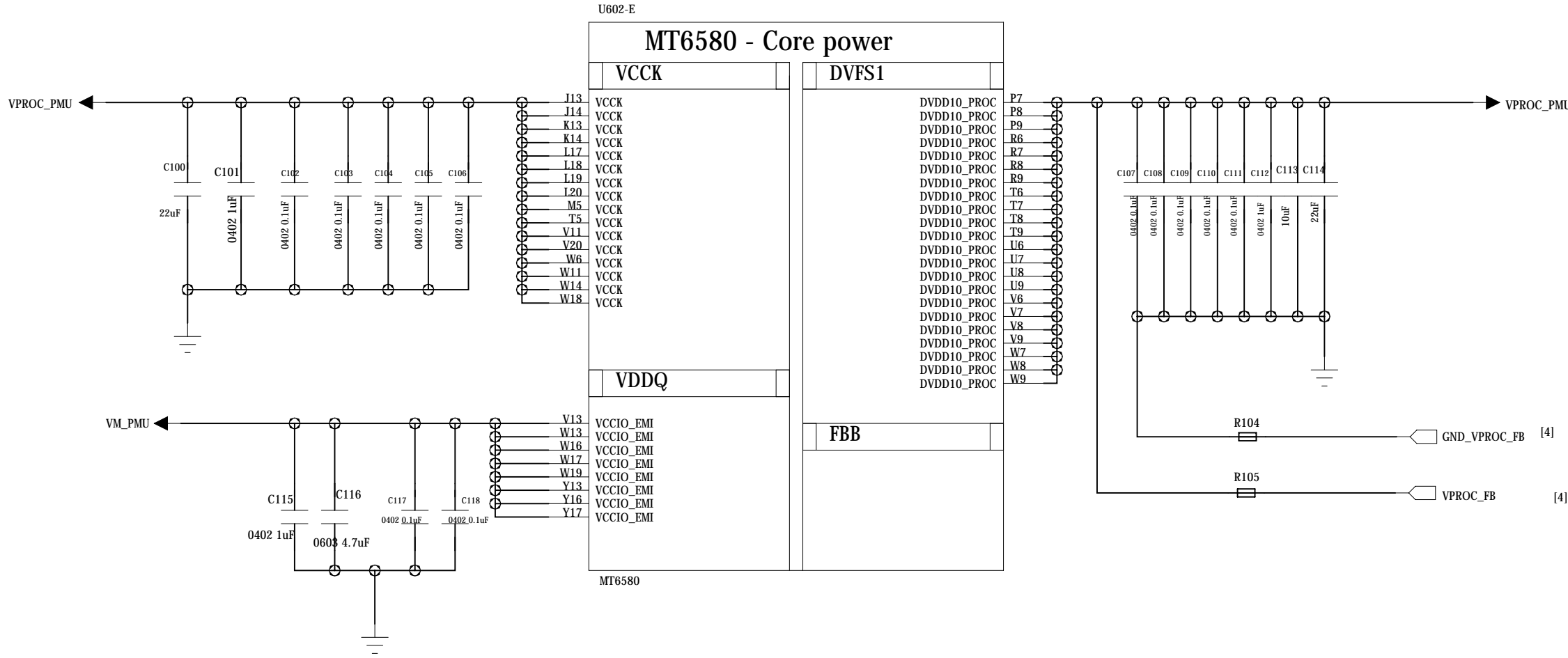


REVISION RECORD			
UTR	ECO NO.	APPROVED:	DATE:

Schematic design notice of "10_BB_POWER" page.

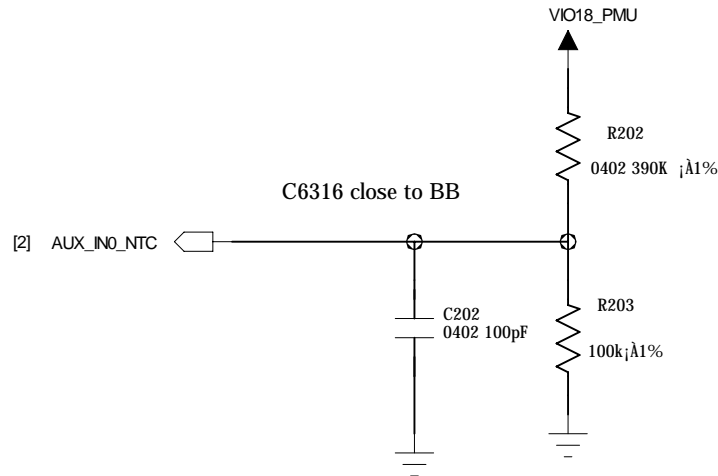


COMPANY:		VSUN	
TITLE:			
V5501			
CODE:	SIZE:	DRAWING NO:	REV:
CODE	A1	Drawing Number	P1
SCALE: <Scale>		SHEET: 08	12

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:

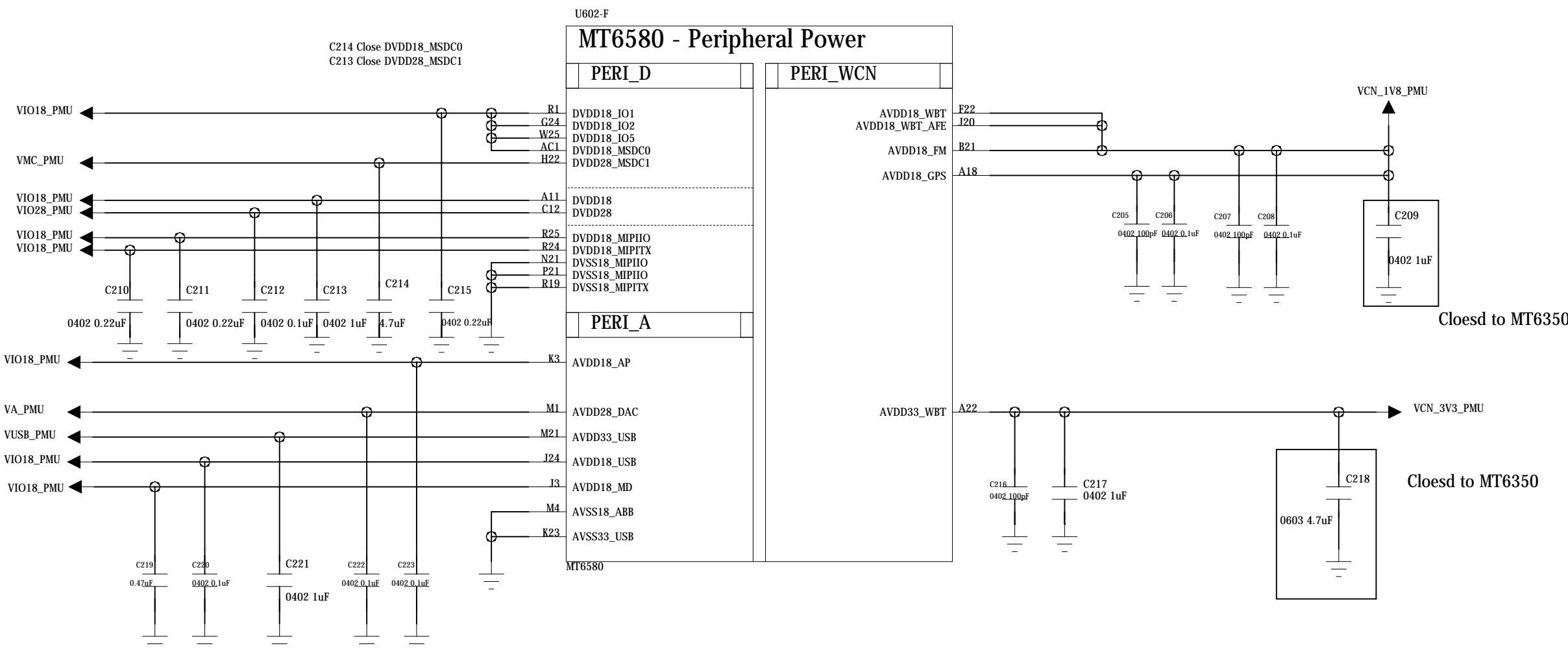
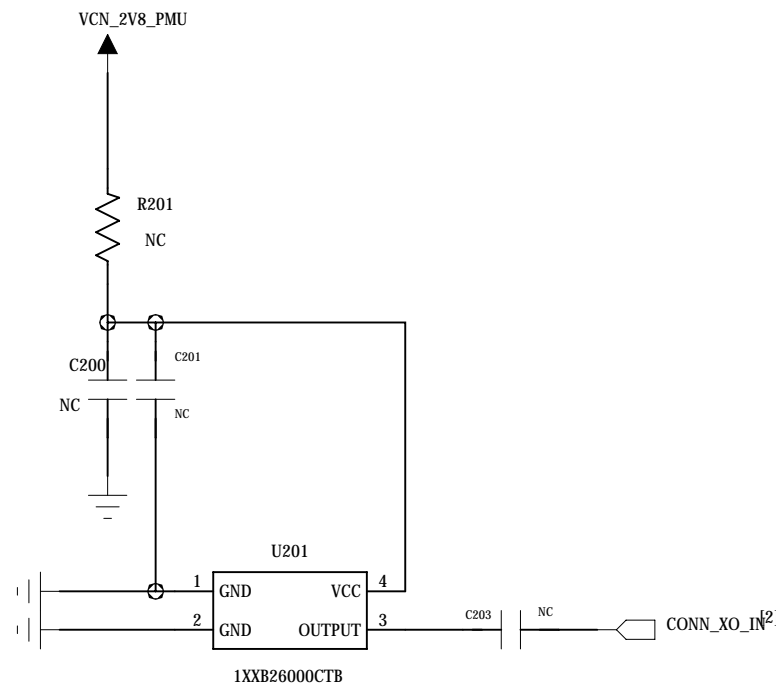
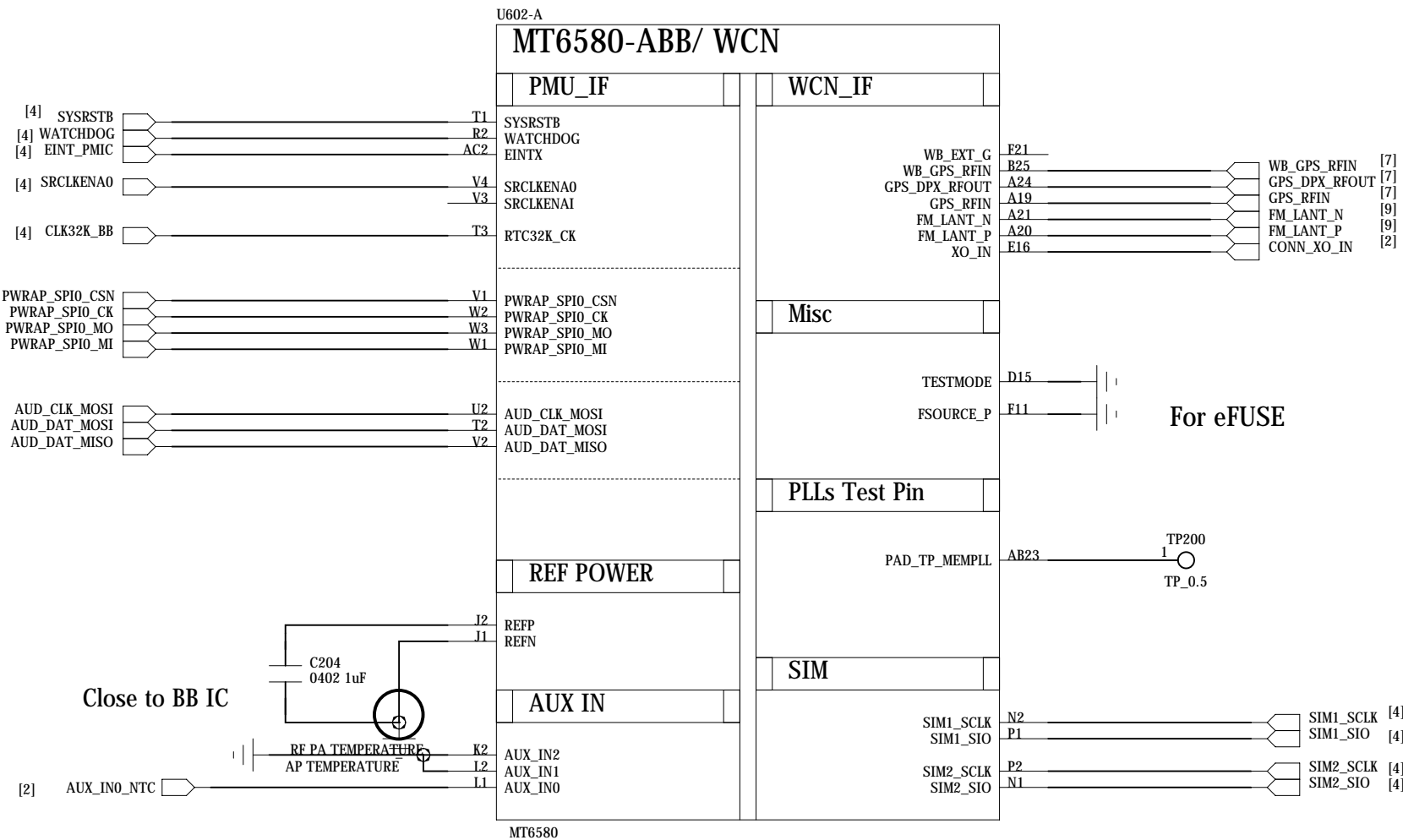
CONNSYS TCXO

Thermistor / To sense board level temperature



NTC6301 close to BB ,and located in the same layer
(can refer to thermal design notice)

NTC6302 close to PA , and located in the same layer



AVDD28_DAC change power source by
"VA_PMU".

COMPANY: VSUN				
TITLE: V5501				
DRAWN: YLW	DATED: 2016/03/18	CODE:	SIZE: A1	DRAWING NO: Drawing Number
CHECKED: <Checked By>	DATED: <Checked Date>	QUALITY CONTROL: <QC By>	DATED: <QC Date>	REV: P1
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	SHEET: 2r	12

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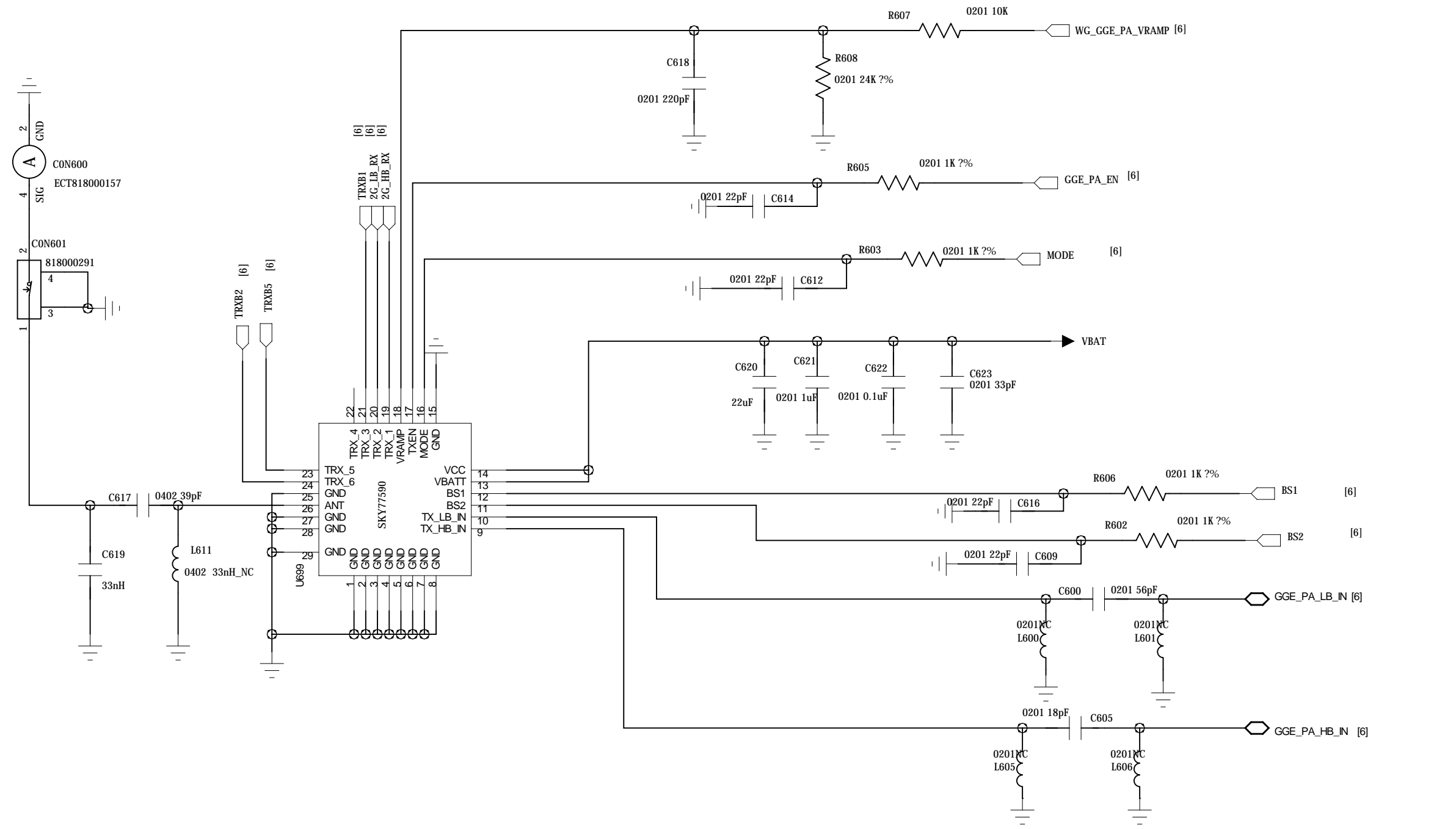
4

3

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1

REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



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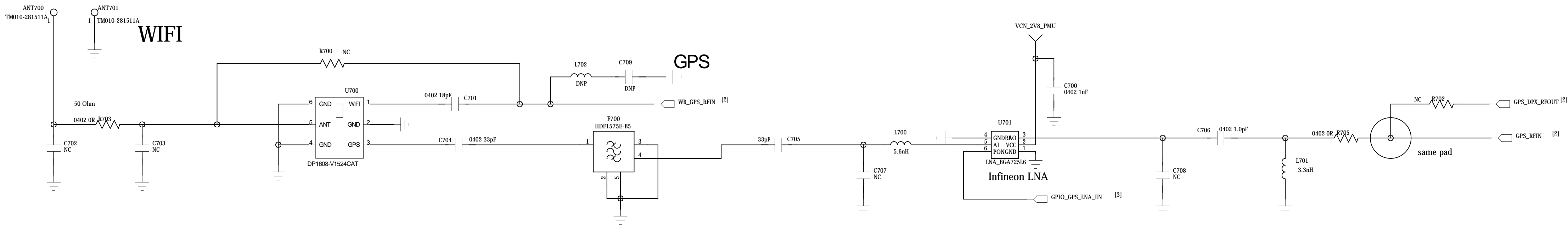
4

3

2

1

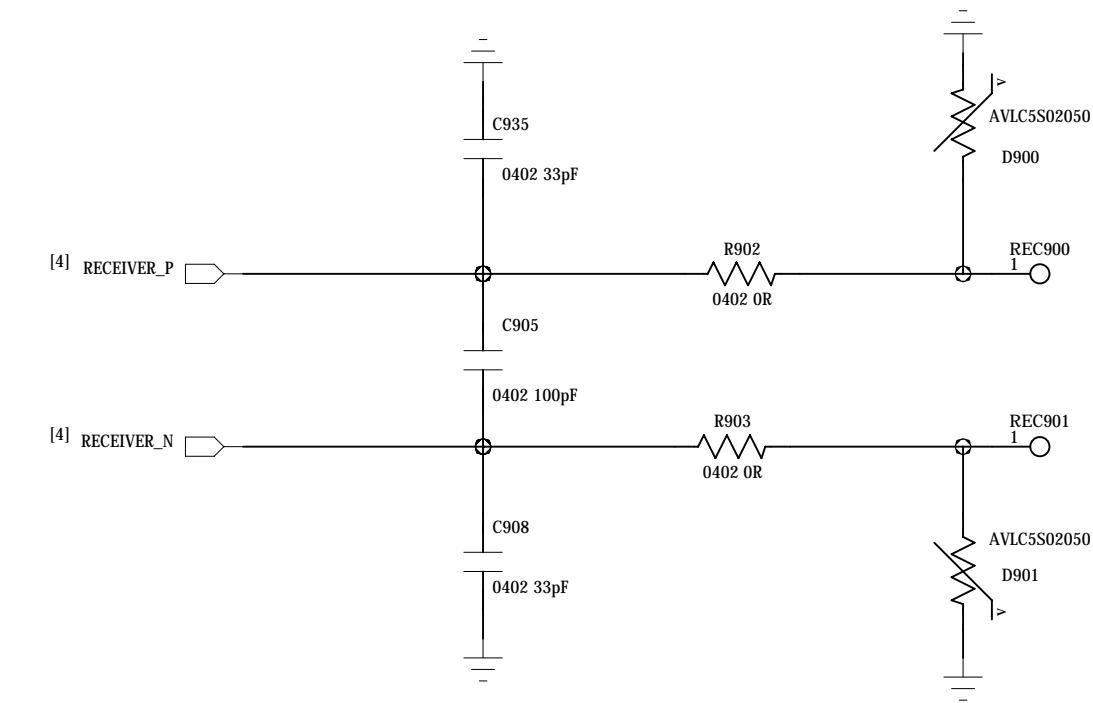
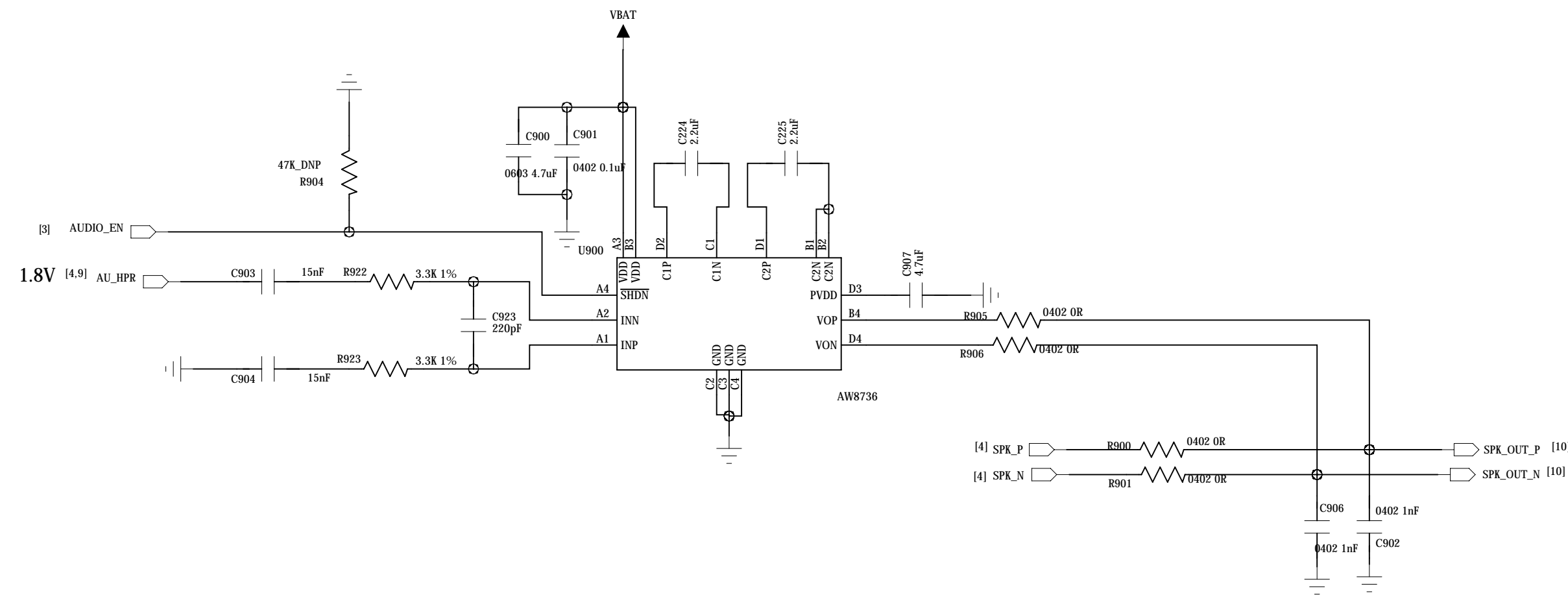
REVISION RECORD			
LTR	ECO NO.	APPROVED:	DATE:



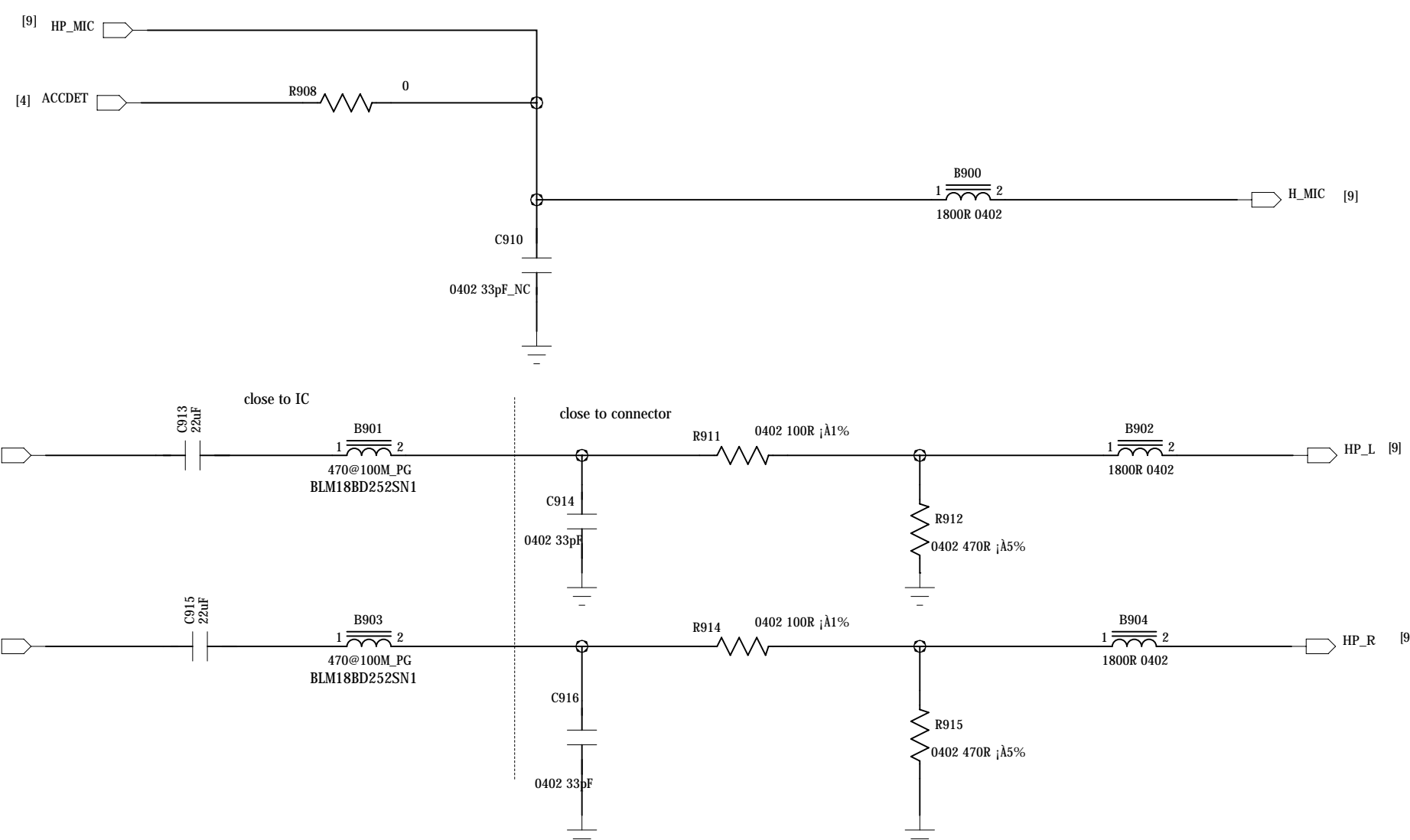
Wireless Connectivity

DRAWN:	YIW	DATED:	2016/03/18
CHECKED:	<Checked By>	DATED:	<Checked Date>
QUALITY CONTROL:	<QC By>	DATED:	<QC Date>
RELEASED:	<Released By>	DATED:	<Release Date>

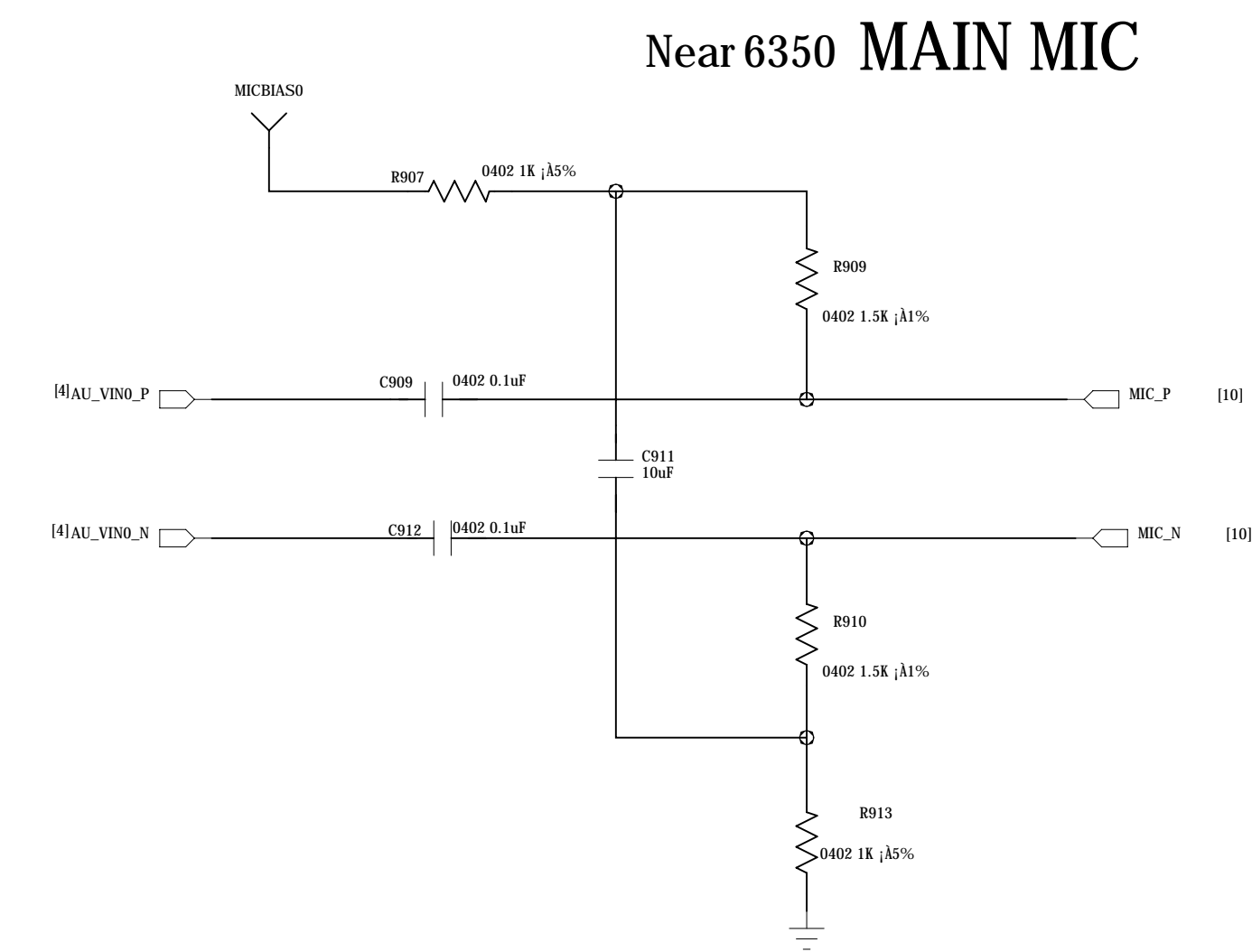
COMPANY: VSUN			
TITLE: V5501			
CODE:	SIZE:	DRAWING NO:	REV:
CODE	A1	Drawing Number	P1
SCALE: <Scale>			SHEET: 8# 12



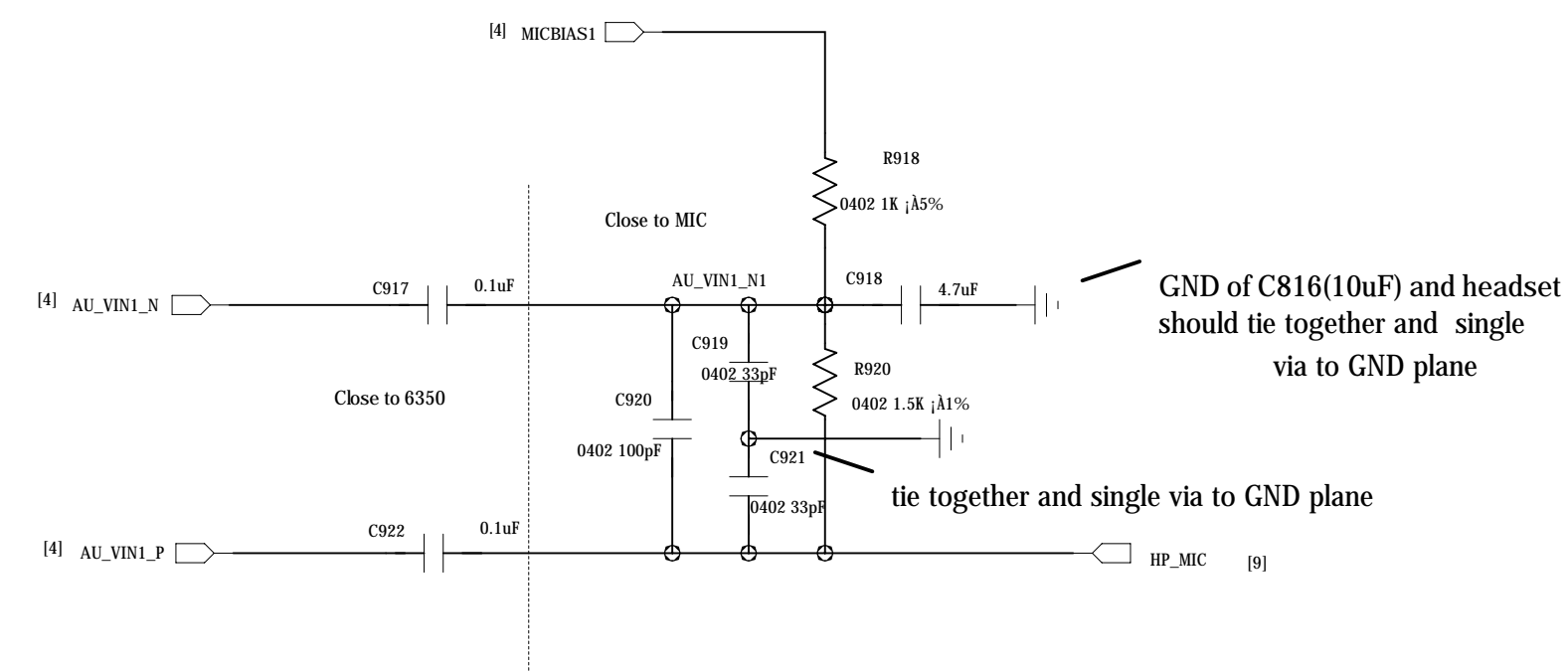
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



Earphone MICPHONE

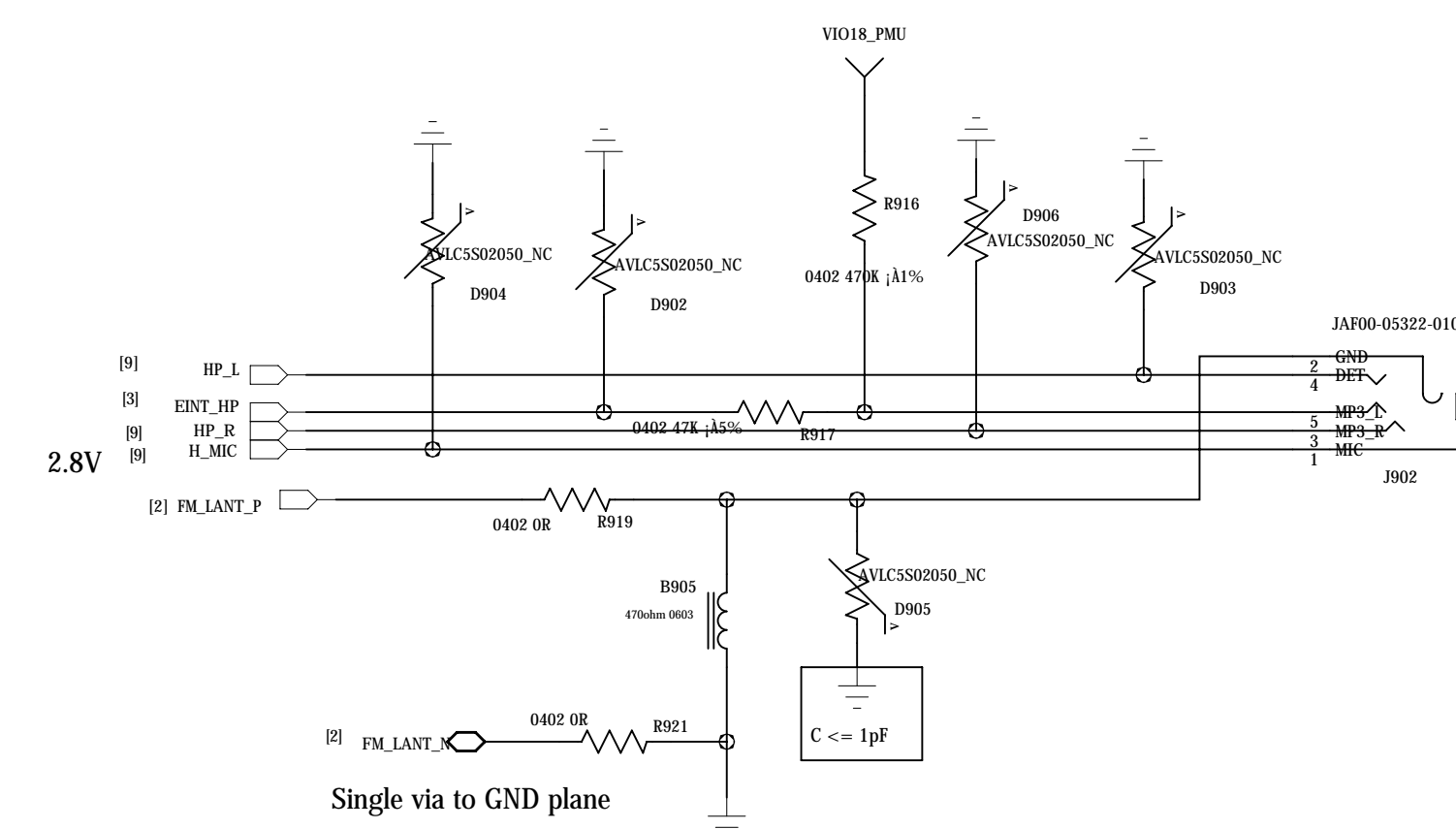


Near 6350 MAIN MIC

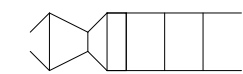


GND of C816(10uF) and headset
should tie together and single
via to GND plane

tie together and single via to GND plane



Same power domain to EARPHONE_DET



MIC Spec. : L-R-G-M

DRAWN: YLW		DATE: 2016/03/18		V5501				
CHECKED: <Checked By>		DATE: <Checked Date>		CODE:	SIZE:	DRAWING NO:		REV:
QUALITY CONTROL: <QC By>		DATE: <QC Date>		CODE	A1	Drawing Number		P1
RELEASED: <Released By>		DATE: <Release Date>		SCALE: <Scale>			SHEET: 09 12	

COMPANY: VSUN

TITLE:	V5501
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e>	CODE:	SIZE:	DRAWING NO:	REV:
	CODE	A1	Drawing Number	P1

>	SCALE: <Scale>	SHEET: 9F	12
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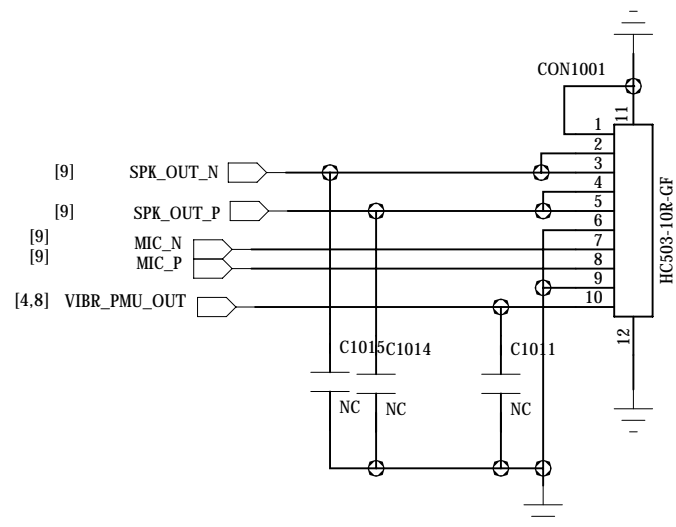
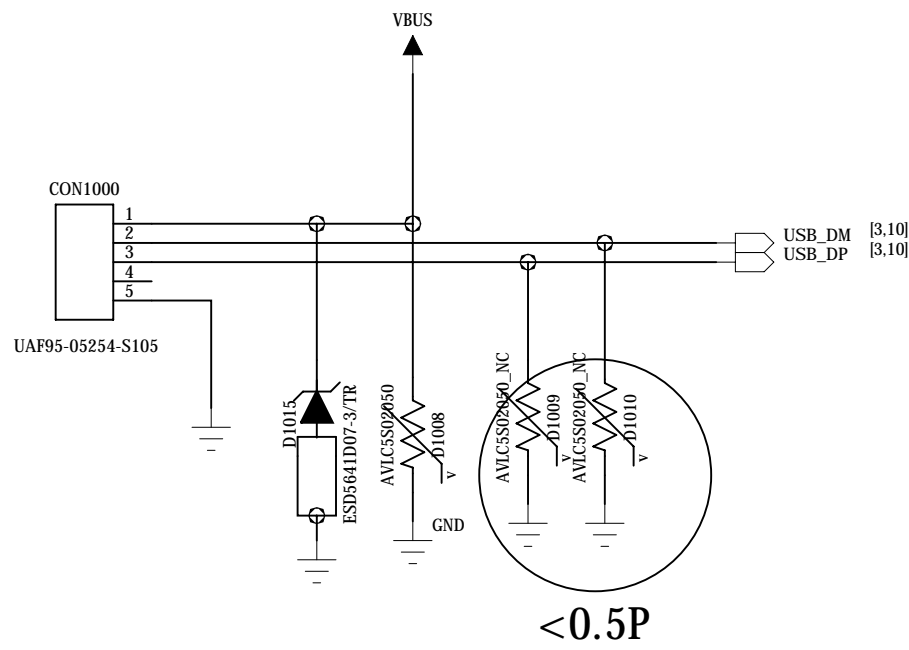
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

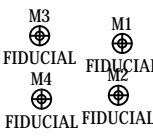
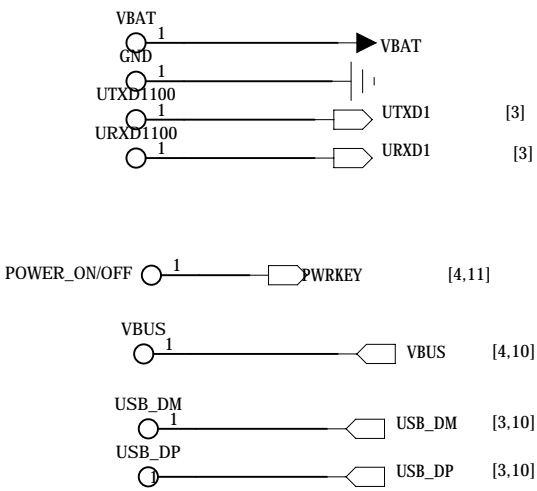
D

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C



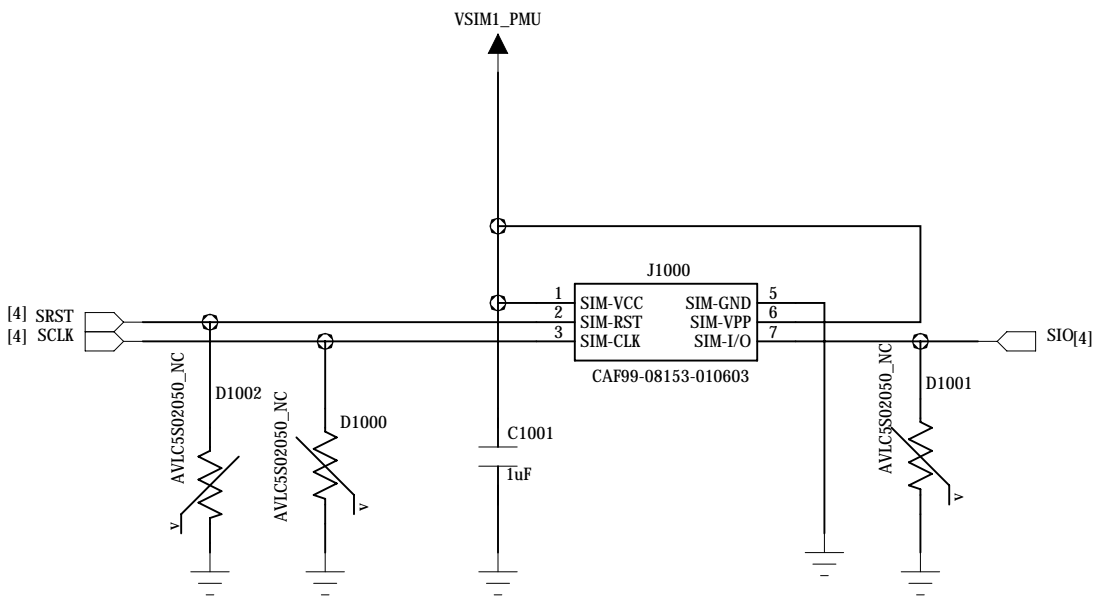
Test Point



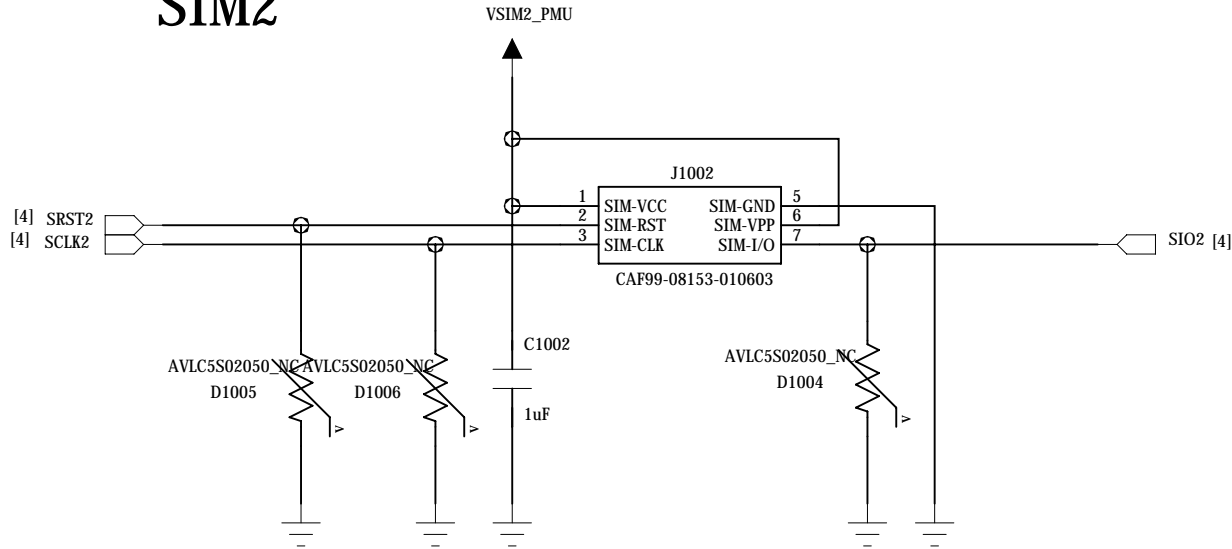
B

B

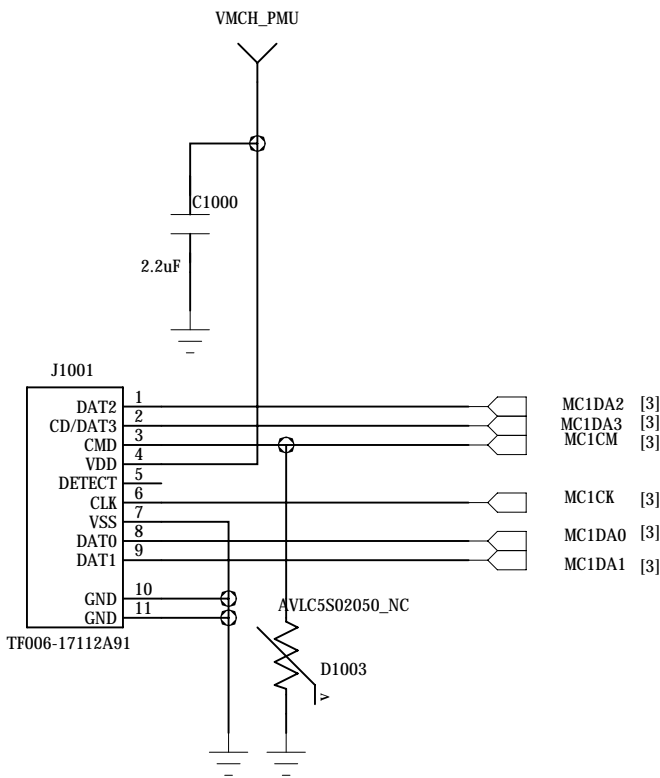
SIM1



SIM2



T-CARD



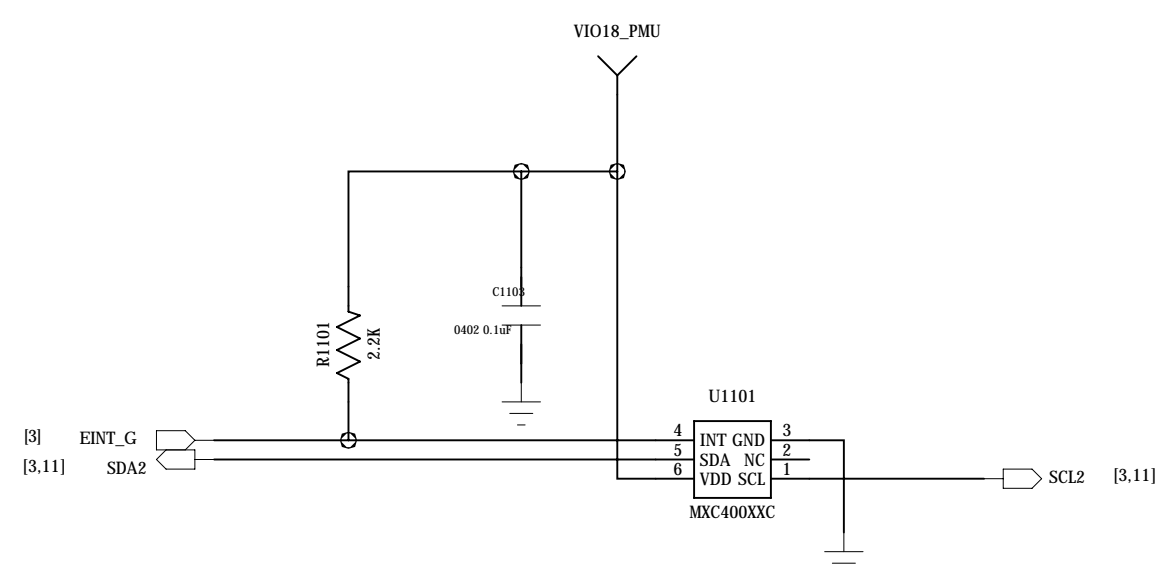
A

A

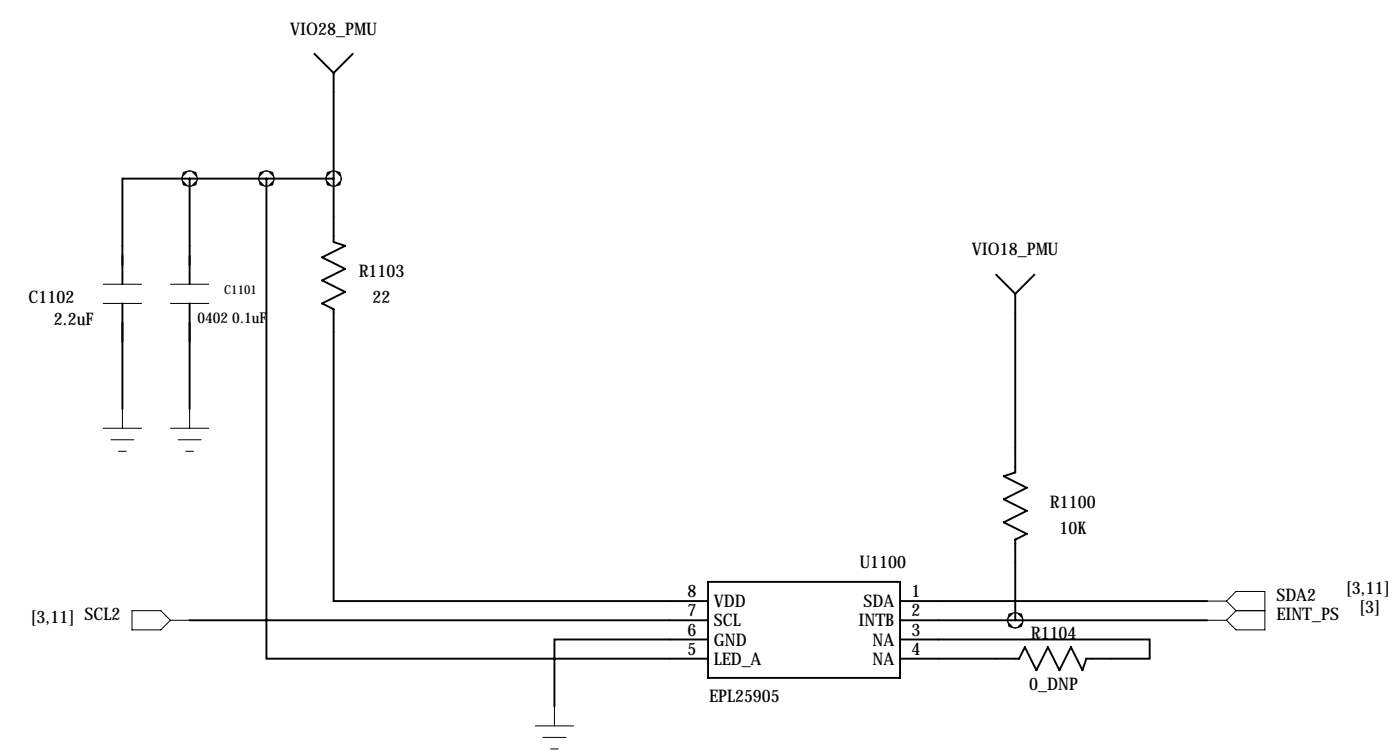
COMPANY: VSUN			
TITLE: V5501			
DRAWN: YLW	DATED: 2016/03/18	CODE: CODE	SIZE: A1
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO: Drawing Number	REV: P1
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: <Scale>	SHEET: 40 12
RELEASED: <Released By>	DATED: <Release Date>		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

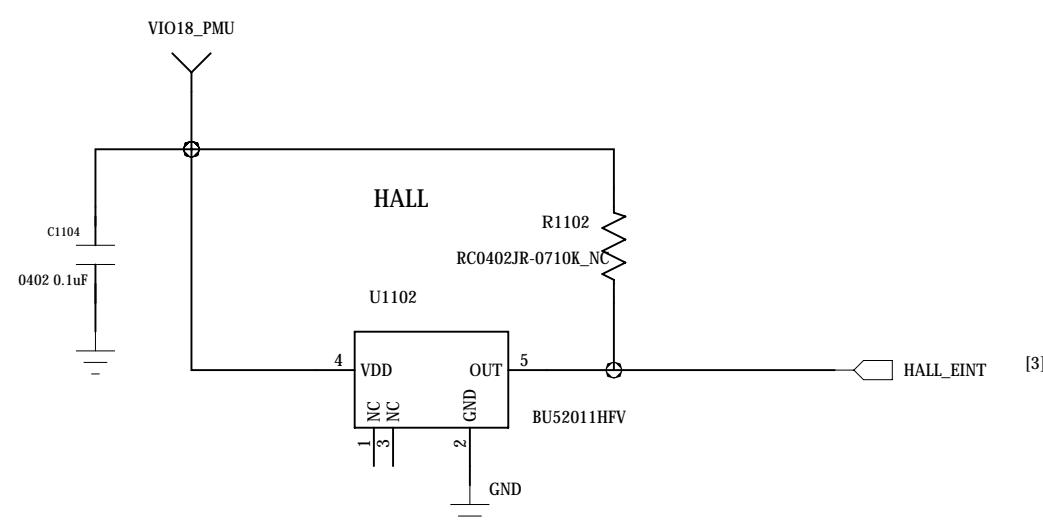
G-Sensor



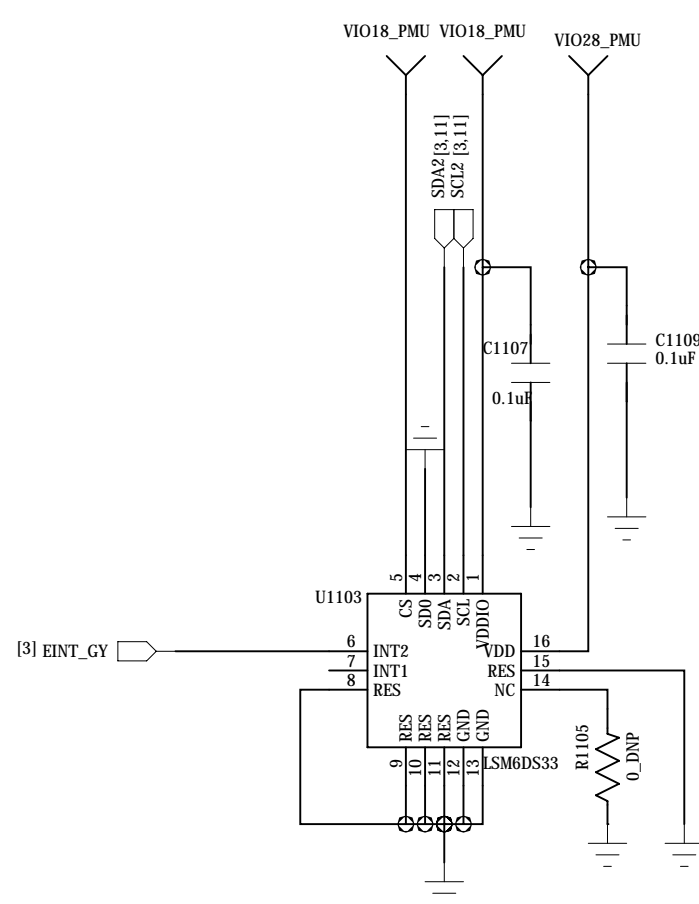
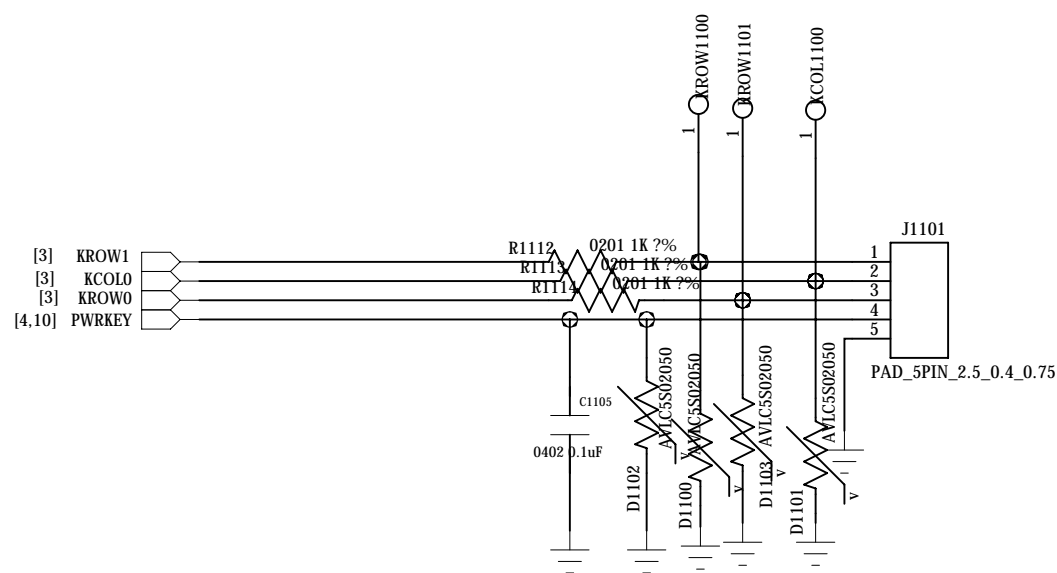
I2C address: 0x0F (Write:0x1E, Read:0x1F)



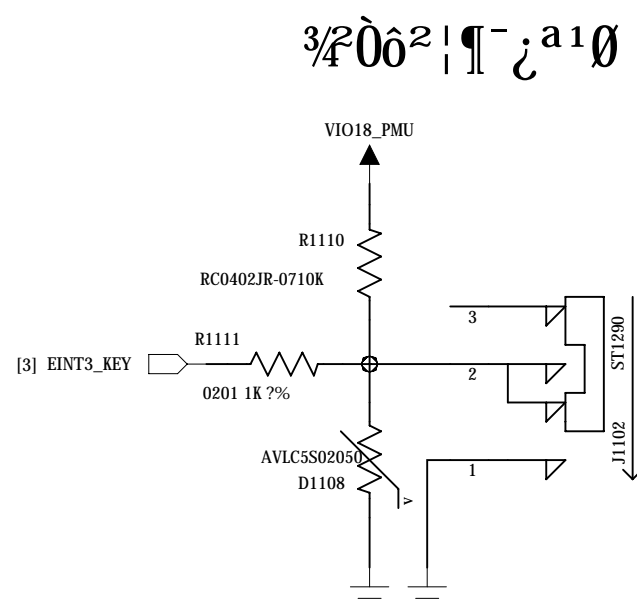
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VOL+/-_KEY POWER_KEY



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		COMPANY: VSUN			
		TITLE: V5501			
DRAWN: YIW	DATED: 2016/03/18	CODE CODE	SIZE: A1	DRAWING NO: Drawing Number	REV: P1
CHECKED: <Checked By>	DATED: <Checked Date>				
QUALITY CONTROL: <QC By>	DATED: <QC Date>				
RELEASED: <Released By>	DATED: <Release Date>				
SCALE: <Scale>				SHEET: bd 12	

DRAWN: YLW		DATED: 2016/03/18		V5501			
CHECKED: <Checked By>		DATED: <Checked Date>					
QUALITY CONTROL: <QC By>		DATED: <QC Date>		CODE: CODE	SIZE: A1	DRAWING NO: Drawing Number	REV: P1
RELEASED: <Released By>		DATED: <Release Date>		SCALE: <Scale>			SHEET: 01 12

CHECKED: <Checked By>	DATED: <Checked Date>	CODE	SIZE A1	DRAWING NO: Drawing Number	REV: P1
QUALITY CONTROL: <QC By>	DATED: <QC Date>				
RELEASED: <Released By>	DATED: <Release Date>				
		SCALE: <Scale>			SHEET: bd 12

