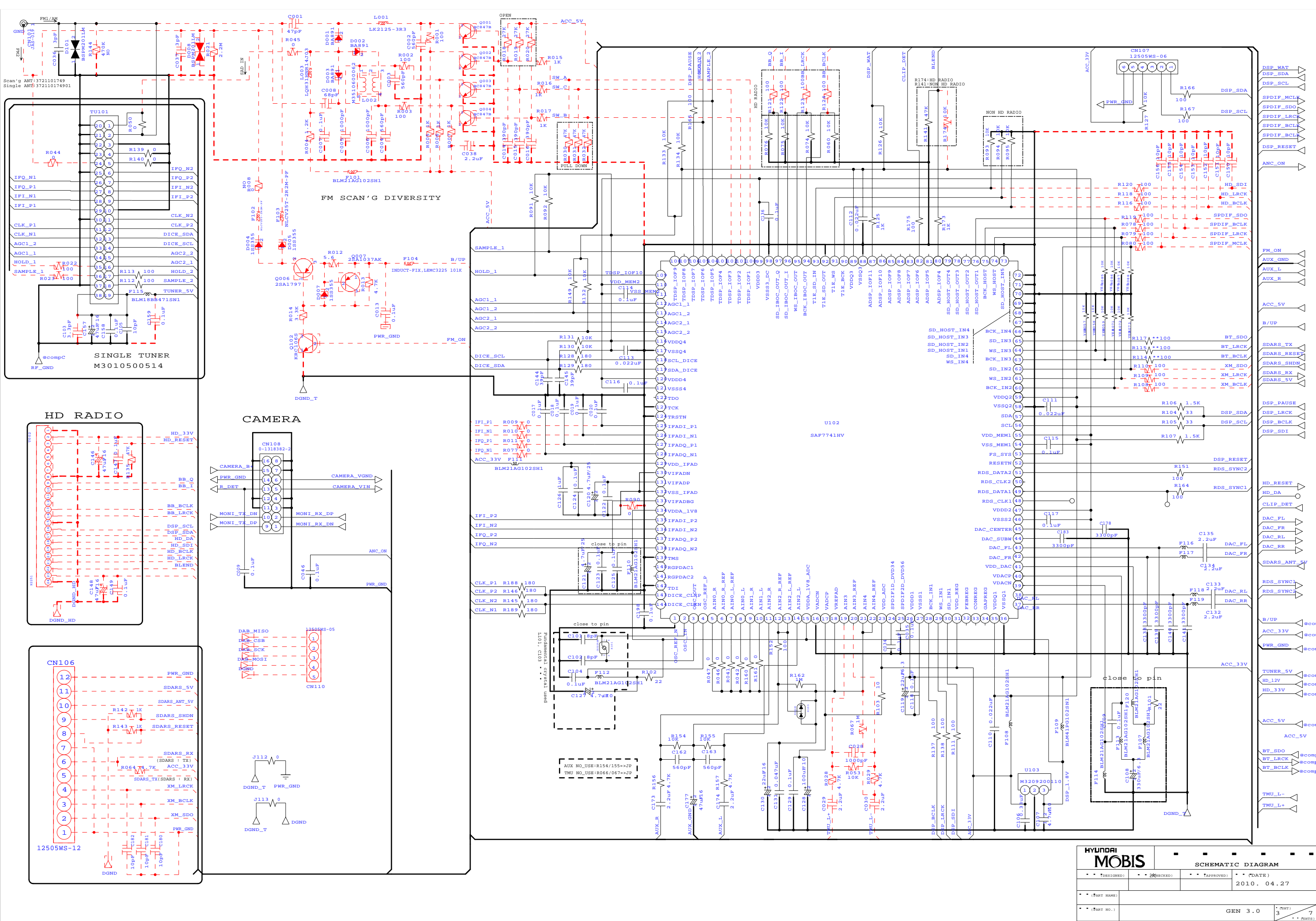
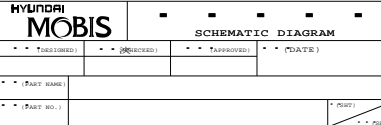
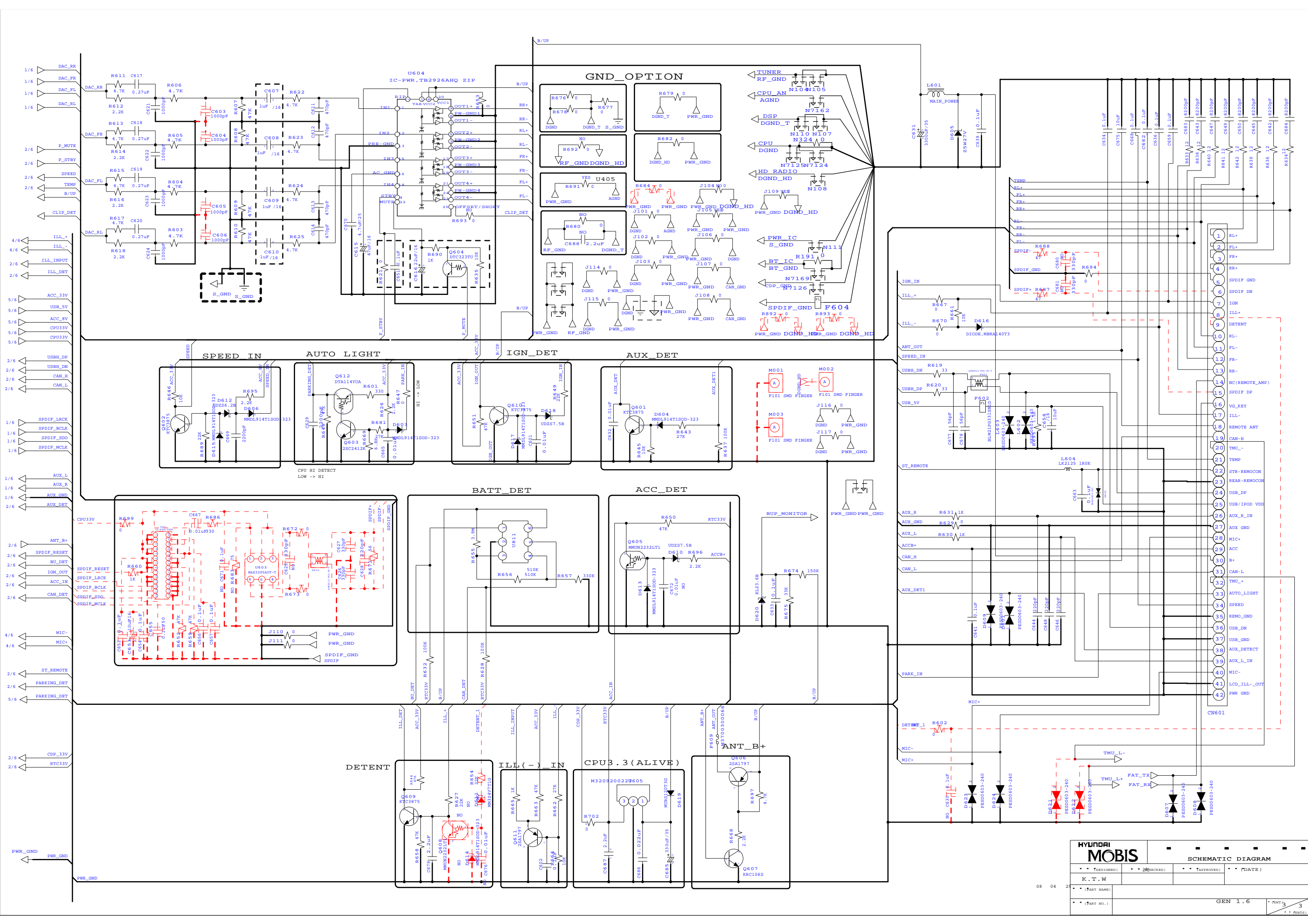


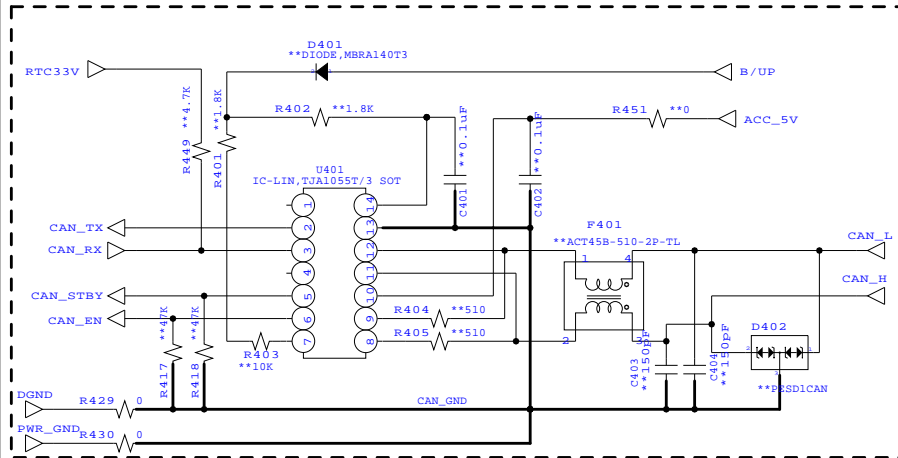
HYUNDAI					
MOBIS		SCHEMATIC DIAGRAM			
* (DESIGNED)	* (CHECKED)	* (APPROVED)	* (DATE)		
S.C.WI	D.S.EO	D.Y.KANG	2011.4.19		
* (PART NAME)	XM_FL B-TYPE AUDIO FRONT PCB				
* (PART NO.)	M3061000703				* (SHEET) 1 / 1



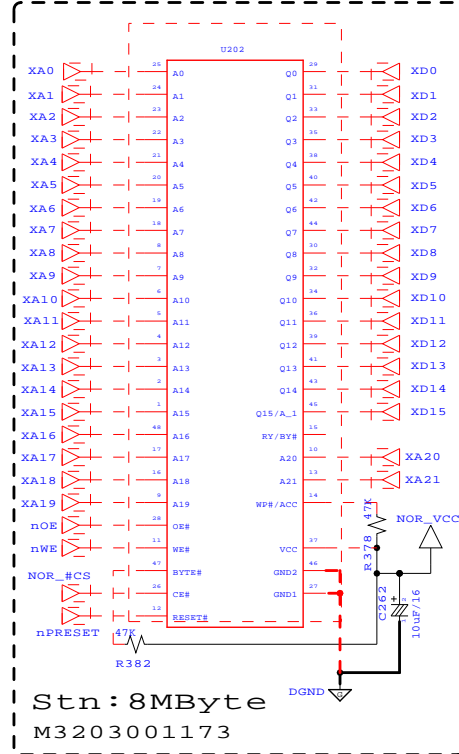




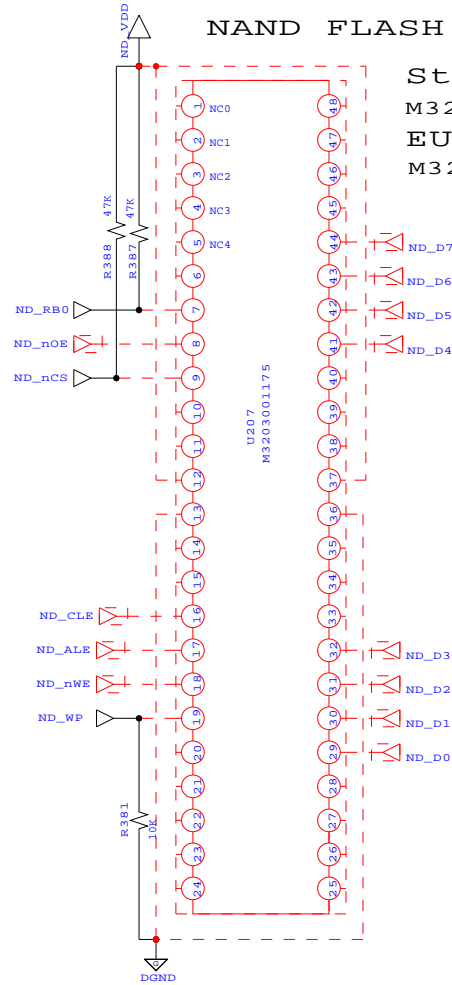
VECTOR_CAN OPTION



NOR FLASH



NAND FLASH

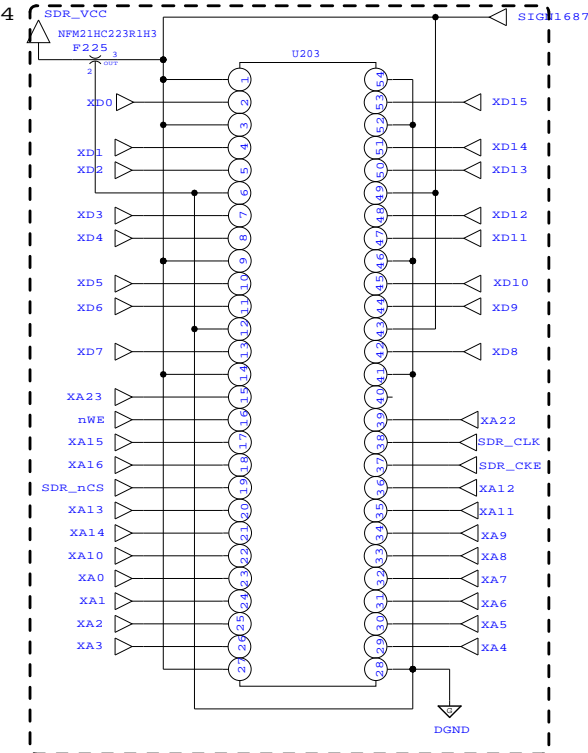


A TYPE:M3203001140 (32MB)

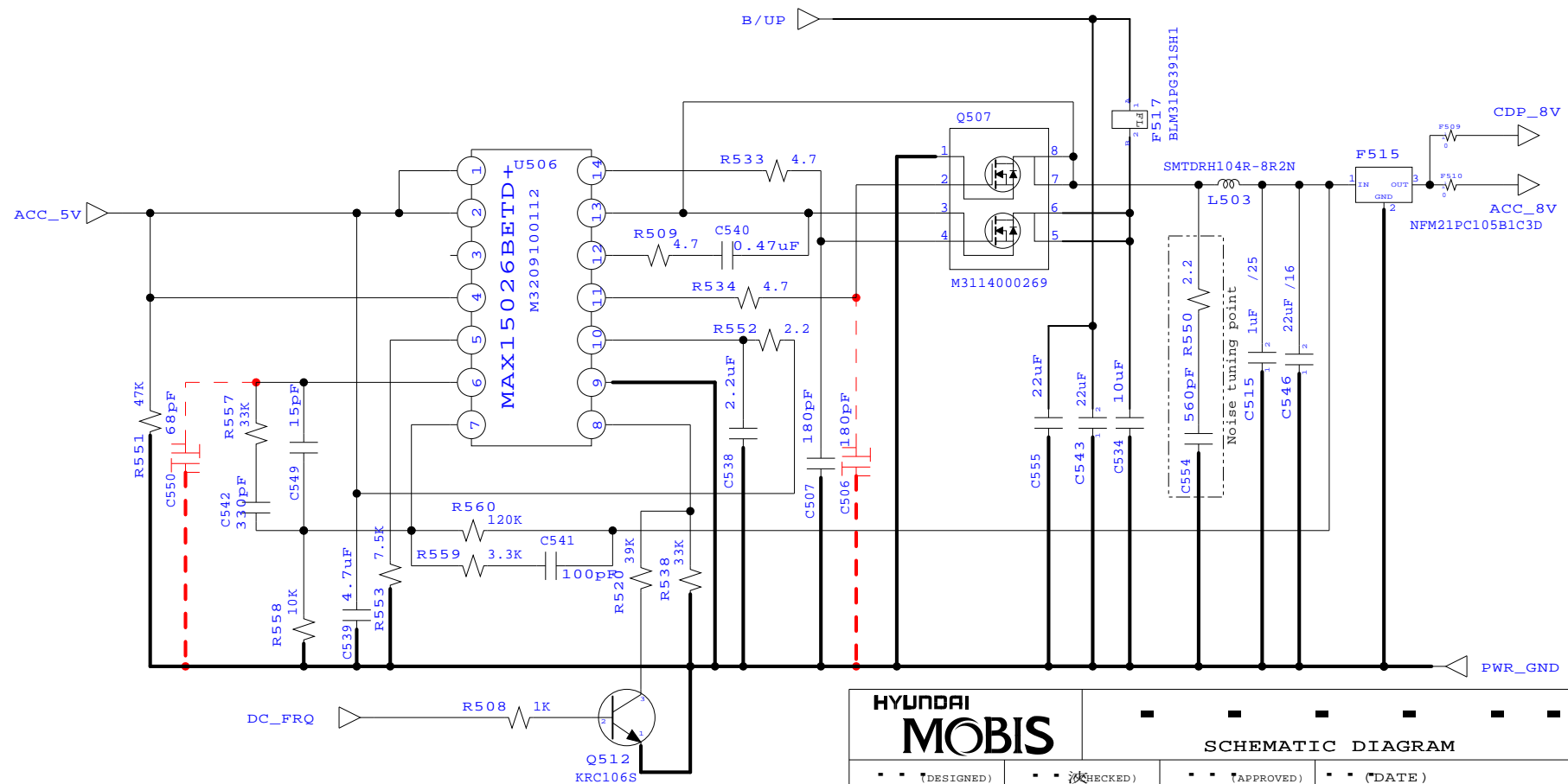
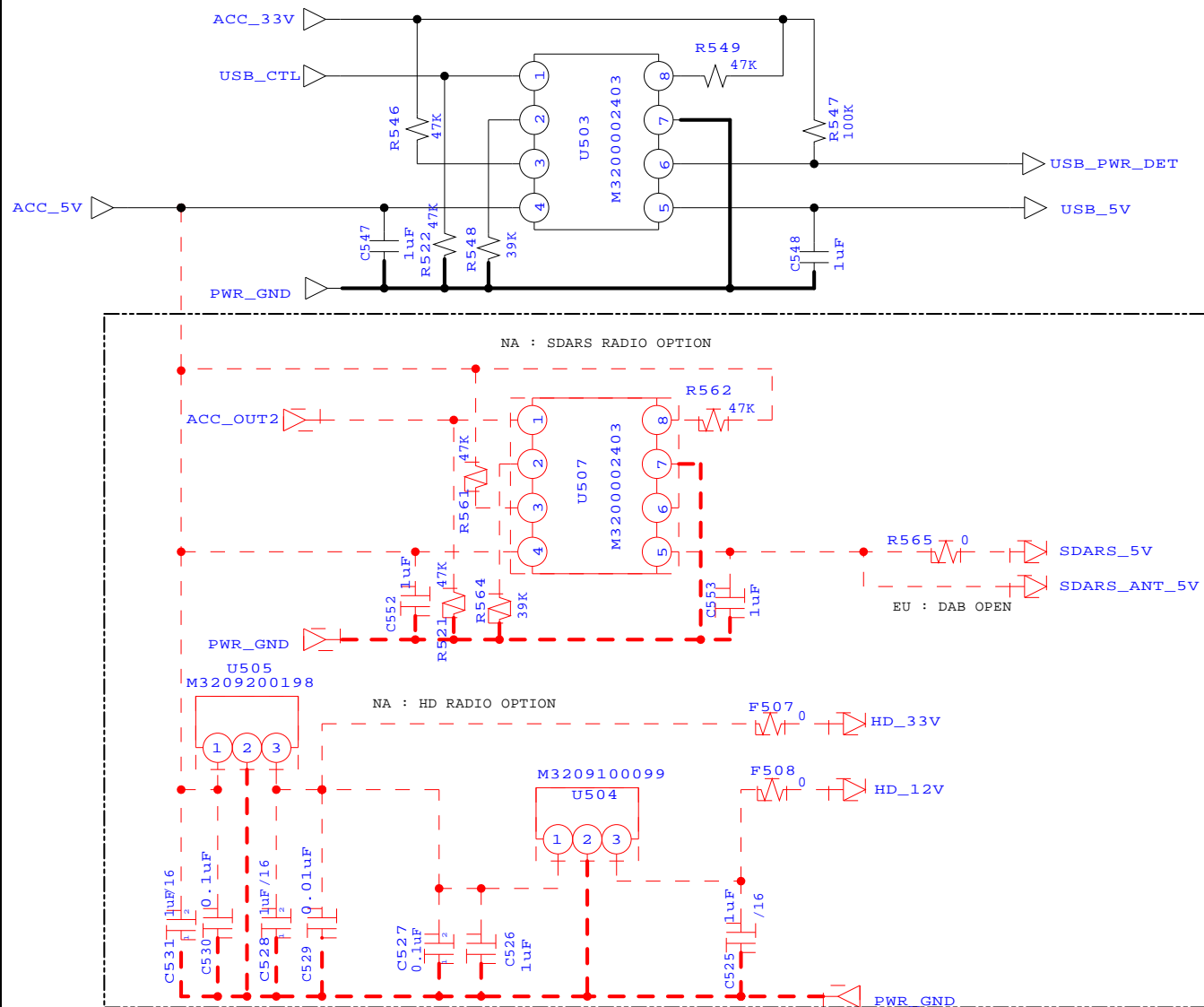
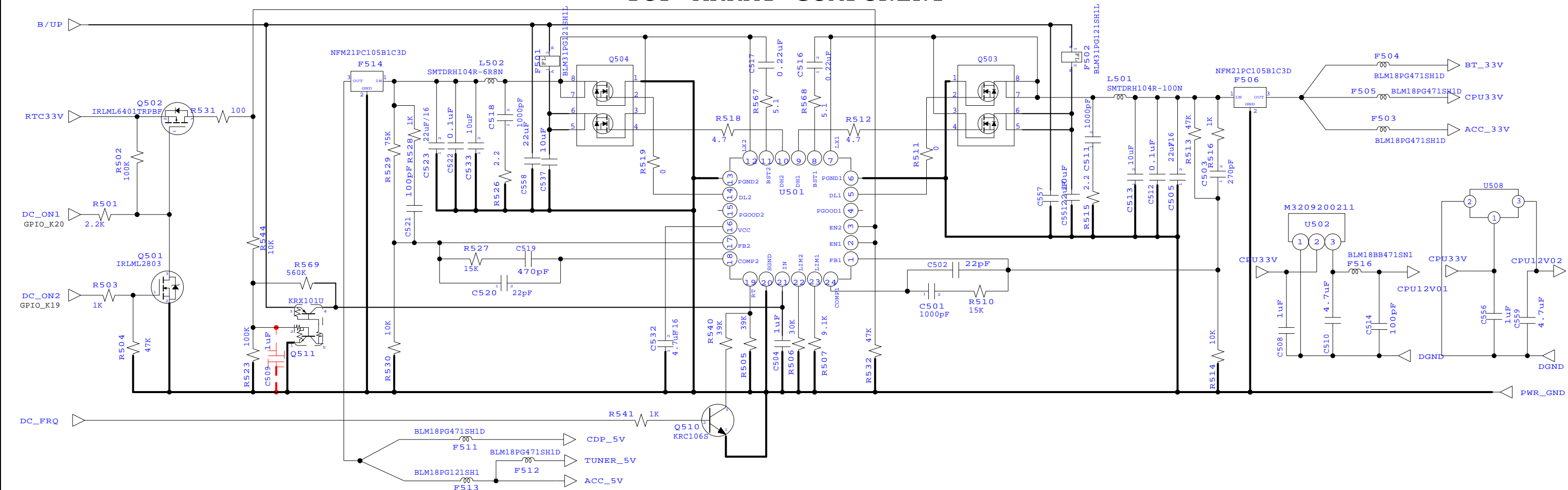
B TYPE:M3203001180 (64MB)


C TYPE:M3203001140 (32MB)

SDRAM



TOP ARRAY COMPONENT



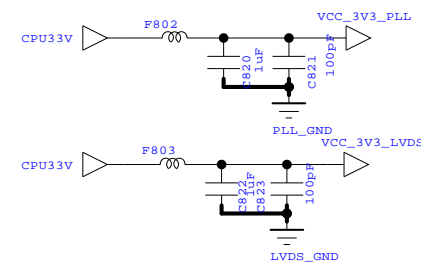
HYUNDAI MOBIS		<div style="text-align: center;">  <p>SCHEMATIC DIAGRAM</p> </div>			
□ □ (DESIGNED)	□ □ (CHECKED)	□ □ (APPROVED)	□ □ (*DATE)		
□ □ (*PART NAME)					
□ □ (*PART NO.)					□ (*SHT) <hr/> □ □ (*SHTS)

The schematic diagram illustrates the camera module interface circuit, organized into three main functional blocks: Power Regulation, Signal Processing, and Video Output.

Power Regulation and Camera_B+ Generation: The circuit starts with power inputs: ACC_8V, ACC_33V, ANC_ON, and PWR_GND. The ACC_33V line passes through a 47K resistor (R801) and is connected to pin 1 of the U801 (M3209200197) IC. The ANC_ON signal is connected to pin 2. The PWR_GND is connected to pin 3. The output of the IC (pin 4) is connected to the anode of a diode (D803, MBRA140T3). The cathode of D803 is connected to the anode of a MOSFET (F8Q1, BLM31PG121SH1L). The MOSFET's gate is connected to the output of the IC (pin 5). The MOSFET's drain is connected to the CAMERA_B+ output. The MOSFET's source is connected to the PWR_GND. A 4.7uF capacitor (C801) is connected between the ACC_33V line and the PWR_GND. A 4.7uF capacitor (C802) is connected between the output of the IC (pin 4) and the PWR_GND. A 6802-01A diode (C828) is connected between the CAMERA_B+ output and the PWR_GND.

Signal Processing and Camera_INT Generation: The CAMERA_INT output is generated by a circuit involving a 47K resistor (R806) connected to the ACC_33V line. The other end of R806 is connected to the input of a diode (D802, UD2S7.5B). The cathode of D802 is connected to the PWR_GND. The output of D802 is connected to the base of a transistor (Q801, KTC3875). The emitter of Q801 is connected to the PWR_GND. The collector of Q801 is connected to the CAMERA_INT output. A 0.1uF capacitor (C803) is connected between the input of D802 and the PWR_GND. A 0.1uF capacitor (C804) is connected between the output of D802 and the PWR_GND. A 22K resistor (R805) is connected between the output of D802 and the base of Q801. A 0.1uF capacitor (C805) is connected between the CAMERA_INT output and the PWR_GND.


Video Output Stage: The video output stage consists of a differential pair of diodes (D805, PESD0603-240) connected to the CAMERA_VIN and CAMERA_VGND inputs. The output of the differential pair is connected to the CAMERA_VOUT output. A 330pF capacitor (C806) is connected between the CAMERA_VIN and CAMERA_VGND inputs. A 75 ohm resistor (R894) is connected between the CAMERA_VOUT output and the PWR_GND. A 0 ohm resistor (R802) is connected between the PWR_GND and the PWR_GND. A 0 ohm resistor (R807) is connected between the PWR_GND and the PWR_GND. A 0 ohm resistor (R808) is connected between the CAMERA_VOUT output and the PWR_GND.

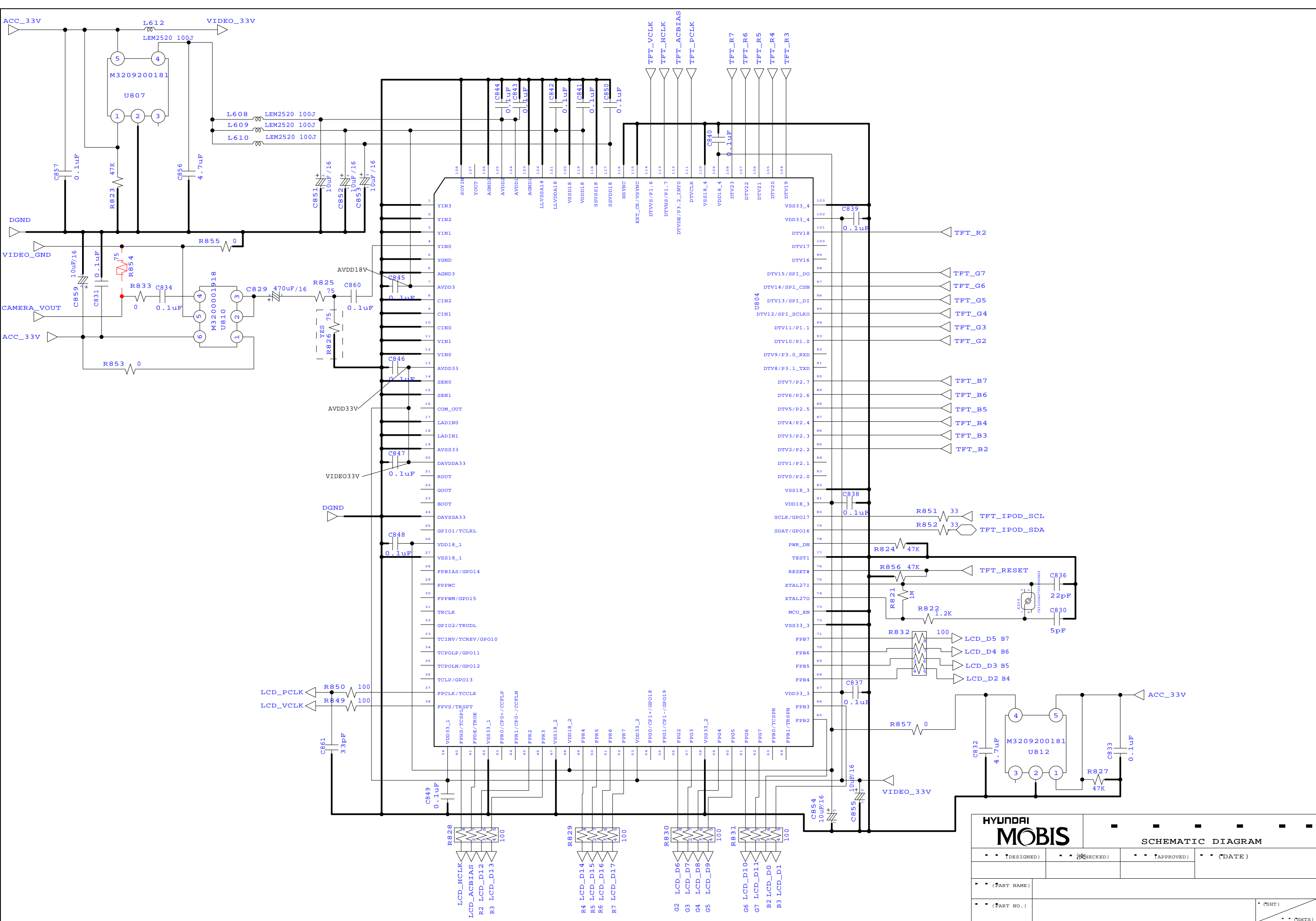


100 OHM DIFFERENTIAL STP CABLE

LVDS Serializer

- ```
- REAR CAMEAR "
- GEN3.0 AUDIO "TFT LCD" REAR CAMERA "
- REAR CAMERA POWER
- CAMERA DETECT "(DETECT HIGHT/LOW DETECT
- CAMERA VIDEO SIGNAL BUFFER ("BUFFER"
```

|                         |               |                                                                                       |                        |
|-------------------------|---------------|---------------------------------------------------------------------------------------|------------------------|
| HYUNDAI<br><b>MOBIS</b> |               |  |                        |
| SCHEMATIC DIAGRAM       |               |                                                                                       |                        |
| • • (DESIGNED)          | • • (CHECKED) | • • (APPROVED)                                                                        | • • (DATE)             |
|                         |               |                                                                                       |                        |
| • • (PART NAME)         |               |                                                                                       |                        |
| • • (PART NO.)          |               |                                                                                       | • • (SHT)<br>• • (SHT) |



|                  |         |                   |      |
|------------------|---------|-------------------|------|
| HYUNDAI<br>MOBIS |         | SCHEMATIC DIAGRAM |      |
| DESIGNED         | CHECKED | APPROVED          | DATE |
| PART NAME        |         |                   |      |
| PART NO.         |         | SHT               |      |