

Circuit Description

U1 (BC4131569A10) use CSR chipset, this chipset provides a fully compliant Bluetooth system to V2.0+EDR of the specification for data and voice communications. It integrated switch-mode regulator and battery charger with programmable.

U2 is an EEPROM, it is required to store the HMI configuration data.

LD1, LD2 indicate the headset working mode

SW1, SW2, SW3 are human control interface, It control the headset difference operation

The RF circuit is consist of C11, C21, C22, L1, L4, L5, T1, F1, C15, ANT1