



Bluetooth Module

NF2301 User Manual

JS88

淇譽電子科技股份有限公司 JAZZ HIPSTER CORPORATION

Product Description

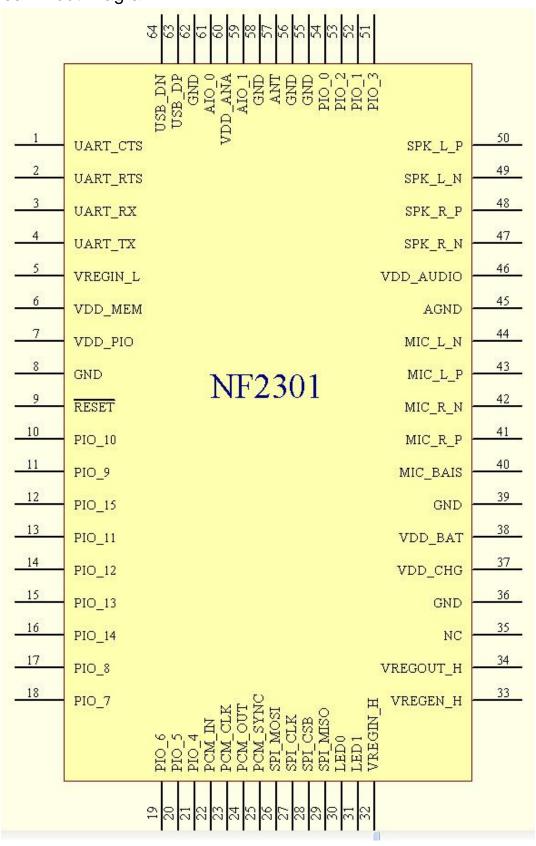
The NF2301 is a highly integrated Bluetooth 2.1+EDR stereo module, designed for high data rate, short-range wireless communication in the 2.4 GHz ISM band. With Bluetooth stack and profile, the NF2301 provides a low power and ultra-low cost Bluetooth 2.1+EDR solution for wireless voice/audio applications.

Features & Specification

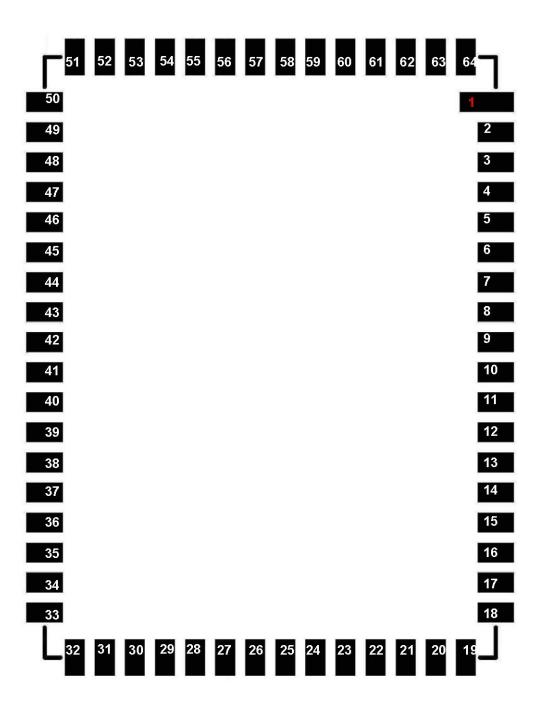
- _ Main Chip: CSR BlueCore5
- _ Bluetooth 2.1+EDR compliant,
- _ Build in Bluetooth Stack, Profiles and DSP Audio/Speech solution
- _ Typical +2dBm Class 2 output power
- _ Receiver Sensitivity: GFSK typical -90dBm
- _ DSP Co-Processor 16-bit Internal Stereo CODEC 95dB SNR for DAC
- _ Supports up to 16 Mbits on module Flash Memory
- _ USB and UART with Dual Port Bypass Mode up to 4Mbits/s
- _ Enhanced Audibility, DSP post procession
- _ Support for 802.11 Co-existence
- _ Audio DAC: 92dB SNR
- _ Microphone Input: -66dB THD
- _ Build in Max. 100mAH Li-ion battery charger
- _ HSP, HFP, A2DP, AVRCP profile support
- Low-Power 1.5V Operation, 1.8V to 3.6V I/O
- _ Integrated 1.5V and 1.8V Linear Regulators
- _ ROM version: 32Kb EEPROM
- _ 64 pins for SMT module
- _ External PCB Antenna
- _ Frequency Range : 2402-2480MHz
- _ Max. Output Power : 0dBm
- _ Number of Channel: 79



Device Pinout Diagram









Pin Definition

Pin#	Pin Name	Pad Type	Description
1	UART_CTS	CMOS input with weak internal pull-down	UART clear to send active low
2	UART_RTS	Bi-directional CMOS output, tri-state, with weak internal pull-up	UART request to send active low
3	UART_RX	CMOS input with weak internal pull-down	UART data input
4	UART_TX	Bi-directional CMOS output, tri-state, with weak internal pull-up	UART data output
5	VDD_MEM	VDD Regulator input	Input to internal low-voltage linear regulator for non-audio core circuitry (1.8V~2.7V)
6	VREGIN_L		Positive supply for Flash pads (2.7V~3.6V)
7	VDD_PIO	VDD	Positive supply for PIO,AUX DAC and SPI/PCM ports (3.1V~3.6V)(1)
8	GND	Ground	Digital Ground
9	RESET#	CMOS input with weak internal pull-up	Reset if low. Input debounced so must be low for
9	NLOL I#		>5ms to cause a reset
10	PIO[10]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
11	PIO[9]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
12	PIO[15]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
13	PIO[11]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
14	PIO[12]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
15	PIO[13]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
16	PIO[14]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
17	PIO[8]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
18	PIO[7]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
19	PIO[6]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line



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20	PIO[5]	Bi-directional with programmable strength internal	Programmable input/output line
		pull-up/down	
21	PIO[4]	Bi-directional with programmable strength internal	Programmable input/output line
		pull-up/down	
22	PCM_IN	CMOS input, with weak internal pull-down	Synchronous data input
23	PCM_CLK	Bi-directional with weak internal pull-down	Synchronous data clock
24	PCM_OUT	CMOS output, tri-state, with weak internal	Synchronous data output
٥٢	DOM CVNO	pull-down	Consequences deta como
	PCM_SYNC	Bi-directional with weak internal pull-down	Synchronous data sync
26	SPI_MOSI	CMOS input, with weak internal pull-down	SPI data input
27	SPI_CLK	Input with weak internal pull-down	SPI clock
28	SPI_CSB	Input with weak internal pull-up	Chip select for Serial Peripheral Interface
	0002	при	(SPI),active low
29	SPI_MISO	CMOS output, tri-state, with weak internal pull-down	SPI data output
30	LED[0]	Open drain output	LED driver
31	LED[1]	Open drain output	LED driver
00	VREGIN_H	Regulator input	Input to internal high-voltage linear regulator
32			(2.5V~4.9V)
20	VREGEN_H	lAnalogue I	Take high to enable high-voltage linear regulator
33			and switch-mode regulator
34	VREGOUT_H	Supply	High-voltage linear regulator output (1.8V out)
35	N.C.	N.C.	Leave unconnected
36	GND	Ground	Digital Ground
07	VDD_CHG	Charger input	Lithium ion/polymer battery charger input
37			(4.5V~6.5V)
	VDD_BAT		Lithium ion/polymer battery positive terminal.
38			Battery charger output and input to switch-mode
			regulator (4.2V out)
39	GND	Ground	Digital Ground
	MIC_BIAS	Analogue	Microphone bias
	MIC_R_P		Microphone input positive, right
	MIC_R_N		Microphone input negative, right
	MIC_L_P		Microphone input positive, left
	MIC_L_N		Microphone input negative, left
	AGND		Analogue Ground
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46	VDD_AUDIO	VDD/Low-voltage regulator output	Positive supply output for audio circuitry and 1.5V regulated output (from internal low-voltage regulator)
47	SPK_R_N	Analogue	Speaker output negative, right
48	SPK_R_P	Analogue	Speaker output positive, right
49	SPK_L_N	Analogue	Speaker output negative, left
50	SPK_L_P	Analogue	Speaker output positive, left
51	PIO[3]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
52	PIO[1]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line (external TXEN)
53	PIO[2]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
54	PIO[0]	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line (external RXEN)
55	GND	Ground	Digital Ground
56	GND	Ground	Digital Ground
57	ANT	RF	RF In/Out
58	GND	Ground	Digital Ground
59	AIO[1]	Bi-directional	Analogue programmable input/ output line
60	VDD_ANA	VDD/Low-voltage regulator output	Positive supply output for analogue circuitry and 1.5V regulated output (from internal low-voltage regulator)
61	AIO[0]	Bi-directional	Analogue programmable input/ output line
62	GND	Ground	Digital Ground
63	USB_DP	Bi-directional	USB data plus with selectable internal 1.5kΩ pull-up resistor
64	USB_DN	Bi-directional	USB data minus

(1)When UART port is not used, VDD_ PIO may be supported 1.7~3.6 voltage.

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FCC Statement

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

Reorient or relocate the receiving antenna.

Increase the separation between the equipment and receiver.

Connect the equipment into an outlet on a circuit different from that to which the receiver is connected. Consult the dealer or an experienced radio/TV technician for help.

FCC Caution: Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate this equipment. This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Radiation Exposure Statement:

This equipment complies with FCC/IC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator & your body. This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter. End-users and installers must be provide with antenna installation instructions and consider removing the no-collocation statement.

End Product Labeling

This transmitter module is authorized only for use in device where the antenna may be installed such that 20cm may be maintained between the antenna and users. The final end product must be labeled in a visible area with the following: "Contains FCC ID: TQYBMNF2301000M and IC: 6233A-BM2301MA".

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Manual Information To the End User

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module. The end user manual shall include all required regulatory information/warming as shown in this manual.

IC Statement: (English & Francian)

IC Regulations: This device complies with Industry Canada license-exempt RSS standard(s). Operation is subject to the following two conditions:

- (1) this device may not cause interference, and
- (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Le présentappareilestconformeaux CNR d'IndustrieCanada applicablesaux appareils radio exempts de licence. L'exploitation estautorisée aux deux conditions suivantes:

- (1) l'appareilne doitpas produirede brouillage, et
- (2) l'utilisateurde l'appareildoitaccepter tout brouillageradioélectriquesubi, mêmesile brouillageestsusceptible d'encompromettrele fonctionnement.

"This Class B digital apparatus complies with Canadian ICES-003.

Cetappareilnumériquede la classeB estconformeàla normeNMB-003 du Canada.

This Category II radiocommunicationdevice complies with Industry Canada Standard RSS-310.

Cedispositifde radiocommunicationde catégoriell respectela normeCNR-310 d'IndustrieCanada.

IMPORTANT NOTE:

IC Radiation Exposure Statement

This device and its antenna(s) must not be co-located or operating in conjunction with any other antenna or transmitter.

l'appareil et son antenne ne doivent pas être co-localisées ou opérant en conjonction avec une autre antenne ou transmetteur.

NCC警語:



經型式認證合格之低功率射頻電機,非經許可,公司、商號使用者均不得擅自變更頻率、加大功率或變更原設計之特性及功能。

低功率射頻電機之使用不得影響飛航安全及干擾合法通信;經發現有干擾現象時,應立 即停用,並改善至無干擾時方得繼續使用。

前項合法通信,指依電信法規定作業之無線電通信。低功率射頻電機須忍受合法通信或工業、科學及醫療用電波輻射性電機設備之干擾。

本模組於取得認證後將依規定於模組本體標示審驗合格標籤,並要求平台廠商於平台上標示『本產品內含射頻模組 (CCAx11LPxxxxTx』