



D1	Key Define
Open	CPI+CPi+Tilt(T2+T1) (+) (-)
Short	F1+F2+Tilt(T2+T1)

S_Line	
SPI 2 Line (SDIO/SCLK)	0 R26=Open=R27 R25=Short=R28
SPI 2 Line (MOSI/MISO/SCLK)	1 R25=Open=R28 R26=Short=R27

Sleep	
8 minute	0 R30=Open=R31 R29=Short=R32
3 minute	1 R29=Open=R32 R30=Short=R31

F3/T2(right)	F4/T1(left)	Cpi default
0 R7=Open=R3 R6=Short=R2	0 R13=Open=R5 R14=Short=R4	1000 (Real)
0 R7=Open=R3 R6=Short=R2	1 R14=Open=R4 R13=Short=R5	800 (Real)
1 R6=Open=R2 R7=Short=R3	0 R13=Open=R5 R14=Short=R4	400 (Real)
1 R6=Open=R2 R7=Short=R3	1 R14=Open=R4 R13=Short=R5	600 (Real)

U1 93C46B

VDD3 2 VCC GND 7

EP CS 3 CS DO 6

BTL / EP SK 4 SK DI 5 EP DIO

Internal DC/DC

External DC/DC

Have R9,R11,R12 No U2,R10

Have U2,R10,R12 No R9,R11

VBT

BT1 2AA*2

C2 220uF

C3 100uH

C5 1uF

R12 4R

D61

RB421

C7 220uF

C6 0.1uF

VDD3

J17L

J17H

J18L

J18H

U2 BL8530-30ISM

SPI DI

SPI CK

NRZ IO

DCVR

R11 100K

VDD3

U3 MA2628

VDD3 1

VSS 2

S4 Line 3

Sleep 4

TYPE1 5

MISO 6

MOSI/SDIO 7

SCLK 8

EP CS 9

F3/T2(right) 10

F4/T1(Left) 11

SW_B 12

Z2 13

Z1 14

SW_F 15

BTL / EP SK 16

SW_ID 17

SW_R 18

SW_M 19

SW_L 20

SW_F 21

SW_B 22

SW_ID 23

SW_R 24

SW_M 25

SW_L 26

SW_F 27

SW_B 28

SW_ID 29

SW_R 30

SW_M 31

SW_L 32

SW_F 33

SW_B 34

SW_ID 35

SW_R 36

SW_M 37

SW_L 38

SW_F 39

SW_B 40

SW_ID 41

SW_R 42

SW_M 43

SW_L 44

SW_F 45

SW_B 46

SW_ID 47

SW_R 48

SW_M 49

SW_L 50

SW_F 51

SW_B 52

SW_ID 53

SW_R 54

SW_M 55

SW_L 56

SW_F 57

SW_B 58

SW_ID 59

SW_R 60

SW_M 61

SW_L 62

SW_F 63

SW_B 64

SW_ID 65

SW_R 66

SW_M 67

SW_L 68

SW_F 69

SW_B 70

SW_ID 71

SW_R 72

SW_M 73

SW_L 74

SW_F 75

SW_B 76

SW_ID 77

SW_R 78

SW_M 79

SW_L 80

SW_F 81

SW_B 82

SW_ID 83

SW_R 84

SW_M 85

SW_L 86

SW_F 87

SW_B 88

SW_ID 89

SW_R 90

SW_M 91

SW_L 92

SW_F 93

SW_B 94

SW_ID 95

SW_R 96

SW_M 97

SW_L 98

SW_F 99

SW_B 100

U5 BL8553-24PRM

VOUT 3

VIN 2

VSS 1

VDD3

C8 100uF

C10 0.1uF

V25

X8

C25

C24

C23

C22

C21

X1 6MHz

R16 10M

X2 32.768KHz

R21 10M

U6 BL8553-25PRM

VOUT 3

VIN 2

VSS 1

VDD3

C9 100uF

C11 0.1uF

V25

X9

C25

C24

C23

C22

C21

X1 6MHz

R16 10M

X2 32.768KHz

R21 10M

ANT

GND

VCC

SCLK

SO

SDIO

NRZ IO

SW CS

U7RM RFMS010C

VDD

R81 10R

SSR MISO

XY_LED

R82 2.7R

VDD

R83 330R

C83 1000P

EP DIO / SSR CS

U4 ADNS-5030

MISO

MOSI

VDD3

GND

NRESET

SCLK

SSR SCLK

C81 0.1uF

C82 4.7uF

PD

R14 10K

Q3 2N3906

VDD3

VDD

SW_ID

SW_R

SW_M

SW_L

SW_F

SW_B

SW_ID

SW_R

SW_M

SW_L

SW_F

SW_B

SVDIN

R23 1.2M

VBT

R24 430K

R22 560

LD3

BTL / EP SK

JR1

Short

Open

SWZ

Z1

Z2

R16 47K

R17 47K

R18 560

PT1

VDD

C4 104

IR

IR-PD

Mechanical IR-PT

Title		
MA2628 For ADNS5030		
Size	Number	Revision
A3		V1.0
Date:	7-Oct-2008	Sheet of
File:	E:\vuhpmp\RD\2216.Ddb	Drawn By: