

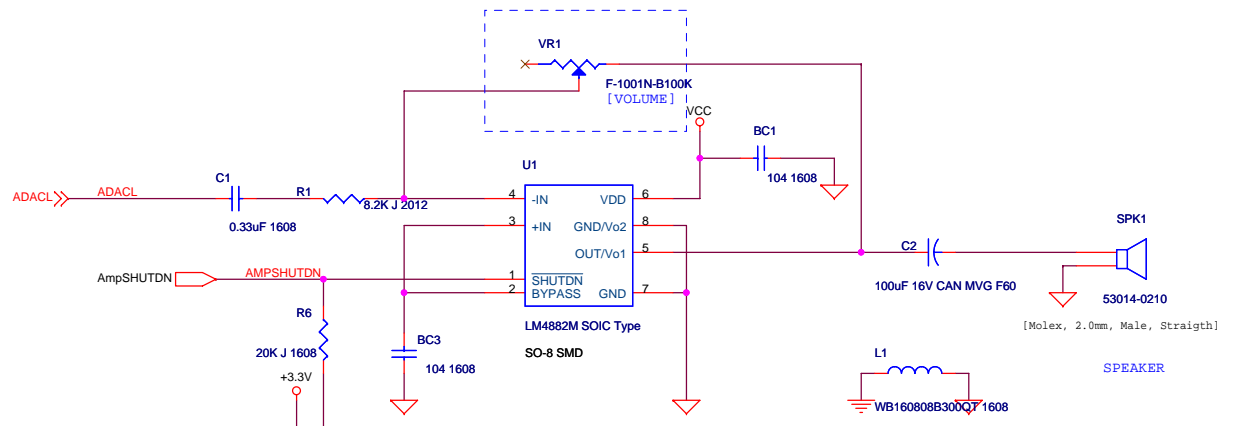
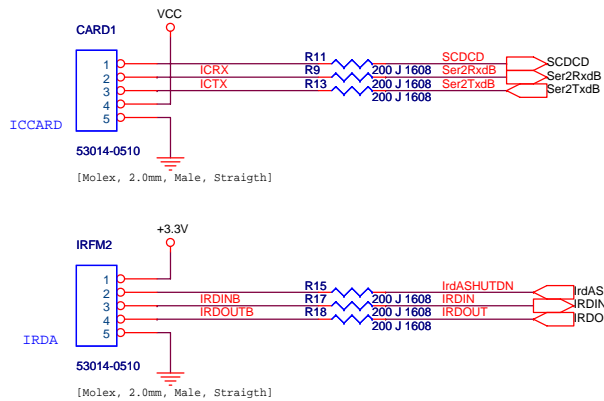
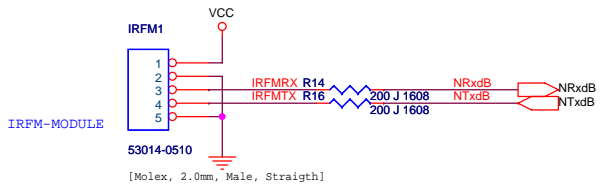
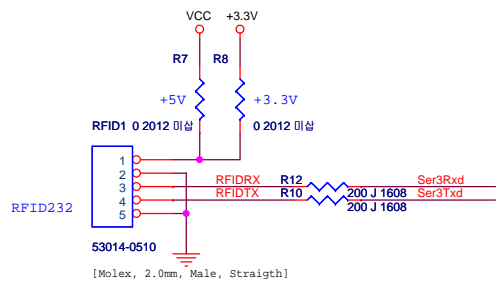
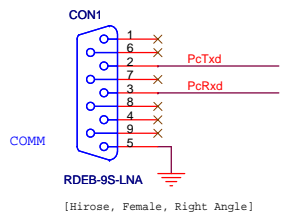
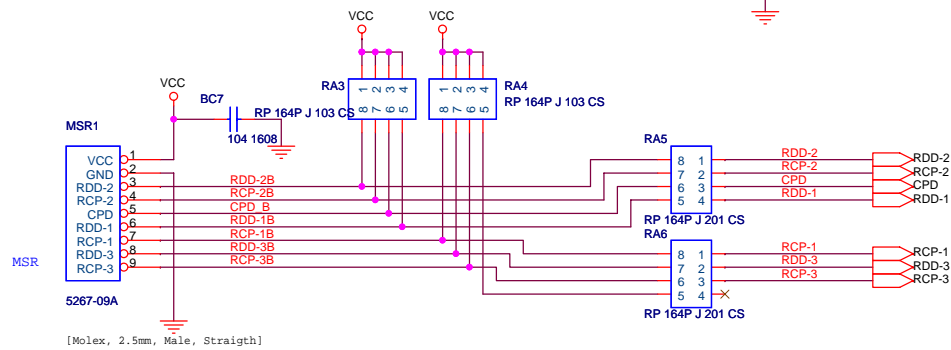
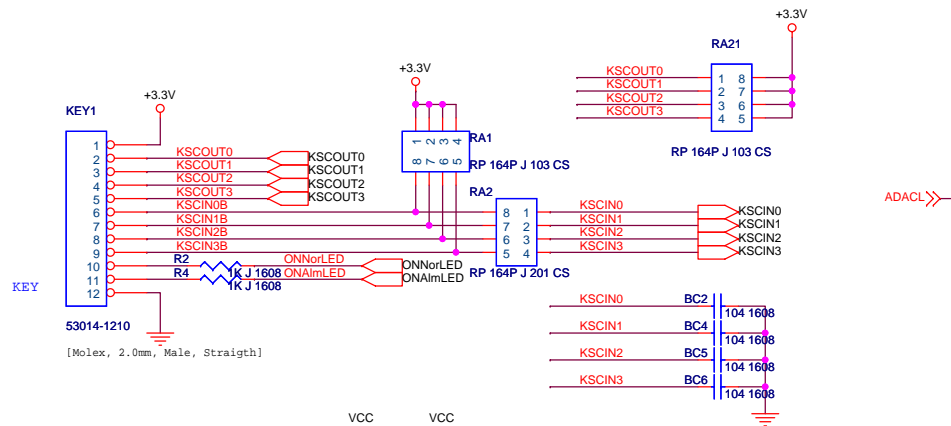
· PIN PAD MIT-2000B MAIN 회로도

1. PROJECT 명 : PHIPHI(MIT-2000B)
2. BOARD 명 : MAIN B'D(05-TD-PHIPHI-101A-4)
3. SET 구성
MAIN(101), KEY(131)
4. 회로도 VERSION : V 4.0
5. ART WORK 유의 사항
 - * HOLE GND 처리
 - * 전원용량 주의(회로도에 굵게 표시함)
 - * SILK 표시(회로도에 푸른색 글씨로 표시함)
 - * 기구도면 주의
6. PCB SIZE :
7. PCB 수량 :
8. 조립 수량 :
9. LAYER : 4L, 1.6T

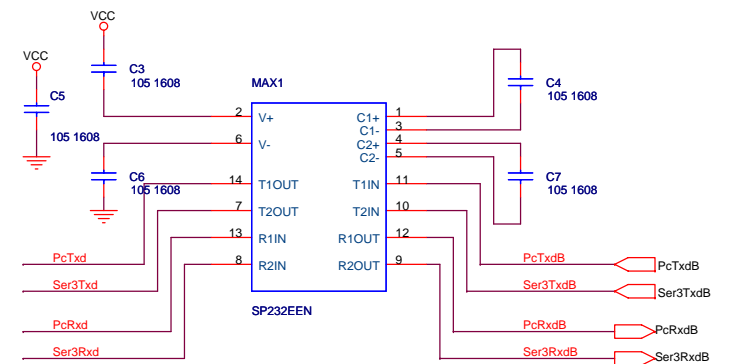


	NAME	DATE	SIGNATURE
DESIGN	윤병규	2005.07.	
CHECK	김시용	2005.07.	
APPROVAL	김현수	2005.07.	

아 침 정 보 기 술[www.morningit.com]			
Title			
05-TD-PHIPHI-101A-4			
Size A3	Document Number 표 지		Rev 4.0
Date:	Sheet 1 of 7		

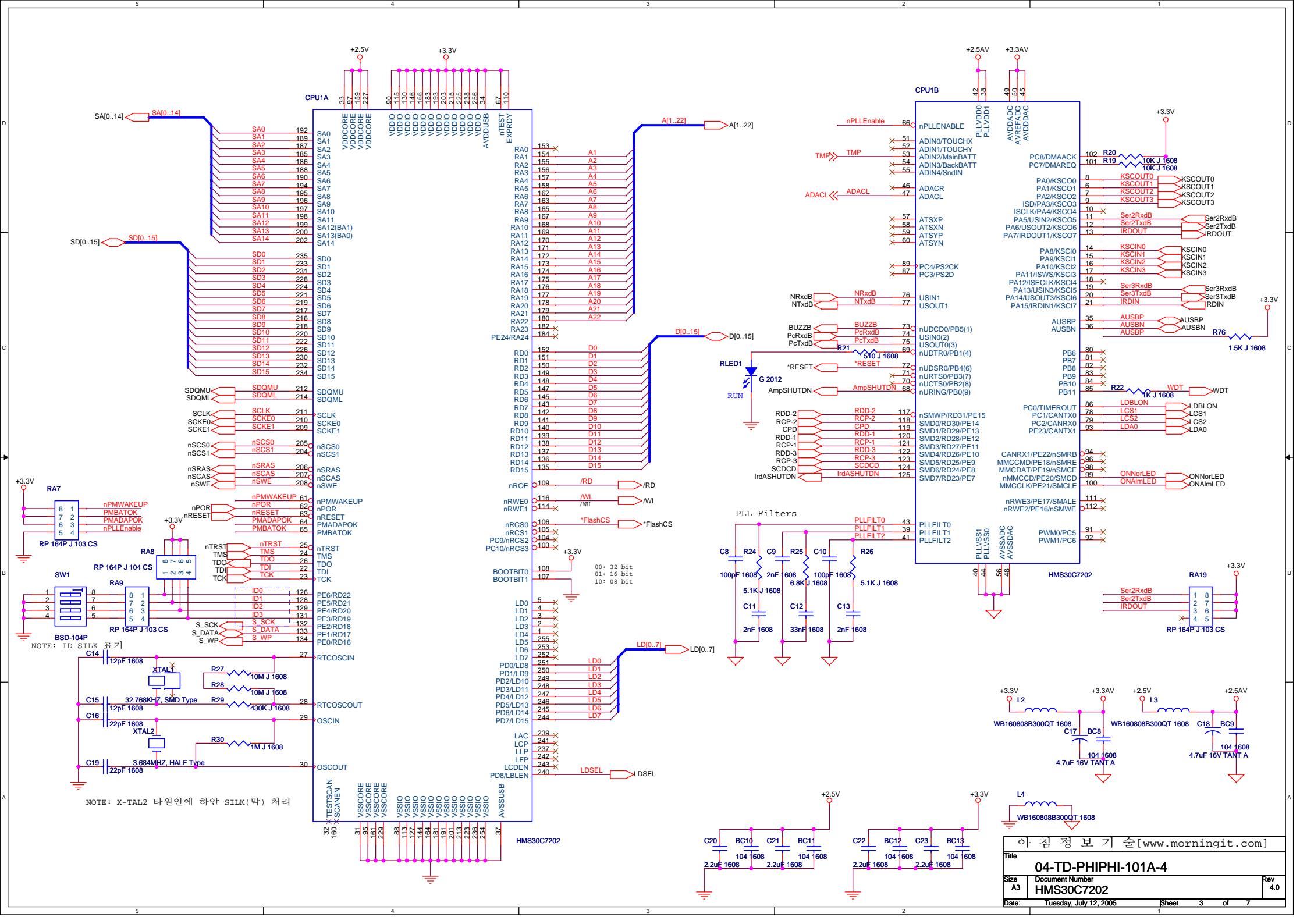


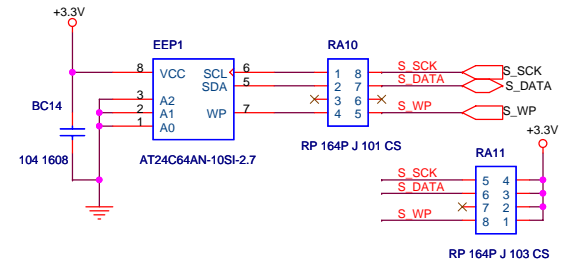
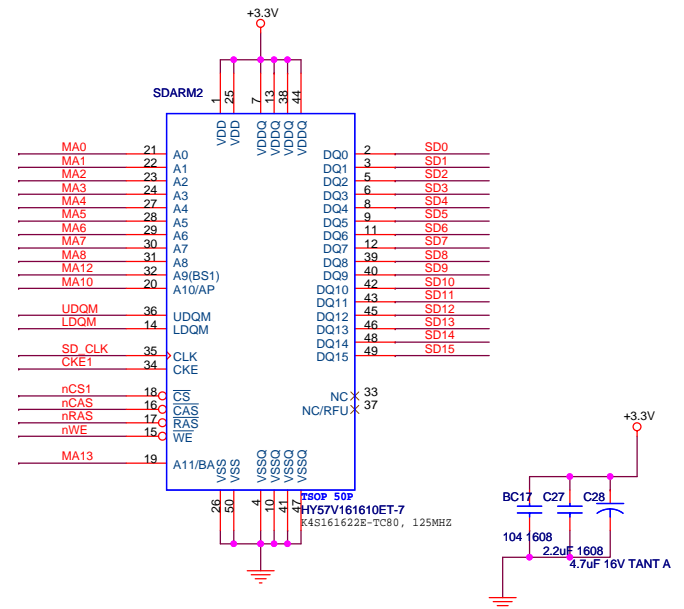
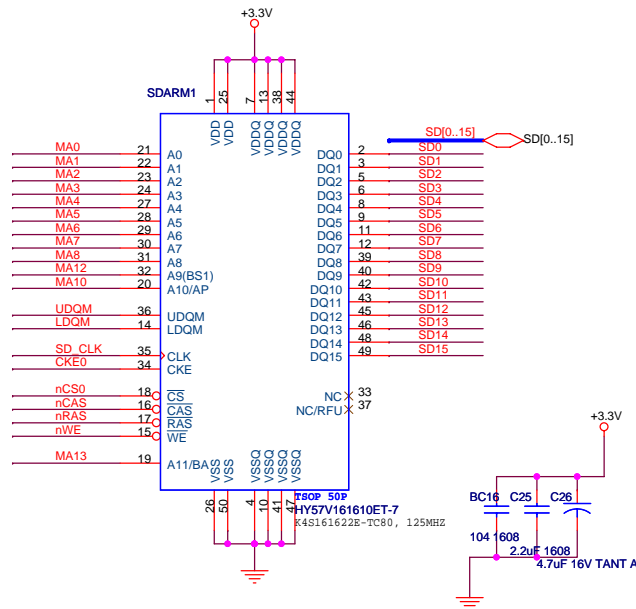
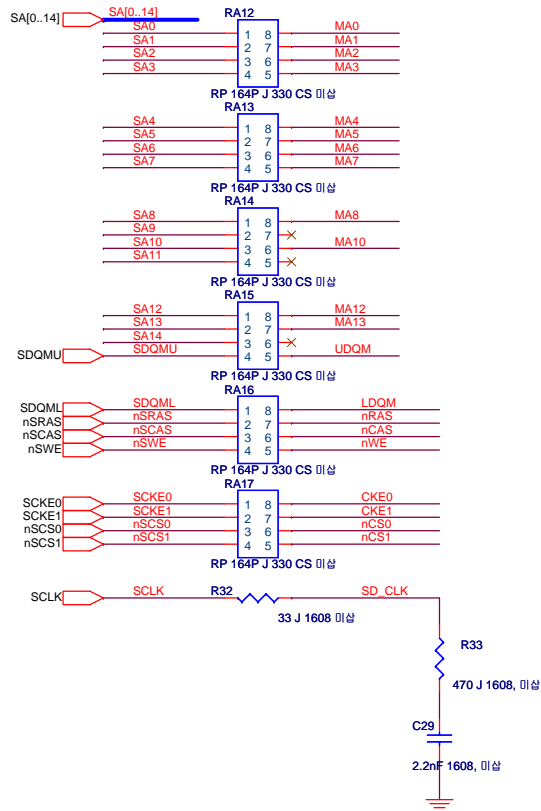
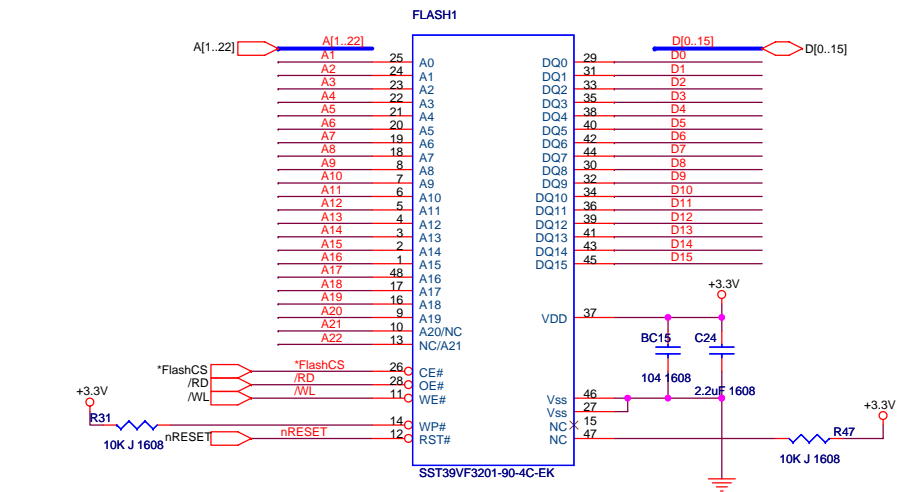
NOTE:
R1: 1608 -->2012로 변경

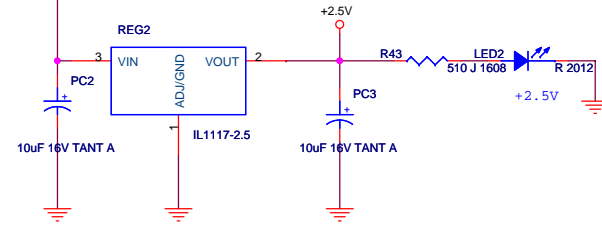
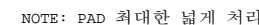
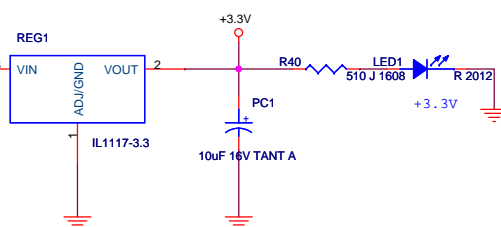
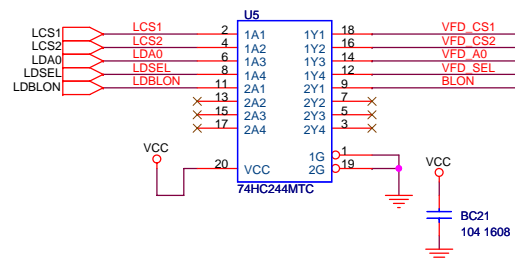
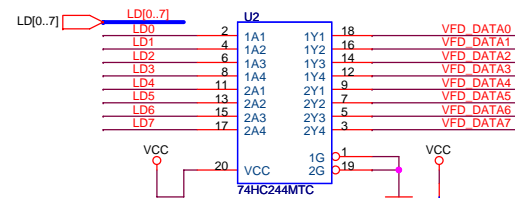


[NOTE: WSB-011219-2 1608 대체품: HB-1H1608-300 1608, 30ohm, 500mA]

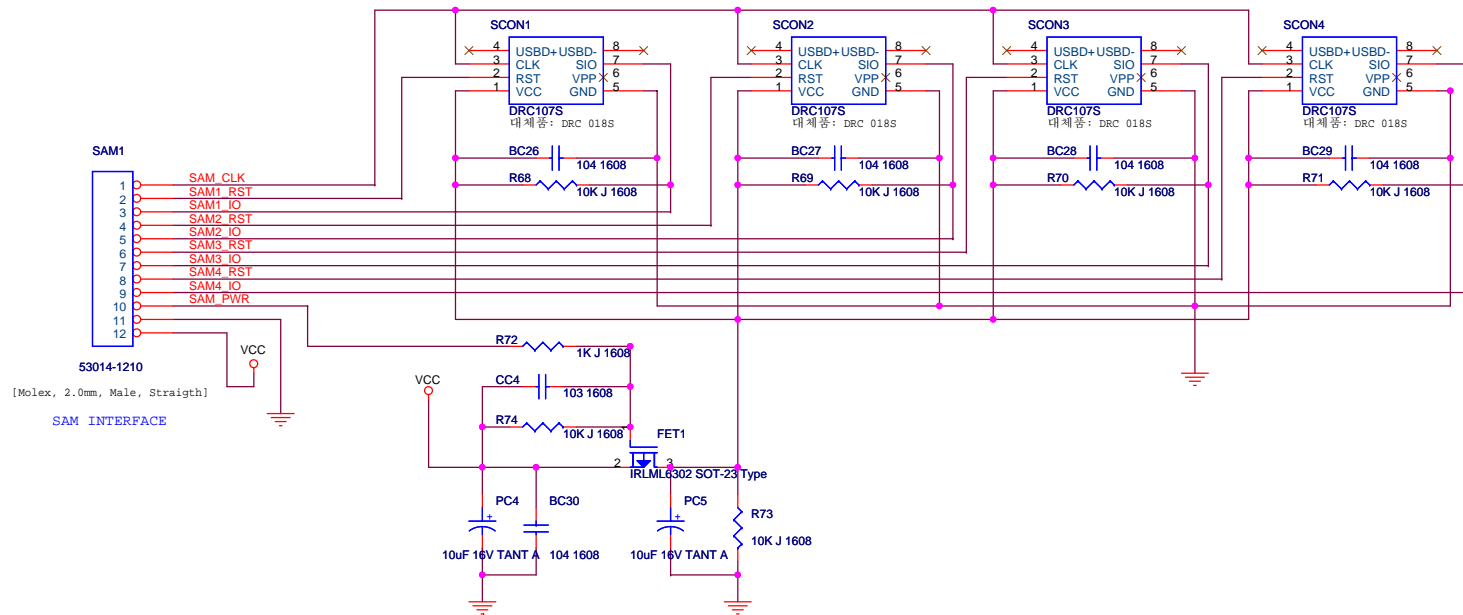
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NOTE: PAD 최대한 넓게 처리



NOTE: DRC 018S는 DRC017S로 ARTWORK 요망

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