

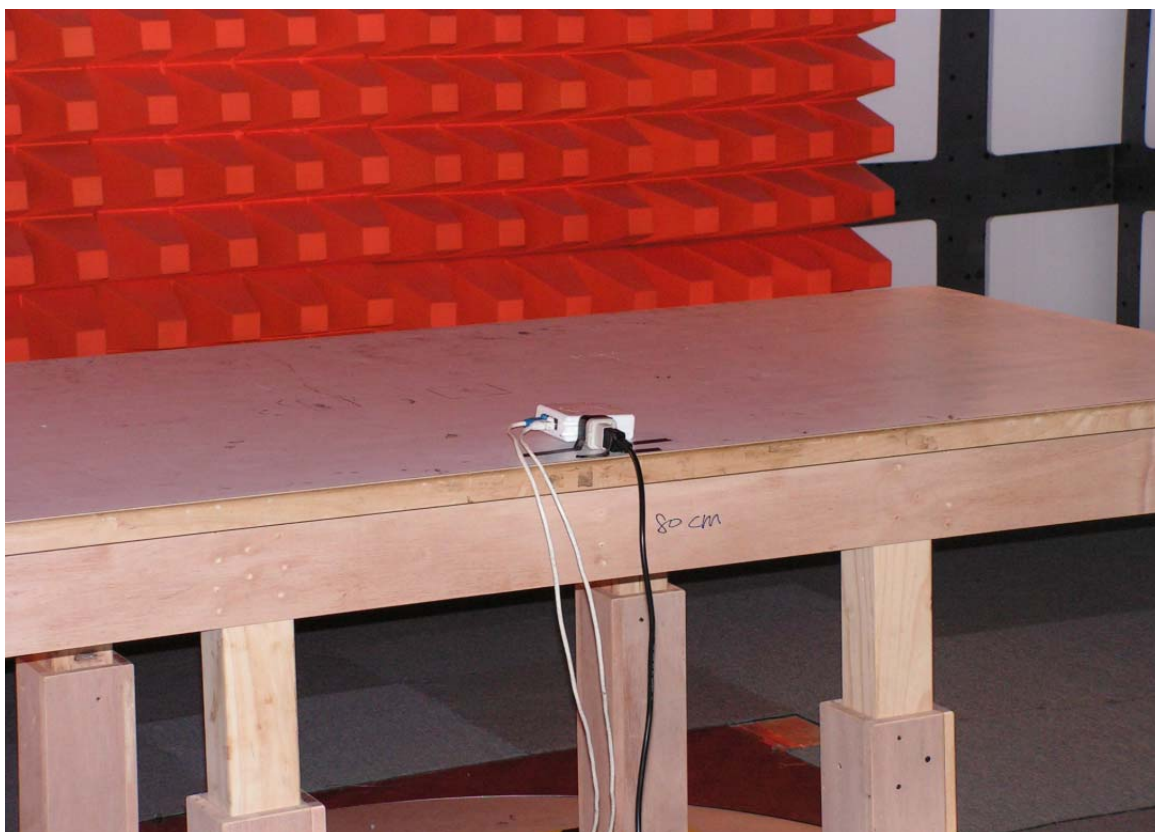
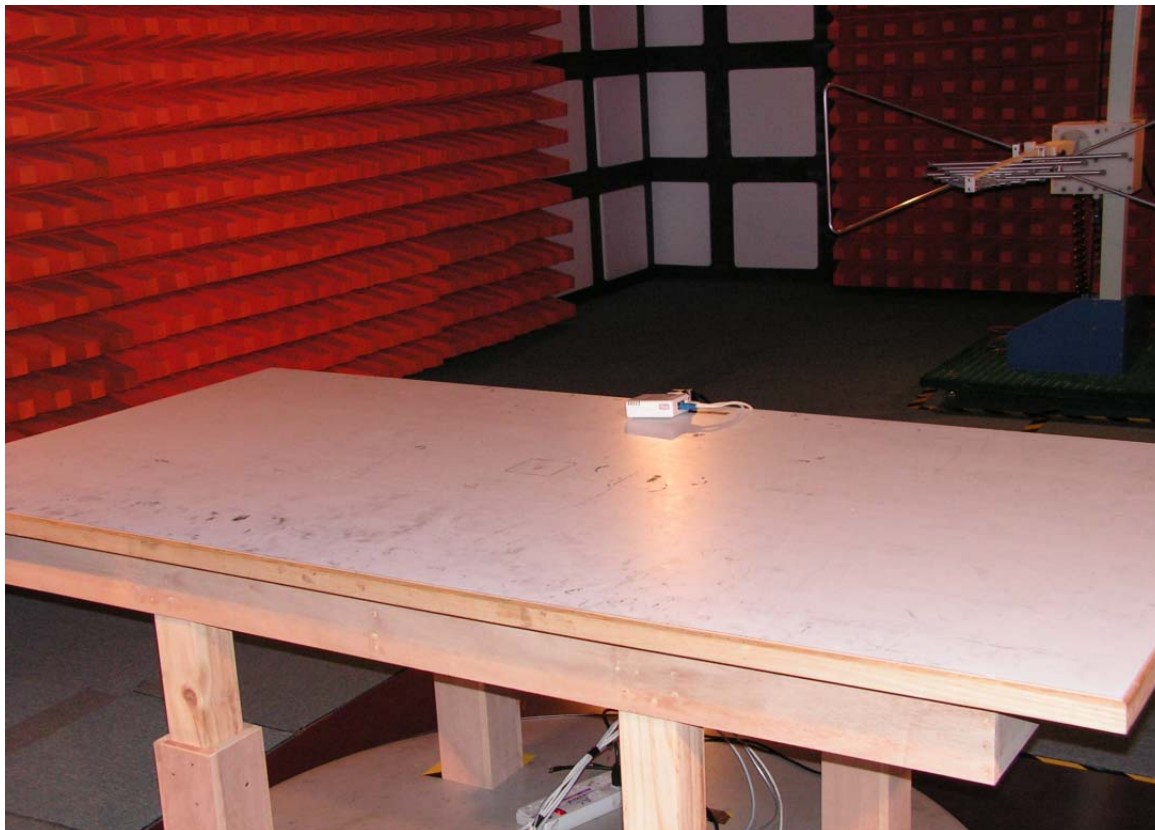
## 9. PHOTOGRAPHS FOR TEST

### 9.1 TEST PHOTOGRAPHS FOR CONDUCTION



## 9.2 TEST PHOTOGRAPHS FOR RADIATION

30-1000MHz





**Above 1GHz**

## 10. PHOTOGRAPHS FOR PRODUCT

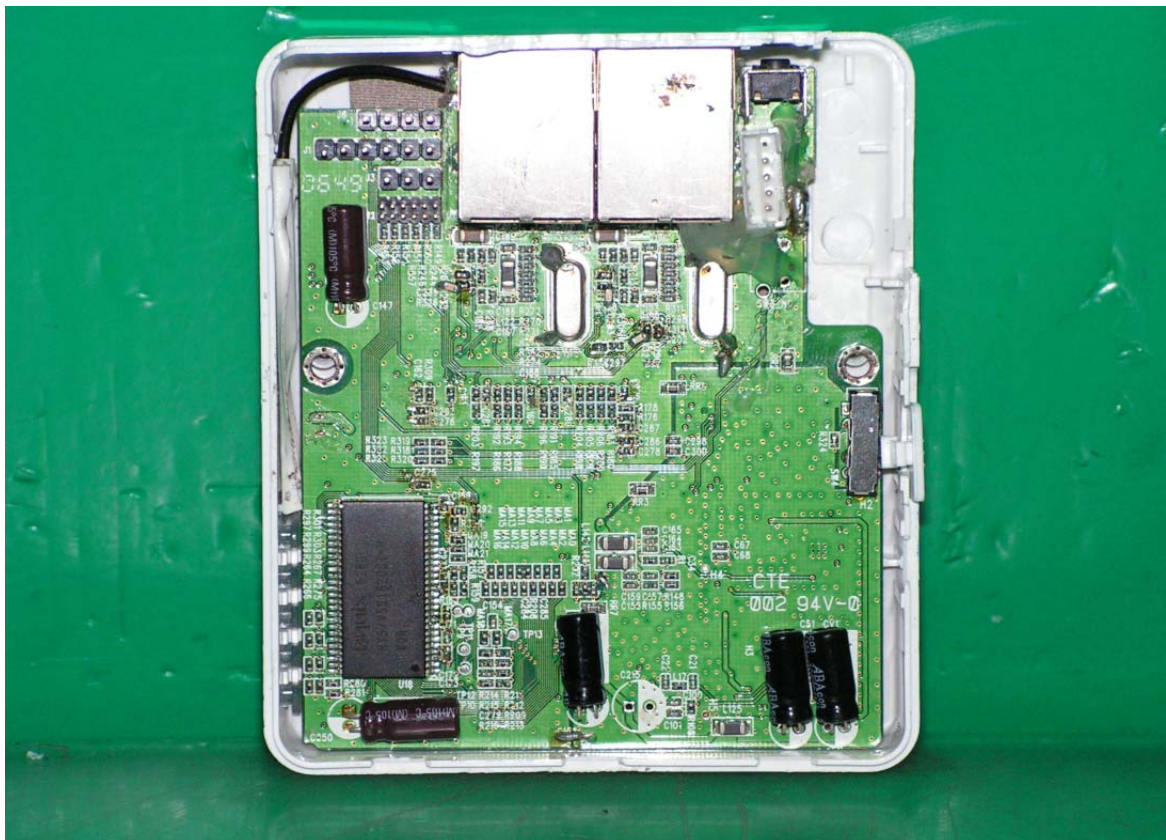
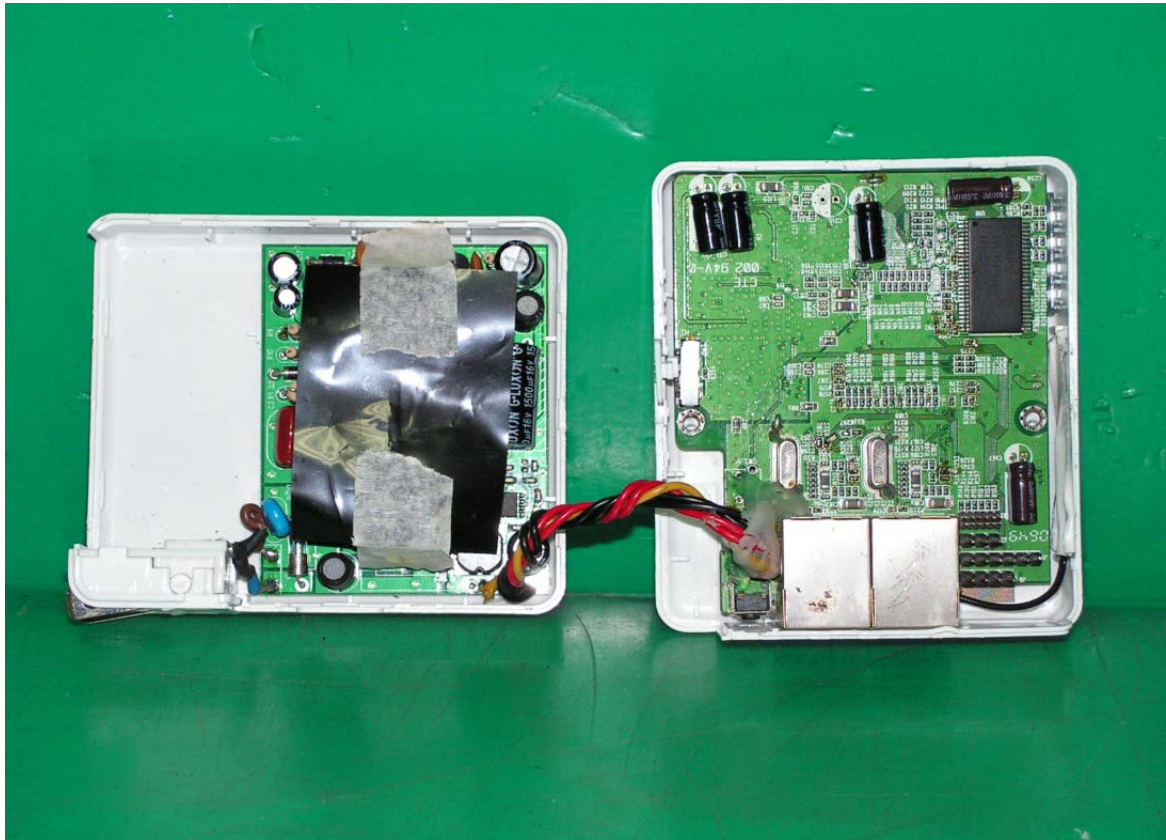
- 1.
- 2.





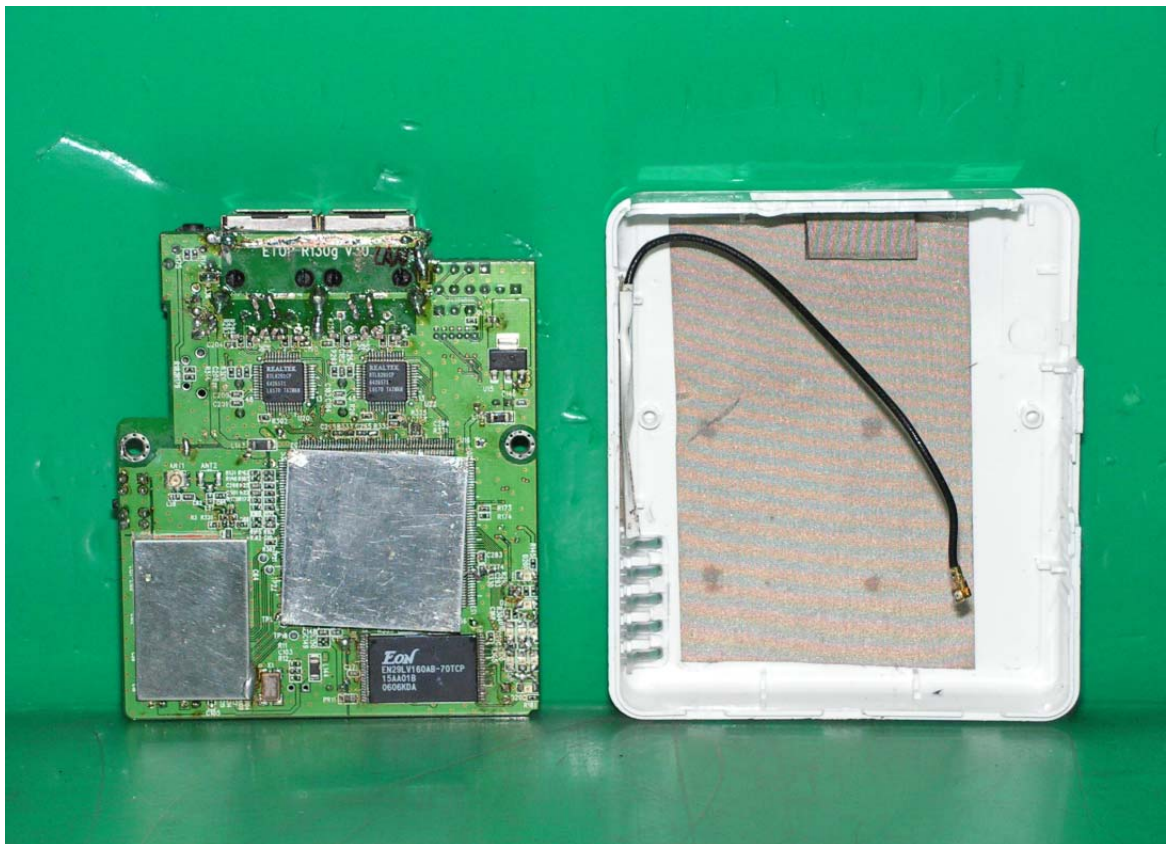
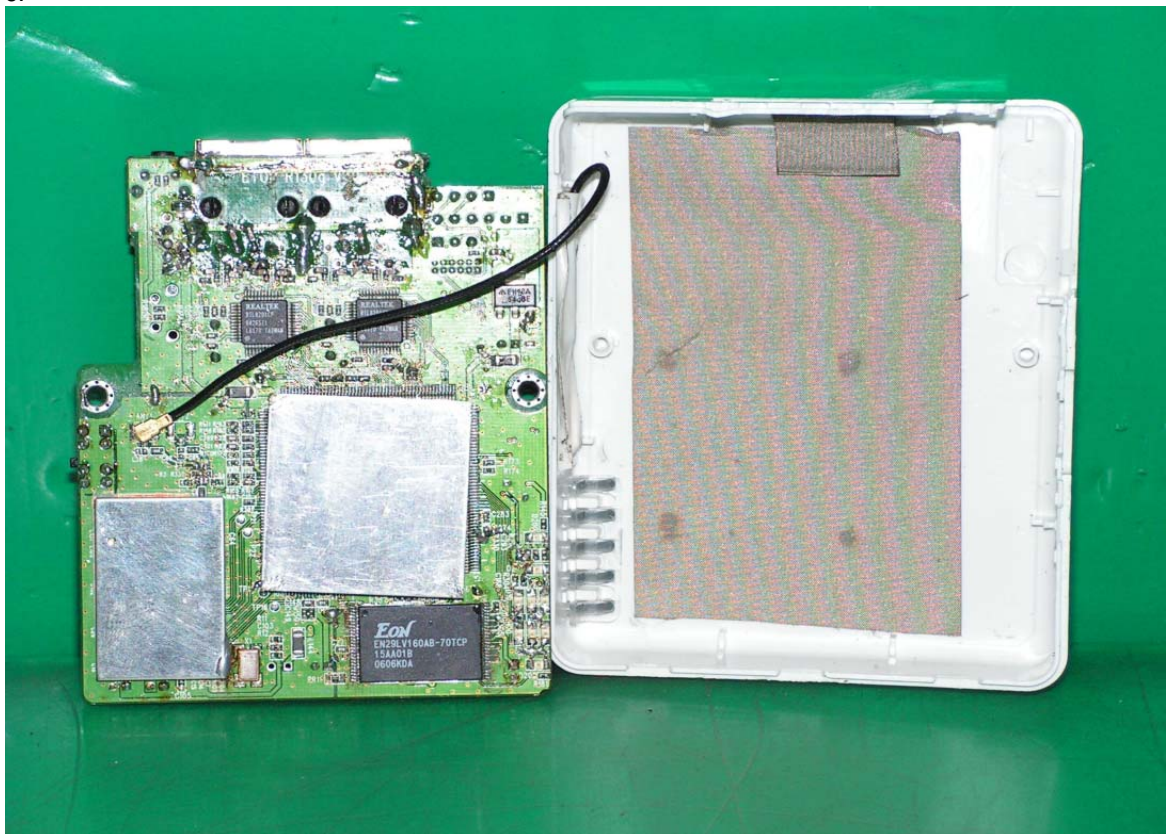
3.

4. Main Board





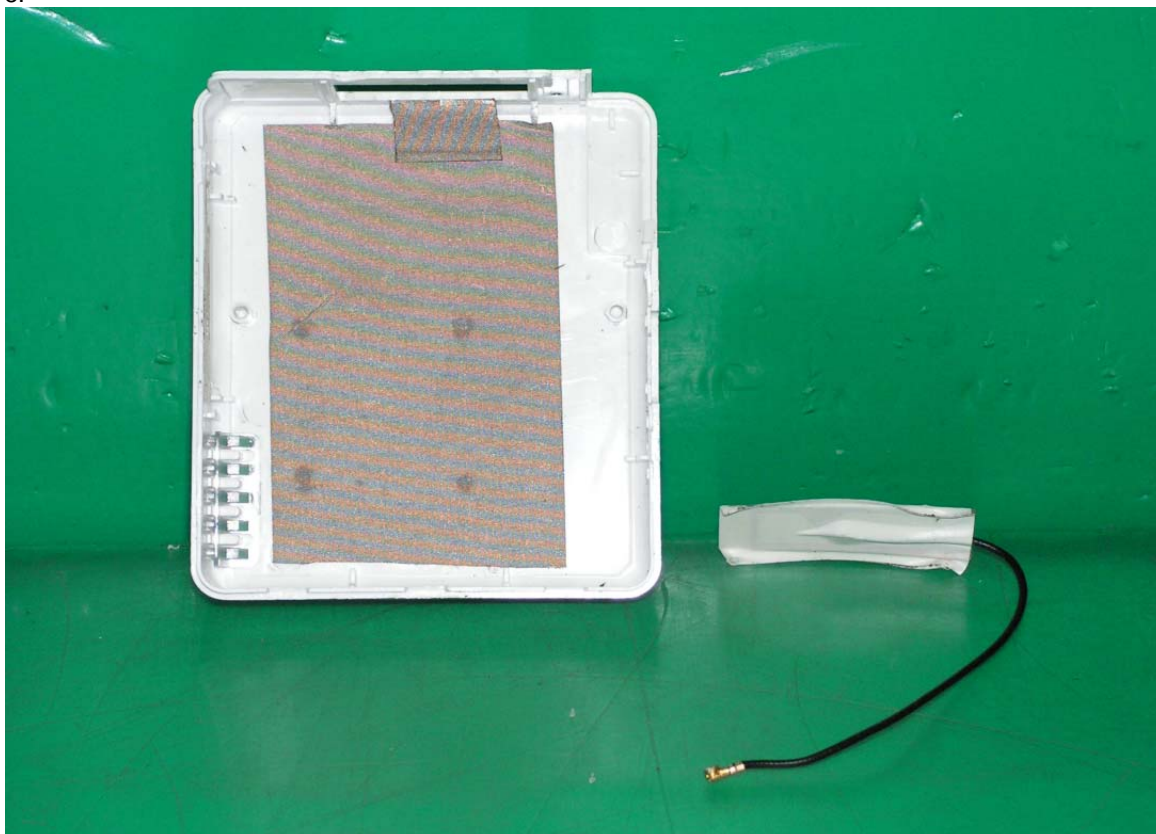
5.  
6.





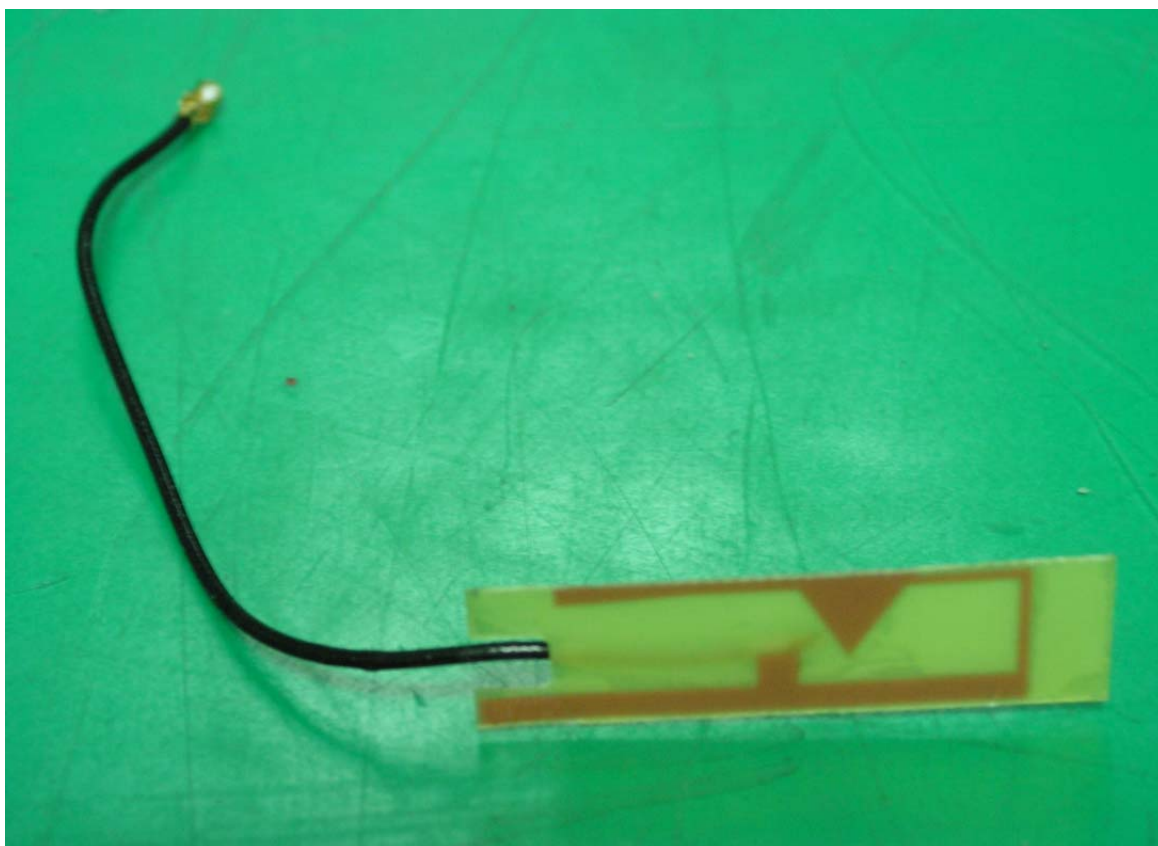
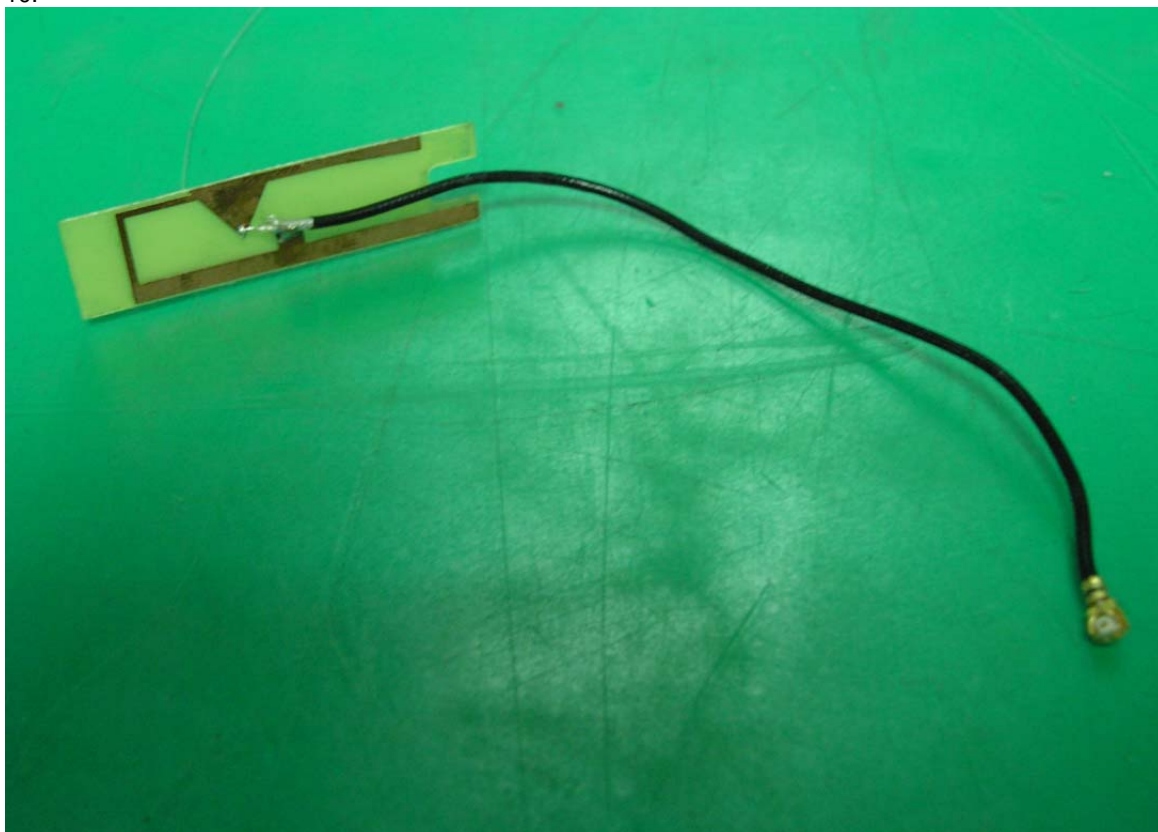
7. Cover and ANT

8.



9. ANT

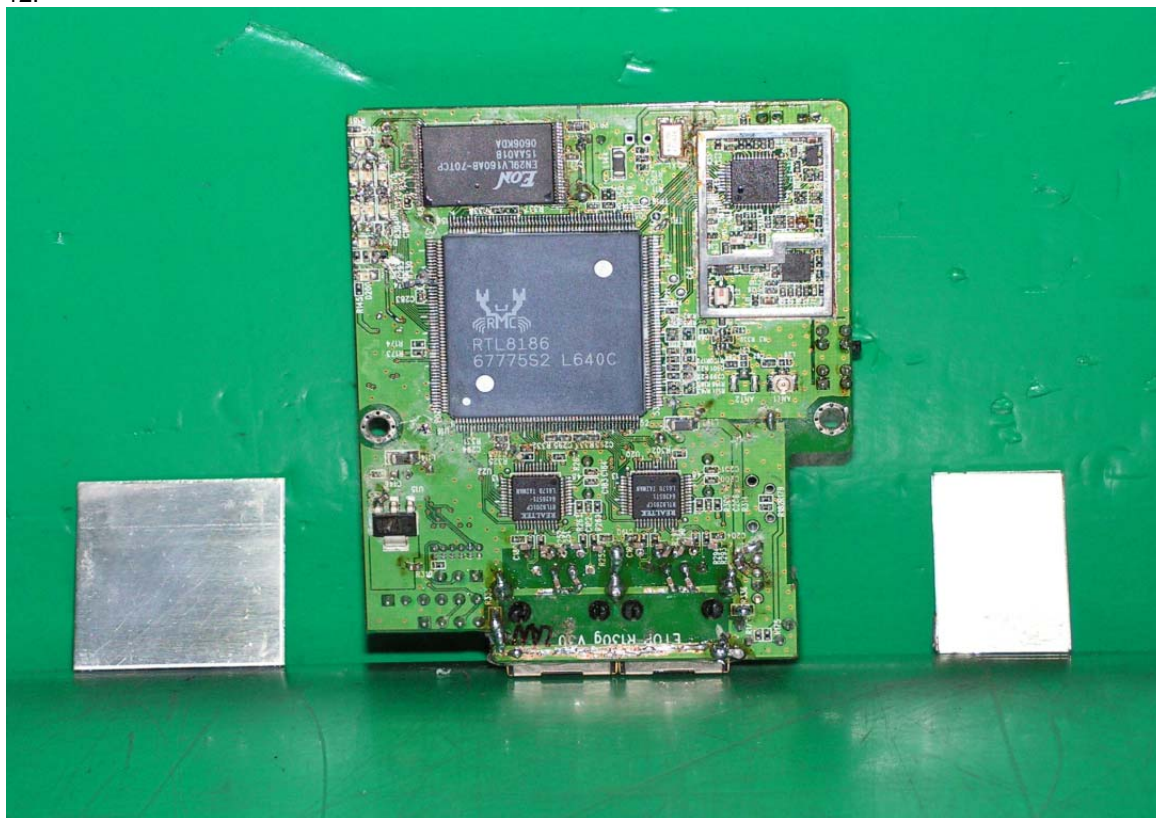
10.





11. Main Board Front Side Inner

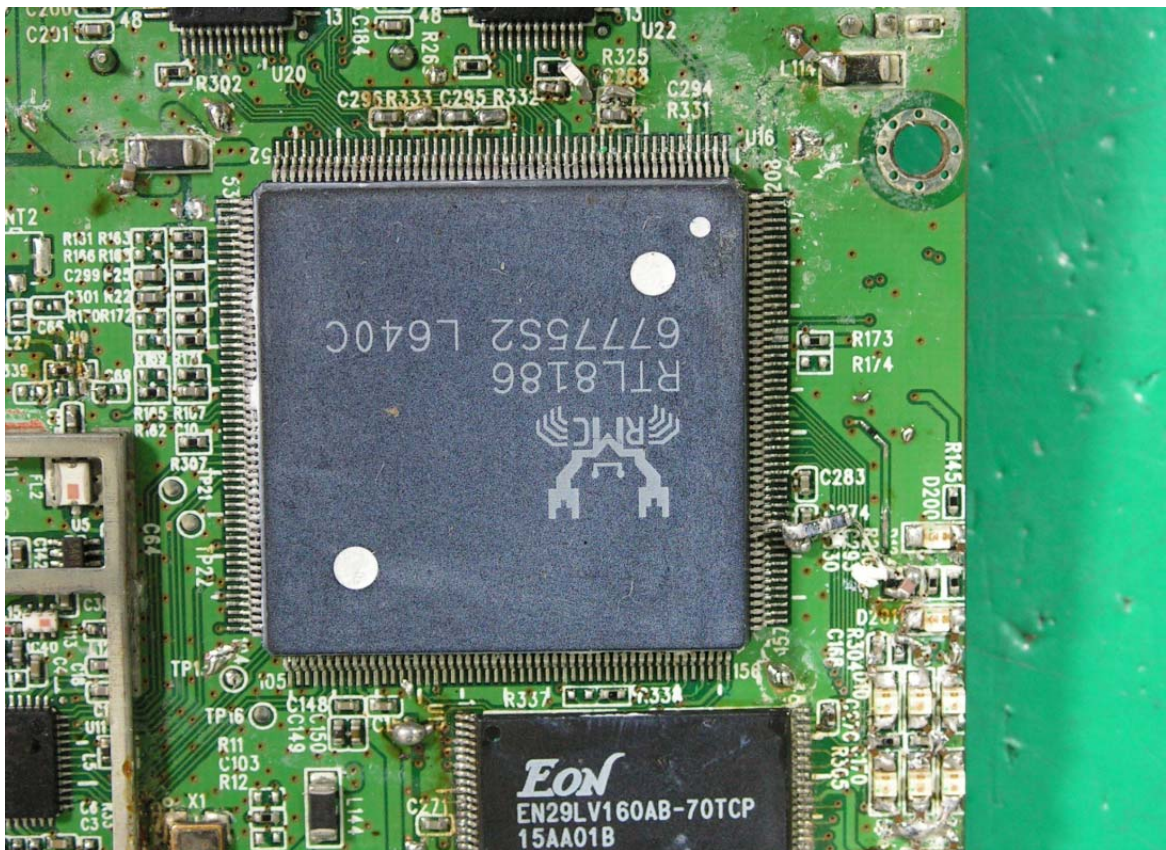
12.





13.

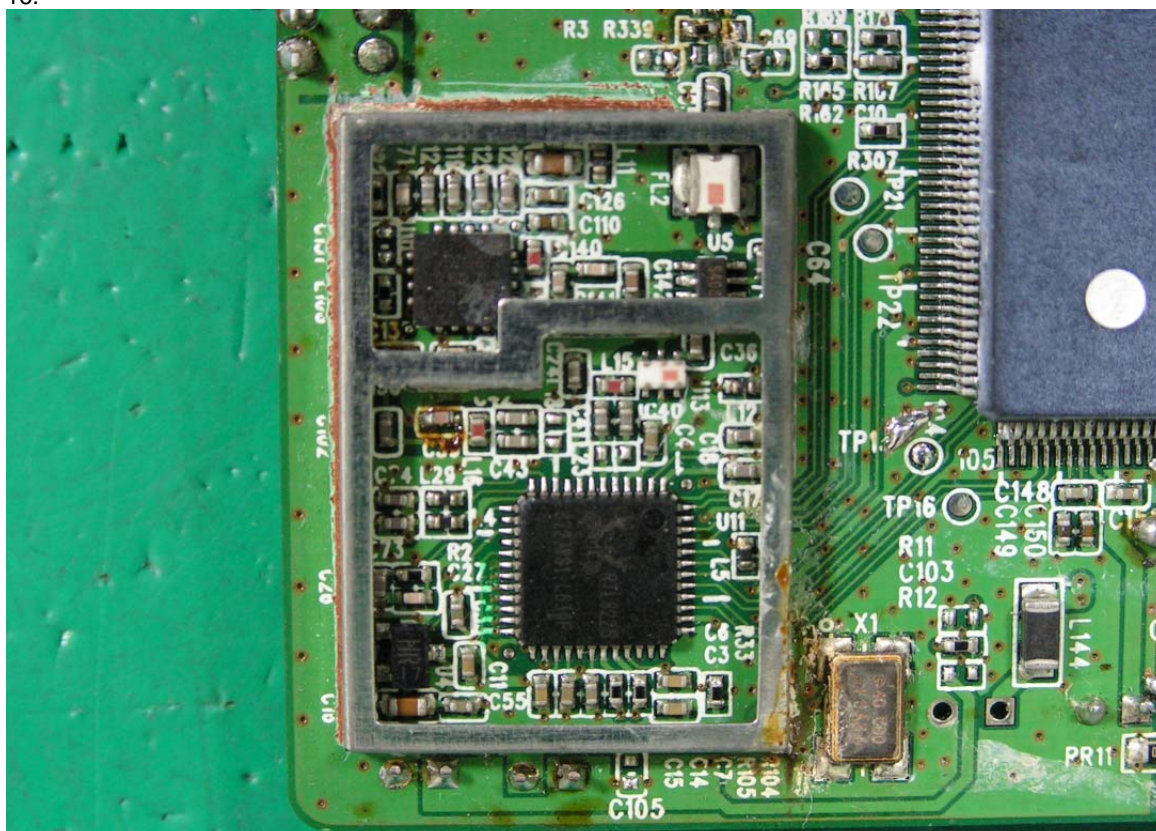
14. Main Chip





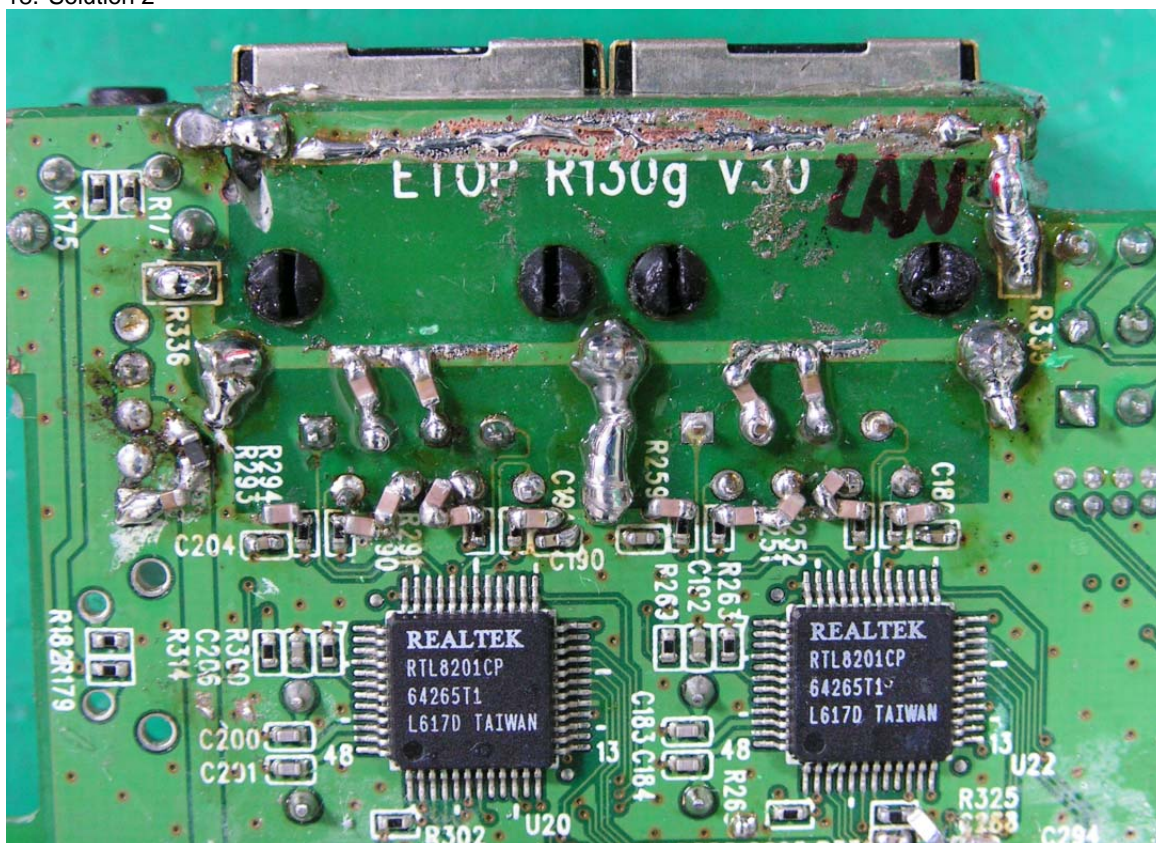
## 15. RF Chip

16.





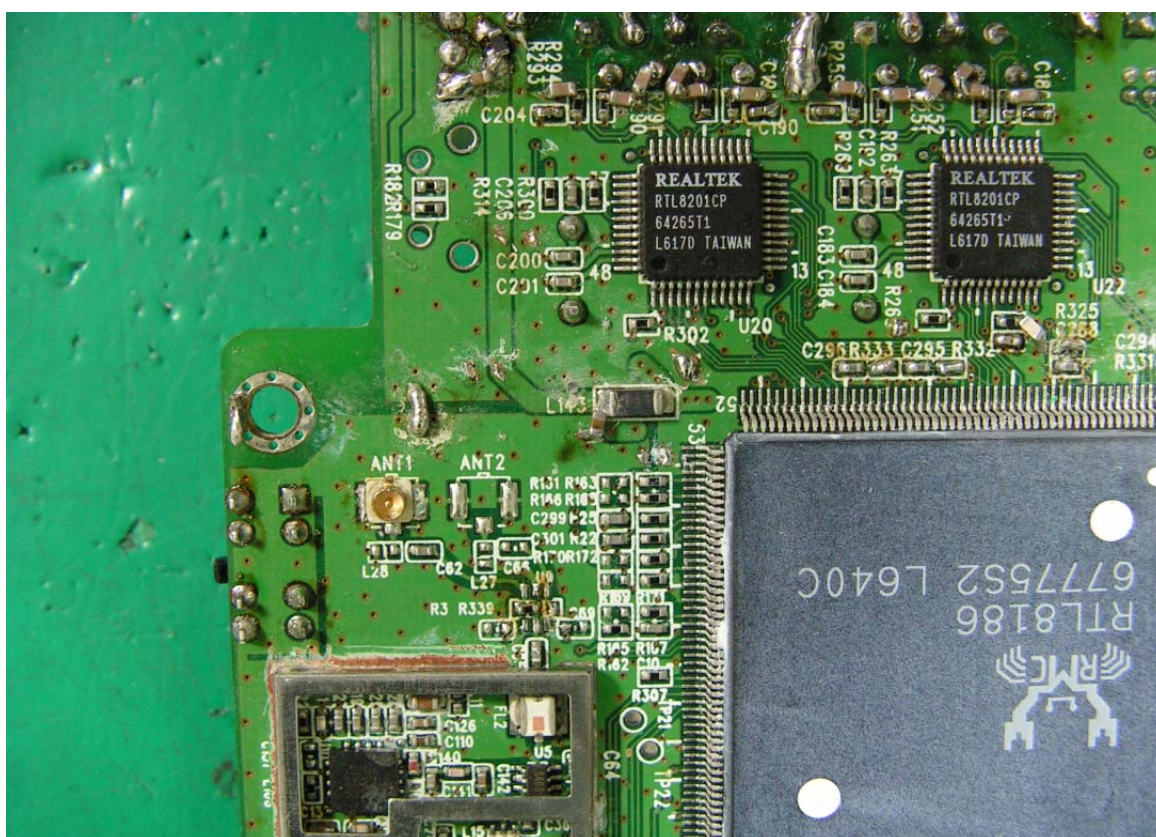
17. Solution 1  
18. Solution 2





19. Solution 3

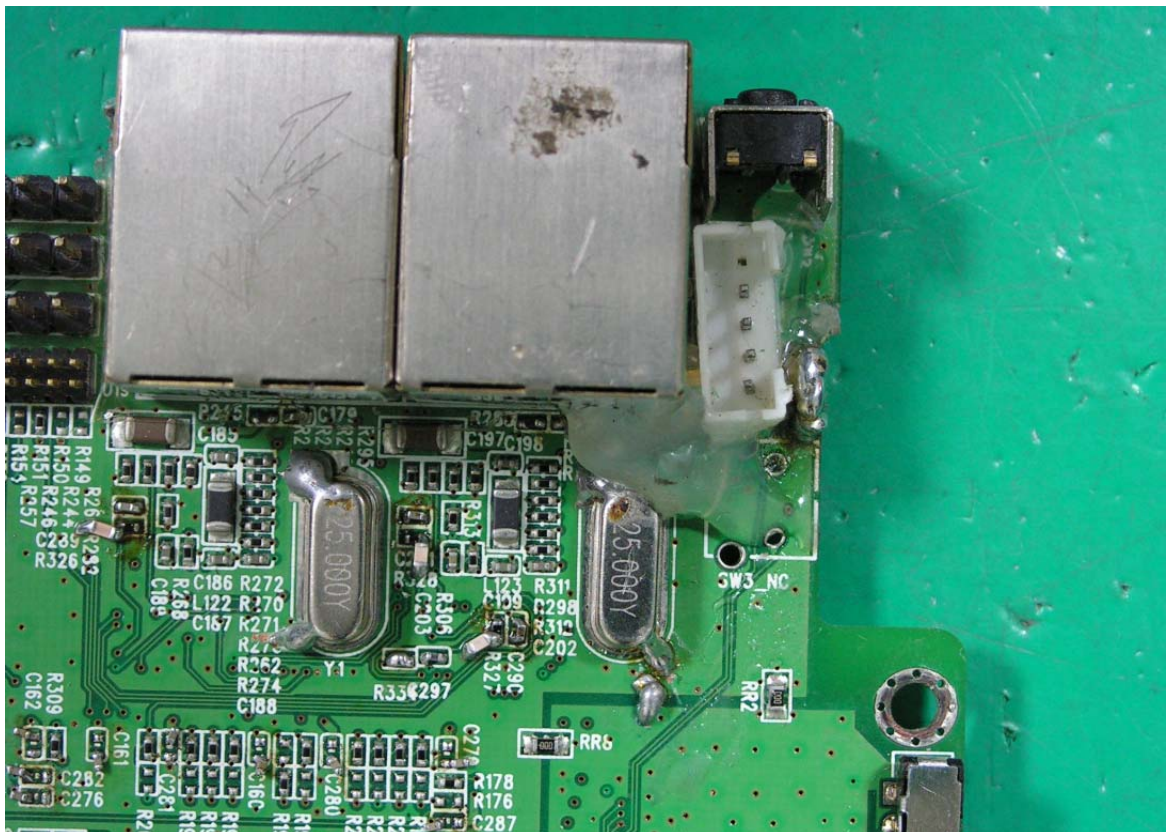
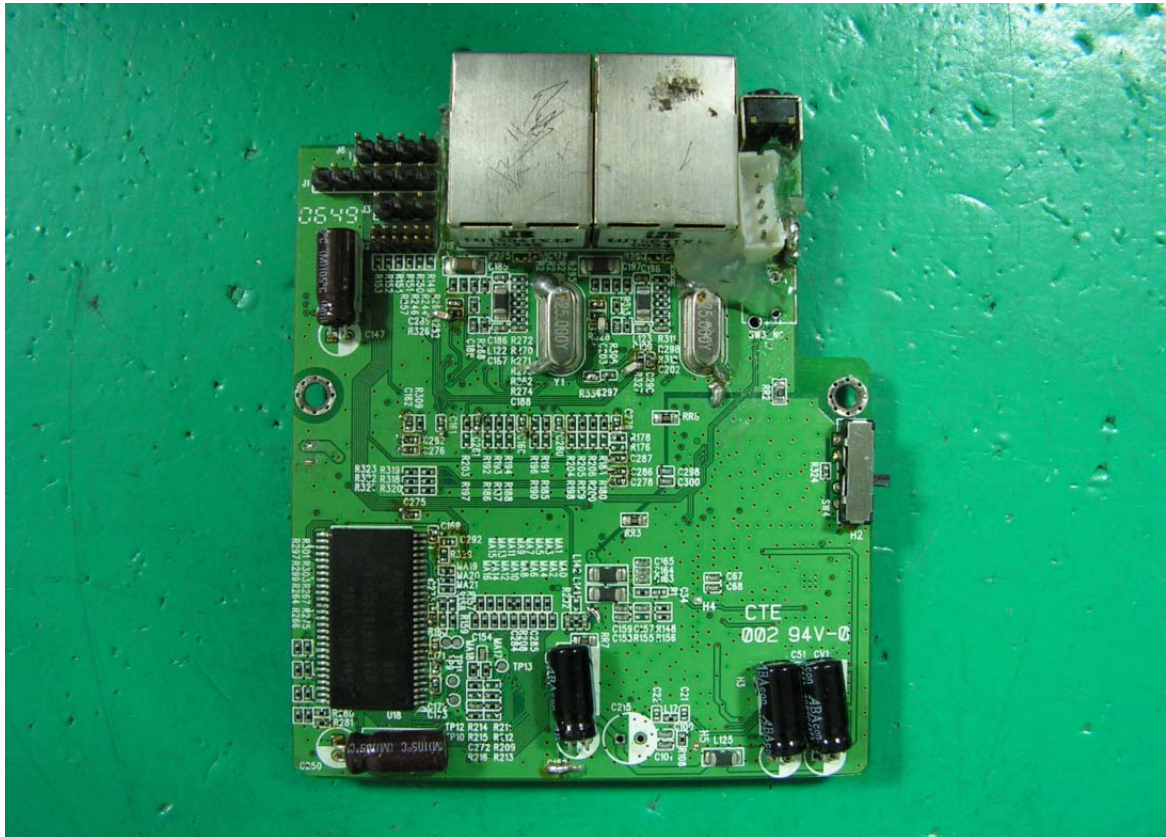
20. Solution 4





21. Main Board Rear Side

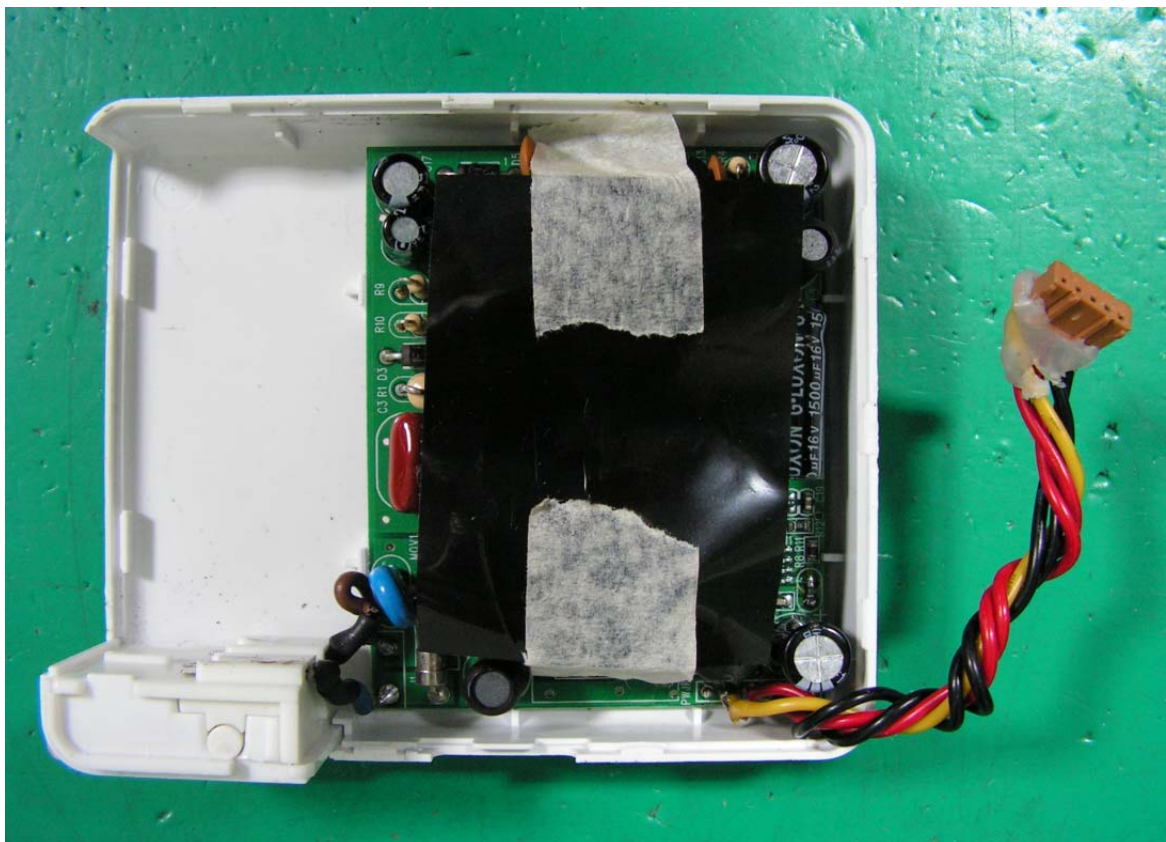
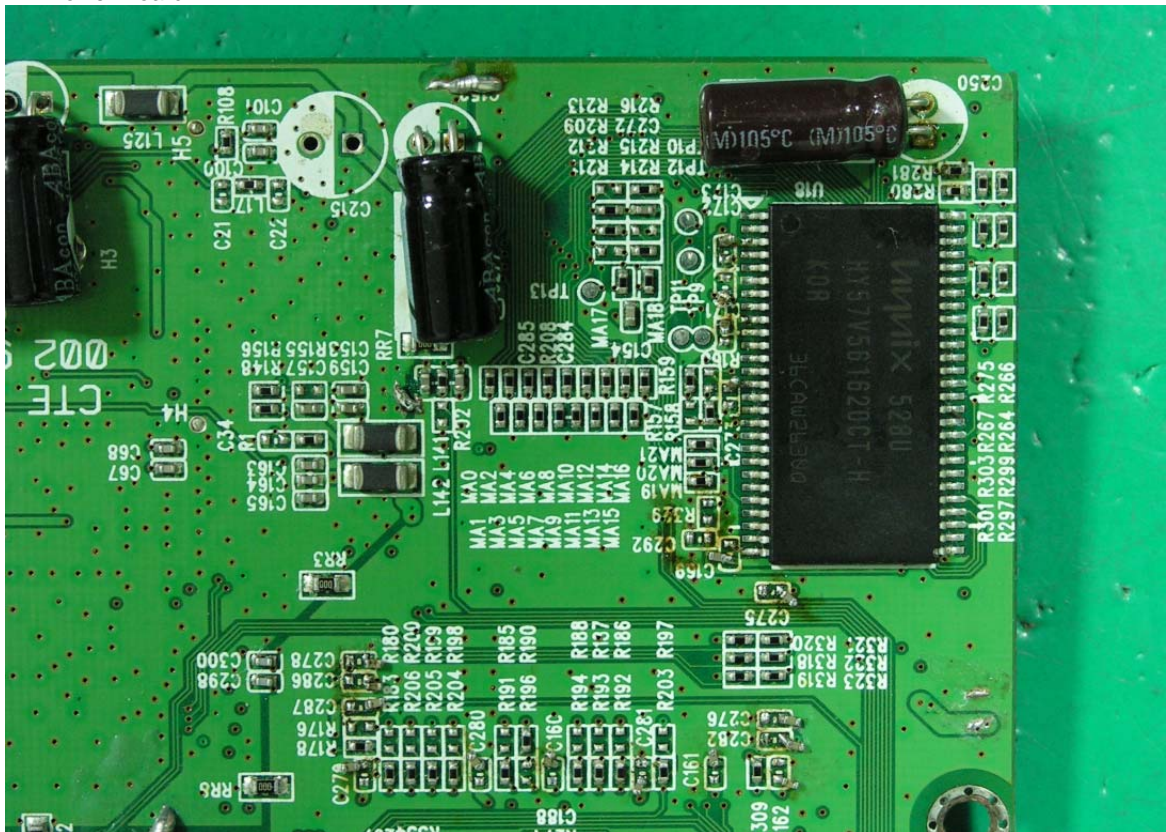
22. Solution 5





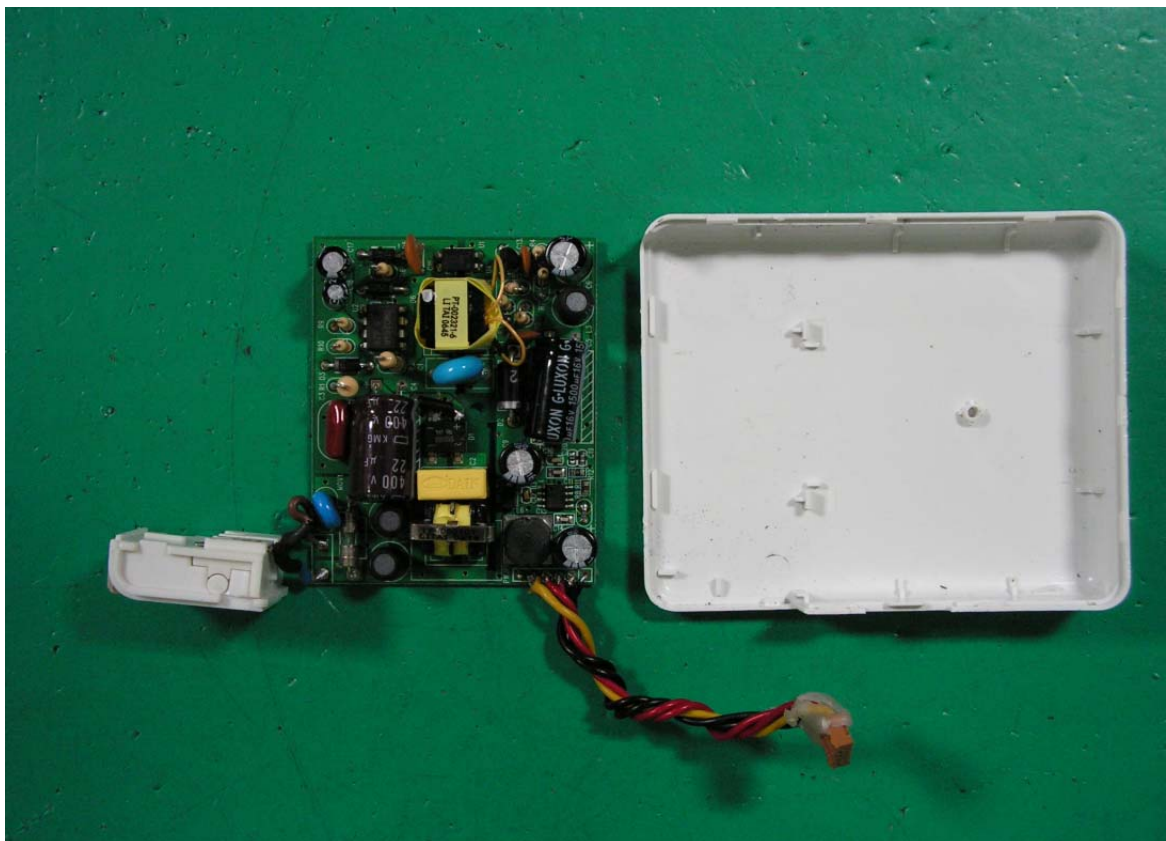
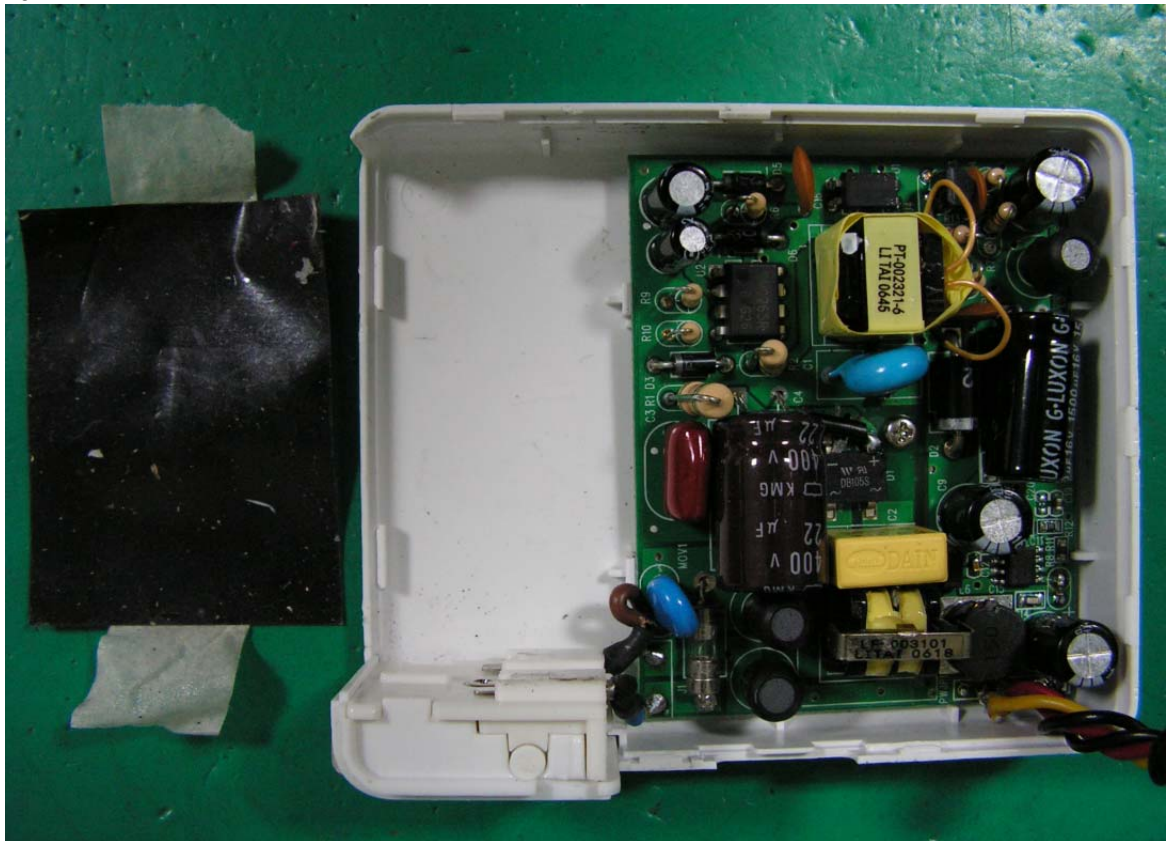
23. Solution 6

24. Power Board



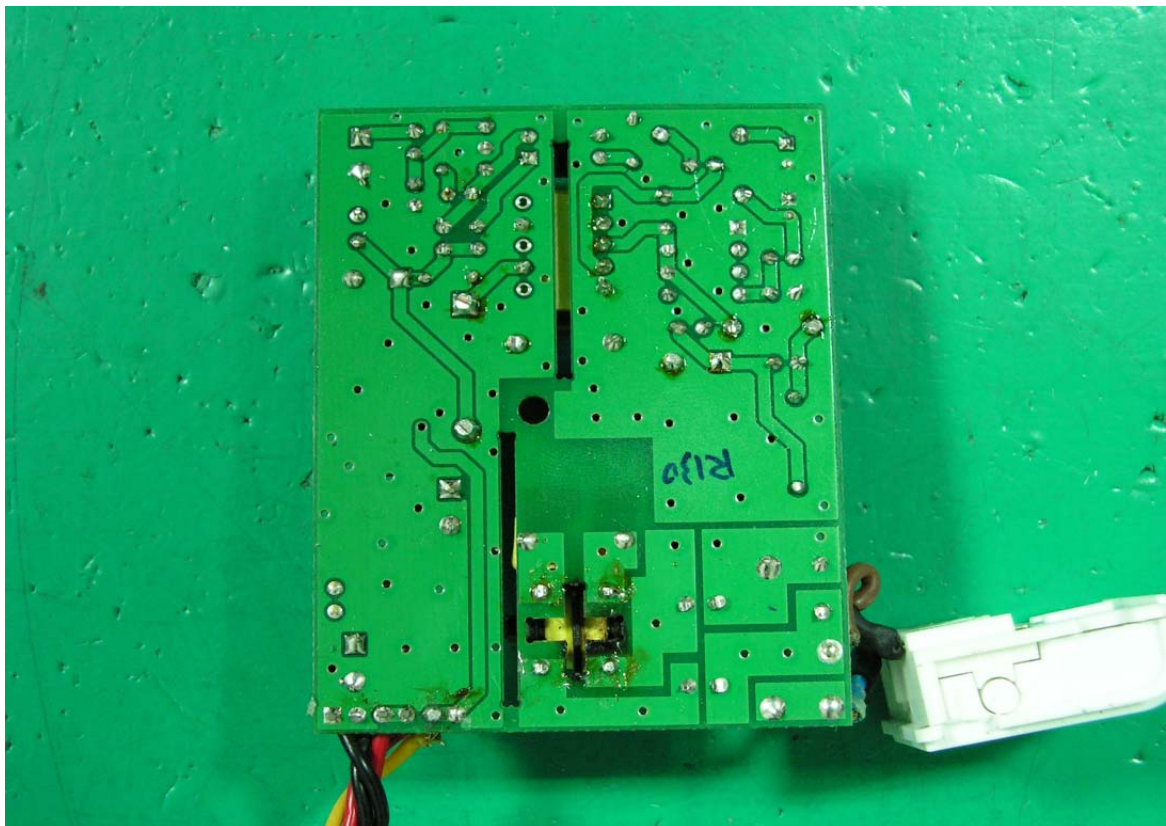
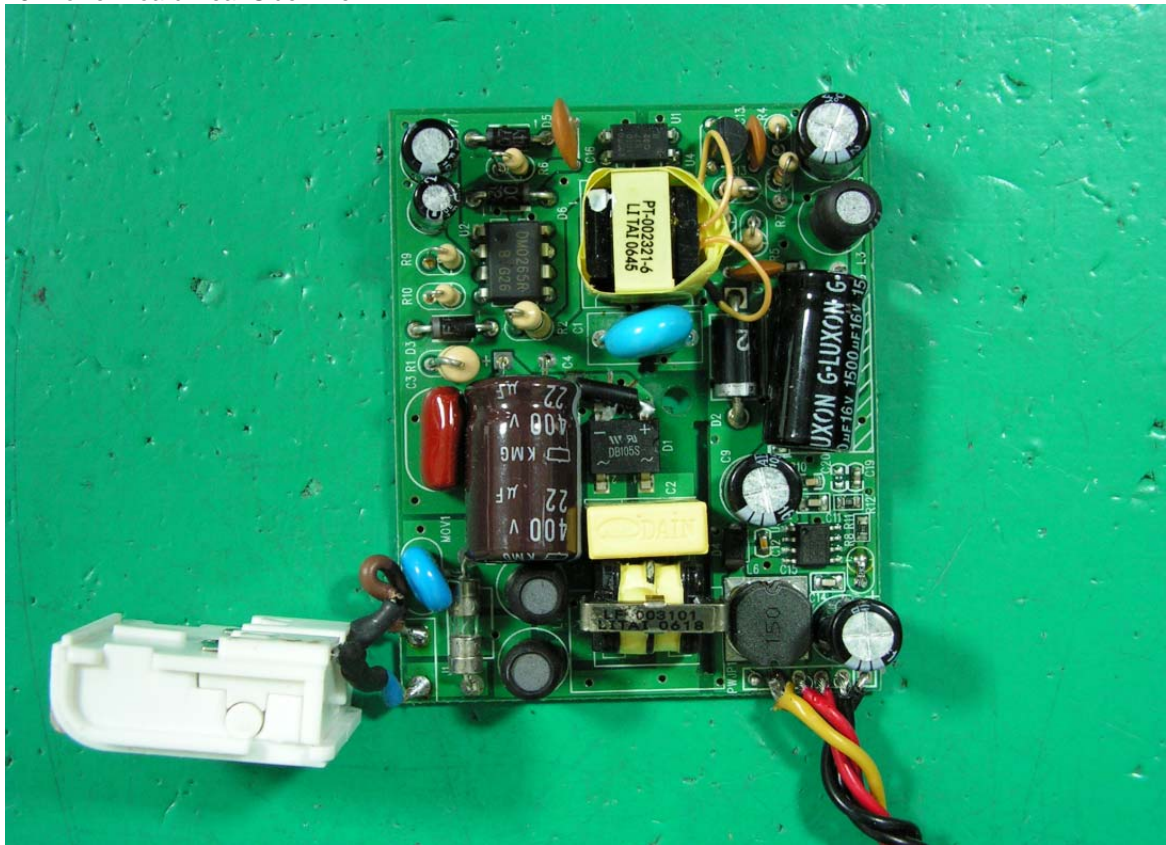


25.  
26.





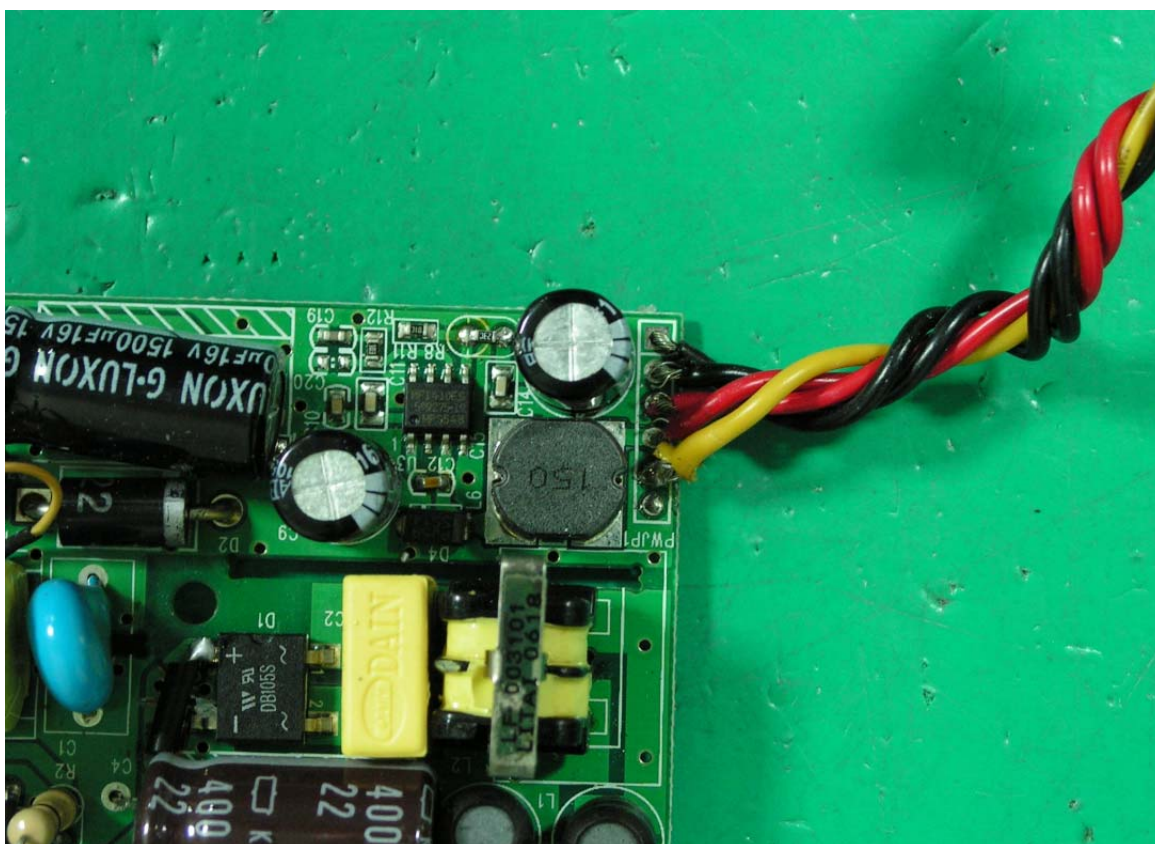
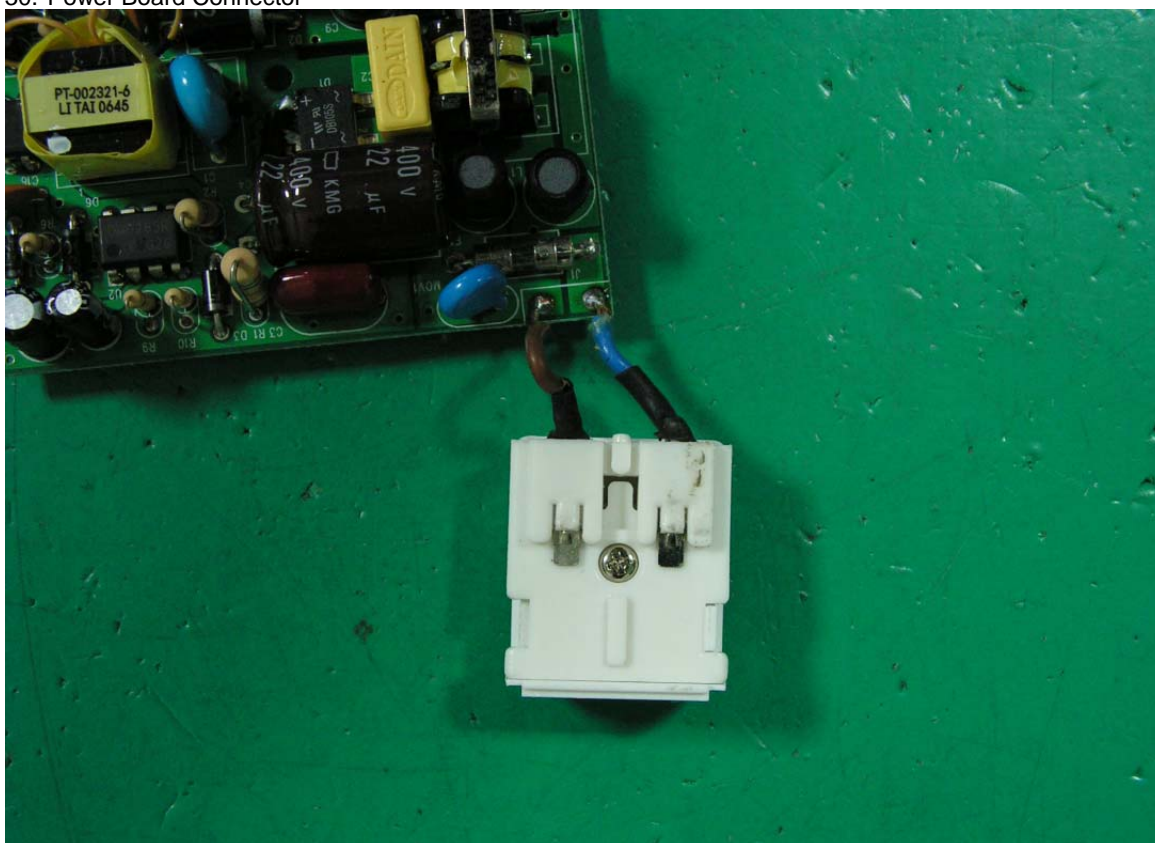
27. Power Board Front Side Inner  
28. Power Board Rear Side Inner





29. Power Board Connector

30. Power Board Connector





31. Label Here





## 11. EMI REDUCTION METHOD DURING COMPLIANCE TESTING

No modification was made during testing.



## **Appendix A**

### **Circuit (Block) Diagram**

(Shall be added by Applicant)



# **Appendix B**

## **User Manual**

(Shall be added by Applicant)