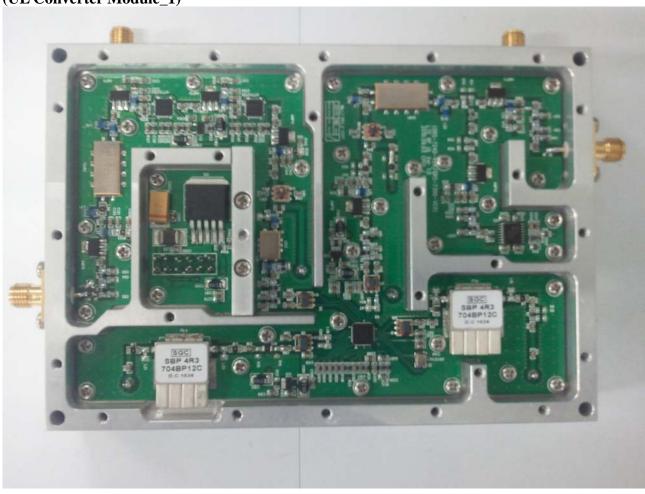


(Open)





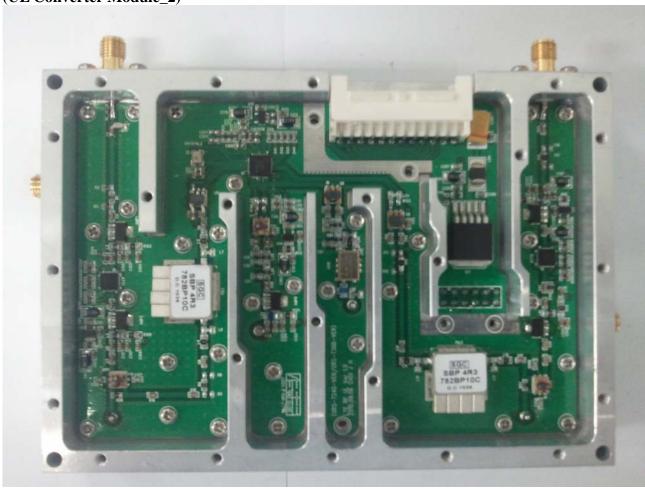
(UL Converter Module_1)







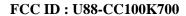
(UL Converter Module_2)





(DL Converter Module_1)

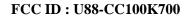






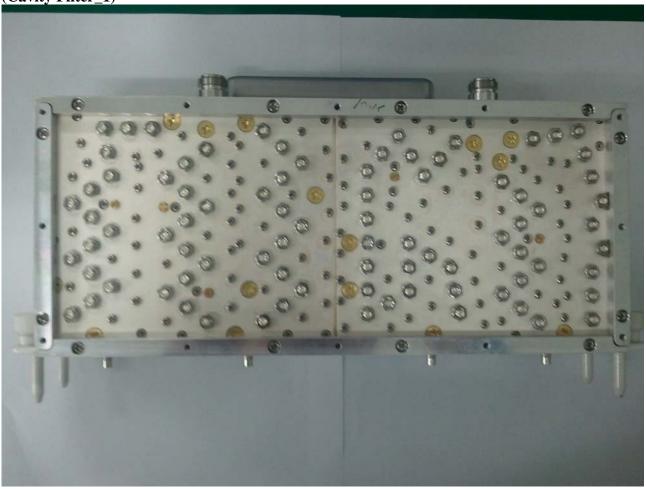
(DL Converter Module_2)



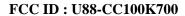




(Cavity Filter_1)

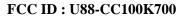


(Cavity Filter_2)





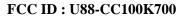




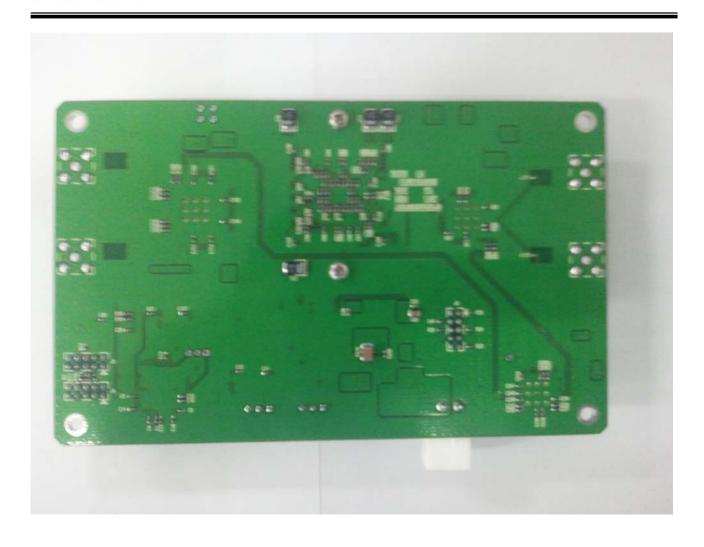


(Digital Filter_1)





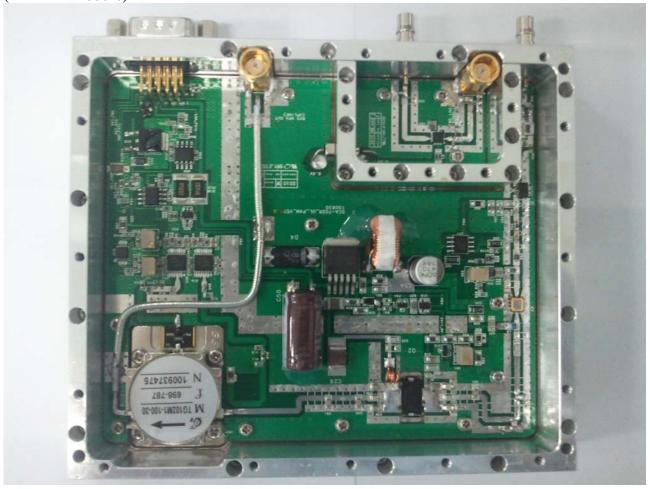








(DL HPA Module)



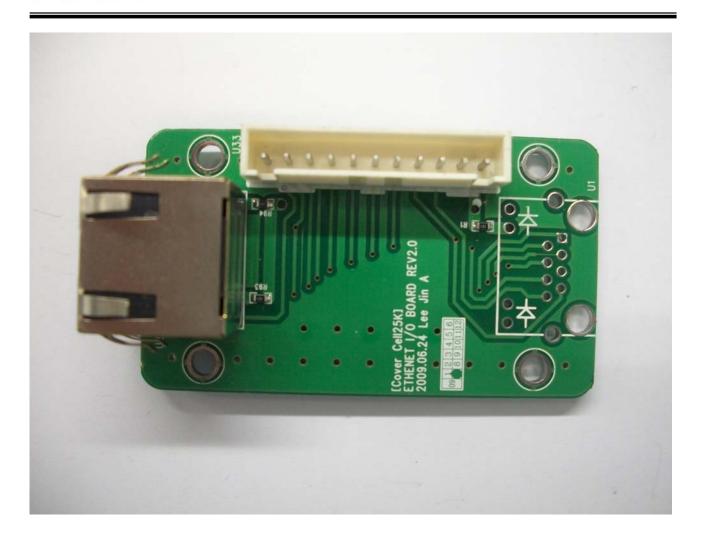


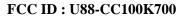
(UL HPA Module)



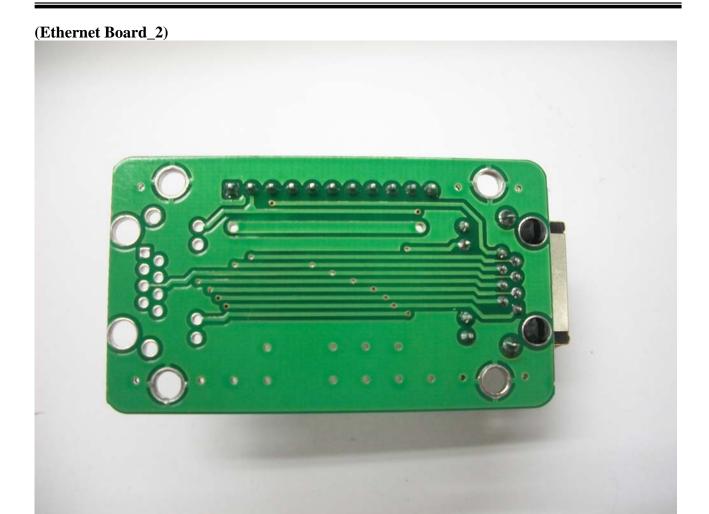


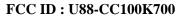






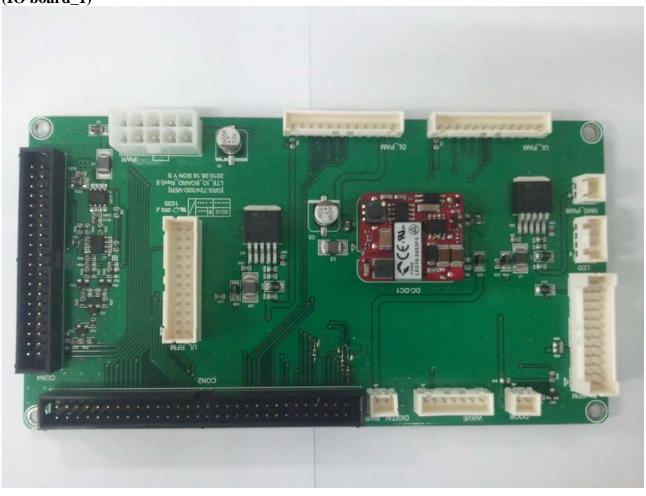


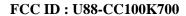






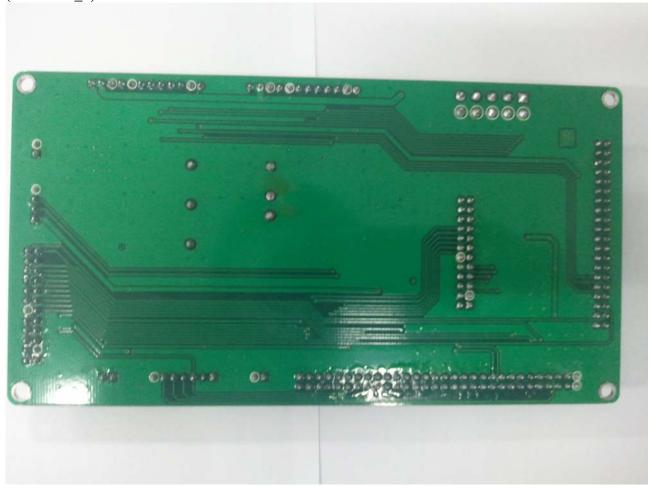
 $(IO\ board_1)$

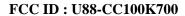




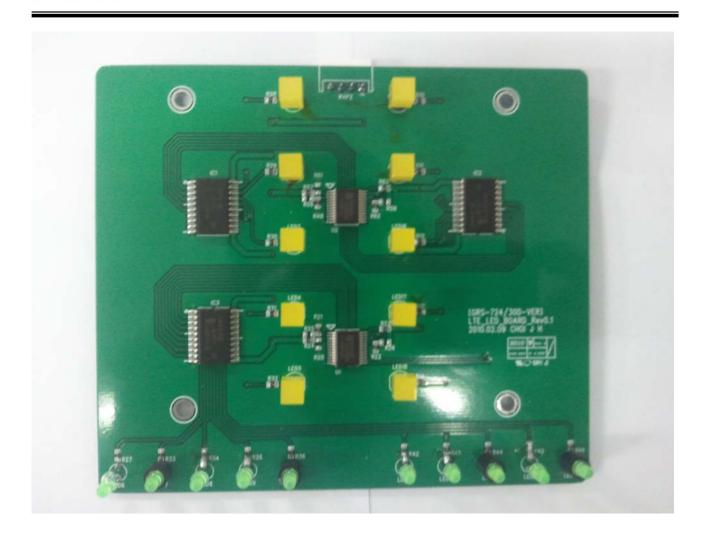


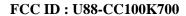
(IO board_2)





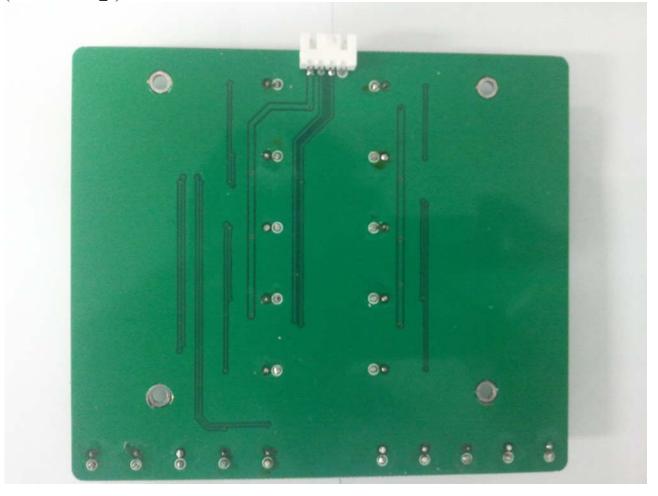


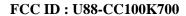






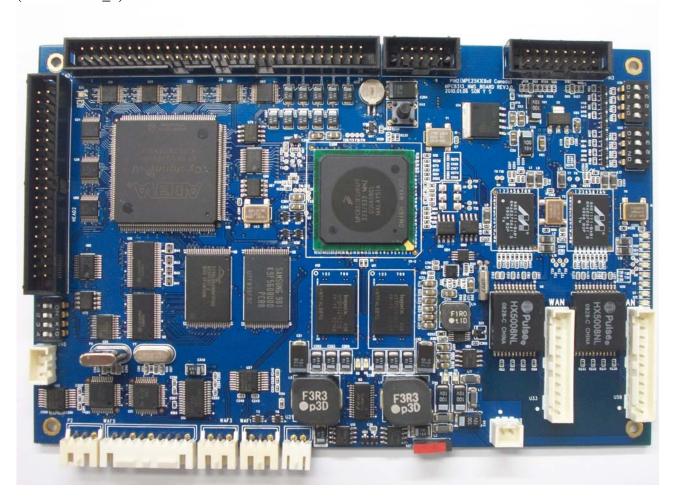
(LED Module_2)







(NMS board_1)





(NMS board_2)

