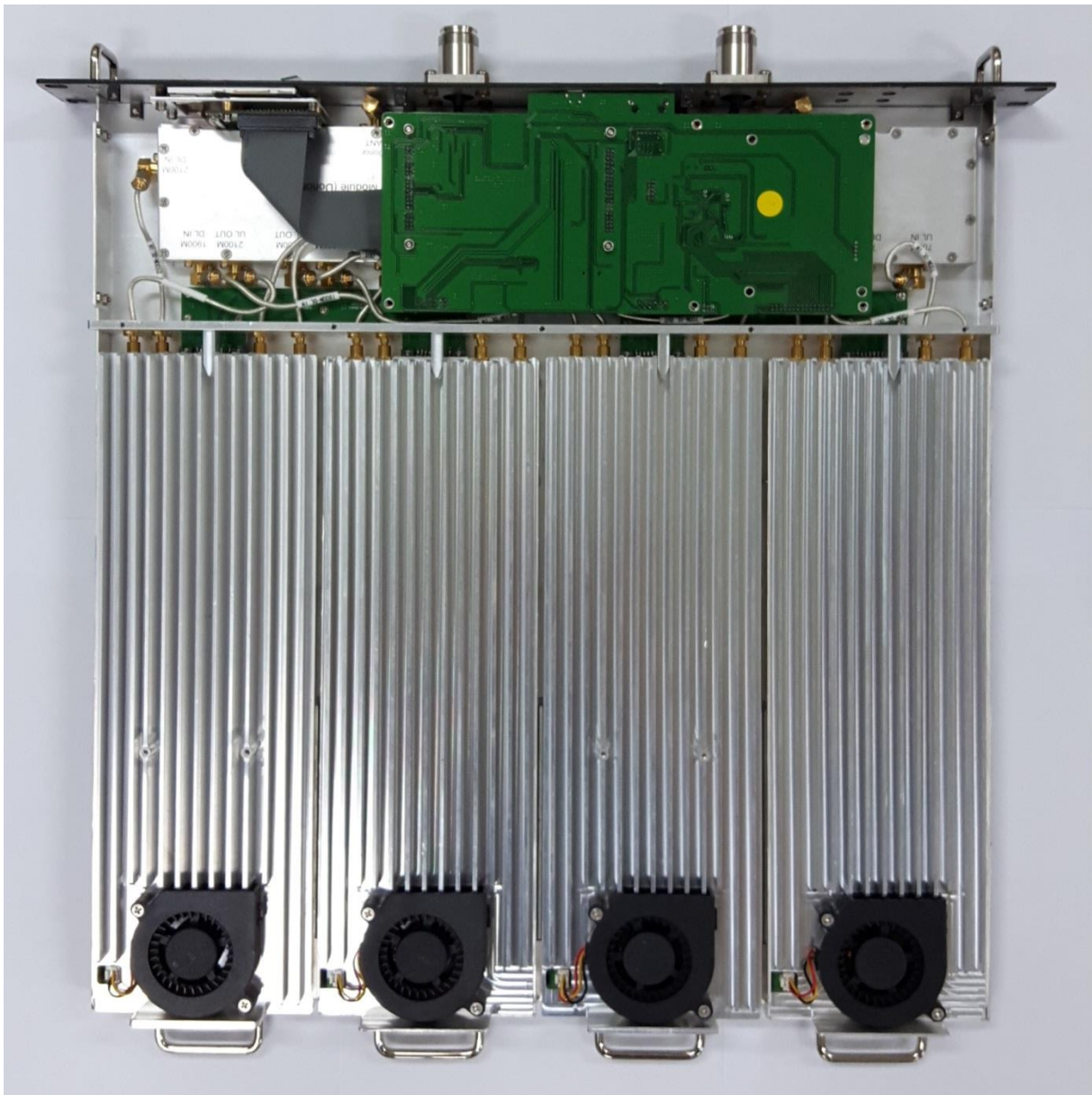
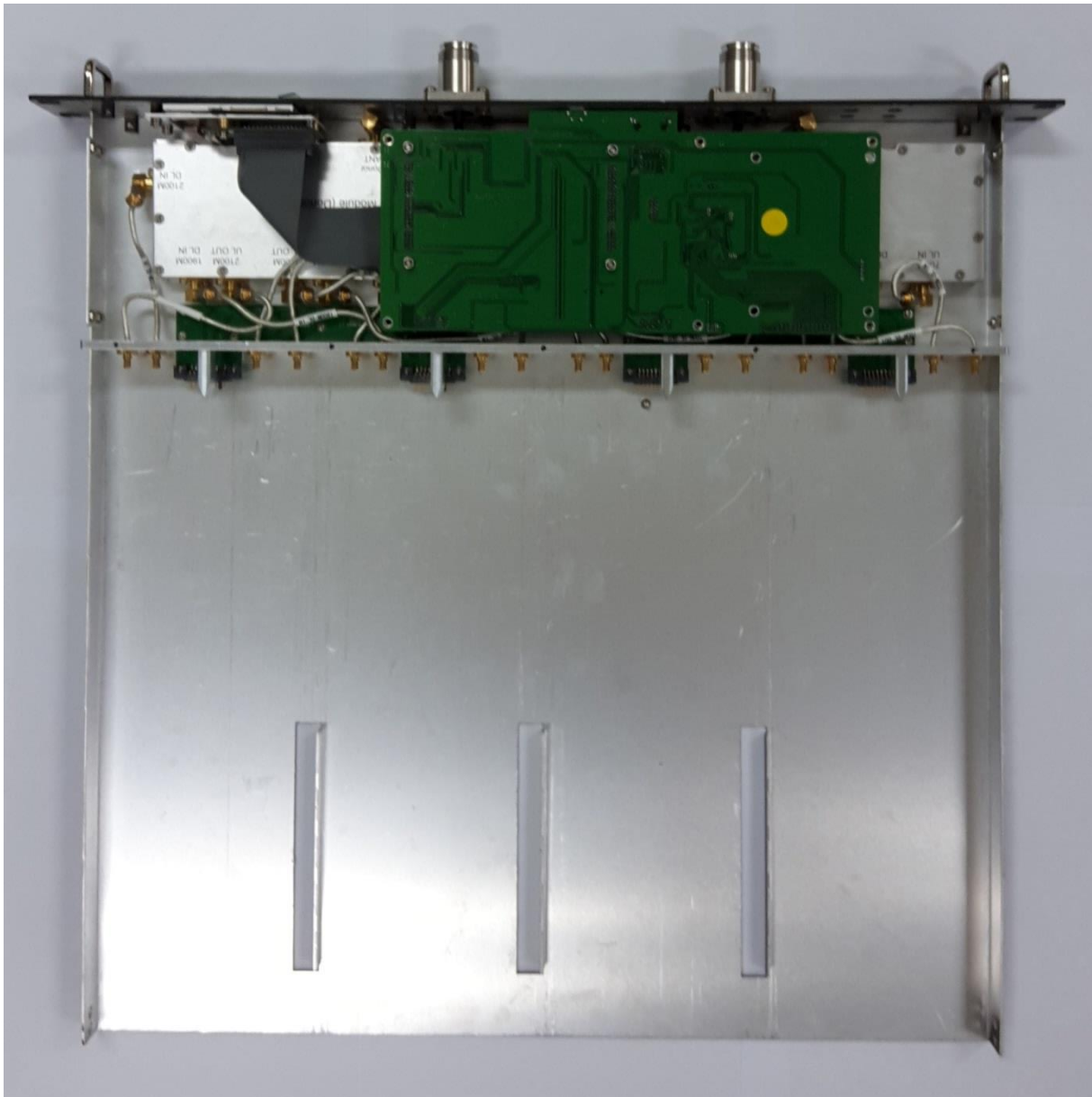


## **INTERNAL PHOTOGRAPHS**

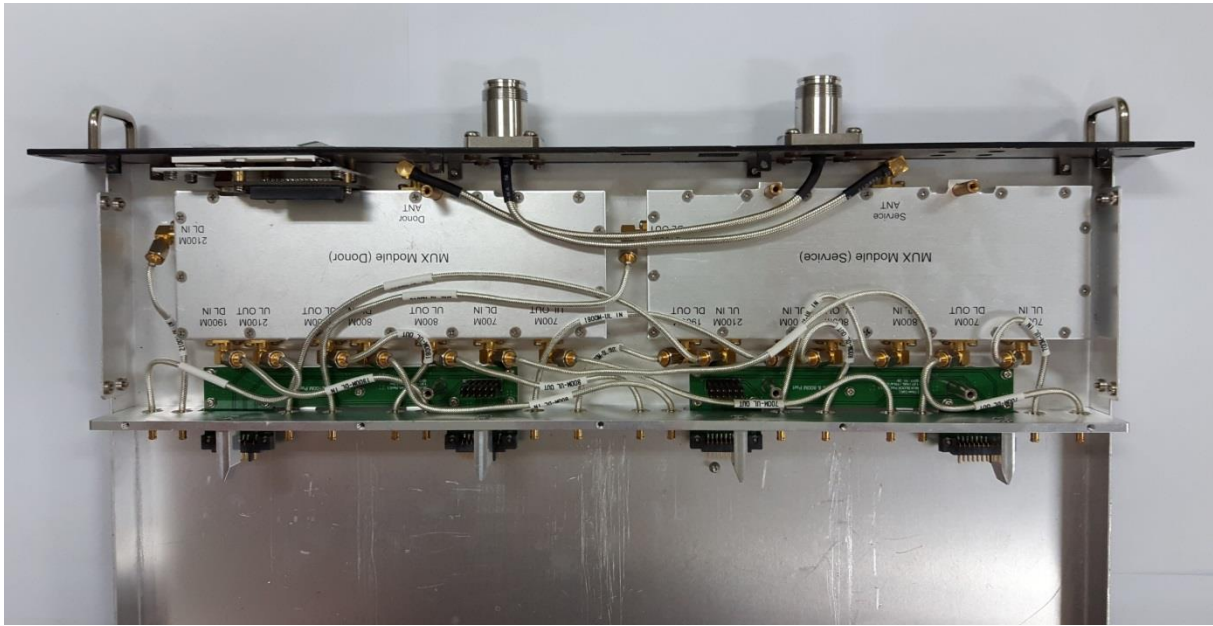
---



**[IN CASE]**



**[IN CASE]**

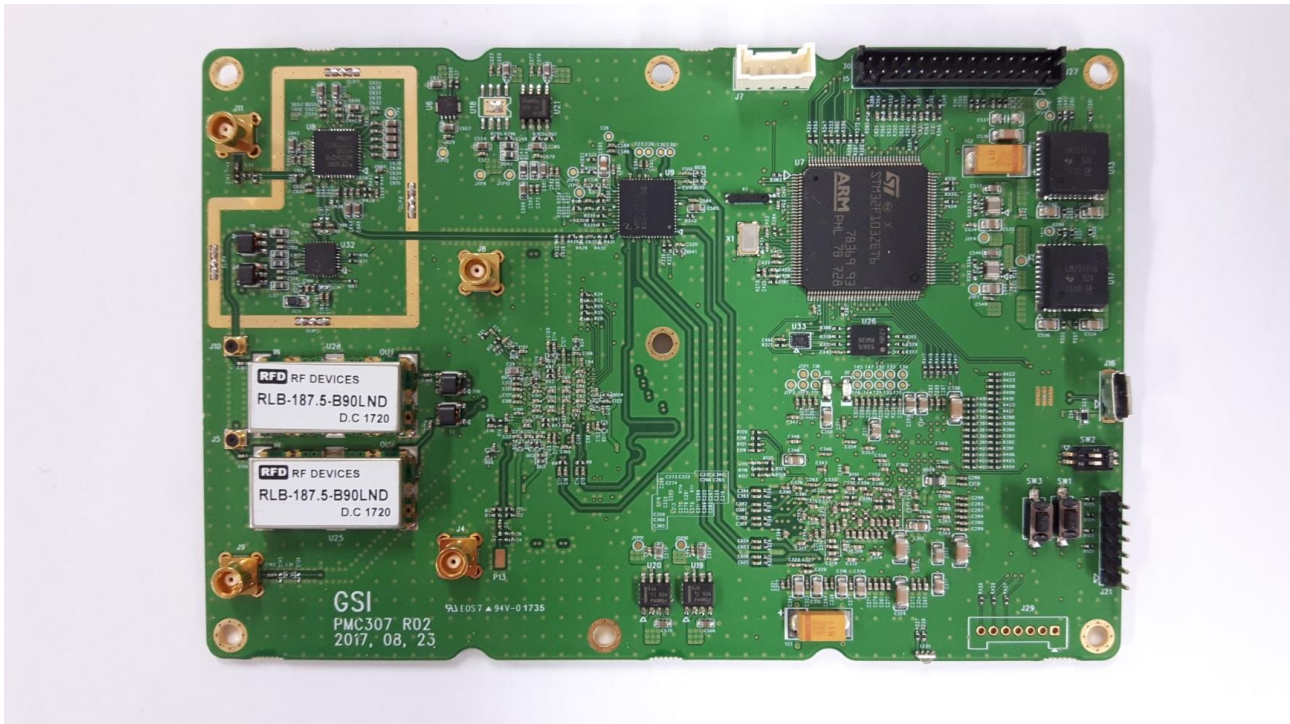


**[IN CASE]**

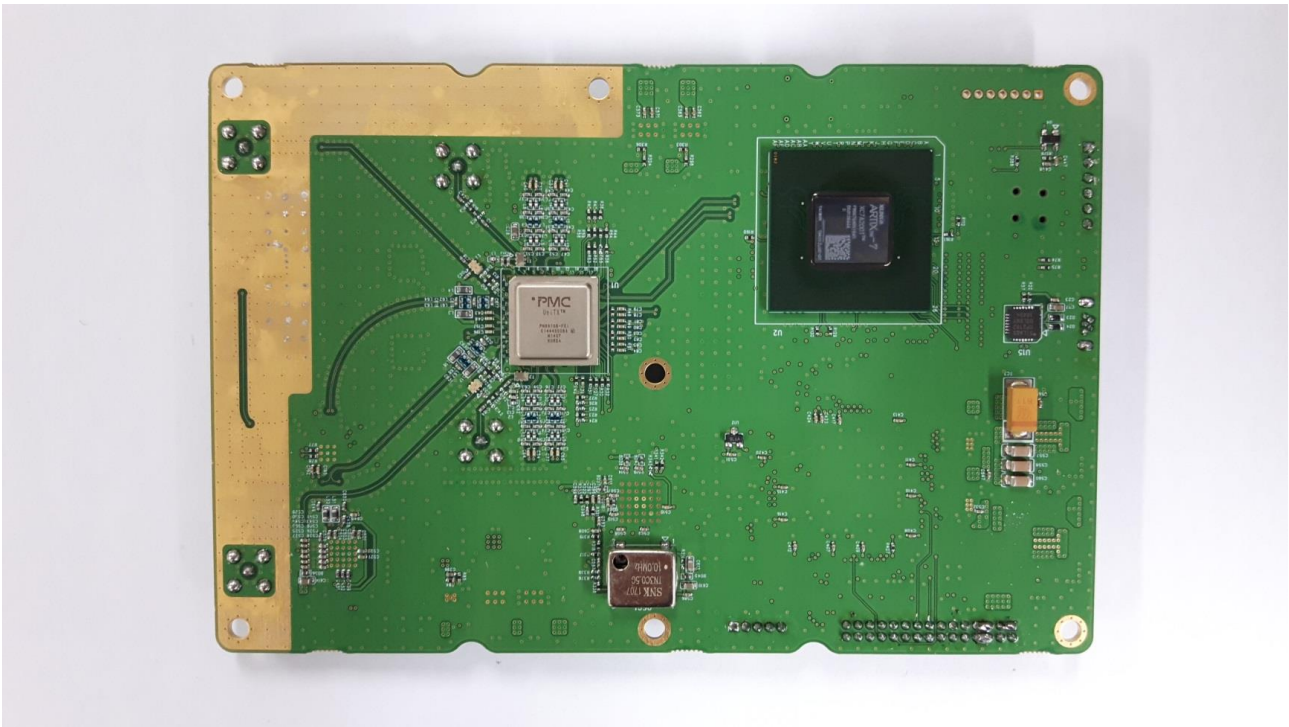


## **[Module - IN CASE]**

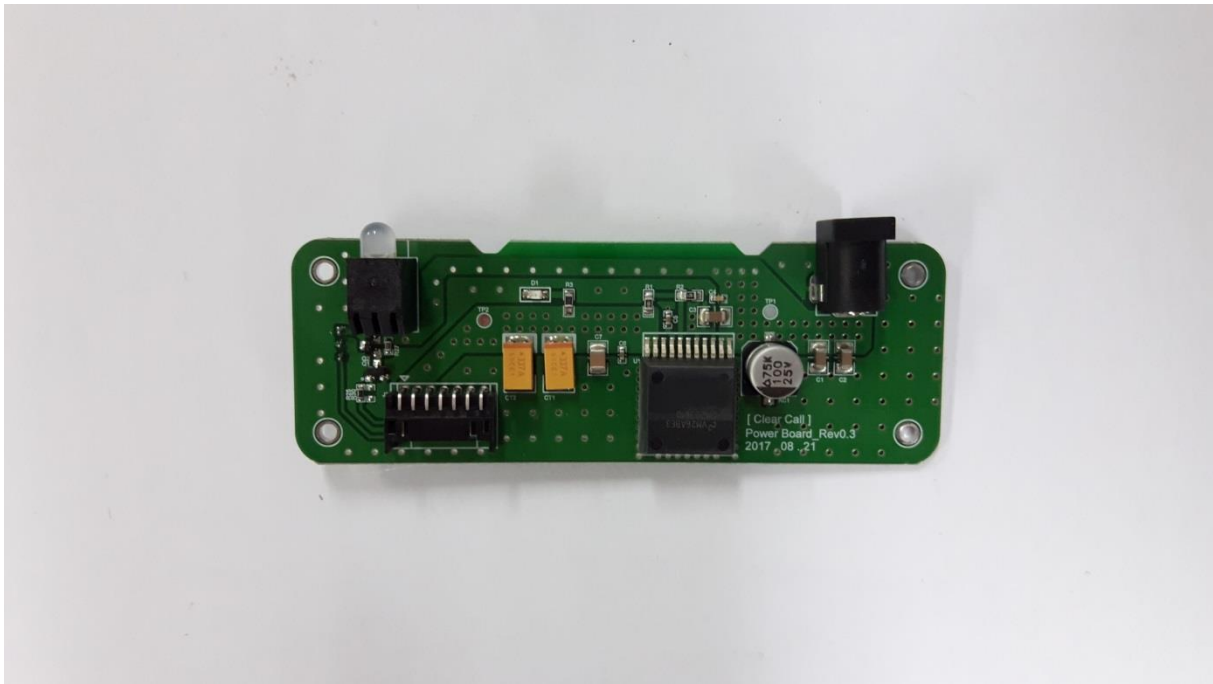




**[DFM - Top]**

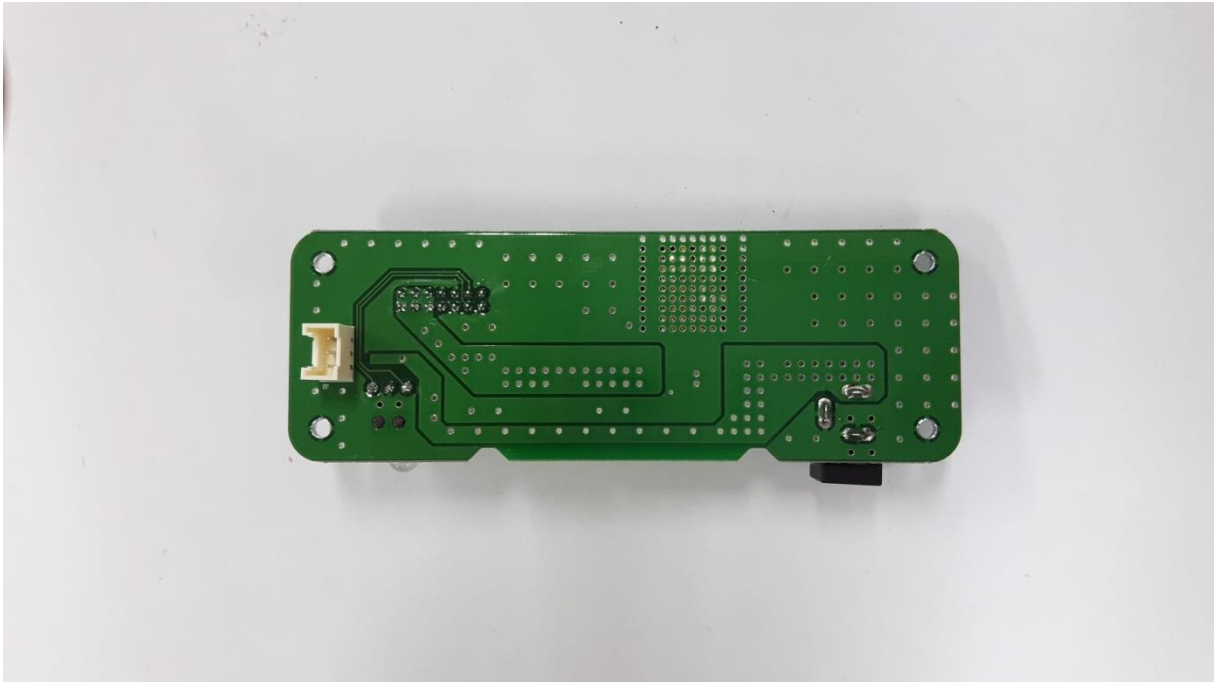


**[DFM - Bottom]**

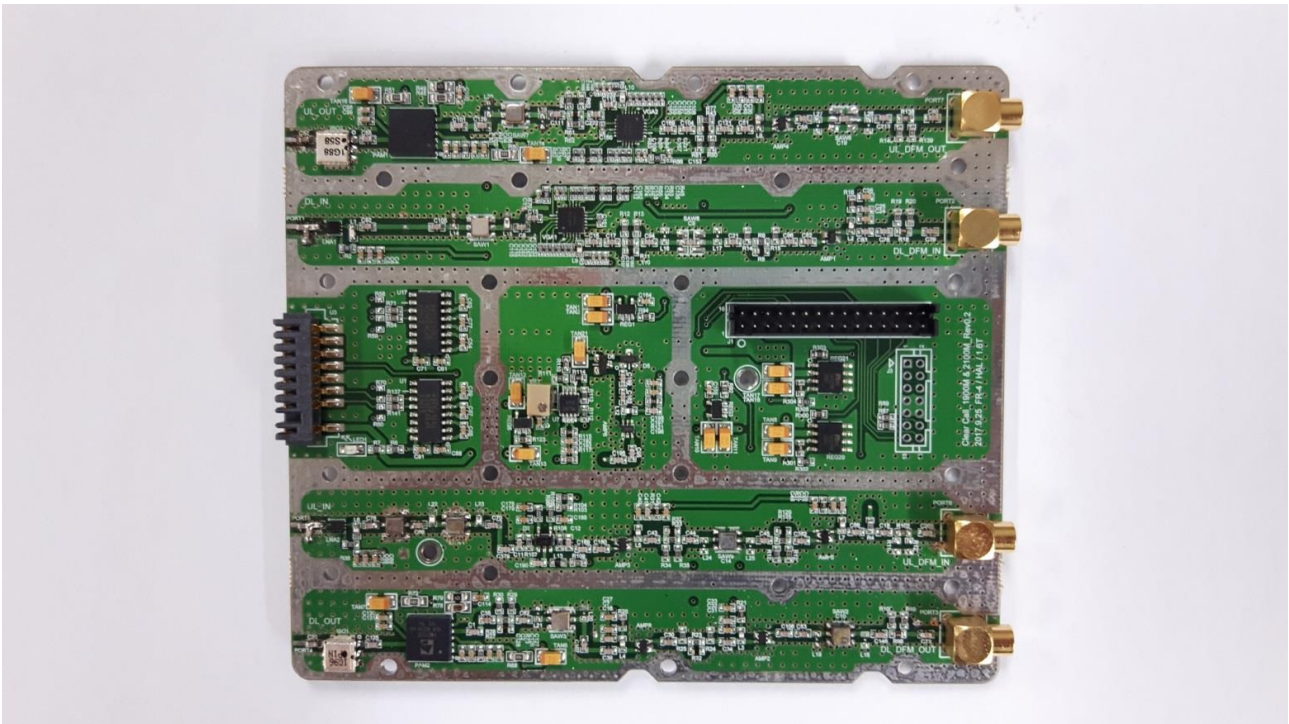


**[Power - Top]**

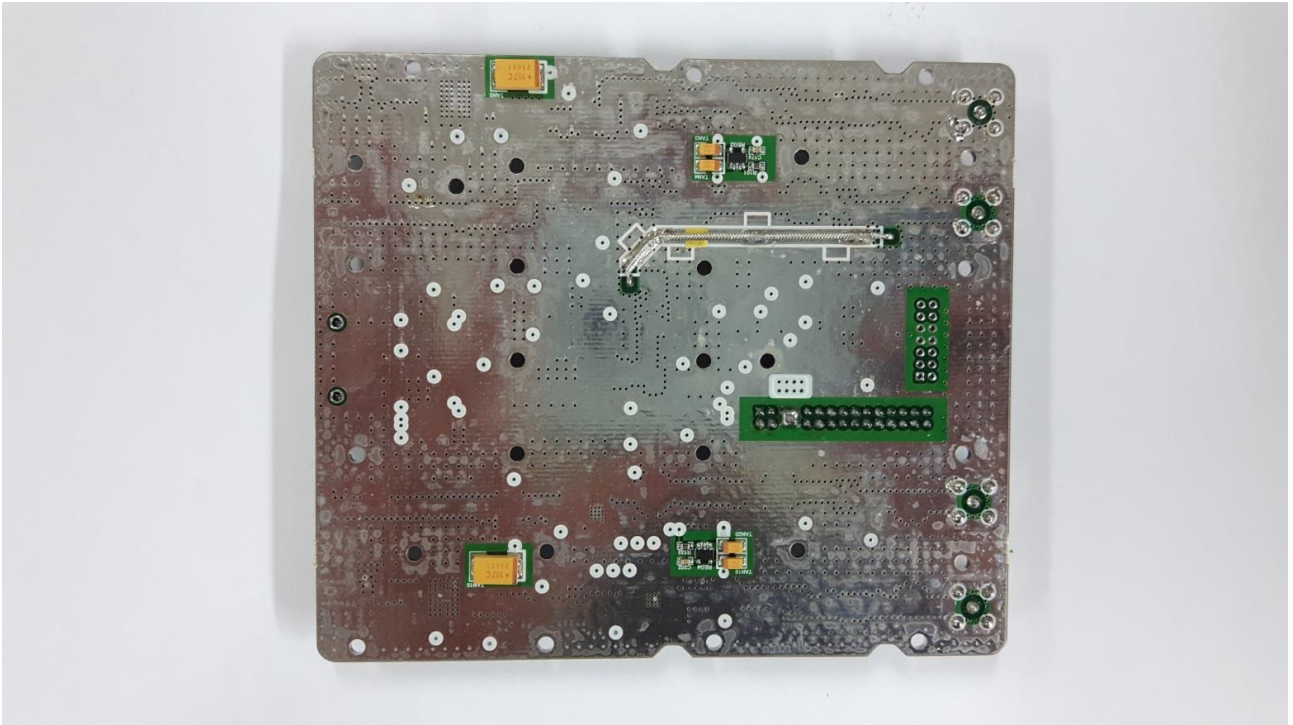




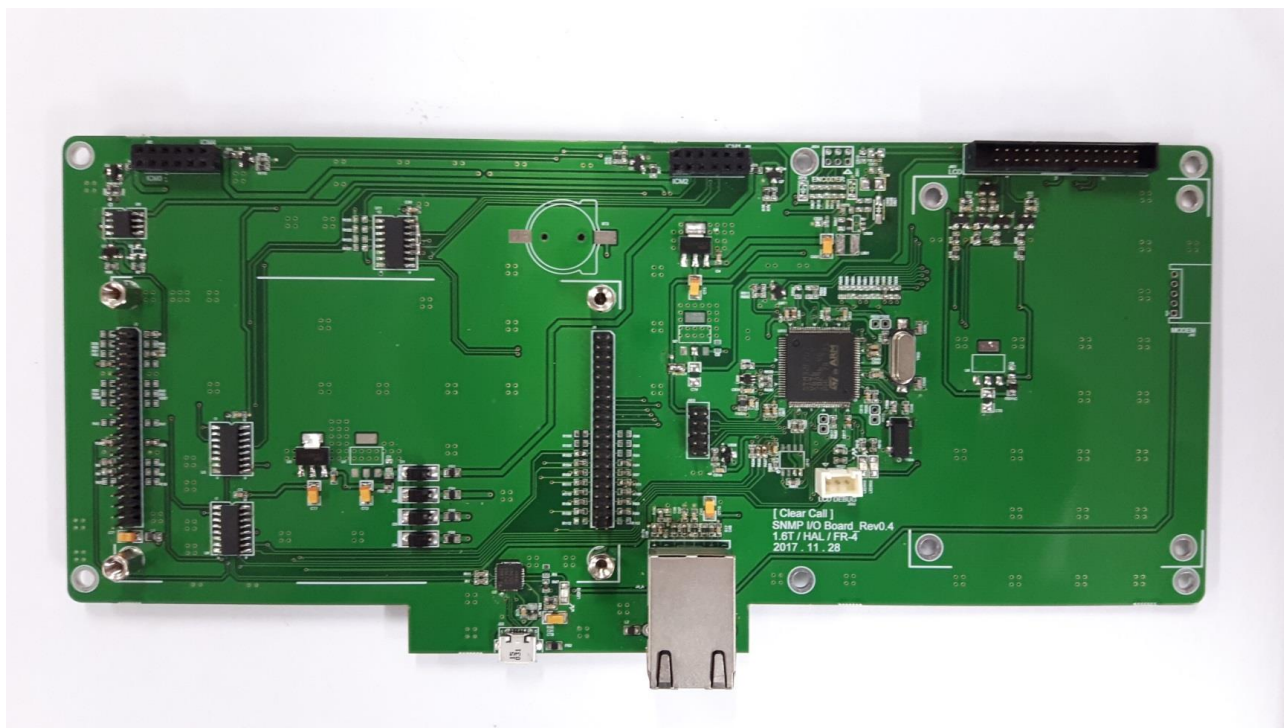
**[Power - Bottom]**



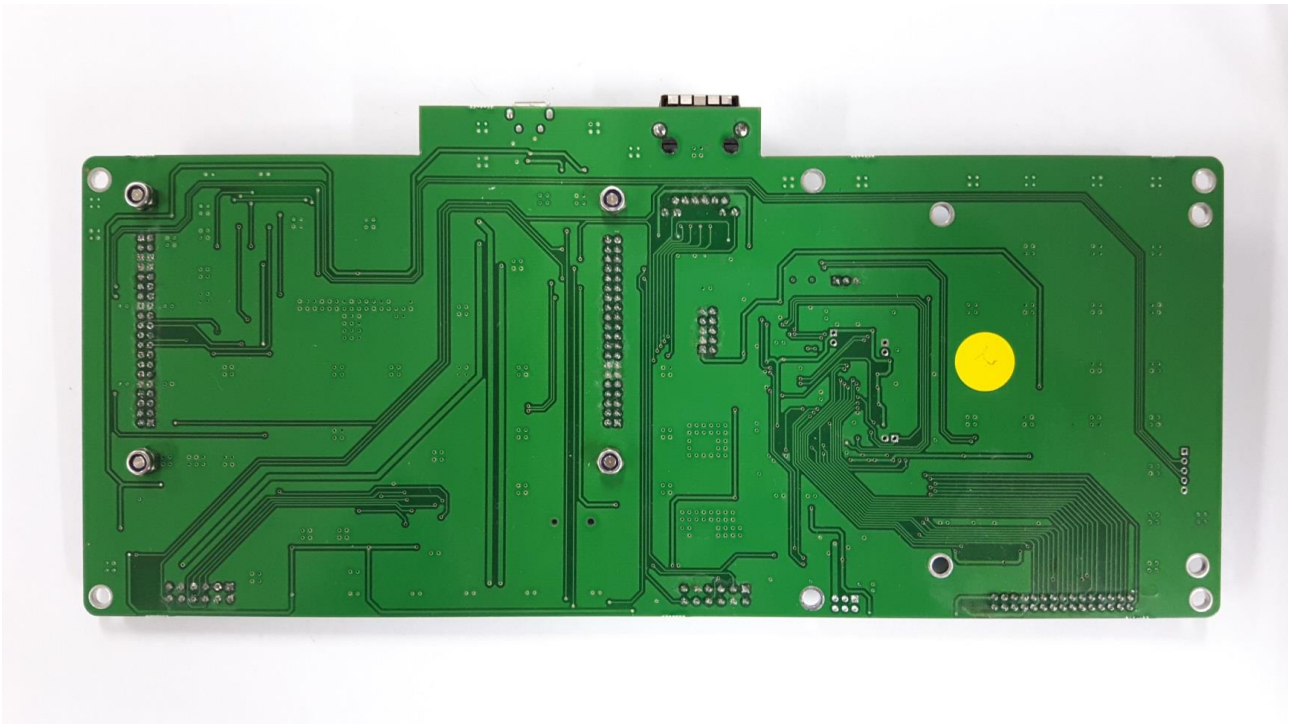
**[1900 MHz - Top]**



**[1900 MHz - Bottom]**

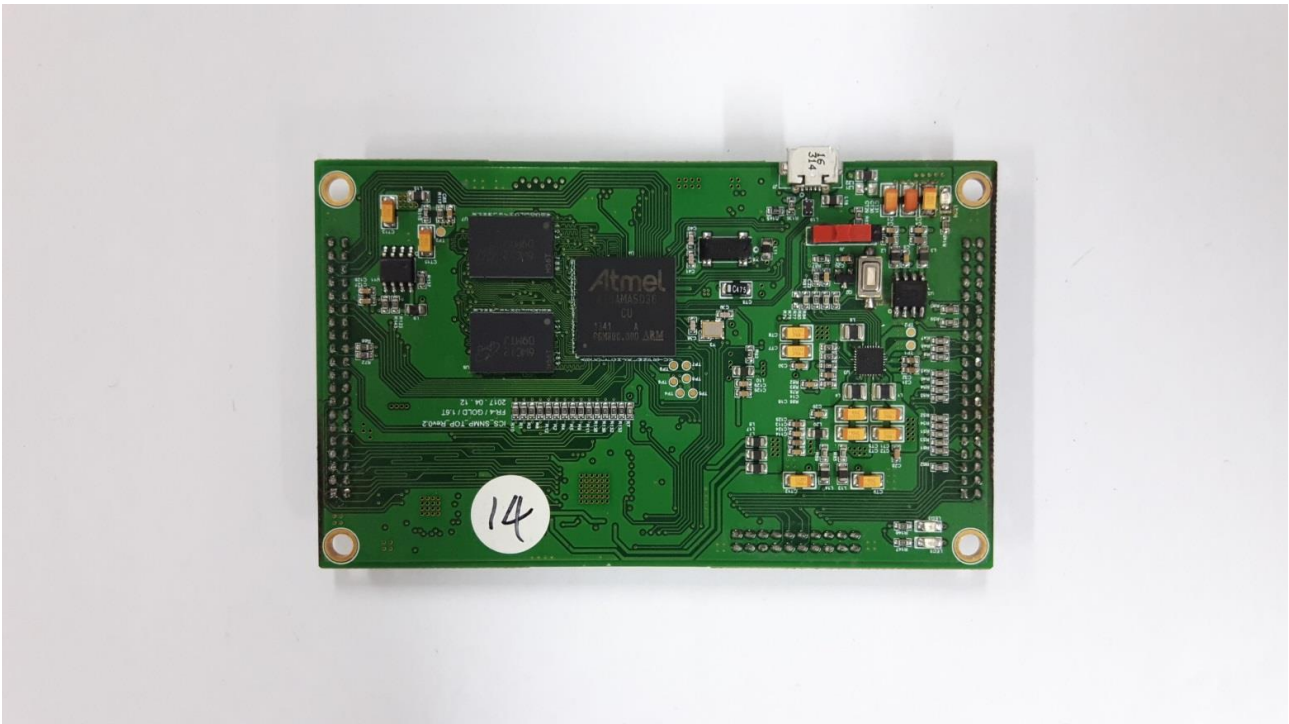


**[SNMP IO - Top]**



**[SNMP IO - Bottom]**

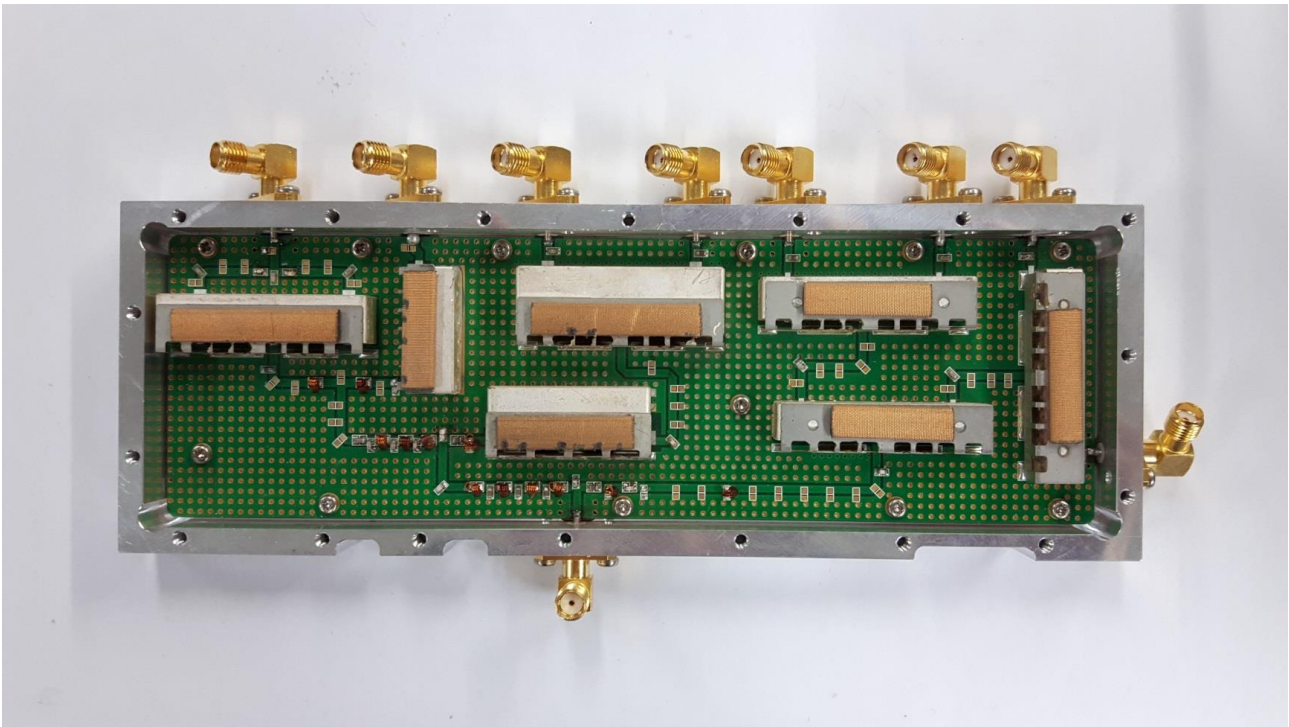




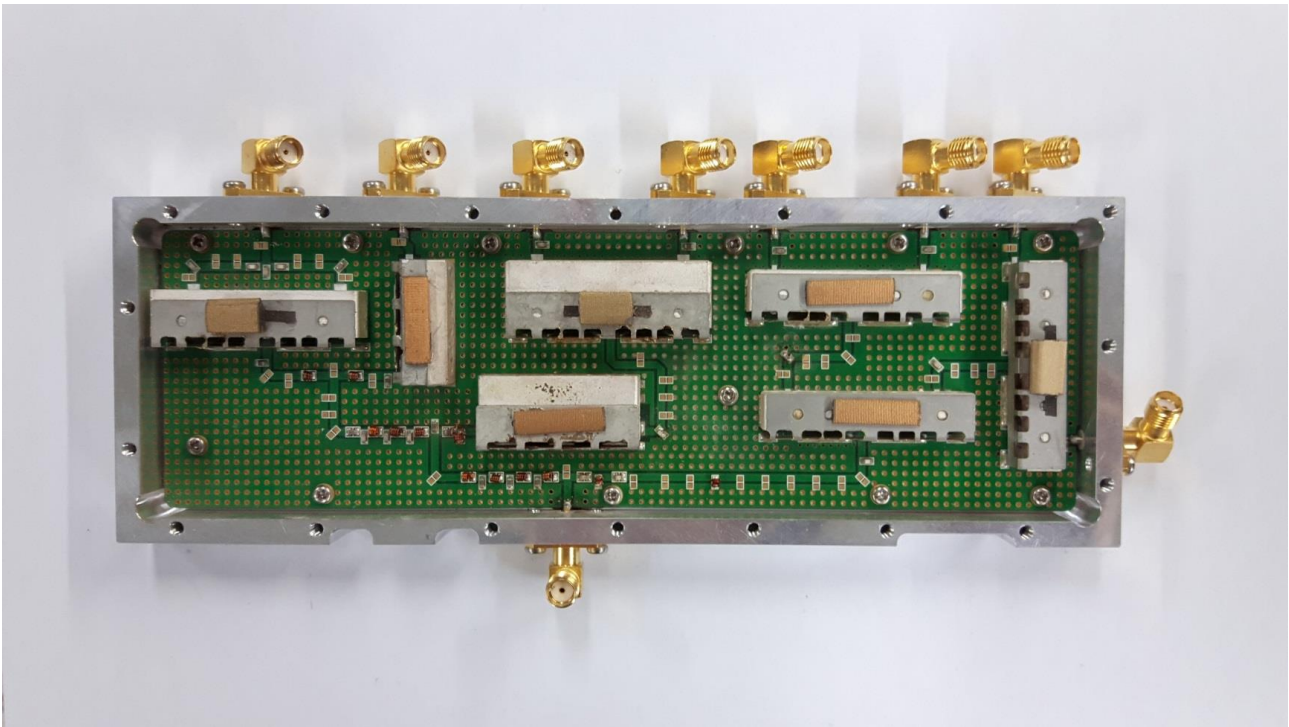
**[SNMP - Top]**



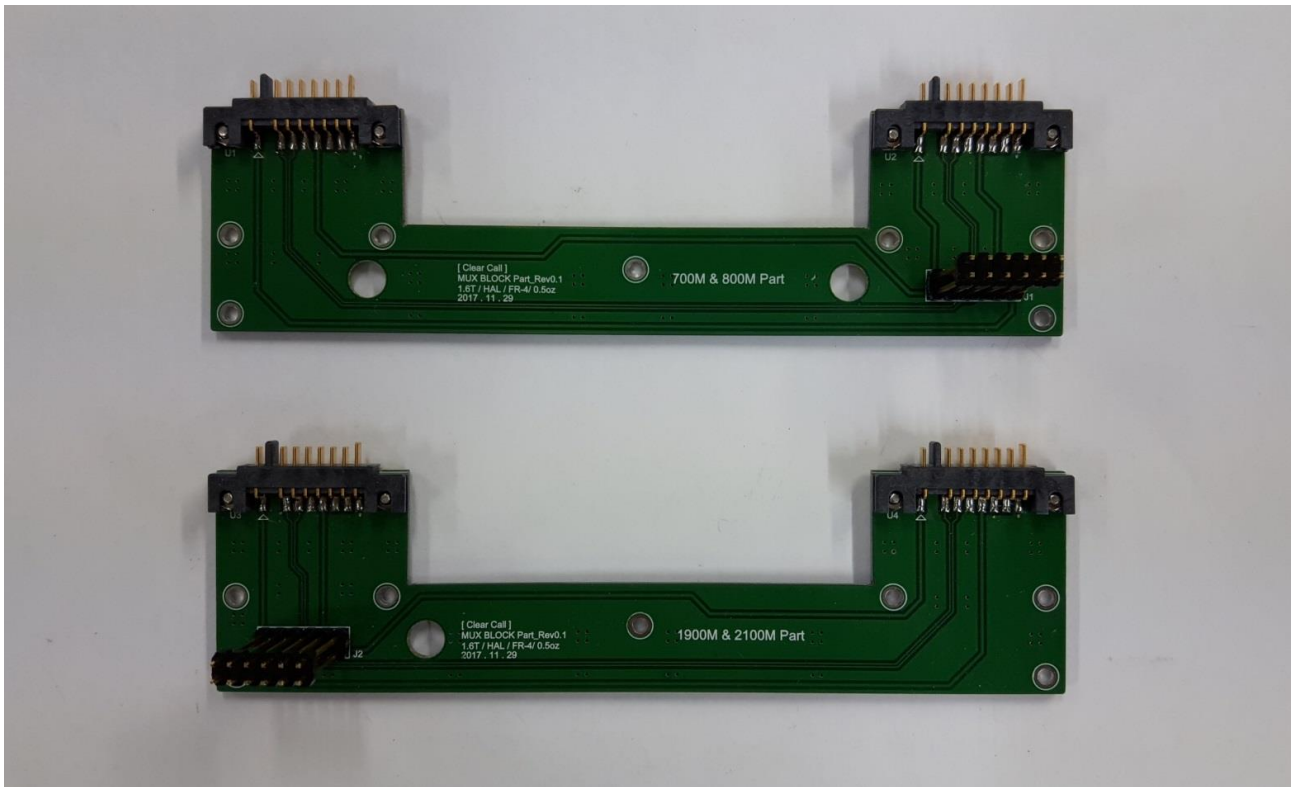
**[SNMP - Bottom]**



**[MUX Donor]**

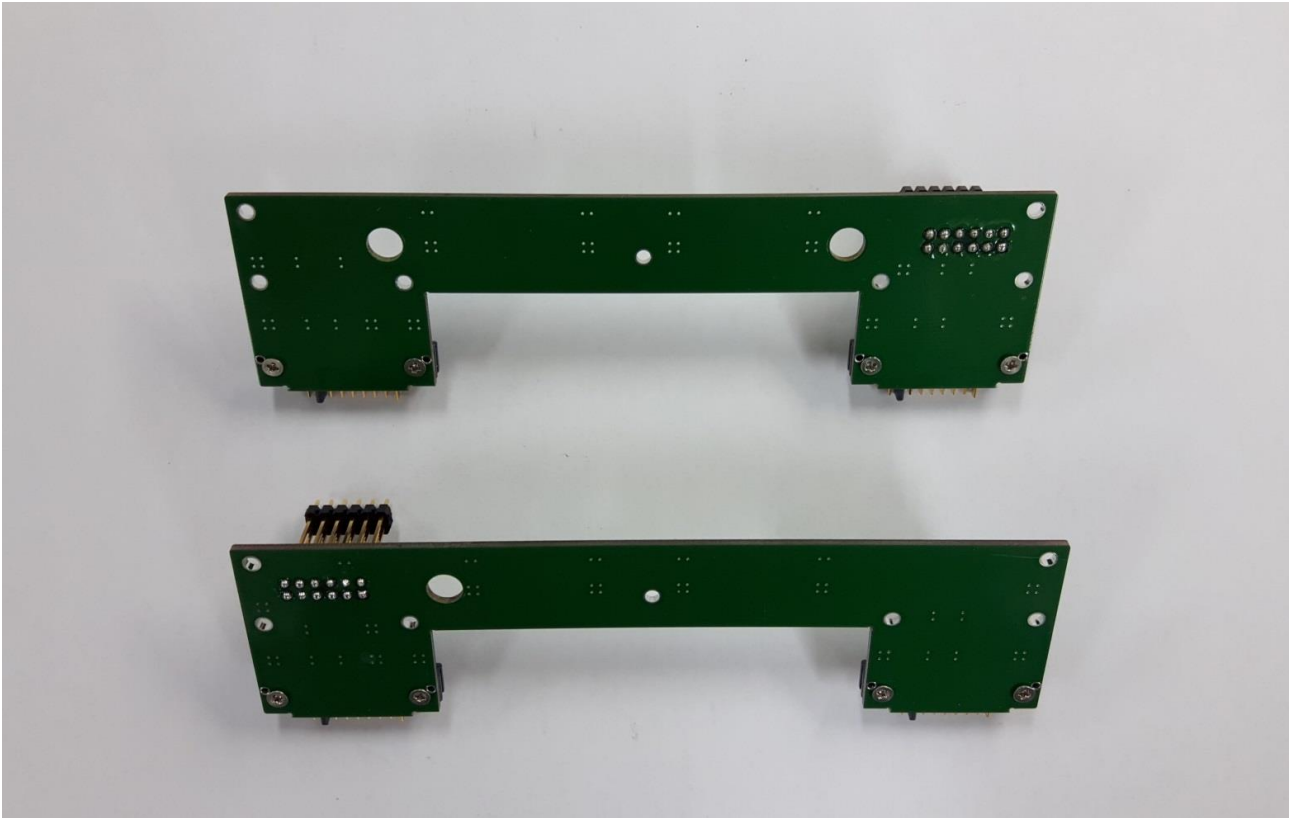


**[MUX Service]**

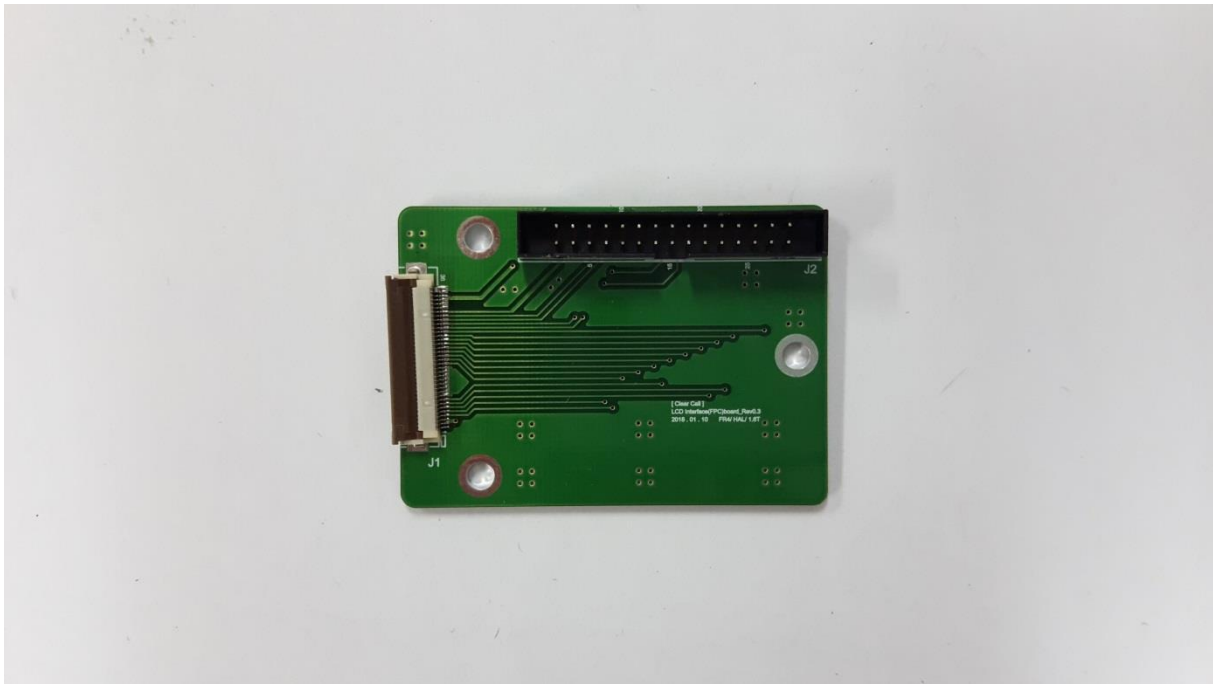


**[MUX Block - Top]**

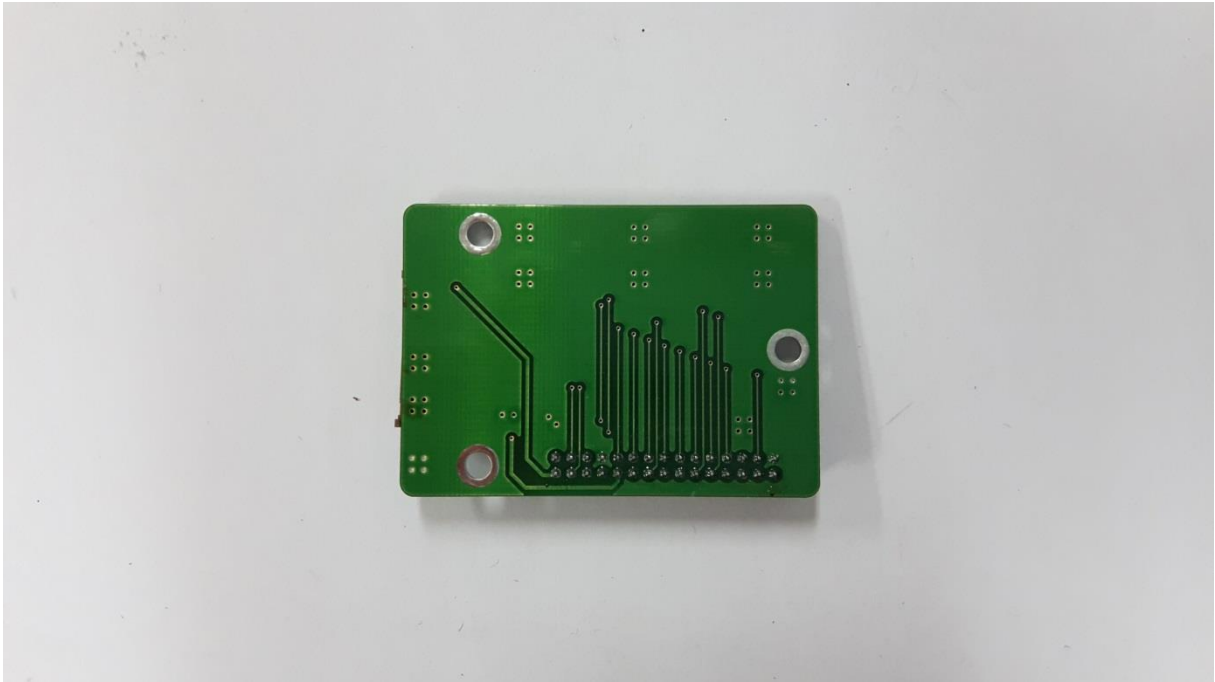




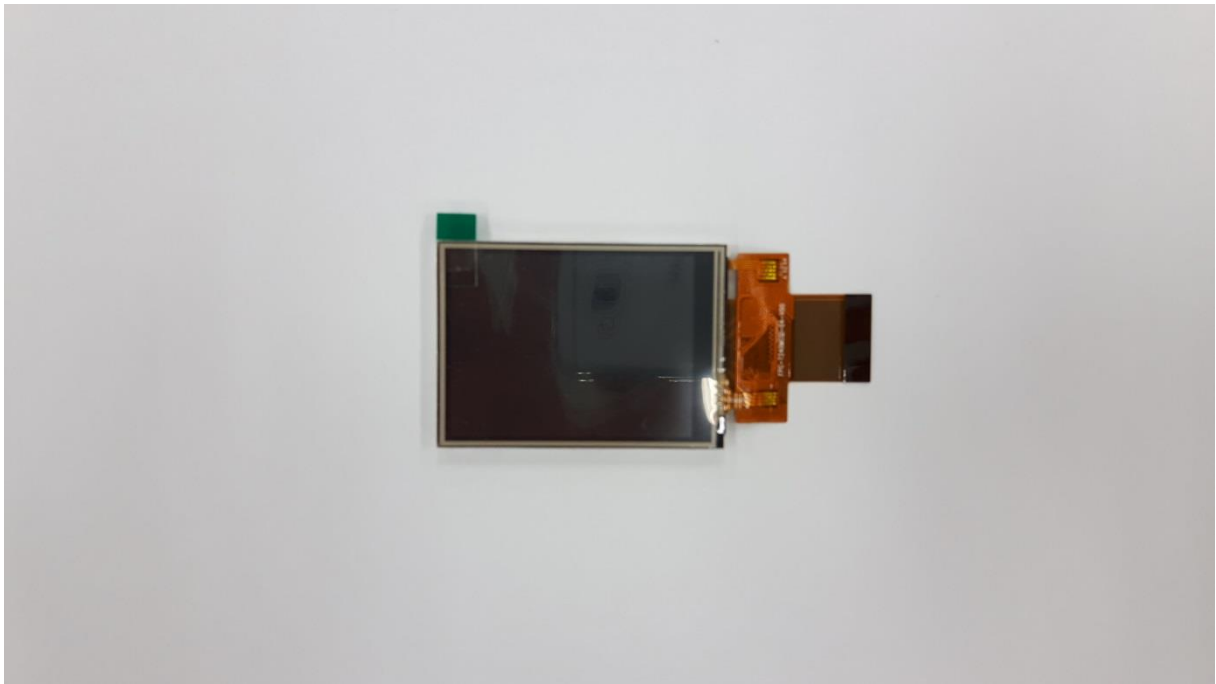
**[MUX Block - Bottom]**



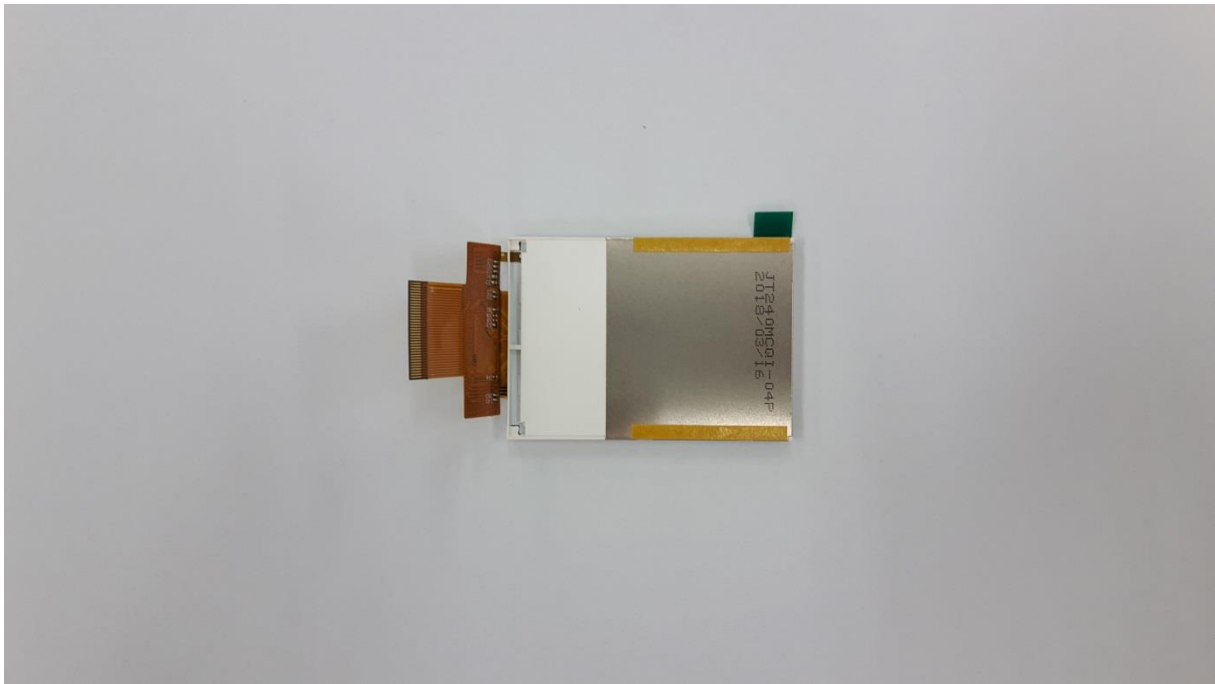
**[LCD Interface - Top]**



**[LCD Interface - Bottom]**



**[LCD - Top]**



**[LCD - Bottom]**