

## **EMC Block Diagram Form**

System Configuration Block Diagram -- Provide a line drawing identifying the EUT, simulators, support equipment, I/O cables, power cables, and any other pertinent components to be used during testing. Use a dashed line to separate the equipment in the testing field versus equipment outside testing field. Signal Conditioning RF Connector **RFIF Controller** Oscillator X1 13.56Mhz +-100ppm (U1)Processor (U2) Linear Regulator U5 5VDC RS-422 Driver (U3)13.56MHz - no other clocks are used.

Authorization Signatures		
101 - Park	2-13-08	
Customer authorization to perform tests according to this test plan.	Date	
Test Plan/CDF Prepared By (please print)	Date	· · · · · · · · · · · · · · · · · · ·