

## Briggs, Mark

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**From:** alex.gioe@meraki.net on behalf of Alex Gioe <agioe@cisco.com>  
**Sent:** Thursday, December 04, 2014 12:40 AM  
**To:** Briggs, Mark; Park, Daniel  
**Cc:** Monica Lam; Lee, Timothy K; Hsu, Ron; Leitner, Steven; Matthew Landry; Morgan Teachworth; Clinton Jang; Rincand, Grace; Raj Krishna  
**Subject:** Re: Cisco Systems, //UDX-60031010 //AN14T0623 Project 14U18854- TCB Comments  
**Attachments:** MR32\_KDB 594280 D02 v01r01 U-NII Device SW Security Statement FINAL 12-03-14.pdf; UDX-60031010\_FCC WLAN 15.407\_rev2 (15E).pdf; UDX-60031010\_Theory of Operation\_rev2 12-03-14.pdf

Mark, Daniel, All,

Here are the final 3 documents for the submittal. See pages 111-114 of the report for the 99% bandwidth plots. See answers below.

Please let us know as soon as this has been officially submitted to the FCC for PBA review.

Thank you for the support.

Best,  
Alex

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1. As this is following the new rules we will need the Security description required by the new rules in addition to the SDR description – this was made clear during the October training and so not brought up in our original comments. Attached is the template which needs to be completed asap.

*Meraki: The software security description has been uploaded.*

2. The 26dB plots show that the signal bandwidth (26dB bandwidth) for the high channel in each mode extends above 5250MHz. This means that those channels (channel 48 for 802.11a and HT20/VT20, channel 46 for HT40/VT40 and channel 42 for VT80) need to support DFS as a master and these channels also need to support TPC.

*Meraki: The test report has been modified to include 99% bandwidth plots for the high channels. In all cases the 99% bandwidth is fully contained below 5250 MHz. Since the 99% bandwidth is below 5250 MHz no DFS tests are applicable for the master device (radar detection is required over the 99% bandwidth of the signal and the 99% bandwidth does not fall into the DFS band).*

*Information has been added to the operational description to confirm that this device supports TPC.*