U2 — MA6221-S7K(B版，WCD版，FWGE版)

Pin	Left		Right	Pin
1	OSC1		VL_NM	28
2	VSS		VL	27
3	OSC2		DCVR	26
4	VDD		SVD_IN	25
5	RBUT		FBUT	24
6	MBUT		BBUT	23
7	LBUT		TXCB	22
8	RFOUT		TYPE	21
9	ORFIN0		BATL	20
10	ORFIN1		SCLK	19
11	ORFO		SDIO	18
12	TXD		Z2IN	17
13	PD		Z1IN	16
14	WA		WB	15

SW_R, SW_M, SW_L, SW_ID, SW_Z, ENCODER

VCC

U1 — PAN3101

Pin	Left		Right	Pin
1	OSC_IN		REFA	8
2	OSC_OUT		VDD	7
3	SDIO		GND	6
4	SCK		LED_CNTL	5

X3 18.432

Q4 2N3906, Q5 2N3906

R9 1K, R12 *68R, R11 100K, R10 1K, R13 20K*

C19 0.1uF, C17 0.1uF, C18 10uF

LED_CNTL LED1

PD, SDIO, SCLK

C1 22P, C2 22P, C3 104
X1 76.8K, R1 10M
C9 75P, L1 1.8uH, C4 33P
L2 0.82uH, C10 22P, C11 24P, C12 12p
C6 22p, X2 27.042MHz, C8 3P, C5 33P

PCB ANT

L3 82uH, C18 104, C17 470U
VBT, VBAT1, UM-4(X2)

D1 RB421, DCVR

VDD3, SVDIN

C19 104, C20 220U

DCVR, R5 *220K, R6 *100K

SVDIN, R3 1.2M, R4 680K

TYPE, J31H 0R, J31L 0R / 10nF, VDD3

LED2 BTL LED, R2 560

R20 short or open

	B版	WCD版	FWGE版
R20	open	open	short
motion wake up	no have	no have	have

Datecode=Bxxxxxxxx			
	Sensor -90	Sensor 0	Sensor +90
J31H	X	0	X
J31L	0	X	10nF

Datecode=FWGExxxxxx			
	Sensor +90	Sensor 0	Sensor -180
J31H	X	0	X
J31L	0	X	10nF

Title	MA6221-S7K-one channel-PAN3101		
Size A4	Number MA6221		Revision
Date: 29-Jul-2006		Sheet of	
File:	C:\PROGRAM FILES\DESIGN EXPLORER\EXAMPLES\Ma6221+PAN3101-1C-060414.DDB		