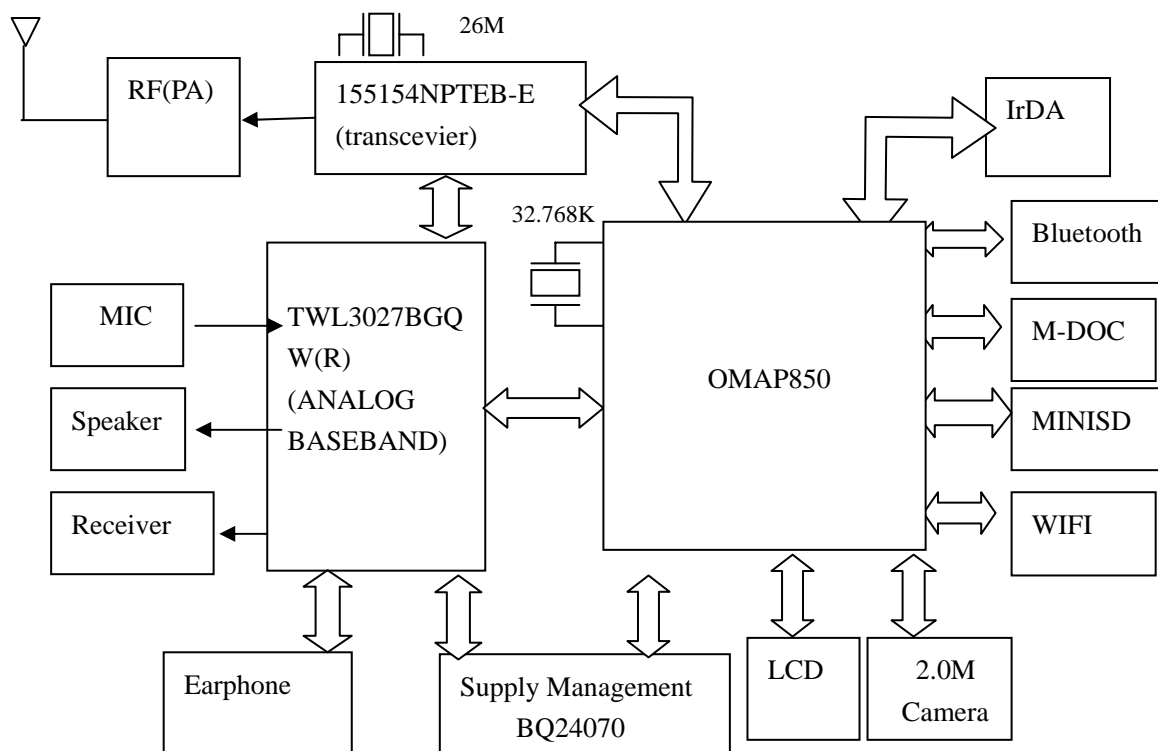


PAM Block Diagram

1.1 System Block Diagram:



1.2 Major device list

U201	OMAP850
U202	TWL3027BGQW(R)
U307	MD8832-D1G-V3-X-P
U5	MT46H32M16LFCK-10
U6	BQ24070RHLR
U682	155154NPTEB-E
U603	PF09016B-TB
E1	MAX7349AEG
U780	RT9284A-15PJ6
U900	BRF6150
U905	WLAN6100EB
U601	TOH2600DPM4DKC (26M 晶振)
Y201	SSP-T6 (32.768KHZ, 20ppm, 12.5pf)

1.3 Main device introduction

U201	Dual-Core Processor
U202	GSM/DCS Baseband and Voice A/D and D/A RF Interface Circuit With Power

Supply Management

U307	1Gbit Flash
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U5	SDRAM(DDR), 512Mbit
U6	SINGLE-CHIP CHARGE AND SYSTEM POWER-PATH MANAGEMENT IC
U682	RF Transceiver IC for GSM850/900, DCS1800 and PCS1900 Triple band cellular systems
U603	MOS FET Power Amplifier Module for Quad-band GSM / GPRS / EDGE Handy Phone
E1	2-Wire Interfaced Low-EMI Key Switch and Sounder Controllers
U780	Low-Power Triple-Output TFT LCD DC-DC Converter
U900	Bluetooth IC
U905	Wifi IC

1.4 Platform benchmark

N/A

1.5 Power consumption

Call max current < 600 mA

Standby current < 6 mA

Off current < 1 mA

1.6 Antenna

Internal Monopole Antenna

1.7 speaker

IEA 0.5W

1.8 Power management

U202 supply for Baseband and Voice A/D and D/A RF Interface Circuit

U309 supply for V-io-2

U301 supply for V-memory

U7 supply for Vcore

U6, U9 supply for camera